

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
9 October 2003 (09.10.2003)

PCT

(10) International Publication Number
WO 03/083505 A1

(51) International Patent Classification⁷: **G01S 7/28**,
7/483, 7/486, H01Q 3/26

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(21) International Application Number: PCT/US03/09452

(22) International Filing Date: 26 March 2003 (26.03.2003) (81) Designated States (*national*): CN, JP.

(25) Filing Language: English

(84) Designated States (*regional*): European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR).

(26) Publication Language: English

(30) Priority Data:
10/106,335 26 March 2002 (26.03.2002) US

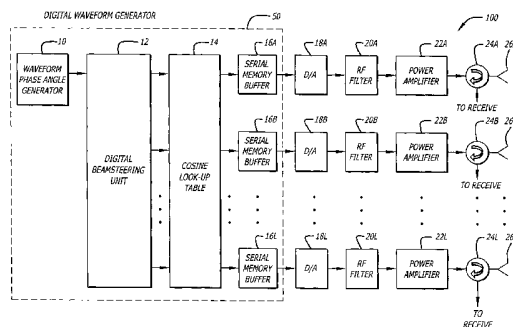
Published:
— with international search report
— before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments

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For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: EFFICIENT WIDEBAND WAVEFORM GENERATION AND SIGNAL PROCESSING DESIGN FOR AN ACTIVE MULTI-BEAM ESA DIGITAL RADAR SYSTEM



(57) Abstract: An efficient digital wideband waveform generation and signal processing system for multi-beam electronically scanned antenna array (ESA) radar systems. The digital waveform generator (50) first produces a series of waveform phase angles corresponding to the phase angles of a common signal to be transmitted by the ESA. Then, for each antenna element, the waveform phase angles are adjusted to steer the beam by adding a time delay and a phase adjustment. A digital signal is then produced for each antenna element by applying a cosine function to the adjusted phase angles. In the illustrative embodiment, this is accomplished using a cosine look-up table (14). Each digital cosine wave is then stored in a serial memory buffer (16A to 16L) in preparation for conversion to digital. Each of digital signals is passed through a digital to analog converter (18A to 18L) and an RF filter (20A to 20L) at each antenna element. The resulting analog signals are each amplified by a power amplifier (22A to 22L), and then sent through a wideband circulator (24A to 24L) to an antenna element (26A to 26L). On receive, at each antenna element (26A to 26L), a wideband linear amplifier (30A to 30L) and an RF filter (32A to 32L) provide rejection of out of band signals while providing amplification and signal conditioning for the following analog to digital converter (34A to 34L). The resulting digital signals (one for each element) are sent through the (36A to 36L) which separate each digital signal into N frequency channels. A digital beam formation unit (38A to 38N) for each frequency channel then forms multiple digital simultaneous beams by combining like frequency channels from each antenna element with an added phase adjustment.

**EFFICIENT WIDEBAND WAVEFORM GENERATION
AND SIGNAL PROCESSING DESIGN FOR AN
ACTIVE MULTI-BEAM ESA DIGITAL RADAR SYSTEM**

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BACKGROUND OF THE INVENTION

Field of the Invention:

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The present invention relates to radar systems. More specifically, the present invention relates to digital waveform generation and receive beam formation in a multiple beam electronic scan antenna array radar system.

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Description of the Related Art:

Certain radar applications use multiple simultaneous transmit and receive beams to enhance their search rate capability. Using multiple simultaneous beams helps maintain situational awareness and allows advanced processing techniques such as clutter cancellation and improved anti-jam capabilities.

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Multiple simultaneous beams can be generated using an electronically scanned antenna array (ESA). An ESA is an array of several individual antenna elements which work together to form a desired overall antenna pattern. Each antenna element radiates a common signal altered in phase. The direction of the antenna beam can be controlled by varying the phase of the signals driving the individual antenna elements. Thus, it is required that the signal along each path between a signal source and the antenna elements have a controlled phase.

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Conventionally, ESA use analog antenna manifolds to distribute outgoing

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signals with the correct phase adjustments to the antenna elements in the transmit mode, and to coherently combine antenna element signal components in the receive mode. Such manifolds include complex circuits such as feed antennas, feed distribution networks, and corporate feeds. These manifolds can get very large and heavy in proportion to the number of elements in the antenna array.

The Butler matrix has been used to feed an antenna array for generating multiple simultaneous beams. The Butler matrix, however, suffers from insufficient bandwidth with respect to current more demanding applications, as well as being an inherently complex and heavy piece of equipment.

Hence, a need exists in the art for a system or method for efficient waveform generation for a multi-beam electronically scanned antenna array that offers wide bandwidth, low cost, and low weight.

SUMMARY OF THE INVENTION

The need in the art is addressed by the efficient digital wideband waveform generation and signal processing design for electronically scanned antenna array (ESA) radar systems of the present invention.

On transmit, a digital waveform generator produces digital forms of the signals to be transmitted by the individual elements of the antenna array. A different signal is produced for each element. Each of these digital signals is passed through a digital to analog converter (D/A) and a radio frequency (RF) filter at each antenna element. The resulting analog signals are each amplified by a power amplifier and then sent through a wideband circulator to an antenna element.

The digital waveform generator first produces a series of waveform phase angles corresponding to the phase of the waveform to be transmitted. These are common to all

the antenna elements. Then, for each antenna element, the waveform phase angles are adjusted to steer the beam by adding a time delay and a phase adjustment. A digital signal is then produced for each antenna element by applying a cosine function to the adjusted phase angles. In the illustrative embodiment, this is accomplished using a cosine look-up table. Each digital cosine wave is then loaded into a serial memory buffer and read out to the D/A converters.

On receive, at each antenna element, a wideband linear amplifier and an RF filter provide rejection of out of band signals while providing amplification and signal conditioning for the following analog to digital converter (A/D). The resulting digital signals (one for each element) are sent through digital frequency channelizers. Like frequency channels from each antenna element are then combined to form multiple digital simultaneous beams. As in the transmit chain, the beam formation can be done with a combination of time delays and phase adjustments.

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BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a block diagram of a transmit circuit of a wideband multi-beam digital radar designed in accordance with the teachings of the present invention.

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Fig. 2 is a flow chart of a digital beamsteering system designed in accordance with the teachings of the present invention.

Fig. 3 is a block diagram of a receive circuit of a wideband multi-beam digital radar designed in accordance with the teachings of the present invention.

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Fig. 4 is a flow chart of a digital beam formation system designed in accordance with the teachings of the present invention.

DESCRIPTION OF THE INVENTION

Illustrative embodiments and exemplary applications will now be described with reference to the accompanying drawings to disclose the advantageous teachings of the present invention.

While the present invention is described herein with reference to illustrative embodiments for particular applications, it should be understood that the invention is not limited thereto. Those having ordinary skill in the art and access to the teachings provided herein will recognize additional modifications, applications, and embodiments within the scope thereof and additional fields in which the present invention would be of significant utility.

The present invention is an efficient digital wideband waveform generation and signal processing design for electronically scanned antenna array (ESA) radar systems.

Advances in digital technology, particularly the larger bandwidth available on analog to digital (A/D) converters and higher throughput processors, now allow the practical implementation of a digital antenna manifold, i.e. digital waveform generation on transmit, and digital beamforming on receive.

Fig. 1 is a block diagram of the transmit circuit 100 of a wideband multi-beam digital ESA radar system designed in accordance with the teachings of the present invention. A digital waveform generator 50 produces digital forms of the signals to be transmitted by the individual elements (26A to 26L) of the antenna array. A different signal is produced for each element. Each of these digital signals is passed through a D/A (18A to 18L) and an RF filter (20A to 20L). The resulting analog signals are each amplified by a power amplifier (22A to 22L), and then sent through a wideband circulator (24A to 24L) to a corresponding antenna element (26A to 26L). The antenna array is comprised of L individual elements (26A to 26L), where L is an integer. In an

illustrative example, the antenna array contains 1000 elements, the D/A converters operate at a sampling frequency of 40 GHz, and the transmitted energy has a center frequency of 10 GHz.

5 Assuming the transmitted signals are to be in the form of cosine (or sine) waves, the digital waveform generator first produces a series of phase angles corresponding to the phase of the waveform to be transmitted. This is performed by a Waveform Phase Angle Generator 10, as shown in Fig. 1. The series of phase angles produced by the Waveform Phase Angle Generator 10 is common to all the antenna elements. Second,
10 the waveform phase angles are adjusted by a Digital Beamsteering Unit 12 to steer the beam by adding a time delay and a phase adjustment for each antenna element (26A to 26L). A series of adjusted phase angles is thus produced for each antenna element (26A to 26L). A digital signal is then produced for each antenna element (26A to 26L) by applying a cosine function to the adjusted phase angles. In the illustrative embodiment
15 shown in Fig. 1, this is accomplished using a Cosine Look-up Table 14. Each digital cosine wave is then loaded into a Serial Memory Buffer (16A to 16L) and read out to the D/A converters (18A to 18L).

 The Waveform Phase Angle Generator 10 produces a series of phase angles $\phi(n)$
20 corresponding to the phase of the signal to be transmitted during a radar pulse. These phase angles $\phi(n)$ are common to all the antenna elements in the array. The Waveform Phase Angle Generator 10 can be implemented by a Direct Digital Synthesizer (DDS) or similar device.

 The waveform phase angles $\phi(n)$ are then input to a Digital Beamsteering Unit 12. The Digital Beamsteering Unit 12 adds a time delay and a phase adjustment for each antenna element. The time delay allows uniform beam pointing over a wide bandwidth, and the phase adjustment compensates for the time delay quantization at the center frequency. In order to steer the transmitted radar beam, each antenna element must have a particular phase angle added to the common waveform signal. This is similar to the

phase shifts required in an analog beamsteering system except that it's being implemented digitally. A digital delay is also required in order to focus the transmitted beam. Otherwise, the beam will squint with frequency, i.e., edge frequencies will not be pointed in the same direction as the center frequency.

For each element, a different phase adjustment and time delay are combined with the waveform phase angles $\phi(n)$ produced by the Waveform Phase Angle Generator 10 to form adjusted phase angles $\theta_l(n)$ for $l = 1$ to L , where the subscript l indicates which antenna element (26A to 26L) the signal is for and L is the total number of
 5 individual elements in the antenna array.

Fig. 2 is a flow chart of a Digital Beamsteering Unit 12 designed in accordance with the teachings of the present invention. The input to the Digital Beamsteering Unit 12 is the series of waveform phase angles $\phi(n)$. The outputs are the adjusted phase
 10 angles $\theta_l(n)$ for $l = 1$ to L .

First, at Step T1, the time delay t_l is computed using the following equation:

$$t_l = \frac{\vec{x}_l \cdot \hat{u}_T}{c} \quad [1]$$

15

where \hat{u}_T is a unit vector along the transmit beam, \vec{x}_l is the position of antenna element l relative to the antenna array center, and c is the speed of light.

20 At Step T2, the quantized sample shift i_l is computed using the following equation:

$$i_l = \text{round}(t_l / t_q) \quad [2]$$

25

where t_q is the time quantization of the Waveform Phase Angle Generator 10.

At Step T3, the residual time t_{rl} is computed using the following equation:

$$t_{rl} = t_l - i_l t_q \quad [3]$$

5

At Step T4, the phase shift γ_l is computed for element l :

$$\gamma_l = 2\pi f_c t_{rl} \quad [4]$$

10

where f_c is the center frequency of the transmitted beam.

Finally, at Step T5, the time delay t_l and the phase shift γ_l are applied to the
15 waveform phase angles $\phi(n)$, resulting in an output signal $\theta_l(n)$:

$$\theta_l(n) = \phi(n - t_l) + \gamma_l \quad [5]$$

20

These steps are repeated for all the antenna elements $l = 1$ to L .

In the preferred embodiment, the Digital Beamsteering Unit 12 is implemented
in software. Digital beamsteering can be performed for all antenna elements using a
single computer, or it can be done separately for each element, with a separate processor
25 for each element.

Fig. 2 shows an implementation of the Digital Beamsteering Unit 12 whereby
the time delays and phase adjustments are calculated. Alternatively, the time delays and
phase adjustments can be calculated beforehand and stored in a look-up table, or they
30 can be calculated and stored for certain discrete locations and derived for intermediate
locations by interpolation. The Digital Beamsteering Unit 12 can thus be implemented
by computation, look-up tables, or a combination of tables and interpolation.

After digital beamsteering is performed, a digital signal $y_l(n)$ is then produced for each antenna element (26A to 26L) by applying a cosine function to each of the series of adjusted phase angles $\theta_l(n)$ for $l = 1$ to L :

5
$$y_l(n) = \cos[\theta_l(n)]$$
 [6]

In the illustrative embodiment, this is accomplished using a Cosine Look-up Table 14.

10

Each digital cosine wave $y_l(n)$ is then loaded into a serial dual memory buffer (16A to 16L) in preparation for conversion to analog. Since the waveform is typically repetitive over several pulses and the beamsteering command is usually fixed for at least 1 millisecond, the serial memory can be updated at a rate significantly lower than 40
15 GHz.

15

For each pulse, the serial memory buffers (16A to 16L) are read out to the D/A converters (18A to 18L) at the sampling rate (40 GHz in the example). An RF filter (20A to 20L) follows each D/A (18A to 18L) to reject the out of band frequencies
20 generated by the D/A. Power amplification is then performed by the power amplifiers (22A to 22L) and the signal is sent through wideband circulators (24A to 24L) to the antenna elements (26A to 26L). The power amplifiers and circulators are common to ESA systems.

20

With the digital system of the present invention, only a D/A converter, RF filter, and power amplifier are required for each element. The digital waveform generator can be implemented using a single computer. As would be known to one of ordinary skill in the art, a high-speed clock signal is also required to drive all the digital equipment at each element. The complex antenna manifold of an analog system is no longer needed.

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Fig. 3 is a block diagram of a receive circuit 200 of a wideband multi-beam digital radar designed in accordance with the teachings of the present invention. The transmitted signal is received by antenna elements (26A to 26L) and corresponding circulators (24A to 24L). The received signals are amplified by linear amplifiers (30A to 30L) for each antenna element, followed by RF filters (32A to 32L) which provide rejection of out of band signals, and analog to digital (A/D) converters (34A to 34L) which convert the received analog signals to digital data. After digitization, the subsequent steps can be performed using a single computer, or multiple processors.

The digital data from each antenna element (26A to 26L) is fed to a corresponding frequency channelizer (36A to 36L) which separates each signal into N different frequency bands. For example, assuming the received signal was in the range from 8 GHz to 12 GHz, each frequency band could have a bandwidth of 100 MHz, such that the first frequency channel ranged from 8.0 to 8.1 GHz, the second channel ranged from 8.1 to 8.2 GHz, etc. This can be implemented using a Fast Fourier Transform (FFT). It is understood that one of ordinary skill in the art may choose to do additional things at this step, such as amplitude weighting to reduce sidelobes, zero padding, etc.

After frequency channelization, Digital Beam Formation Units (38A to 38N) take like frequency channels from each antenna element and combine them to form multiple digital simultaneous beams. Digital beam formation is performed separately for each frequency channel. Hence, there are N Digital Beam Formation Units (38A to 38N), where N is the total number of frequency channels. Each Digital Beam Formation Unit (38A to 38N) has L number of inputs and M number of outputs, where L is the total number of antenna elements, and M is the number of digital beams per channel desired. Like frequency channels from each antenna element are input to Digital Beam Formation Unit (38A to 38N). For instance, the Digital Beam Formation Unit 38A corresponding to the first frequency channel inputs the first channel output from the Frequency Channelizer 36A (from the first antenna element 26A), the first channel output from the Frequency Channelizer 36B (from the second antenna element 26B),

and so on, up to the first channel output from the Frequency Channelizer 36L (from the last antenna element 26L). Each Digital Beam Formation Unit (38A to 38N) then outputs M simultaneous digital beams, forming a total of $M \times N$ beams.

5 Fig. 4 is a flow chart of a Digital Beam Formation Unit 38 designed in accordance with the teachings of the present invention. The inputs to the i -th Digital Beam Formation Unit 38 are outputs $z_{i,l}(n)$ for $l = 1$ to L from the L frequency channelizers (36A to 36L) corresponding to frequency channel i .

10 First, at Step R1, the center frequency f_{ci} for channel i is computed:

$$f_{ci} = \frac{f_{smp} i}{NFFT} + f_0 \quad [7]$$

15 where f_{smp} is the sampling rate of the A/D converters, $NFFT$ is the size of the FFT, and f_0 is the LO frequency.

At Step R2, the phase shifts $\phi_{i,k,l}$ for each receive beam $k = 1$ to M and antenna element $l = 1$ to L for channel i is computed:

20

$$\phi_{i,k,l} = \frac{f_{ci}}{c} \bar{x}_l \cdot \hat{u}_k \quad [8]$$

where \bar{x}_l is the position of antenna element l relative to the antenna array center, \hat{u}_k is a
25 unit vector along the k -th receive beam, and c is the speed of light.

At Step R3, the receive beams $v_{i,k}(n)$ for $k = 1$ to M for channel i is computed by applying the phase shifts and adding signals across all elements:

30

$$v_{i,k}(n) = \sum_l e^{j\phi_{i,k,l}} z_{i,l}(n) \quad [9]$$

where $z_{i,l}(n)$ is the output from the l -th frequency channelizer corresponding to frequency channel i .

These steps are repeated for all channels $i = 1$ to N .

5

Alternatively, an FFT across elements may be performed when fine frequency channelization is performed and full beam coverage over the field of regard is required.

Each formed beam can then be processed as usual by a Radar, Communications,
10 or EW Processing Unit (40A-1 to 40N-M). These systems are used primarily for radar, but can also be used for communications or EW (electromagnetic wave) applications.

Note that the digital radar implementation described here relaxes the dynamic range requirements of the D/A and the A/D. For example, to achieve -140 dB/Hz
15 spectral purity with a 1000 element array and a 20% duty factor waveform a 4 bit D/A is sufficient. On receive for a similar size array, a 6 bit A/D is sufficient to achieve a dynamic range close to that of the linear amplifiers in active ESA systems.

Thus, the present invention has been described herein with reference to a
20 particular embodiment for a particular application. Those having ordinary skill in the art and access to the present teachings will recognize additional modifications, applications and embodiments within the scope thereof.

It is therefore intended by the appended claims to cover any and all such
25 applications, modifications and embodiments within the scope of the present invention.

Accordingly,

WHAT IS CLAIMED IS:

EUROSTYLE CLAIMS

1. A system for generating signals for driving the individual elements of an electronically scanned antenna array (ESA) comprising:
 - a signal generator (10) for generating a first digital signal and
 - a digital beamsteering unit (12) operating on said first digital signal to produce a

5 second digital signal for each individual element of said antenna array.
2. The invention of Claim 1 wherein said first digital signal is a series of waveform phase angles representing the phase angles of a common signal to be transmitted by said ESA.
3. The invention of Claim 1 wherein said digital beamsteering unit (12) includes code for adding a phase adjustment to said first digital signal for each individual element of said antenna array.
4. The invention of Claim 3 wherein said phase adjustment γ_l for antenna element l is computed from the equation $\gamma_l = 2\pi f_c t_{rl}$ where f_c is the center frequency of the transmitted beam and t_{rl} is the residual time for element l .
5. The invention of Claim 1 wherein said digital beamsteering unit (12) includes code for adding a time delay to said first digital signal for each individual element of said antenna array.
6. The invention of Claim 5 wherein said time delay t_l for antenna element l is computed from the equation $t_l = \frac{\vec{x}_l \cdot \hat{u}_T}{c}$ where \hat{u}_T is a unit vector along the transmit beam, \vec{x}_l is the position of antenna element l relative to the antenna array center, and c is the speed of light.

7. The invention of Claim 1 wherein said system further includes a program (14) for generating a third digital signal for each individual element of said antenna array wherein said third digital signal is a function of said second digital signal.

8. The invention of Claim 10 wherein said function is cosine.

9. The invention of Claim 10 wherein said function is implemented through the use of a look-up table.

10. The invention of Claim 1 wherein said system further includes serial memory buffers (16A-16L) for storing said digital signals prior to conversion to analog.

11. The invention of Claim 1 wherein said system further includes a digital to analog (D/A) converter (18A-18L) for each antenna element for converting said digital signals to analog.

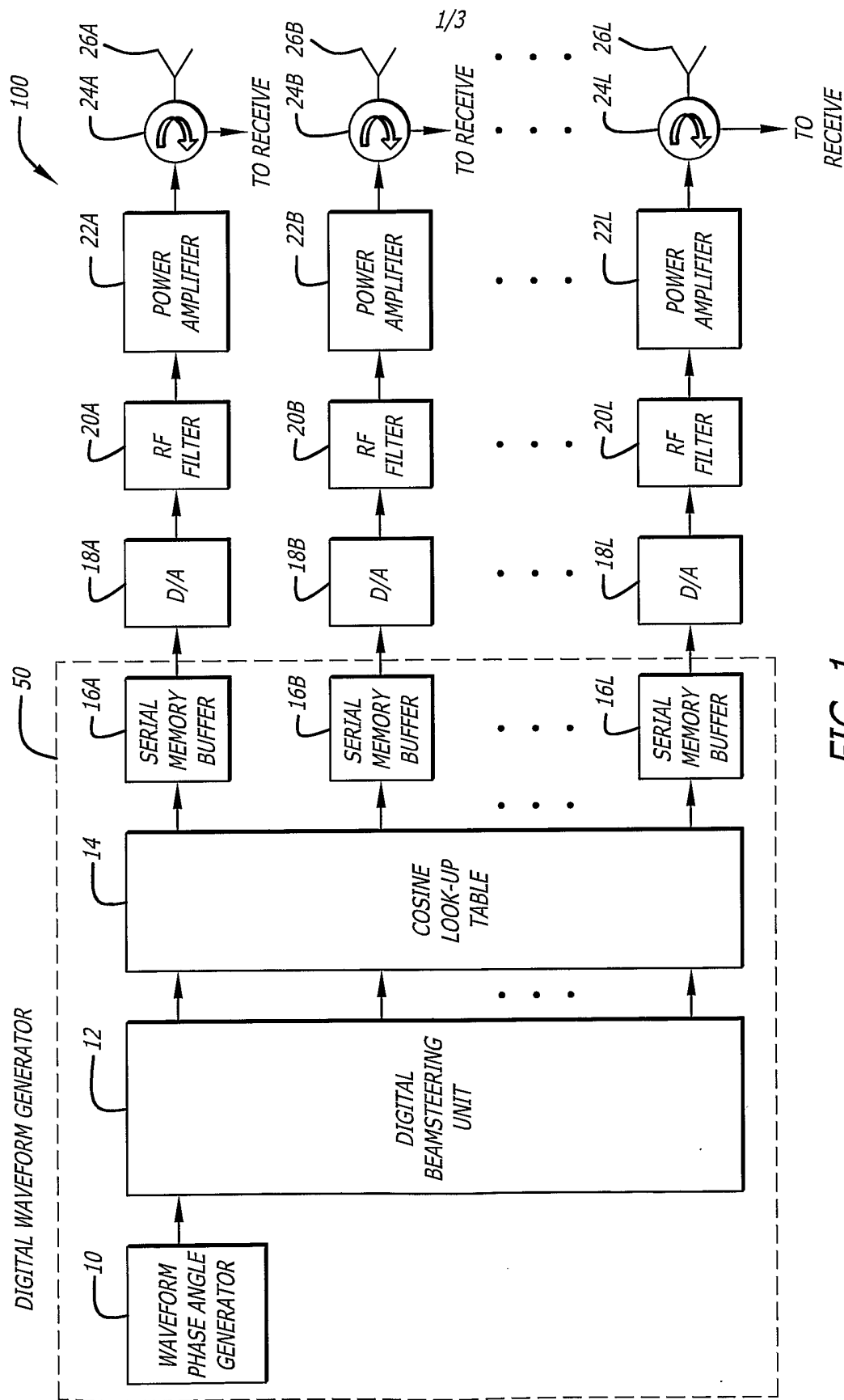


FIG. 1

FIG. 2

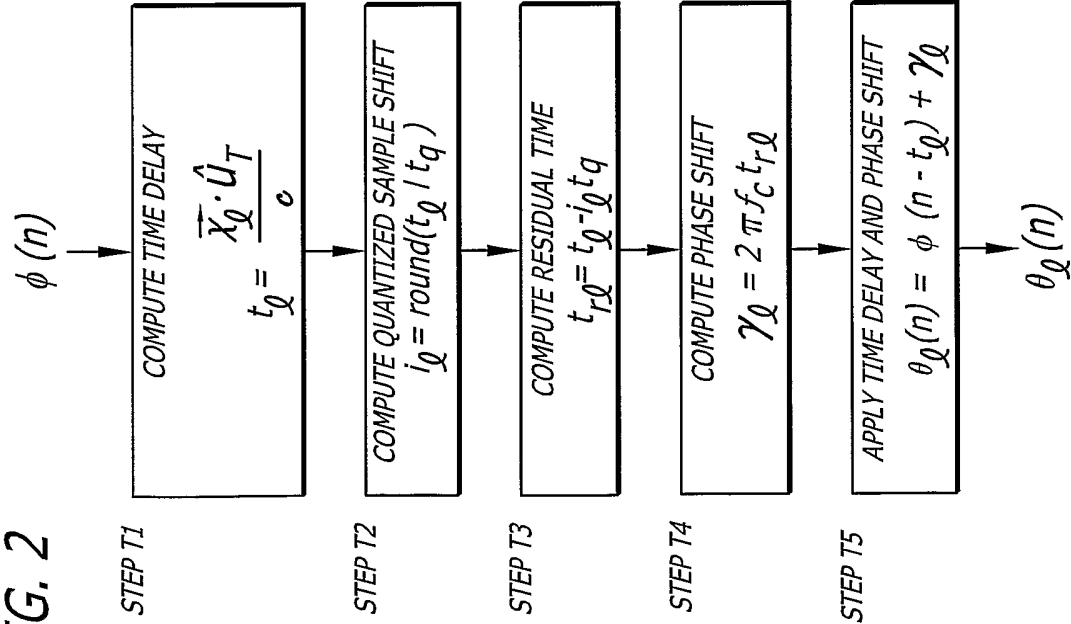
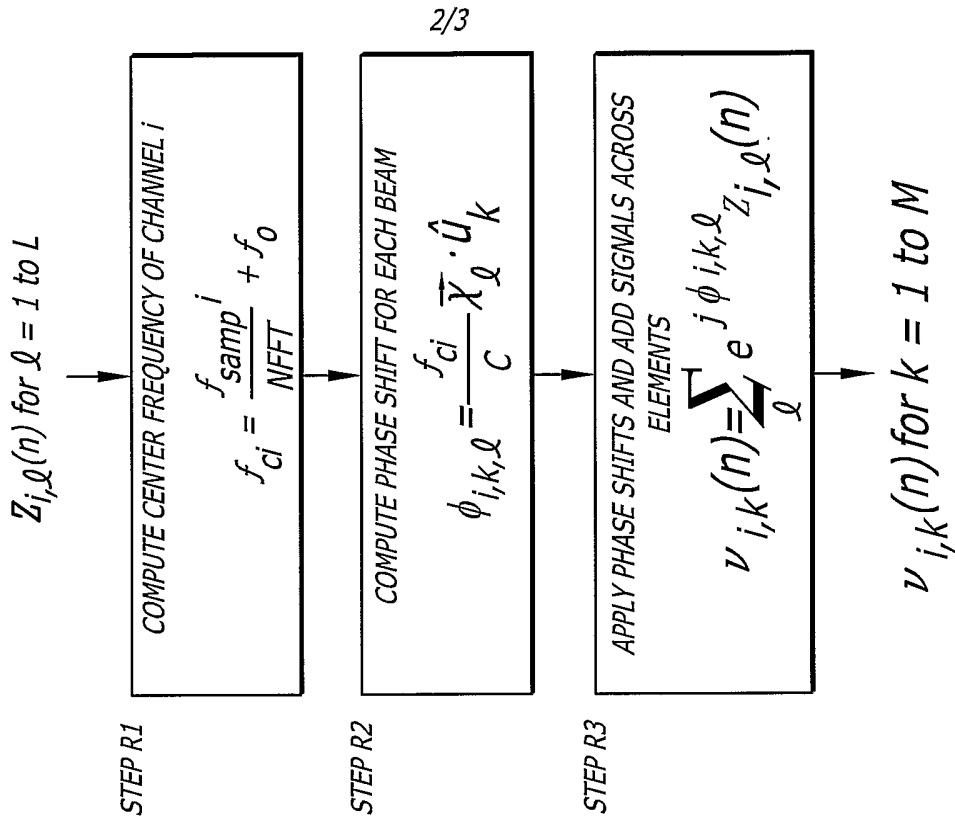
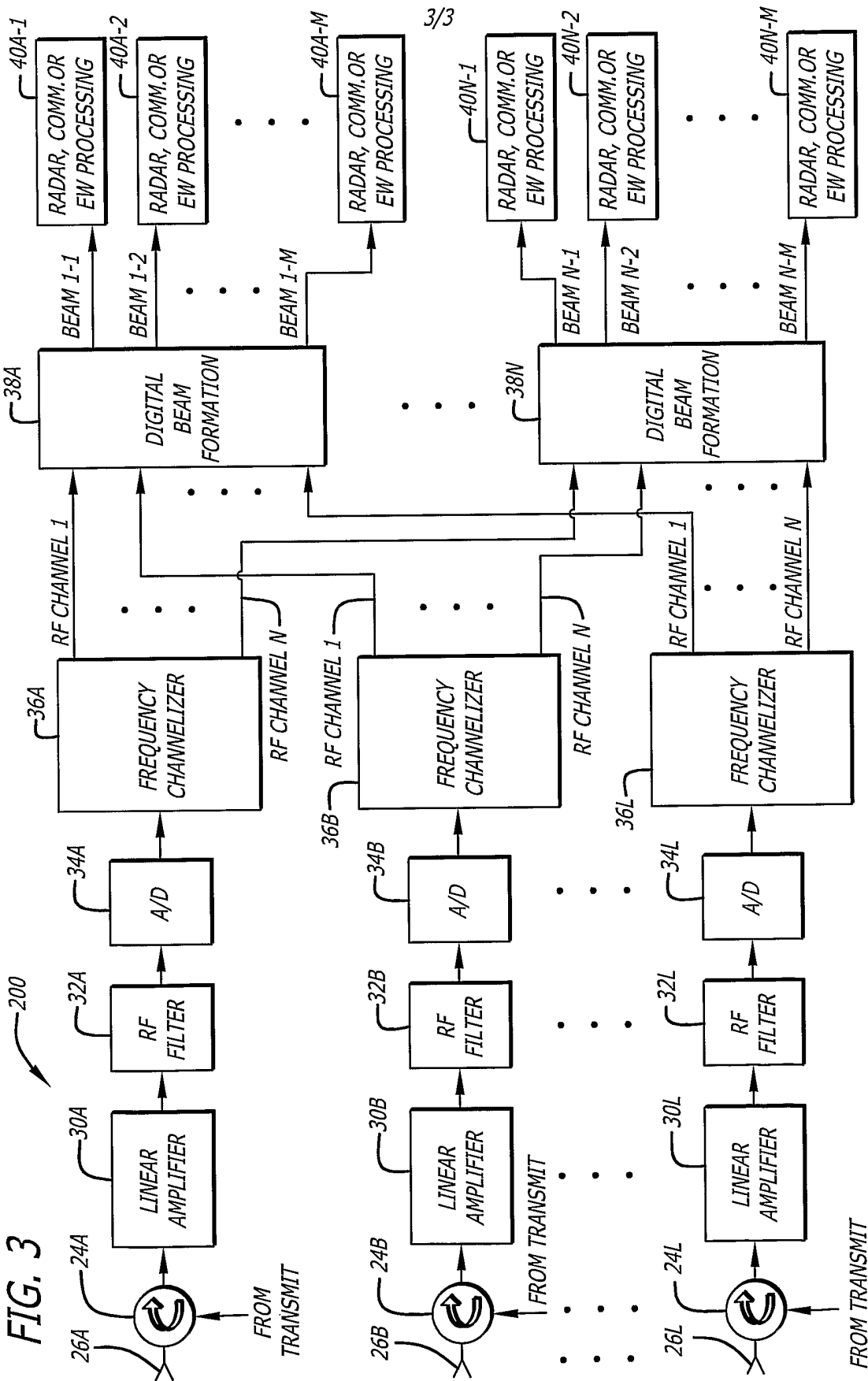


FIG. 4





INTERNATIONAL SEARCH REPORT

International Application No

PCT/US 03/09452

A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 G01S7/28 G01S7/483 G01S7/486 H01Q3/26

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 G01S H01Q

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5 943 010 A (MAGILL EDWARD G ET AL) 24 August 1999 (1999-08-24)	1-3, 5, 11
Y	column 5, line 3 -column 6, line 14; figures 8, 8A, 8D claims 1, 4	4, 6-10
Y	WO 01 97331 A (IZADPANAH HOSSEIN ;STEPHENS RONALD REGIS (US); HRL LAB LLC (US); T) 20 December 2001 (2001-12-20) page 3 page 6, line 12 -page 12, line 13 -/--	4, 6-10

☒ Further documents are listed in the continuation of box C.☒ Patent family members are listed in annex.

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Date of the actual completion of the international search

17 July 2003

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INTERNATIONAL SEARCH REPORT

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	<p>GARROD A: "Digital modules for phased array radar" PHASED ARRRAy SYSTEMS AND TECHNOLOGY, 1996., IEEE INTERNATIONAL SYMPOSIUM ON BOSTON, MA, USA 15-18 OCT. 1996, NEW YORK, NY, USA, IEEE, US, 15 October 1996 (1996-10-15), pages 81-86, XP010204726 ISBN: 0-7803-3232-6 the whole document</p> <p>----</p>	1-11
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INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

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