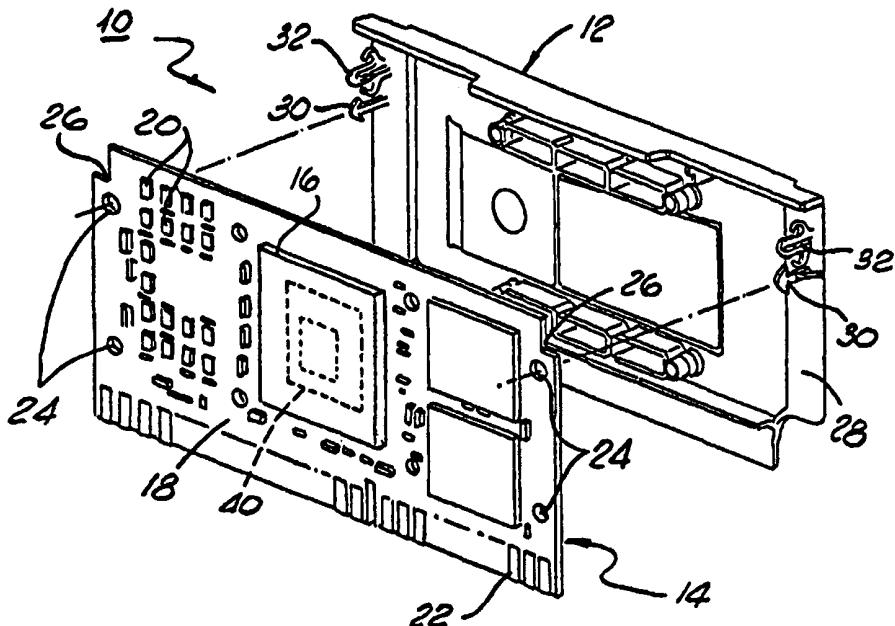




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(54) Title: A COVER FOR AN ELECTRONIC CARTRIDGE



(57) Abstract

A cover (12) includes a snap-in pin (30) and a barbed pin (32).

FOR THE PURPOSES OF INFORMATION ONLY

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A COVER FOR AN ELECTRONIC CARTRIDGE**BACKGROUND OF THE INVENTION****1. FIELD OF THE INVENTION**

The present invention relates to a cover for an electronic assembly.

2. BACKGROUND INFORMATION

Figure 1 shows a product marketed by Intel Corporation, the assignee of this application, which is referred to as a single edge contact cartridge (SECC). The Intel SECC includes a microprocessor which is assembled into a package 1 that is mounted to a substrate 2. The SECC may also have other integrated circuit packages 3 which contain static random access memory (SRAM) integrated circuits.

One edge of the substrate 2 has a plurality of contact pads 4 which can be inserted into an electrical connector 5. The electrical connector 5 can be mounted to a printed circuit board 6 such as the motherboard of a computer. The pads 4 and connector 5 electrically couple the substrate 2 to the circuit board 6.

The substrate 2 and integrated circuit packages 1 and 3 are enclosed by a cover 7 and a thermal plate 8. The cover 7, plate 8 and substrate 2 provide an electronic cartridge which can be plugged into a computer. The cover 7 and

thermal plate 8 are attached to each other and the substrate 2 by a number of pins and spring clips (not shown). Having individual pins and spring clips increases the cost and complexity of assembling the cartridge. It would be desirable to provide a cartridge that has less parts than the assembly shown in Fig. 1.

SUMMARY OF THE INVENTION

One embodiment of the present invention is a cover for a substrate of an electronic assembly. The cover may include a snap-in pin that can be inserted through an opening of the substrate. The cover may also have a barbed pin that exerts a force onto the substrate.

BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 is a perspective view of an electronic assembly of the prior art;

Figure 2 is an exploded view of an embodiment of electronic assembly of the present invention;

Figure 3 is a perspective view of a partially assembled electronic assembly;

Figure 4 is a side sectional view showing a cover attached to a substrate of the assembly;

Figure 5 is a side sectional view showing that attachment of a thermal element to the cover;

Figure 6 is an enlarged sectional view showing a barbed fastener embedded within a boss of the cover.

DETAILED DESCRIPTION

Referring to the drawings more particularly by reference numbers, Figures 2 and 3 show an embodiment of an electronic assembly 10 of the present invention. The assembly 10 may include a cover 12 that is coupled to a printed circuit board sub-assembly 14. The sub-assembly 14 may include one or more integrated circuit packages 16 that are mounted to a substrate 18. The sub-assembly 14 may also include a number of passive elements 20 such as capacitors and termination resistors that are soldered to the substrate 18.

The substrate 18 may be a printed circuit board which has a plurality of contact pads 22 that can be plugged into an electrical connector (not shown). The substrate 18 may also have a plurality of openings 24 and a pair of notches 26.

The cover 12 may include a panel 28 that covers one side of the printed circuit board sub-assembly 14. Extending from the panel 28 are a pair of snap-in pins 30 and a pair of adjacent barbed pins 32 that are used to attach the cover 12 to the substrate 18. The cover 12 may be constructed as a plastic injection molded part which is relatively inexpensive to produce in mass production quantities.

As shown in Figure 4, the snap-in pins 30 may be inserted through the openings 24 of the substrate 18. Each pin 30 may have a V-shaped head 34 that prevents the pin 30 from being pulled out of the opening 24. The heads 34 are deflected when the pins 30 are inserted through the openings

24. The pins 30 prevent separation of the cover 12 and the substrate 18 after assembly.

The barbed pins 32 each have a center pin 36 that extends through a notch 26 of the substrate 18. The center pins 36 provide lateral restraint of the cover 12. Each pin 32 may also have a pair of barbs 38 which engage the substrate 18. The barbs 38 are deflected when the cover 12 is assembled onto the substrate 18. The deflected barbs 38 exert spring forces and corresponding moments that press the substrate 18 into the V-shaped heads 34 of the pins 30. The moments prevent relative movement between the cover 12 and substrate 18 so that the assembly 10 does not "rattle" when subjected to vibrational loads.

As shown in Fig. 5, the integrated circuit package 16 may contain an integrated circuit 40 that generates heat. The assembly may include a thermal element 42 that is coupled to the package 16 to remove the heat generated by the integrated circuit 40. The thermal element 42 may have a pair of fasteners 44 that extend through corresponding clearance holes 46 in the substrate and are attached to bosses 48 of the cover 12.

As shown in Figure 6, each fastener 44 may be barbed and become embedded into an inner channel 50 of a boss 48. The inner channel 50 may have a diameter that is approximately equal to the major diameter of the barbed fastener 44 to create an interference fit. Each boss 48 may have a tapered/countersunk opening 52 that allows a nut or other fastener (not shown) to be attached to a threaded non-barbed fastener (alternate

embodiment) of the thermal element 42. The boss 48 of the present invention can thus accommodate different means for attaching the thermal plate 42 to the cover 12.

Referring to Figs. 2 and 3, the electronic assembly 10 can be easily assembled by merely pushing the snap-in pins 30 of the cover 12 through the substrate holes 24 and pushing the fasteners 44 of the thermal element 42 into the bosses 48 of the cover 12. The cover 12 of the present invention reduces the number of parts and the complexity of assembling an electronic assembly similar to the assembly shown in Fig. 1.

While certain exemplary embodiments have been described and shown in the accompanying drawings, it is to be understood that such embodiments are merely illustrative of and not restrictive on the broad invention, and that this invention not be limited to the specific constructions and arrangements shown and described, since various other modifications may occur to those ordinarily skilled in the art.

CLAIMS

What is claimed is:

1. A cover for an electronic assembly that includes a substrate which has an opening and a notch, comprising:
 - a panel;
 - a snap-in pin that extends from said panel and can be inserted through the opening of the substrate; and,
 - a barbed pin that extends from said panel and applies a force to the substrate when said snap-in pin is inserted through the substrate opening.
2. The cover of claim 1, wherein said barbed pin has a center pin that extends through the notch of the substrate when the snap-in pin is inserted through the substrate opening.
3. The cover of claim 2, wherein said barbed pin has a pair of barbs that exert a spring force on the substrate when the snap-in pin is inserted through the substrate opening.
4. The cover of claim 1, wherein said snap-in pin includes a V-shaped head.
5. The cover of claim 1, further comprising a boss that extends from said panel.

6. The cover of claim 1, wherein said panel, said snap-in pin and said barbed pins are constructed as an integrally molded part.

7. An electrical assembly, comprising:
a substrate which has a opening and a notch;
an integrated circuit that is coupled to said substrate; and,

a cover which has a snap-in pin that extends from a panel and through said opening of said substrate, and a barbed pin which extends from said panel and applies a force on said substrate.

8. The assembly of claim 7, wherein said barbed pin has a center pin that extends through said notch of said substrate.

9. The assembly of claim 7, wherein said barbed pin has a pair of barbs that exert a spring force on said substrate.

10. The assembly of claim 9, wherein said barbs create a moment on said substrate.

11. The assembly of claim 7, wherein said snap-in pin includes a V-shaped head.

12. The assembly of claim 7, wherein said cover includes a boss that extends from a panel.

13. The assembly of claim 7, wherein said panel, said snap-in pin and said barbed pin are constructed as an integrally molded part.

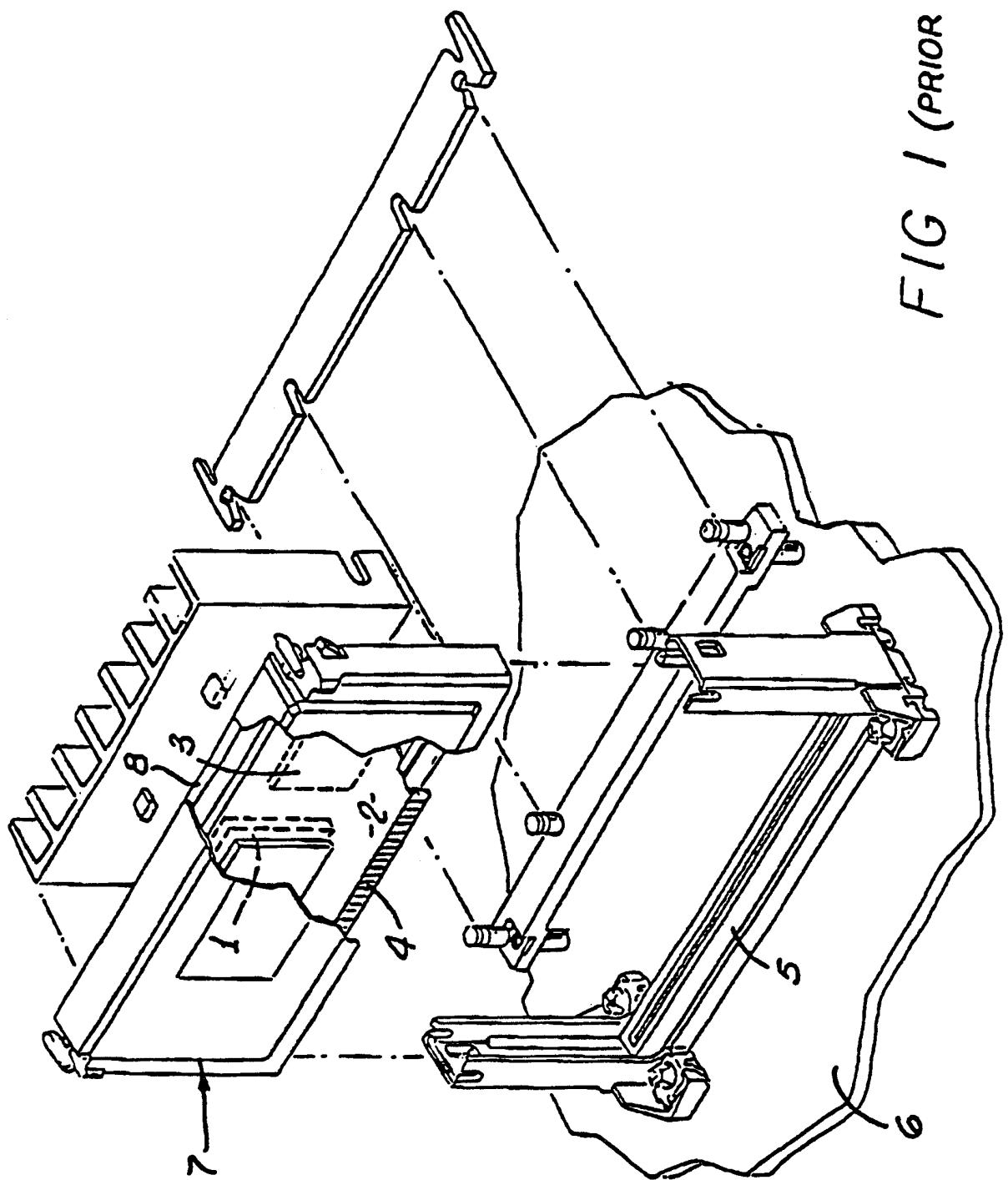
14. The assembly of claim 12, further comprising a thermal element that is thermally coupled to said integrated circuit by a fastener that is attached to said boss of said cover.

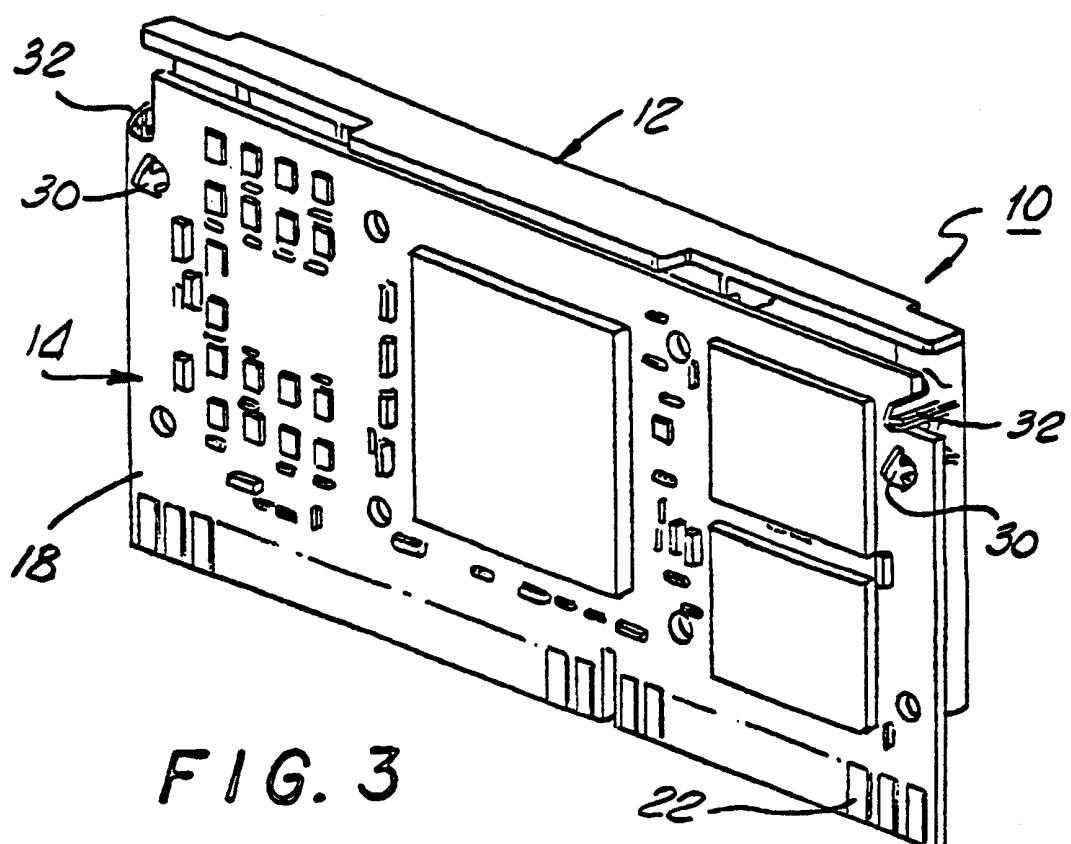
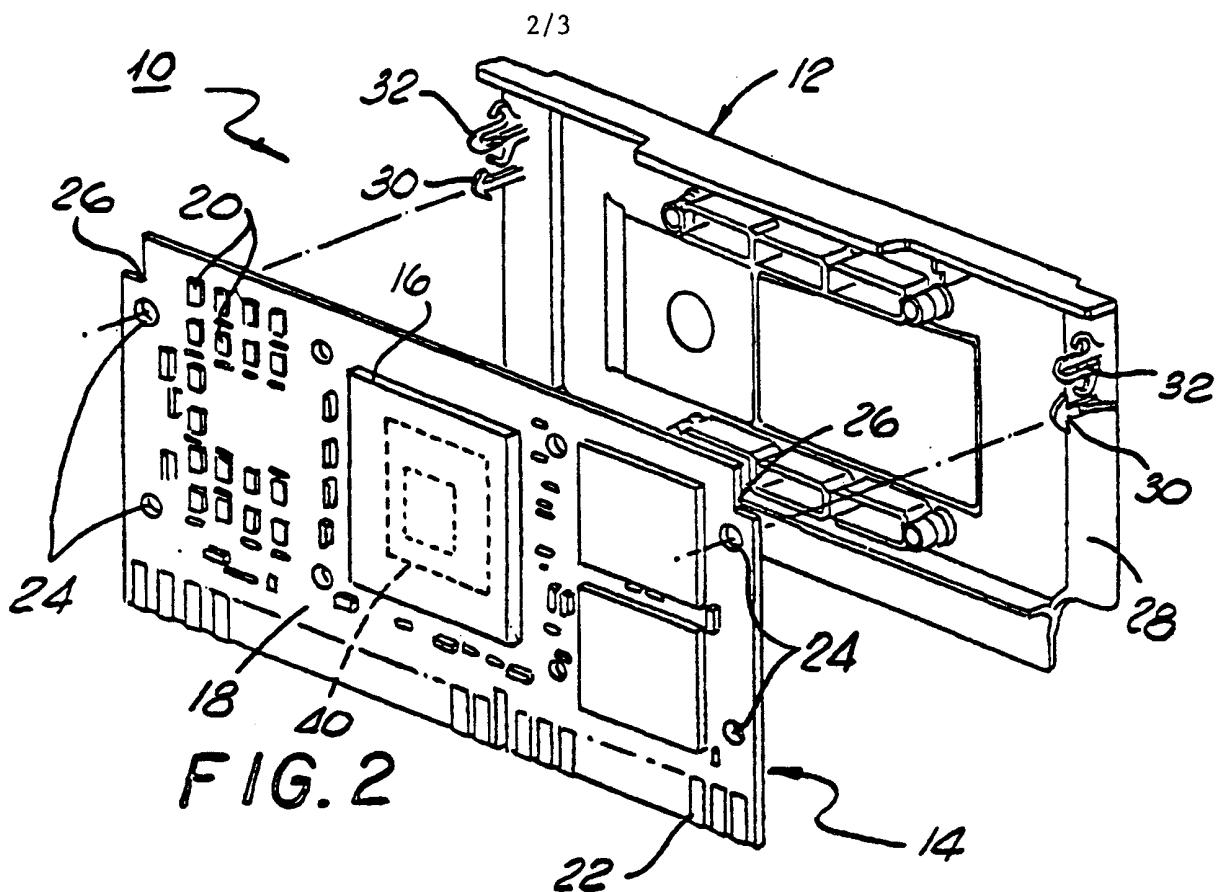
15. A method for assembling an electronic assembly, comprising:

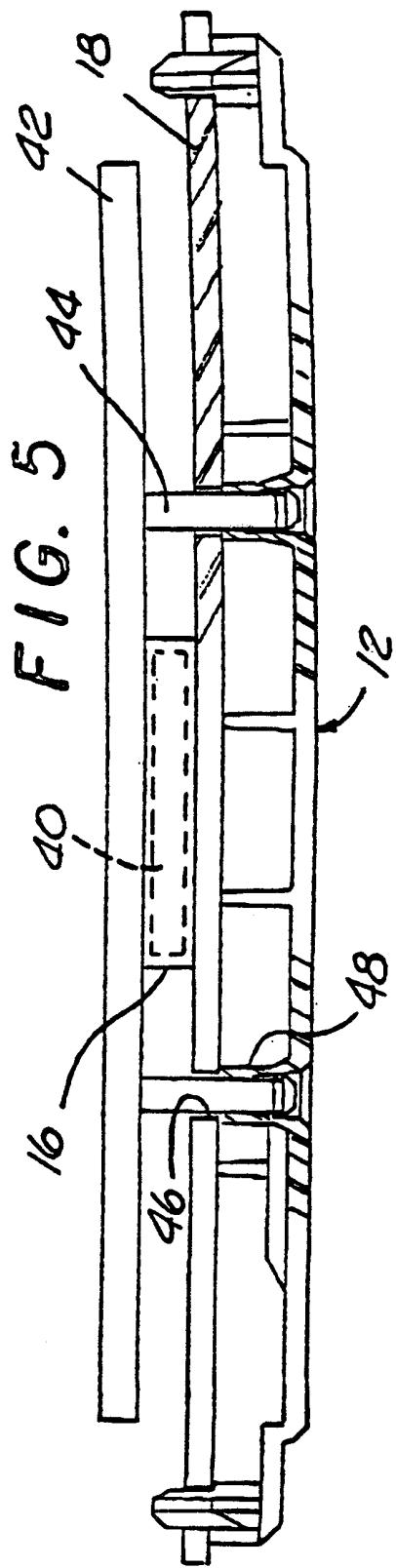
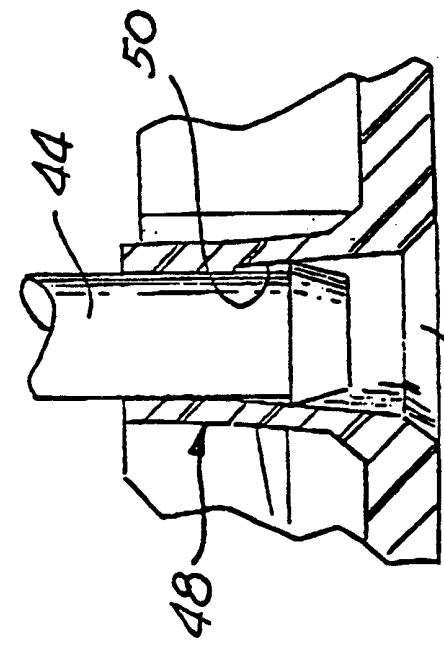
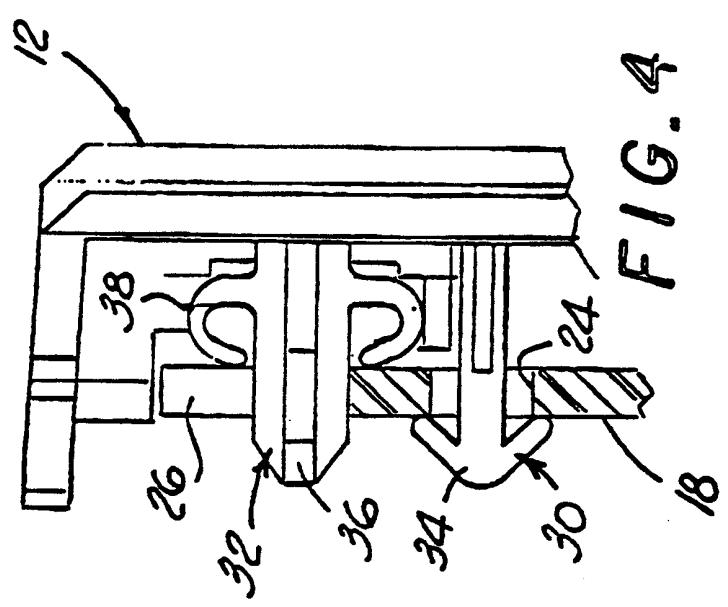
inserting a snap-in pin of a cover through a substrate opening and a barbed pin into engagement with the substrate so that the barbed pin exerts a spring force on the substrate.

16. The method of claim 15, further comprising the step of attaching a thermal element to the cover.

FIG 1 (PRIOR ART)







INTERNATIONAL SEARCH REPORT

International application No.
PCT/US99/26259

A. CLASSIFICATION OF SUBJECT MATTER

IPC(6) :H05K 7/20

US CL : 361/704,715,752,759

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 361/704,715,752,759

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched
NONE

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

NONE

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 3,631,299A(Meyer et al) 28 DECEMBER 1971 (28.12.71), cols.1-6.	1-16
A	US 5,894,408 A (STARK et al) 13 APRIL 1999 (13.04.99), cols.1-3.	1-16
A	US 5,856,910 A (YURCHENCO et al) 05 JANUARY 1999 (05.01.99), cols.1-5.	1-16
A	US 5,838,542 A (NELSON et al) 17 NOVEMBER 1998 (17.11.98), cols. 1-5.	1-16
A	US 4,573,104 A (KAMADA) 25 FEBRUARY 1986 (25.02.86), cols. 1-3.	1-16
A	US 4,226,491 A (KAZAMA et al) 07 OCTOBER 1980 (07.10.,80), cols. 1-4.	1-16

Further documents are listed in the continuation of Box C.

See patent family annex.

* Special categories of cited documents:	"T"	later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
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