

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
4 March 2004 (04.03.2004)

PCT

(10) International Publication Number
WO 2004/019117 A2

- (51) International Patent Classification⁷: **G02F**
- (21) International Application Number:
PCT/US2003/026829
- (22) International Filing Date: 26 August 2003 (26.08.2003)
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data:
60/405,999 26 August 2002 (26.08.2002) US
10/643,063 18 August 2003 (18.08.2003) US
- (71) Applicants: **UNIVERSITY OF CENTRAL FLORIDA**
[US/US]; 12443 Research Parkway, Orlando, FL 32826
(US). **AU OPTRONICS CORPORATION** [CN/CN]; No.
1, Li-Hsin Rd. 2, Science-Based Industrial Park, Hsinchu
300, Taiwan (CN).
- (72) Inventors: **CHOI, Wing-Kit**; 4525 Black Knight Drive,
#203, Orlando, FL 32817 (US). **WU, Shin-Tson**; 1388 Tall
Maple Loop, Oviedo, FL 32765 (US).
- (74) Agent: **STEINBERGER, Brian, S.**; 101 Brevard Avenue,
Cocoa, FL 32922 (US).
- (81) Designated States (*national*): AE, AG, AL, AM, AT, AU,
AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU,
CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH,
GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC,
LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW,
MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SC, SD, SE,
SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VC,
VN, YU, ZA, ZM, ZW.
- (84) Designated States (*regional*): ARIPO patent (GH, GM,
KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW),
Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM),
European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE,
ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO,
SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM,
GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).
- Published:**
— *without international search report and to be republished
upon receipt of that report*
- For two-letter codes and other abbreviations, refer to the "Guid-
ance Notes on Codes and Abbreviations" appearing at the begin-
ning of each regular issue of the PCT Gazette.*

(54) Title: HIGH SPEED AND WIDE VIEWING ANGLE LIQUID CRYSTAL DISPLAYS

(57) Abstract: Novel structural configurations of a TFT-LCD (Thin Film Transistor Liquid Crystal Display) which results in both fast response to input data and provides wide-viewing-angles. The structure of the device is comprised of one pixel electrode layer and two common electrode layers. The structure of the invention can be used with liquid crystal display television (LCD-TV) mon-itors that require both fast-response as well as wide-viewing-angle. In addition, other liquid crystal technologies which require high speed response would benefit from the TFT-LCD of the present invention.



WO 2004/019117 A2

HIGH SPEED AND WIDE VIEWING ANGLE LIQUID CRYSTAL DISPLAYS

This invention claims the benefit of priority to U.S. Provisional Application
5 No. 60/405999 filed August 26, 2002.

FIELD OF INVENTION

This invention relates to displays, in particular to methods and apparatus for
providing a TFT-LCD (Thin Film Transistor Liquid Crystal Display) having fast
10 responses to high input data rates and allowing for wide viewing angles for viewers.

BACKGROUND AND PRIOR ART

Slow response time and poor viewing angle of the conventional TFT-LCD are
two of the major limitations for its otherwise potentially unlimited wide range of
15 applications.

Figure 1 shows the structure of a conventional TFT-LCD. A liquid crystal
(LC) layer **10** is sandwiched between a top glass substrate **11** and a bottom glass
substrate **12**. A thin layer of transparent electrode, indium tin oxide (ITO), is coated
on each substrate for the application of an electric field to switch the liquid crystals.
20 Normally, the electrode **13** for the top substrate **11** is a common electrode, which has a
constant voltage (e.g. 0V). 0V is used herein to mean low voltage. This common
electrode is continuous and extends to all pixels in the whole display and hence is
called a common electrode. On the other hand the electrode **14** on the bottom
substrate **12** is called the pixel electrode since a transistor assigned to each individual
25 pixel controls it. The voltage applied to the LC **10** is varied through this electrode.
The electric field profile E is also shown in Figure 1, when the pixel voltage is > 0 .
As can be seen clearly there is only one type of electric field, a vertical field, for this

device. This electric field is used to turn on the device by switching the liquid crystal molecules, which is a fast process. However, when we turn off the device, the pixel voltage is either removed or reduced such that the molecules gradually relax back to a lower state. With only one type of electric field generated, this results in a very slow relaxation and hence slow turn off time, and is a major limitation of liquid crystals for many potential applications today.

Discussed below are various relevant prior art references. The references are related to three key concepts used in the present invention: crossed-field effect, fringing field switching and multi-domain technology.

10 The concept of crossed-field effect first appeared in 1975 in an article by D.J.Channin, Applied Physics Letters, Vol.26, No.11, p.603 (1975) and in a subsequent article by D.J.Channin and D.E Carlson, Applied Physics Letters, Vol.28, No.6 (1976).

Six years later, an article was published by Akihiko Sugimura et al., Proceedings of
15 14th Conference on Solid State Devices, Tokyo(1982) and in 1985 Akihiko Sugimura and Takao Kawamura published another article in, Japanese Journal of Applied Physics, Vol. 24, No. 8, p.905 (1985). The liquid crystal displays employing the crossed-field effect have various disadvantages, such as, high voltage requirement, low contrast, more complicated structure, non-uniform transmission, and more
20 complicated driving. Driving refers to the electronic circuits used to supply (or drive) the required voltages (data or common) to the TFT-LCD. Some driving schemes are more complicated, e.g, requiring different types of voltage at different time intervals. In the crossed field effect, it normally requires the control of two types of electric field (both vertical and lateral) using extra electrodes and hence more complicated driving.
25 The crossed-field effect concept has therefore not been used for TFT-LCDs since it tends to require much higher operation voltage, more complicated structure and

driving and have lower contrast. The present invention however improves many of the above problems by using different electrode designs; thus, making the crossed-field effect possible for use in TFT-LCDs. Moreover, the use of the crossed-field effect in the present invention also provides the inherent wide-viewing-angle property, which is
5 another very important requirement for TFT-LCD television sets (TVs).

Prior art research on fringing-field switching (FFS) has been published by Seung Ho Hong et al., Japanese Journal of Applied Physics, “Hybrid Aligned Fringing Field” Vol. 40, p.L272, (2001) and Seung Ho Hong et al., Japanese Journal Applied Physics, Vol 41, pp. 4571- 4576 (2001). The present invention adopts a
10 structure that is very similar to the Fringing-Field-Switching FFS mode structure described by Seung Ho Hong et al. This mode was used for generating wide-viewing-angle using in-plane-switching with improved efficiency. By adopting this structure in the present invention, the required voltage can be reduced for generating the lateral or fringing field. The reduction in voltage is possible because the gap between
15 electrodes for fringing field generation is small. Hence, the operating voltage is lowered. Moreover, the FFS structure can provide good uniform vertical field without a dead zone, which is defined as a gap between electrodes without an electric field. In the present invention, the gap between electrodes also has an electric field generated by a bottom substrate electrode configuration that consists of an electrode layer with
20 gaps known as a discontinuous electrode separated from a continuous electrode layer by a electrical insulation layer. The segments of the discontinuous electrode are however all connected to the same transistor within a pixel. The bottom substrate electrode structure is similar to the structure of conventional FFS structure.

The present invention however has at least three important differences from
25 the conventional FFS structure. First, the present invention has two common electrodes; whereas, the conventional FFS structure has only one common electrode of

low voltage only. A recently reported FFS mode also used two common electrodes; however, in this case both common electrodes are of low voltage, e.g. 0V. In contrast, in the present invention, one common electrode is high voltage and one common electrode is low voltage. Second, the liquid crystal (LC) mode is different.

- 5 Conventional FFS uses parallel alignment with in-plane-switching whereas the recently reported FFS mode with two common electrodes uses Hybrid-Aligned-Nematic (HAN). The present invention can use any liquid crystal mode and the wide-viewing-angle generation mechanism is also different compared with the FFS Prior Arts. Third, all prior art FFS structures have slow response time since they are not
10 using the crossed-field effect and the turn-off process relies on natural relaxation of the LC molecules and is slow.

Further, prior art references relate to multi-domain technology LCDs. The present invention adopts a mechanism of forming wide-viewing-angle known as multi-domain. The present invention however has important differences from all the
15 prior art using this technology since our invention uses FFS structure for generating the fringing field whereas other prior art references mainly use protrusions for generating multi-domains. See A. Takeda et al., SID' 98, "MVA, Multi-Domain Vertical Alignment" p.1077 (1998). An inter-digital structure for generating the fringing field to cause multi-domain was discussed by K.H. Kim et al., SID '98 p.
20 1085 (1998). Moreover, the present invention can use many different liquid crystal modes compared with mainly Vertical Alignment (VA) mode used in the prior art.

Thus, there is a need for improvement in today's thin-film transistor liquid crystal display (TFT-LCD) technology. It is desirable for crossed-field effect structures to have low operation voltage, high contrast, simple driving and easy fabrication. Faster
25 response is desired for conventional structures using FFS or multi-domain LCDs.

The present invention affords a substantial improvement in the production and performance of TFT-LCDs. Different LC modes can be applied to this structure. Different LC modes can lead to different light efficiency, response time and viewing angle. The choice of the LC mode depends on the type of application.

5

SUMMARY OF THE INVENTION

The first objective of the present invention is to provide configurations and methods for using TFT-LCD (Thin Film Transistor Liquid Crystal Display) having fast response to high input data rate.

10 The second objective of the present invention is to provide configurations and methods for using TFT-LCD (Thin Film Transistor Liquid Crystal Display) having two common electrode, one of a lower voltage, e.g., 0V and one of a higher voltage, e.g., 5V and one pixel electrode so that both vertical and non-vertical fields can be generated to switch the liquid crystals at high rate.

15 The third objective of the present invention is to provide configurations and methods such that the crossed-field effect requires less voltage compared to conventional crossed-field devices and therefore can be applied to TFT-LCD (Thin Film Transistor Liquid Crystal Displays).

20 The fourth objective of the present invention is to provide configurations and methods such that the crossed-field effect allows simple driving scheme for use in TFT-LCD (Thin Film Transistor Liquid Crystal Display).

The fifth objective of the present invention is to provide configurations and methods such that the crossed-field effect has high contrast capability and simple fabrication process for the TFT-LCD (Thin Film Transistor Liquid Crystal Display)

25 The sixth objective of the present invention is to provide configurations and methods for using TFT-LCD (Thin Film Transistor Liquid Crystal Display) having wide viewing angles for viewers.

Further objects and advantages of this invention will be apparent from the following detailed description of a presently preferred embodiment, which is illustrated, schematically in the accompanying drawings.

5 BRIEF DESCRIPTION OF THE FIGURES

Fig. 1 shows a conventional prior art TFT-LCD structure with one common electrode.

Fig. 2 shows a preferred embodiment of the novel TFT-LCD structure with two common electrodes and one pixel electrode layer.

Fig. 3 shows the TFT-LCD structure of Fig. 2 with uniform vertical field generated
10 when the pixel voltage is 0V(dark state).

Fig. 4 shows the TFT-LCD structure of Fig. 2 with a new electric field pattern generated when the pixel voltage is 5V (bright state).

Fig. 5 shows a second preferred embodiment of the novel TFT-LCD structure.

Fig. 6 shows the TFT-LCD structure of Fig. 5 with power being applied to different
15 electrode layers.

Fig. 7 shows a third embodiment using a resistive film with two common electrode.

Fig. 8 shows a fourth embodiment using a dielectric layer with the novel configuration of

Fig. 2.

20 Fig. 9 shows the non-vertical electric field pattern generated with the novel structures.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

Before explaining the disclosed embodiments of the present invention in detail it is to be understood that the invention is not limited in its application to the details of the particular arrangements shown since the invention is capable of other
25 embodiments. Also, the terminology used herein is for the purpose of description and not of limitation.

It should be noted that the present invention comprises a first substrate, a second substrate, a liquid crystal between the first and second substrates, a means for generating an electric field between electrode layers adjacent to both the first and second substrates. The unique features of the present invention are in the arrangement of electrode layers which are now described in detail.

There are common electrodes, which can be high voltage or low voltage, continuous or discontinuous, and the voltage applied does not depend on the input data during the operation of the TFT-LCD. There are pixel electrodes that can be continuous or discontinuous, and are the electrodes to which the voltage applied depends on the input data. One common electrode can be located in the upper substrate or first substrate; a second common electrode can be located in the lower substrate or second substrate; in addition, a third electrode layer in the lower substrate can be discontinuous by design and collectively referred to as one layer, using one numerical reference in all figures presented herein.

It was not obvious that the combination of two common electrodes with unequal voltages and one pixel electrode would provide a TFT-LCD having fast response and wide viewing angle when varying voltage is applied to the pixel electrode. The fast response is achieved when both the turn on and turn off modes of the device are driven by an electric field; with an electric field the LC molecules can both align and relax very quickly.

Figure 2 shows a novel design of the TFT-LCD structure of the present invention. A major novel feature of this design is that instead of just having one common electrode, there are two common electrodes **21**, **23**, one of a lower voltage, e.g. 0V and one of a higher voltage, e.g. 5V. The first common electrode layer **21** in the top substrate **22** has a constant high voltage of 5V whereas the second common

electrode layer **23** in the bottom substrate **24** has a lower voltage of 0V. Common electrode **23** is separated from the pixel electrode **25** by a passivation layer **26**, which is an electrical insulation layer. When a low voltage of 0V is applied to pixel electrode **25**, a uniform vertical field **30** is generated as shown in Figure 3. This
5 uniform vertical field generated when the pixel voltage is 0V usually leads to a dark state and has very fast switching since it is electric-field driven. This is similar to the fast switching produced by the vertical field generated in the conventional TFT-LCD devices.

In Figure 4, when the pixel voltage is 5V there is a bright state. Voltage of 5V
10 is applied to pixel electrode **25**; a new electric field pattern **40** is established quickly due to the fringing field shown in Figure 4. The common electrode **21** in the top substrate has a voltage of 5V, while the common electrode layer **23** in the bottom substrate has a voltage of 0V and the pixel electrode **25** has a voltage of 5V, as mentioned earlier. This leads to a new liquid crystal alignment state with different
15 optical transmission, usually a bright state. The switching speed to this new state is also fast since it is driven by the electric field. Therefore, this new structure of TFT-LCD design leads to both fast turn-on and turn-off speeds since they both are electric-field driven.

20 Example 1 - Lower Voltage for One Common Electrode.

The voltage for common electrode **21** in Figure 2 is 5V; this voltage can be made lower in order to reduce the vertical electric field strength and hence strengthen the lateral field. This can help improve the light efficiency since the lateral field becomes stronger and more molecules are switched to the bright state. This will
25 however increase the corresponding response time for the bright-to-dark state due to

the formation of a weaker vertical field. The remaining voltage readings are for common electrode **23** on bottom substrate **24**, $V = 0V$; for pixel electrode **25** $V=0$ to $5V$. Common electrode **23** is electrically insulated from the pixel electrode **25** by a passivation layer **26**.

5

Example 2 – Higher Voltage to Bottom Electrode.

In Figure 2, common electrode **21** has higher voltage; whereas, common electrode **23** has lower voltage and in principle these two electrodes can be interchanged. This interchange is shown in Figure 5. In Figure 5, the first common electrode **51** on top substrate **52** has a lower voltage ($0V$); whereas the second common electrode layer **53** in the bottom substrate **54** has a higher voltage ($5V$). This alternative design may lead to a less uniform vertical field because of the slightly higher potential difference that is caused by the passivation layer **56**. In Figure 5, a high electric field is emitted from pixel electrode **55** and hence a higher electric field is established across the passivation layer **56** than when the electric field is emitted from the top electrode **51**. It should be noted, that the terminology “passivation layer” in the description of the present invention, is commonly known as an insulation layer. However, the potential difference that is established between pixel electrode **55** and second common electrode layer **53** can in principle be reduced by altering the voltage to second common electrode **53** or voltage to the pixel electrode **55** in order to compensate for the voltage drop.

Example 3 – Common Electrode and Pixel Electrode Interchanged

A first common electrode layer **60** with $5V$ is in top substrate **61** as shown in Figure 6. The bottom substrate **63** supports a pixel electrode layer **62** with 0 to $5V$

and a second common electrode layer **64** with 0V. The pixel electrode layer **62** is electrically insulated from the common electrode **64** by a passivation layer **65**. The positions of common electrode **64** and the pixel electrode **62** in this configuration are interchanged compared with the configuration shown in Figure 2. The choice of the configuration depends on the fabrication process capability and the optimized electrode width and gap.

Example 4 – Use of Resistive Film

In order to extend the distance of the lateral field, a resistive film **70** can be used to connect the pixel electrode and second common electrode in the bottom substrate as shown in Figure 7. A potential gradient is developed between the pixel electrode **72** and second common electrode **71** across the resistive film when the pixel voltage is high. A lateral field is established between the pixel electrode **72** and the second common electrode **71** to switch the LC molecules during the bright state. The first common electrode **74** in the upper substrate has a high voltage of e.g. 5V. This voltage can, however, be reduced to e.g. 2V in order to increase the lateral field strength. On the other hand, when the voltage of pixel electrode **72** is the same as the voltage in common electrode **71**, then there will be no potential difference across the resistive film and a constant uniform potential appears across the film, due to conductive electrons. Figure 7 illustrates that there can be a horizontal electric field generated between pixel electrode **72** and second common electrode **71** which results in a longer lateral fringing field and higher efficiency for the bright state.

Example 5 – Use of Dielectric Layer

As shown in Figure 8, when common electrode layer **80** of the top substrate **81** is 0V, the use of a dielectric layer **82** between common electrode layer **80** and LC layer **83** can increase the lateral field strength in the upper section of the LC cell since the 0V in top substrate is now further away from the bottom electric field. The dielectric layer **82** is adjacent to the common electrode layer **80**. The dielectric layer functions to keep the cell gap small and help make the lateral fringing field stronger because the upper electrode is figuratively further away. The fringing field formed between common electrode layer **84** and pixel electrode layer **85** becomes stronger, thus improving the light efficiency.

Example 6 - Natural Wide-Viewing-Angle Formation.

In Figure 9, the fringing field leads to the formation of multi-domains which are symmetrical about the mid-plane of the gap **90, 91** between the second common electrode layer **92**. This multi-domain formation will lead to wide viewing angle in two directions, left-right or up-down. It can lead to wide-viewing-angle in all four directions by adopting a zig-zig electrode structure known as the Multi-domain Vertical Alignment (MVA). Figure 9 shows the natural formation of multi-domains due to the symmetrical fringing pattern when the first common electrode layer **93** in the top substrate **94** has 5V and the second discontinuous common electrode layer **92** in the bottom substrate **95** has 0V and the voltage in the pixel electrode **96** is 5V. Figure 9 is the same configuration as Figure 4, with the added illustration of how the fringing field allows different poses for the LC molecule resulting in a naturally wide viewing angle.

The detailed description, examples and simulation results of the invention provide a means for advancing the knowledge and development of thin film transistor

liquid crystal display technology. The novel features of this invention, include, but are not limited to, employing crossed-field effect in TFT-LCD; combining the crossed-field effect with wide-viewing-angle for faster response and wider viewing angle; using a two common-electrode structure of both high and low voltages; using a
5 novel structure for generating crossed-field effect; using a novel structure for generating multi-domain LCDs.

While the invention has been described, disclosed, illustrated and shown in various terms of certain embodiments or modifications which it has presumed in practice, the scope of the invention is not intended to be, nor should it be deemed to
10 be, limited thereby and such other modifications or embodiments as may be suggested by the teachings herein are particularly reserved especially as they fall within the breadth and scope of the claims here appended.

We claim:

1. A thin film transistor liquid crystal display having fast response and wide
5 viewing angle, comprising:
 - a first substrate with a first common electrode layer;
 - a second substrate with both a pixel electrode layer and a second common
electrode layer;
 - liquid crystal between the first substrate and the second substrate; and
 - 10 means for generating an electric field between the first common electrode layer
in the first substrate and both the pixel electrode layer and the second common
electrode layer in the second substrate so that the display provides fast responses to
high input data rates and allows for wide viewing angles for viewers.
- 15 2. The display of claim 1, wherein the electric field generating means has:
 - the second common electrode layer separated from the pixel electrode layer by
an insulation layer in the second substrate.
3. The display of claim 1, further comprising:
20 means for supplying a voltage source to the first common electrode layer.
4. The display of claim 1, further comprising:
 - means for supplying a voltage source to the second common electrode layer.
- 25 5. The display of claim 1, further comprising:
 - means for supplying a voltage source to the pixel electrode layer.

6. The display of claims 3, 4 and 5, wherein the means for supplying a voltage source to one of the first common electrode layer and the second common electrode layer results in an unequal voltage.
- 5 7. The display of claim 6, wherein the unequal voltage is higher in the first common electrode layer than the second common electrode layer.
8. The display of claim 6, wherein the unequal voltage is higher in the second common electrode layer than in the first common electrode layer.
- 10 9. The display of claim 1, wherein the electric field generating means includes:
a resistive film in between portions of the pixel electrode layer and the second common electrode layer.
- 15 10. The display of claim 2, further comprising:
a dielectric layer adjacent to the first common electrode layer.
11. The display of claim 1, wherein the electric field generated is non-vertical.
- 20 12. The display of claim 1, wherein the electric field generated is vertical.
- 25 13. A method of providing fast response and wide viewing angle to thin film transistor liquid crystal displays, comprising the steps of:
providing a liquid crystal layer between a first substrate and a second substrate; and

generating an electric field between the substrates, wherein voltage is applied to a first substrate with a first common electrode layer, a second substrate with a second common electrode layer and a pixel electrode layer, so that there are fast responses to input data and wide viewing angles occur for viewers.

5

14. The method of claim 13, wherein the step of generating an electric field includes the step of:

applying voltage to the pixel electrode layer that is approximately equal to the voltage of the second common electrode in the second substrate, so that a uniform,
10 vertical electric field occurs.

15. The method of claim 13, wherein the step of generating an electric field includes the step of:

applying voltage to the pixel electrode layer, that is unequal to the voltage in
15 the second common electrode in the second substrate so that a non-vertical electric field occurs.

16. The method of claim 15, wherein the step of generating a non-vertical electric field includes the step of:

20 forming a resistive layer between the pixel electrode and the second common electrode; and

applying voltage to the pixel electrode that is unequal to the voltage of the second common electrode so that a lateral electric field occurs.

25 17. The method of claim 15, wherein the step of generating a non-vertical electric field includes the step of:

forming a dielectric layer across one of the substrates; and

applying to the pixel electrode so that a strong electric field with improved light efficiency occurs.

18. The method of claim 13, wherein the applied voltage to each of the electrode layers includes the step of applying an unequal voltage between the first and the second common electrodes, wherein pixel electrode voltage depends on the input data
- 5 and the voltage of the first and second common electrodes does not depend on the input data.

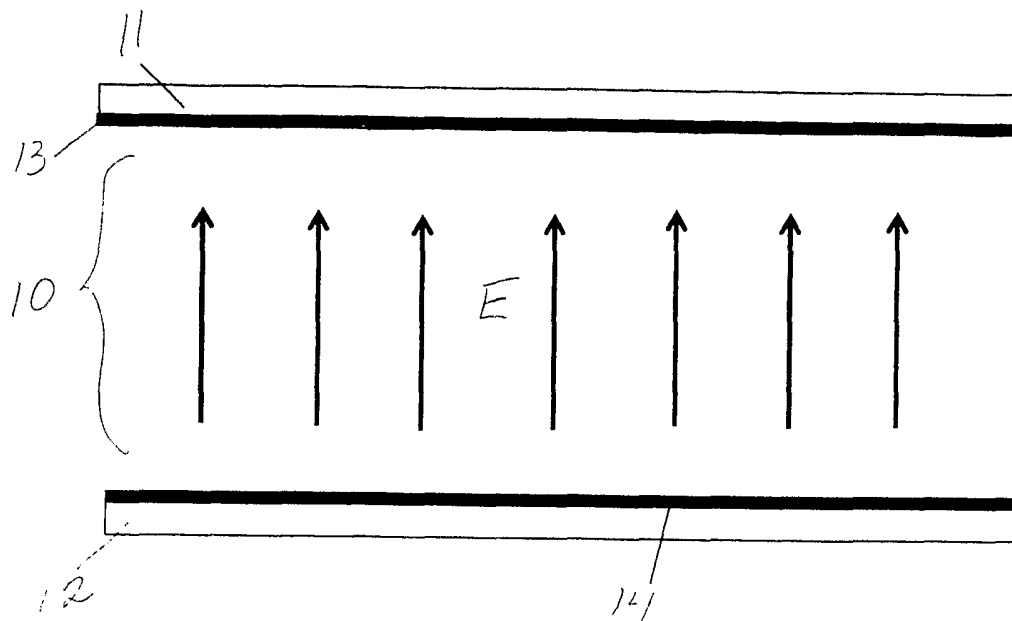


Figure 1
(Prior Art)

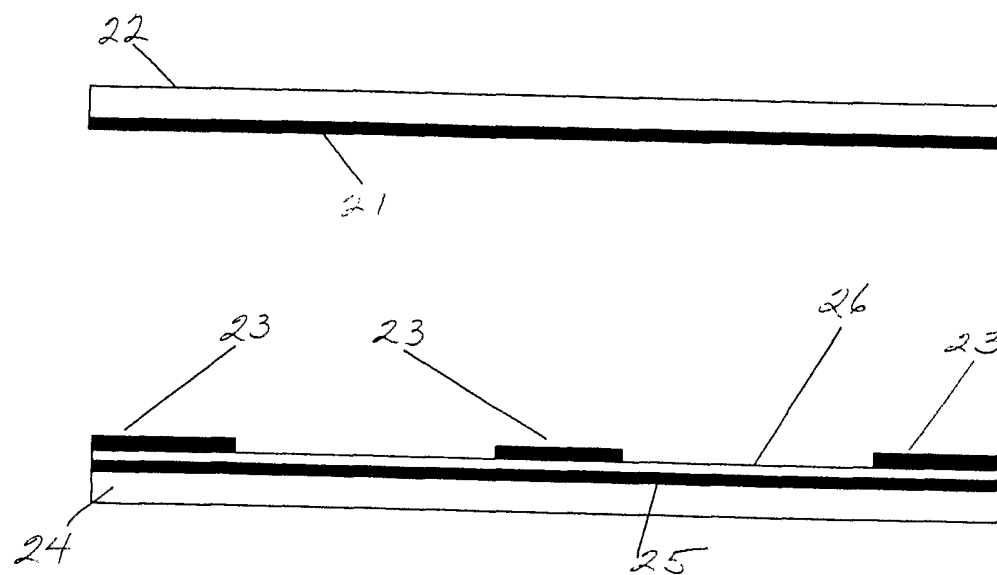


Figure 2

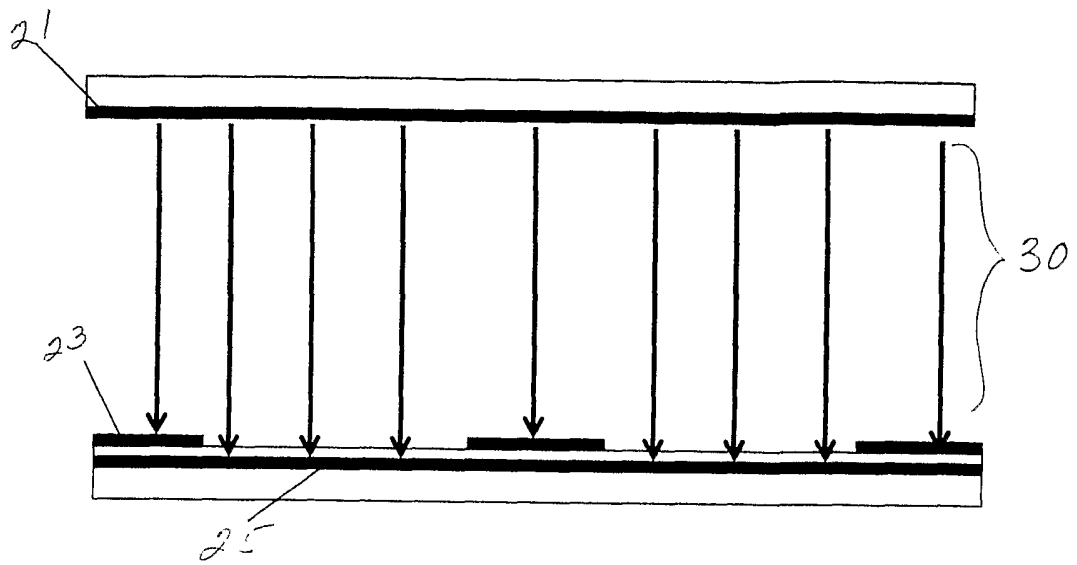


Figure 3

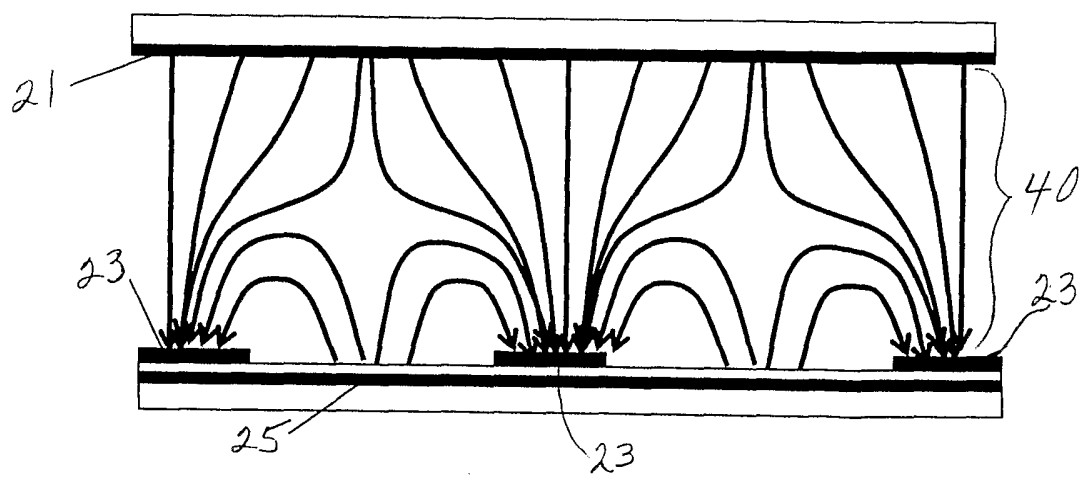
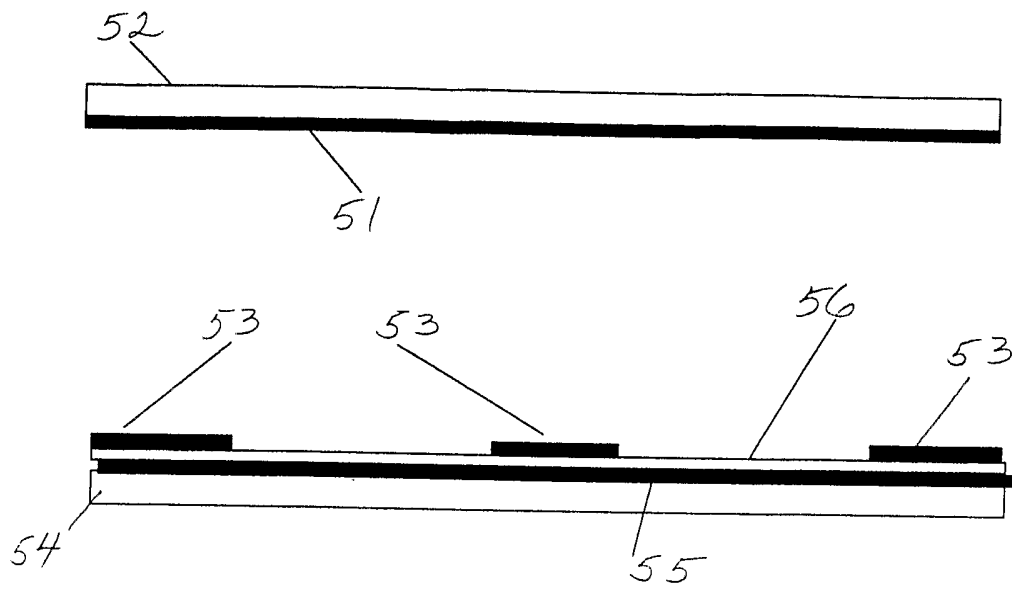


Figure 4

**Figure 5**

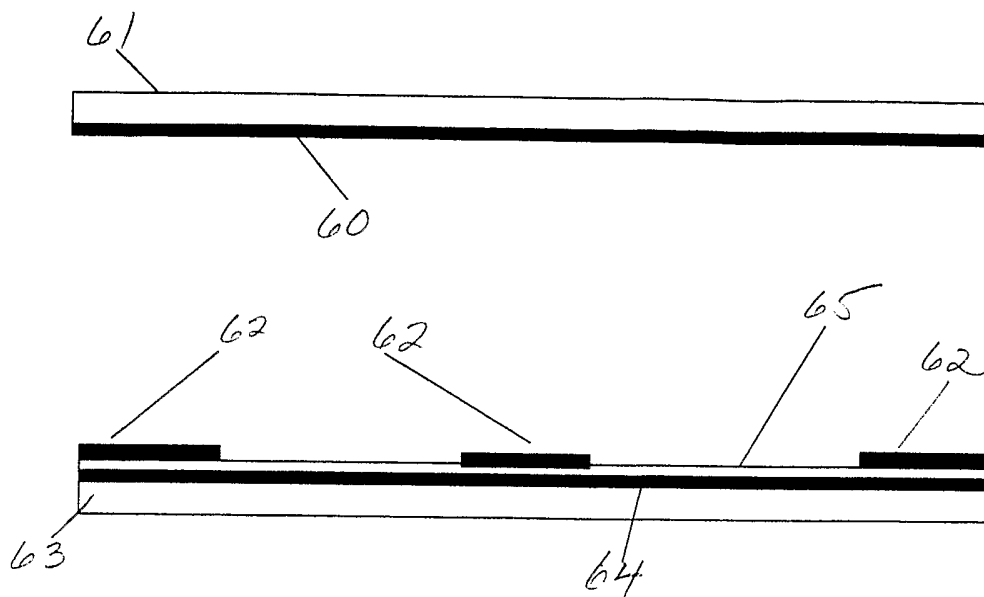


Figure 6

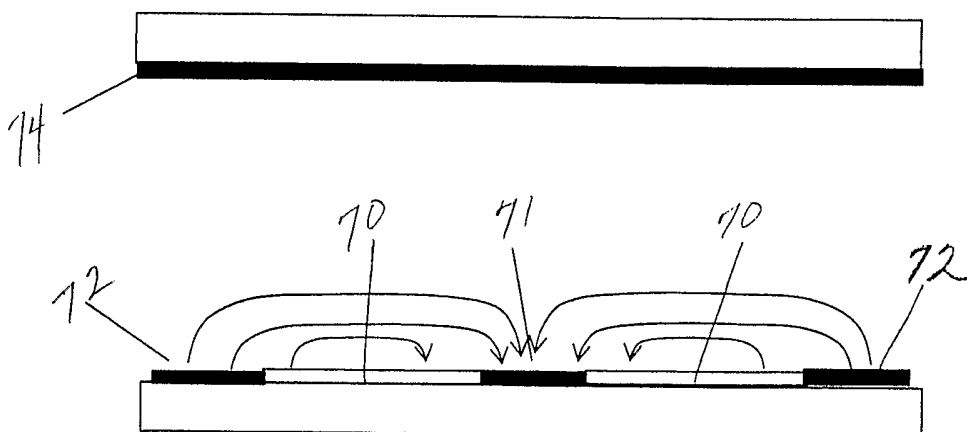


Figure 7

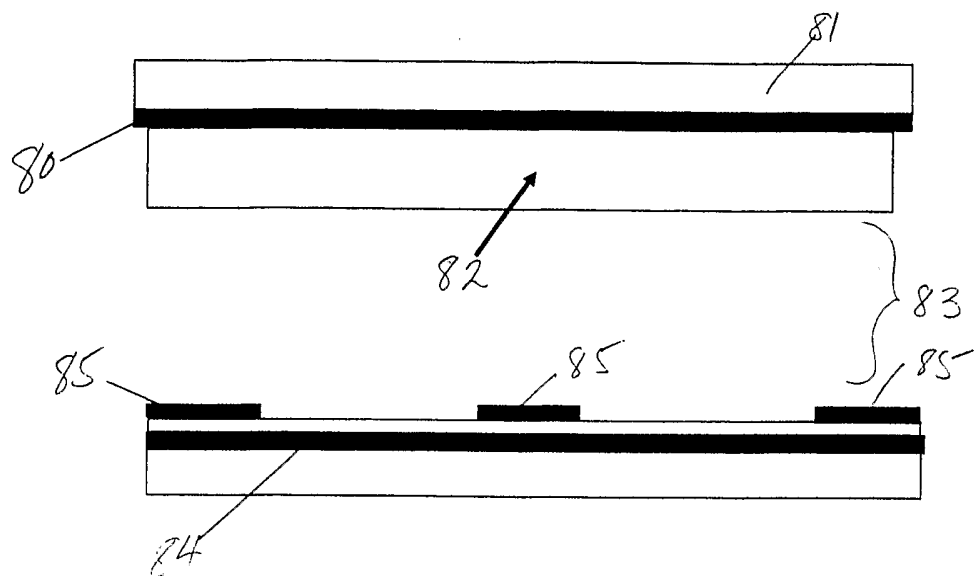


Figure 8

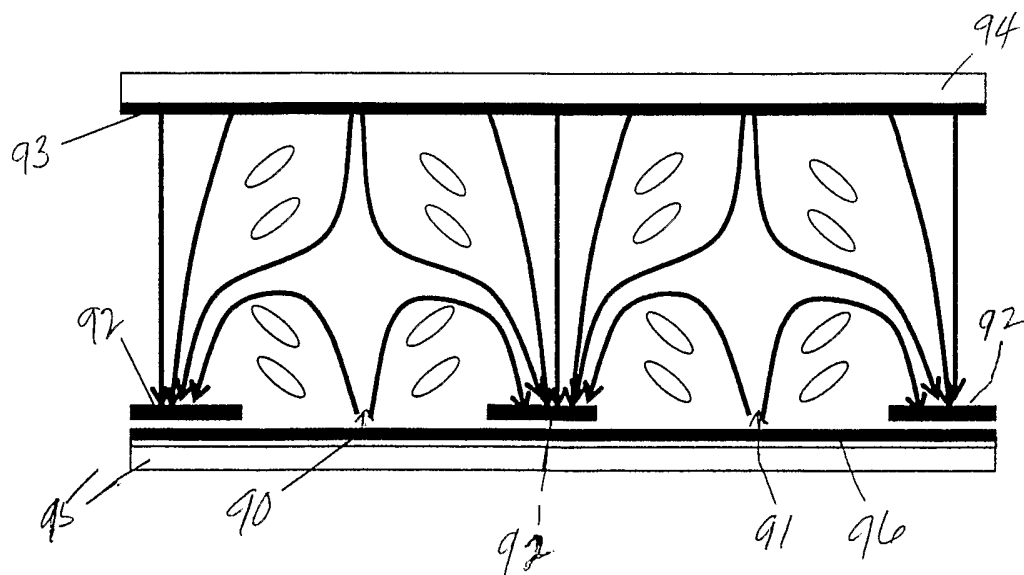


Figure 9