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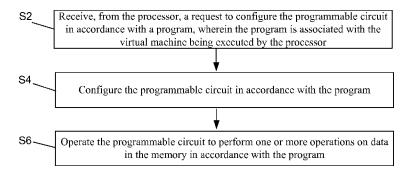
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Fig. 6



(57) Abstract: Technologies are generally described for systems, devices and methods effective to accelerate memory access. A memory unit, including a memory and a programmable circuit, may be in communication with a processor executing a virtual machine. The memory unit may receive from the processor, a request to configure the programmable circuit in accordance with a program. The program may be associated with the virtual machine. The programmable circuit may be configured in accordance with the program. The programmable circuit may then be operated to perform one or more operations on data in the memory.





# INTERNATIONAL SEARCH REPORT

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CLASSIFICATION OF SUBJECT MATTER

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USPC - 718/1

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#### FIELDS SEARCHED

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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched CPC - G06F 9/4856, G06F 9/3834, G06F 13/4243, G06F 9/466, G06F 9/526, G06F 17/30445, G06F 12/08, G06F 12/0253 (2013.01)

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) Orbit, Google Patents, Google, Google Scholar.

# C. DOCUMENTS CONSIDERED TO BE RELEVANT

Further documents are listed in the continuation of Box C.

| Category* | Citation of document, with indication, where appropriate, of the relevant passages  Relevant to |                                       |  |  |
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| Υ         | WO 2004/042569 A2 (RAPP et al) 21 May 2004 (21.05.2004), entire document                        | 3, 4, 10, 11, 14, 15, 18, ·<br>19, 22 |  |  |
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| Mail Stop PCT, Attn: ISA/US, Commissioner for Patents     |   | Blaine R. Copenheaver                              |  |  |
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