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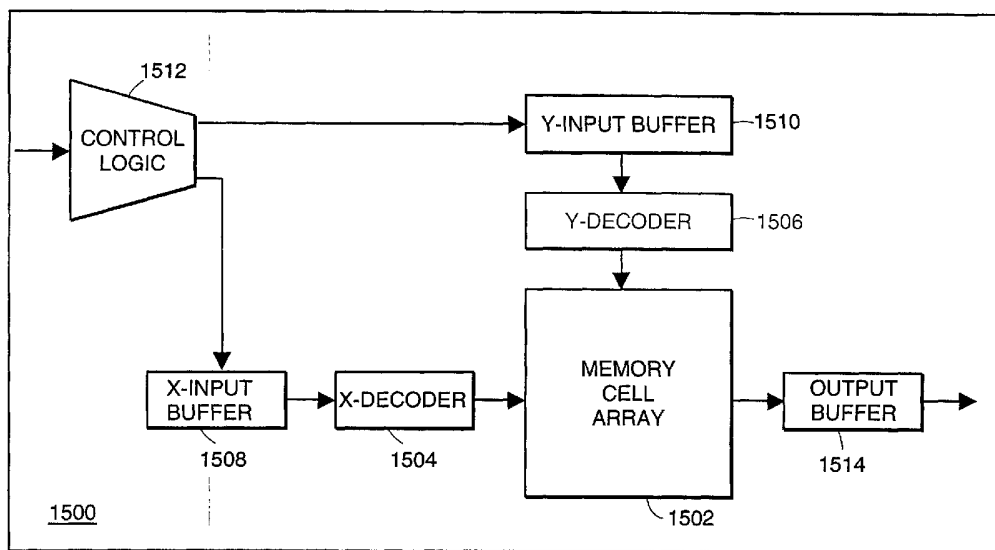
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(54) Title: HYBRID CIRCUIT HAVING NANOTUBE ELECTROMECHANICAL MEMORY



(57) Abstract: A hybrid memory system having electromechanical memory cells (103) is disclosed. A memory cell core circuit has an array of electromechanical memory cells, in which each cell is a crossbar junction at least one element of which is a nanotube or a nanotube ribbon (101). An access circuit provides array addresses to the memory cell core circuit to select at least one corresponding cell. The access circuit is constructed of semiconductor circuit elements.

INTERNATIONAL SEARCH REPORT

International application No.

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A. CLASSIFICATION OF SUBJECT MATTER

IPC(7) :G11C 11/00

US CL :365/151, 153, 158

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 365/151, 153, 158

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched
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USPTO APS EAST

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 6,128,214 A (KUEKES et al) 03 October 2000 (03.10.2000), see entire document.	1-6
A	US 6,144,481 A (KOWARZ et al) 07 November 2000 (07.11.2000), see entire document.	1-6
A	US 6,159,620 A (HEATH et al) 12 December 2000 (12.12.2000), see entire document.	1-6

☐ Further documents are listed in the continuation of Box C.
 ☐ See patent family annex.

* Special categories of cited documents:	"I" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
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