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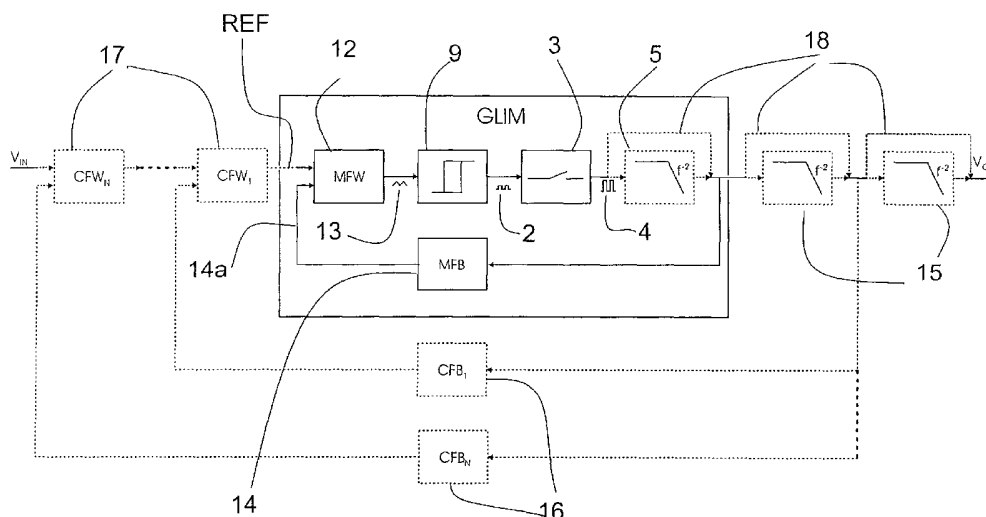
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- (54) Title:** GLOBAL LOOP INTEGRATING MODULATOR



- (57) Abstract:** A switch-mode modulator operating at a two-level voltage and including an alternating output stage (3), an optional output filter (5) and a feedback including a function block (14) with a transfer function (MFB). The modulator furthermore includes a forward block (12) provided with means for calculating the difference between the signal (14a) originating from the function block (14) and a reference signal (REF) as well as with a transfer function (MFW). The output (13) of the forward block (12) is the input of a Schmitt-trigger (9), which generates switch on signals (2) for changing the output stage. The output voltage (V_{OUT}) of the modulator applying either after the optional output filter (5) or the output stage (3) is fed back through the function block (14) so as to generate the signal (14a) fed back. The transfer function of the function block (14) and of the forward block (12) is chosen both in response to the transfer function of the output filter (5) and in response to the desired total open-loop-transfer function of the modulator.



— *before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments*

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According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H03F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

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A	US 5 521 549 A (NELSON ET AL) 28 May 1996 (1996-05-28) column 3, line 63 - column 4, line 12; figure 2 -----	1,2
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Further documents are listed in the continuation of box C.



Patent family members are listed in annex.

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