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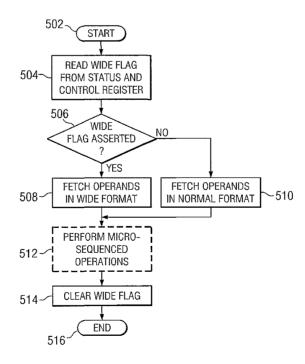
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(54) Title: OPERAND WIDTH INDICATION FOR MICRO-SEQUENCE PROCESSING



(57) Abstract: Methods and apparatus for data processing provide operand width indication for micro-sequence execution. Described embodiments include methods and apparatus that fetch a first opcode, assert a flag if the first opcode modifies an operand width of a subsequent opcode, fetch a second opcode, trigger a micro-sequence based on the opcode, read the flag by instructions of the micro-sequence (504), and fetch an operand of the second opcode by the micro-sequence (the bit width of the operand based on a state of the flag) (508, 510).



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FR, GB, GR, HU, IE, IS, IT, LT, LU, LV, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

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B. FIELDS SEARCHED			
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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched			
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)			
C. DOCUMENTS CONSIDERED TO BE RELEVANT			
Category *	Citation of document, with indication, where a		Relevant to claim No.
X	US 5,898,850 (Dickol et al.) 27 April 1999, Column 2, Lines 1-20, and Column 7, Table 1.		1-5, 7-9
Y	Y US 4,825,355 (Kurakazu et al.) 25 April 1989, Column 5		1-9
Y	US 5155807 A (Blevins et al.) 13 October, 1992, Column 1, Lines 25-30.		6
A	US 4,258,419 A (Blahut et al.) 24 March 1981		1-9
Further	documents are listed in the continuation of Box C.	See patent family annex.	
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