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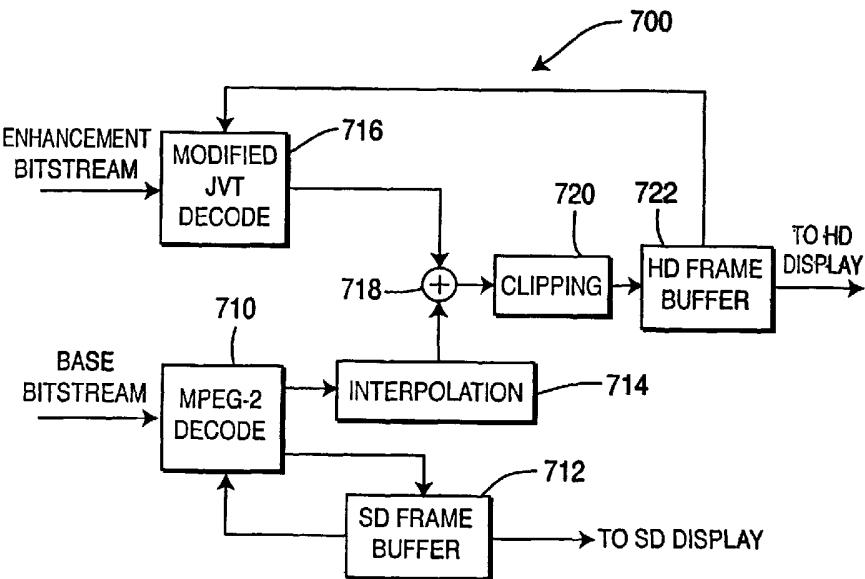
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(54) Title: TWO-LAYER DECODING FOR HYBRID HIGH-DEFINITION DVD



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(57) Abstract: A hybrid high-definition decoder (700) and method are disclosed, for processing signal data as a plurality of block transform coefficients for each of base layer data and enhancement layer data, where the decoder (700) includes a standard-definition decoder (710) for decoding the standard-definition data from a base layer bitstream embodying a standard-definition data sequence, a high-definition decoder (716) coupled to the standard-definition decoder for decoding the difference between the high-definition data and the standard-definition data from an enhancement layer bitstream stored as picture user data embodying a high-definition data sequence, and a two-layer composition unit (718) for composing a high-definition signal data sequence from the standard-definition data and the difference between the high-definition data and the standard-definition data.



*For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.*

## TWO-LAYER DECODING FOR HYBRID HIGH-DEFINITION DVD

### Cross-Reference to Related Application

This application claims the benefit of U.S. Provisional Patent Application Serial No. 60/428,853, entitled "Interleaving Two Encoding Layers For Hybrid High Definition DVD" and filed November 25, 2002, which is incorporated herein by reference in its entirety.

### Field of the Invention

The present invention is directed towards video decoders, and in particular, towards video decoders for decoding standard-definition and high-definition versions of video data on a single digital videodisc.

### Background of the Invention

Video data is generally processed in the form of bit streams by video encoders and decoders (collectively "CODECs"), and stored on digital videodisc ("DVD") media. There is a substantial installed user base of red laser DVD players using MPEG-2 decoders. Red laser devices used with MPEG-2 encoding result in storage capacities sufficient to support standard-definition ("SD") versions of typical movies, while there is great interest in supporting high-definition ("HD") versions of these movies on the same disc. Unfortunately, the installed user base of red laser players used with MPEG-2 support a storage capacity insufficient for holding both SD and HD versions of typical movies.

Red laser and blue laser technologies have both been considered for reaching storage capacities sufficient to support high-definition recordings ("HD-DVD"). The blue laser technology has the advantage of providing sufficient storage to hold multiple high-quality HD movies on one disc using MPEG-2, but it is not yet economically feasible to use blue lasers to supplant the installed user base of red laser devices. Hence, there is a need for a coding scheme usable for HD-DVD that can also support SD-DVD on the same disc readable by current red laser devices using MPEG-2 decoders.

Thus, with red laser HD-DVD, it is desirable to store on a single disc, in addition to the HD version, an SD version of a movie that can be read by a current player having an MPEG-2 decoder. This would mean that content creators would not

need to make a separate HD-DVD disc in addition to the SD disc, and retailers would need to stock only one disc per movie, using just one stock keeping unit ("SKU").

This may avoid problems such as those encountered with broadcast high-definition television ("HDTV"), where broadcasters did not want to transmit HD until more HD

5 sets were sold, but consumers did not want to buy HD sets until there was more HD content available.

The present invention assumes the use of single-sided storage media. While double-sided discs are an option for obtaining more storage, there is some resistance to using both sides of the disc. This is due, in part, to the disadvantages of increased 10 cost and the fact that storing content on both sides interferes with the labeling that is normally put on one side of a disc. Accordingly, the approaches for HD-DVD of (i) blue laser technology; (ii) double-sided red laser discs; and (iii) separate red laser discs for HD and SD versions of a movie; each have significant drawbacks and disadvantages.

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### Summary of the Invention

These and other drawbacks and disadvantages of the prior art are addressed by an apparatus and method for hybrid high-definition ("HD") video decoding.

A hybrid high-definition decoder retrieves signal data as a plurality of block 20 transform coefficients for each of a base layer and an enhancement layer, where the decoder includes a standard-definition decoder for decoding the standard-definition data from a base layer bitstream embodying a standard-definition data sequence, a high-definition decoder coupled to the standard-definition decoder for decoding the difference between the high-definition data and the standard-definition data from user 25 data embodying a high-definition data sequence, and a two-layer composition unit for composing a high-definition signal data sequence from the standard-definition data and the difference between the high-definition data and the standard-definition data.

These and other aspects, features and advantages of the present invention 30 will become apparent from the following description of exemplary embodiments, which is to be read in connection with the accompanying drawings.

### Brief Description of the Drawings

The present invention utilizes hybrid interleaving video CODECs for integrating standard-definition ("SD") with high-definition ("HD") on a single digital videodisc ("DVD") in accordance with the following exemplary figures, in which:

- 5        Figure 1 shows a schematic data structure for a hybrid DVD player in accordance with the principles of the present invention;
- Figure 2 shows a schematic data structure for two program chains in accordance with the principles of the present invention;
- 10      Figure 3 shows a schematic data structure for presentation of interleaved units in accordance with the principles of the present invention;
- Figure 4 shows a table of the video data in a video object unit in accordance with the principles of the present invention;
- Figure 5 shows a block diagram for another hybrid interleaved encoder in accordance with the principles of the present invention; and
- 15      Figure 6 shows a block diagram for a hybrid interleaved decoder in accordance with the principles of the present invention.

### Detailed Description of Preferred Embodiments

- The present invention provides for both standard-definition ("SD") and high-definition ("HD") versions of a movie, for example, suitable for storage on a single red-laser disc. The approach is that of providing single-disc SD and HD using MPEG-2 for the SD data and MPEG-4 AVC (also known as JVT, MPEG-4 Part 10, or AVC) stored as user data of the base layer for the difference between the SD data and the HD data. A first exemplary embodiment of the approach supports a total peak bitrate up to about 9.8 Mbps on a single-sided red-laser videodisc, for example. Alternate embodiments may be used to support higher bitrates. A second exemplary embodiment supports higher bitrates for high-bandwidth segments while continuing to provide single-disc SD and HD by using a hybrid interleaved approach with MPEG-2 for the SD data and MPEG-4 AVC for the HD data in such high-bandwidth segments.
- 30      For storing SD and HD on one disc, the approach of storing the HD difference data as base layer user data is described, with or without optional interleaving for high-bandwidth segments. Due to the storage capacity constraint with red laser DVD, MPEG-2 is not sufficient for the HD layer. Embodiments of the present invention store hybrid encoding data on DVD discs that can be played back by

current DVD players. Two exemplary embodiments are described. The user data approach saves the higher layer data as user data of the base layer, and the interleaving approach stores base layer and enhancement layer data in an interleaved data structure for high-bandwidth video segments. Thus, these and other 5 embodiments allow two versions of a movie, one standard definition and one high definition, to be stored on a single DVD disc.

The hybrid high-definition DVD has two layers. The base layer is MPEG2, and the enhancement layer is MPEG-4 AVC (or JVT). These two layers can be interleaved such that current generation SD DVD players can play back the base 10 layer MPEG2 stream, and the hybrid HD DVD player can play back both layers. Thus, such DVD discs are backward compatible.

Of the two exemplary embodiments, the first saves enhancement layer data as user data of the base layer. To do that, the current DVD authoring tools can be used to author the hybrid DVD disc without any significant changes.

15 The second exemplary embodiment uses interleaving blocks for high-bandwidth segments in combination with the user data approach of the first embodiment for lower bandwidth segments. Two video objects ("VOBs") and two program chains ("PGCs") are created, one for the base layer and the other for the enhancement layer. Current authoring tools should be modified to accommodate 20 these changes.

For the first embodiment, where MPEG-4 AVC data is to be saved as MPEG2 picture user data, the enhancement layer data can be saved as GOP user data or picture user data. Because GOP user data is used for closed captioning in DVD, the picture user data is the preferred modality to store the enhancement layer data. The 25 picture user data will not be decoded when the disc is played on current SD DVD players.

As shown in Figure 1, a buffer arrangement 100 can be used on current DVD players with one track buffer 110 and four secondary buffers 114-120. The video data is demultiplexed with a demultiplexor 112, and transferred to an MPEG video buffer 114, where the user data will be processed or skipped over. The new hybrid HD DVD player has at least one additional buffer 122, an MPEG-4 AVC Video Buffer. The enhancement layer data is demultiplexed and sent to this buffer 122 for decoding. The data stored in the track buffer 110 is stored sector by sector 111, so the enhancement layer data is sector aligned.

Turning to Figure 2, an interleaved data structure for two PGCs is indicated generally by the reference numeral 200. Here, an interleaved block 210 includes interleaving more than one VOB 212, 214 to enable seamless presentation of more than one path. Each VOB is divided into the same number of interleaved units ("ILVUs") 213, 215, respectively. By appropriately setting the size of the interleaved units, the time needed for jumping may be kept within the value permitted, and seamless playback can be realized.

As shown in Figure 3, an ILVU includes one or more VOBUs. In this exemplary embodiment, each ILVU has one or two VOBUs. The presentation of ILVUs is indicated generally by the reference numeral 300. The ILVUs 310 belong to an enhancement layer PGC while the other ILVUs 312 belong to a base layer PGC.

The playback time of an ILVU is about 1.0 second in this exemplary embodiment. The exemplary maximum jump distance is 660 sectors. If the base layer is 1/3 of the data, then it has 220 sectors, and the enhancement layer ILVU has 440 sectors. On average, the jump distance is 440 sectors, which guarantees seamless playback.

The preferred identifications in video management information ("VMGI") and in video title set information (VTSI) to help a player to recognize the disc type are:

VMGI--VMGI\_MAT: Bytes 16 -27: reserved to "MPEG2AVC-VMG"  
VTSI--VTSI\_MAT: Bytes 16 -27: reserved to "MPEG2AVC-VTS"

Turning now to Figure 4, the video data in a video object unit ("VOBU") can be organized as shown in the table 400. Thus, a picture has three types of data, and they are each sector aligned: 1. picture header without including picture user data; 2. picture user data; and 3. picture data.

The second exemplary embodiment uses interleaving blocks to interleave the two layers. Two program chains ("PGCs") are created, one for the base layer and the other for the high or enhancement layer. Here, the first VOB is used for the MPEG2 base layer, and the other is used for the MPEG-4 AVC enhancement layer. The base layer and enhancement layer are not in the same VOBU, but are interleaved. To avoid playback of the second PGC, the second PGC is not assigned an entry point. The hybrid HD DVD player merges these two PGCs after it recognizes a hybrid HD DVD disc.

As shown in Figure 5, a hybrid interleaving encoder is indicated generally by the reference numeral 600. The encoder includes a downsampling unit 610 for receiving an original HD sequence and downsampling to provide Base layer pixels. The encoder 600 also includes a first summing block 611 for receiving the original HD sequence at a non-inverting input. The downsampling unit 610 is coupled in signal communication with an MPEG-2 encoder 612 that receives the Base layer pixels from the downsampling unit and provides a Base layer bitstream output. The MPEG-2 encoder 612 is coupled to an SD frame buffer 614 and provides reconstructed base pixels to the buffer 614. The buffer 614 is coupled in feedback signal communication with the encoder 612, and is further coupled to an interpolator 616. The interpolator 616 is coupled to an inverting input of the first summing block 611. The interpolator 616 is further coupled to a first input of a second summing block 618, which has its output coupled to a clipper 620. The clipper 620, in turn, is coupled to an HD frame buffer 622, which is coupled to a modified MPEG-4 AVC encoder 624. The encoder 624 has an input coupled to the output of the first summing block 611 for receiving Enhancement layer pixels, and an output coupled to a second input of the second summing block 618 for providing reconstructed Enhancement layer pixels to the second summing block 618. The encoder 624 provides an Enhancement layer bitstream output.

Turning to Figure 6, a hybrid interleaving decoder is indicated generally by the reference numeral 700. The decoder 700 includes an MPEG-2 decoder 710 for receiving a Base layer bitstream. The MPEG-2 decoder is coupled to an SD frame buffer 712 for buffering the standard-definition frames. The SD frame buffer 712 provides an output suitable for an SD display, and is coupled back to the MPEG-2 decoder 710. The MPEG-2 decoder is further coupled to an interpolator 714, which is coupled, in turn, to a non-inverting input of a two-layer composition unit or summing block 718. The hybrid interleaved decoder 700 further includes a modified JVT decoder 716 for receiving an Enhancement layer bitstream. The modified JVT decoder is coupled to a second non-inverting input of the summing block 718. The output of the summing block 718 is coupled to a clipping unit 720, which is coupled, in turn, to an HD frame buffer 722. The HD frame buffer 722 provides an output suitable for an HD display, and is also coupled back to the modified JVT decoder 716. In operation, the exemplary hybrid interleaving scheme uses MPEG-2 encoding for the base layer and modified MPEG-4 AVC encoding for the enhancement layer.

In one exemplary system, the resolution for the HD layer is 1280x720, and the SD layer is 704x480.

In operation, Figure 5 shows a high-level block diagram of the hybrid interleaving encoder. First, the original HD material is decomposed into a base layer containing the low frequency content and an enhancement layer containing the high frequency content of the original sequence. The base layer is encoded using MPEG-2 and the enhancement layer is encoded using a modified version of MPEG-4 AVC. The base and enhancement bitstreams are recorded on the disc in an interleaved fashion. The reconstructed HD frames are obtained by interpolating the reconstructed base layer pixels and adding the result to the reconstructed enhancement layer pixels.

The hybrid interleaving decoder includes an MPEG-2 decoder, a modified MPEG-4 AVC decoder, and an interpolator for the base layer. The output of the interpolator and the MPEG-4 AVC decoder are summed to give the reconstructed HD frames.

Referring back to Figure 5, the enhancement layer pixels are reconstructed and added to the interpolated reconstructed base layer pixels to form the reconstructed HD frames. These reconstructed HD frames are used as reference frames for coding of future enhancement layer data.

Interleaving of bitstreams on disc may be accomplished as follows. The standard-definition data is stored in the base stream, and the high-definition enhancement data is stored as user data of the base stream. Only the standard-definition data would be playable by pre-existing DVD players. The current DVD standard specifies constraints for seamless branching (for example, maximum jump sectors, minimum buffer sectors) that, if met, should ensure seamless playback on such pre-existing players.

In accordance with the principles of the present invention, the disclosed CODEC provides the ability to decode two bitstreams from a single disc at the same time to obtain the HD version, or to decode a single bitstream to obtain the SD version. New players will play a disc with a non-interleaved MPEG-4 AVC stream, in addition to the interleaved streams. Embodiments of the present invention may encourage content creators, video stores and consumers to build up stocks of HD material before the new HD players have reached an installed quantity that would justify creating, selling and buying HD-only discs.

Thus, embodiments of the present invention allow two versions of a movie, one standard-definition ("SD") and one high-definition ("HD"), to be read from a dual-layer, single-sided, red laser DVD disc without the need to store the information included in the SD version a second time as part of the HD version. The coding is done using a hybrid MPEG-2 and MPEG-4 AVC interleaving. The use of MPEG-2 for the base layer provides an SD bitstream that can be played by a current SD DVD player. The enhancement layer is coded using an MPEG-4 AVC scheme to provide the coding efficiency needed to put both the SD and HD content on a single disc.

5 The present invention may also be applied to streaming and/or volatile content, such

10 as, for example, streaming Internet video, in addition to DVD media. Although described in a manner in which the standard-definition version is encoded in MPEG-2 format and the high-definition format is encoded in MPEG-4 AVC format, it would be apparent to those skilled in the art that the invention equally applies if standard formatting other than those formats are used, and the claims are to be construed as 15 also including formats other than MPEG-2 and MPEG-4 AVC. Similarly, although described as being encoded on a DVD, it would be apparent to those skilled in the art that the invention equally applies to optical disc formats other than DVD; and that claims that recite incorporation of a digital videodisc are intended to include other optical disc formats other than a DVD.

20 This exemplary description merely illustrates the principles of the invention. It will thus be appreciated that those skilled in the art will be able to devise various arrangements, which, although not explicitly described or shown herein, embody the principles of the invention, and are included within its spirit and scope. Furthermore, all examples and conditional language recited herein are principally intended 25 expressly to be only for pedagogical purposes to aid the reader in understanding the principles of the invention and the concepts contributed by the inventor(s) to furthering the art, and are to be construed as being without limitation to such specifically recited examples and conditions. Moreover, all statements herein reciting principles, aspects, and embodiments of the invention, as well as specific examples 30 thereof, are intended to encompass both structural and functional equivalents thereof. Additionally, it is intended that such equivalents include both currently known equivalents as well as equivalents developed in the future, i.e., any elements developed that perform the same function, regardless of structure.

Thus, for example, it will be appreciated by those skilled in the art that the block diagrams herein represent conceptual views of illustrative circuitry embodying the principles of the invention. Similarly, it will be appreciated that any flow charts, flow diagrams, state transition diagrams, pseudocode, and the like represent various 5 processes which may be substantially represented in computer readable media and so executed by a computer or processor, whether or not such computer or processor is explicitly shown.

The functions of the various elements shown in the figures, (including functional blocks) may be provided through the use of dedicated hardware as well as 10 hardware capable of executing software in association with appropriate software. When provided by a processor, the functions may be provided by a single dedicated processor, by a single shared processor, or by a plurality of individual processors, some of which may be shared. Moreover, explicit use of the term "processor" or "controller" should not be construed to refer exclusively to hardware capable of 15 executing software, and may implicitly include, without limitation, digital signal processor ("DSP") hardware, read-only memory ("ROM") for storing software, random access memory ("RAM"), and non-volatile storage. Other hardware, conventional and/or custom, may also be included. Similarly, any switches shown in the figures are conceptual only. Their function may be carried out through the operation of 20 program logic, through dedicated logic, through the interaction of program control and dedicated logic, or even manually where the particular technique being selectable by the implementer is more specifically understood from the context.

In the claims hereof any element expressed as a means for performing a specified function is intended to encompass any way of performing that function 25 including, for example, a) a combination of circuit elements which performs that function or b) software in any form, including, therefore, firmware, microcode or the like, combined with appropriate circuitry for executing that software to perform the function. The invention as defined by such claims resides in the fact that the functionalities provided by the various recited means are combined and brought 30 together in the manner which the claims call for. Applicant thus regards any means that can provide those functionalities as equivalent to those shown herein.

These and other features and advantages of the present invention may be readily ascertained by one of ordinary skill in the pertinent art based on the teachings herein. It is to be understood that the principles of the present invention may be

implemented in various forms of hardware, software, firmware, special purpose processors, or combinations thereof.

Most preferably, the principles of the present invention may be implemented in a combination of hardware and software. Moreover, the software may be

5 implemented as an application program tangibly embodied on a program storage unit. The application program may be uploaded to, and executed by, a machine comprising any suitable architecture. The machine may be implemented on a computer platform having hardware such as one or more central processing units (“CPU”), a random access memory (“RAM”), and input/output (“I/O”) interfaces. The  
10 computer platform may also include an operating system and microinstruction code. The various processes and functions described herein may be either part of the microinstruction code or part of the application program, or any combination thereof, which may be executed by a CPU. In addition, various other peripheral units may be connected to the computer platform such as an additional data storage unit and a  
15 printing unit.

It is to be further understood that, because some of the constituent system components and methods depicted in the accompanying drawings are preferably implemented in software, the actual connections between the system components or the process function blocks may differ depending upon the manner in which the

20 present invention is programmed. Given the principles described herein, one of ordinary skill in the pertinent art will be able to contemplate these and similar implementations or configurations of the present invention without departing from the spirit and scope of the present invention.

25 Although the illustrative embodiments have been described herein with reference to the accompanying drawings, it is to be understood that the present invention is not limited to those precise embodiments, and that various changes and modifications may be effected therein by one of ordinary skill in the pertinent art without departing from the scope or spirit of the present invention. All such changes and modifications are intended to be included within the scope of the present  
30 invention as set forth in the appended claims.

CLAIMS

1. A decoder (700) for decoding encoded signal data from a plurality of block transform coefficients for each of base layer data and enhancement layer data,

5 the decoder comprising:

a standard-definition decoder (710) for decoding the standard-definition data from a base layer bitstream embodying a standard-definition data sequence;

a high-definition decoder (716) coupled to the standard-definition decoder for decoding the difference between the high-definition data and the standard-definition  
10 data from an enhancement layer bitstream stored as picture user data embodying a high-definition data sequence; and

a two-layer composition unit (718) for composing a high-definition signal data sequence from the standard-definition data and the difference between the high-definition data and the standard-definition data.

15

2. A decoder as defined in Claim 1 wherein the enhancement layer data is interleaved with the base layer data for at least one segment.

20

3. A decoder as defined in Claim 2 wherein the enhancement layer data is interleaved with the base layer data for each segment having a bandwidth greater than about 9.8 Mbps.

4. A decoder as defined in Claim 1 wherein the signal data comprises video pixel data.

25

5. A decoder as defined in Claim 1 wherein the standard-definition decoder meets an MPEG-2 standard.

30

6. A decoder as defined in Claim 1 wherein the high-definition decoder meets an MPEG-4 AVC standard.

7. A decoder for decoding signal data from a plurality of block transform coefficients for each of a base layer and an enhancement layer, the decoder comprising:

- first receiver means for receiving an encoded base layer bitstream;
- first decoder means for decoding standard-definition data from the base layer bitstream;
- second receiver means for receiving an encoded enhancement layer bitstream
- 5 as base layer picture user data;
- second decoder means for decoding the difference between high-definition data and the standard-definition data from the enhancement layer bitstream; and
- composition means for composing a high-definition signal data sequence from the decoded standard-definition data and the decoded difference between high-  
10 definition data and the standard-definition data.

8. A method for decoding signal data from a plurality of block transform coefficients for each of base layer data and enhancement layer data, the method comprising:

- 15 receiving an encoded base layer bitstream;
- decoding standard-definition data from the base layer bitstream;
- receiving an encoded enhancement layer bitstream as base layer picture user data;
- decoding the difference between high-definition data and the standard-  
20 definition data from the enhancement layer bitstream; and
- composing a high-definition signal data sequence from the decoded standard-definition data and the decoded difference between the high-definition data and the standard-definition data.

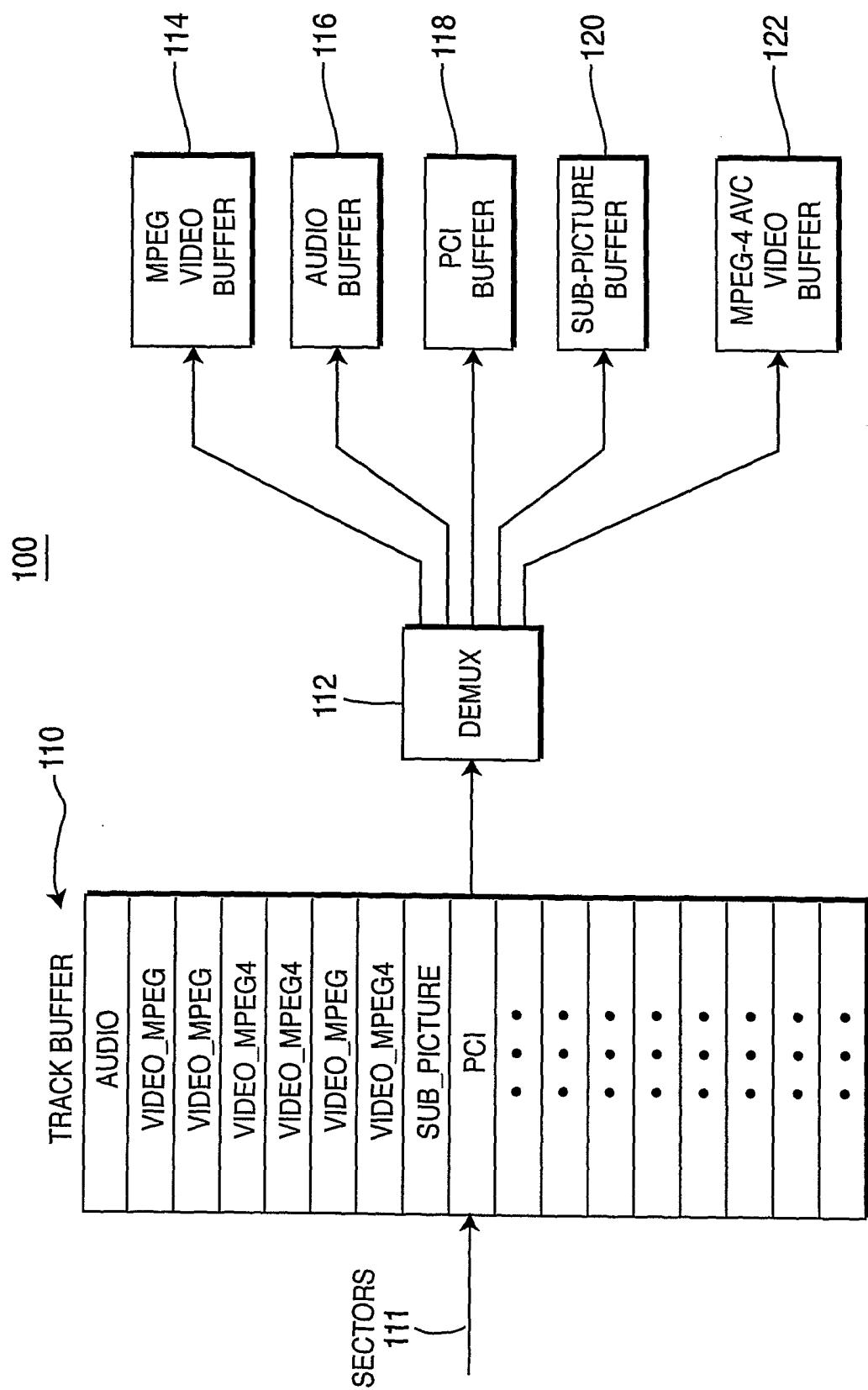


FIG. 1

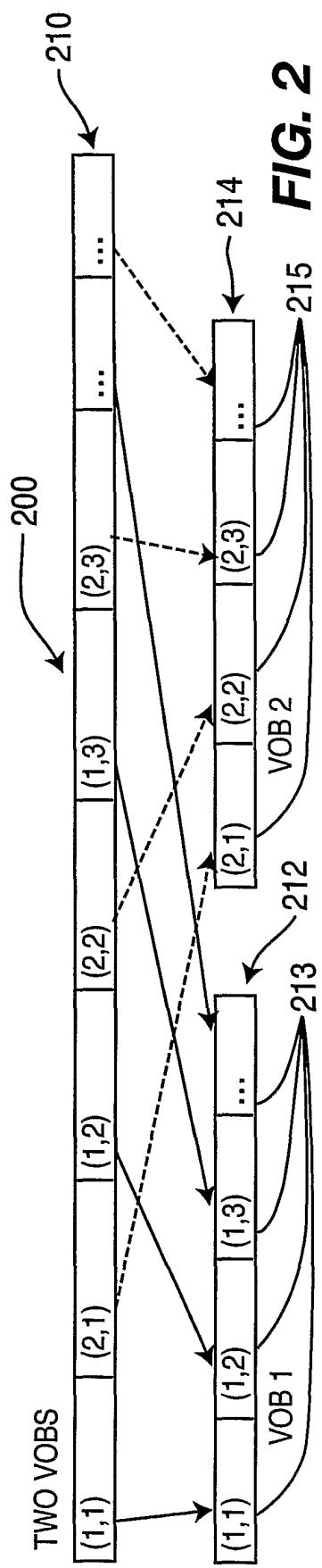
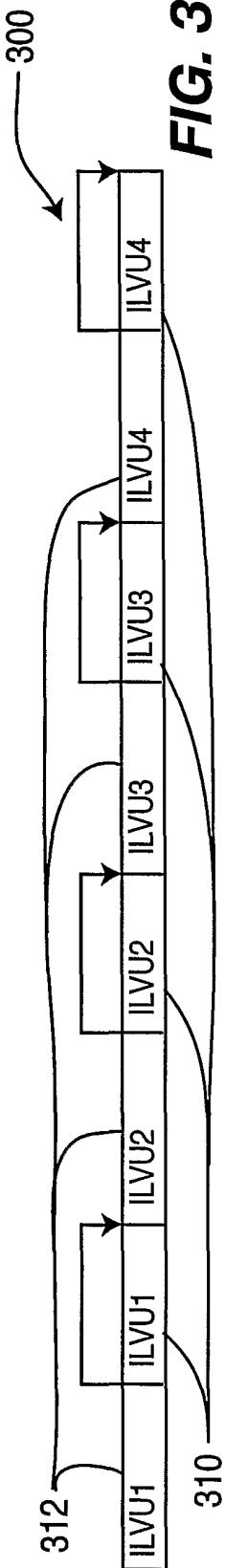


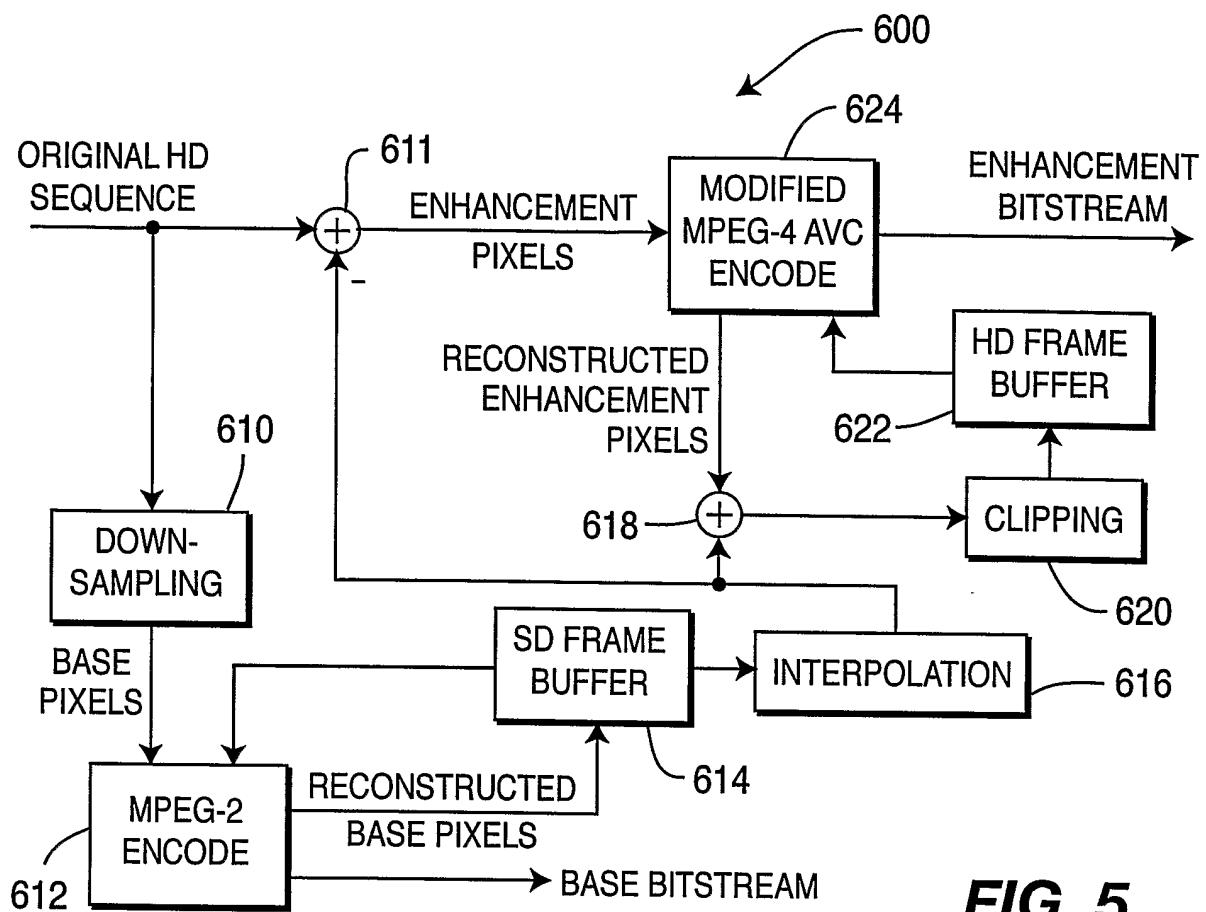
FIG. 2



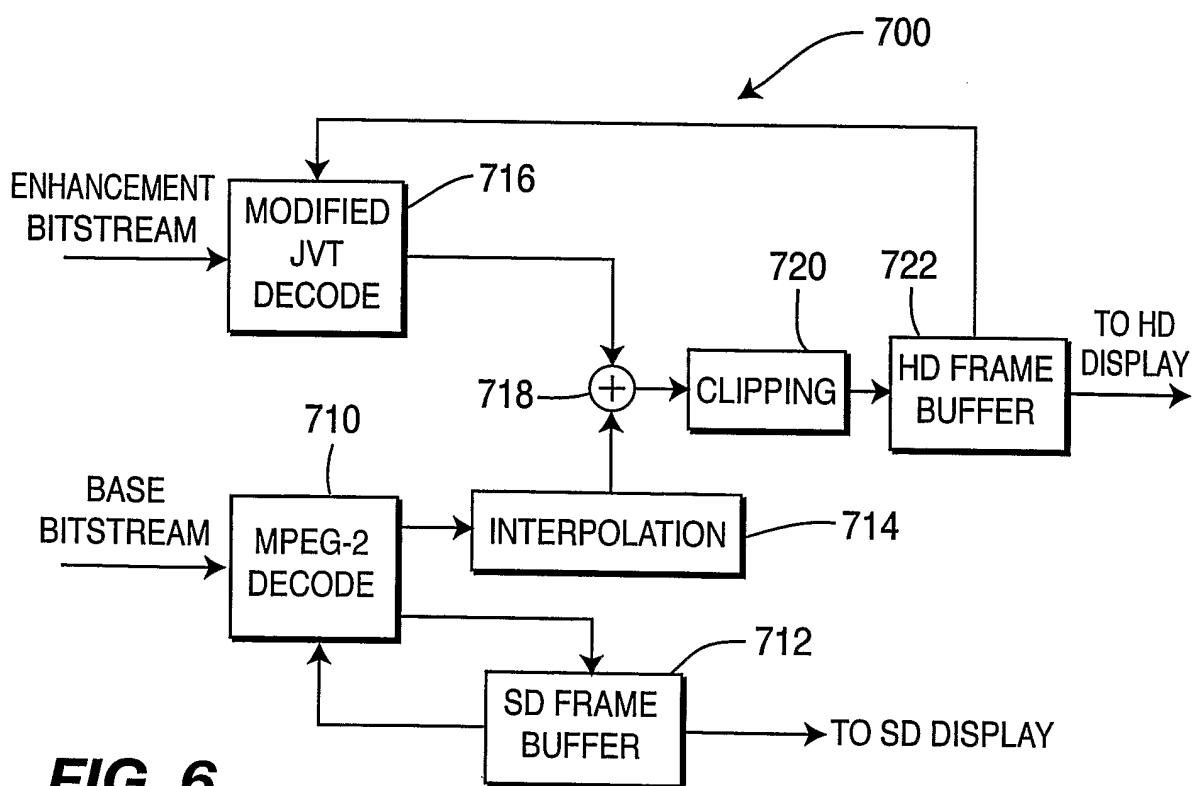
### FIG. 3

SECTOR #	DATA
1 TO i	SEQUENCE HEADER, GOP HEADER, PICTURE HEADER WITHOUT USER DATA, PLUS PADDING DATA
i + 1 TO j	FIRST PICTURE ENHANCEMENT LAYER DATA PLUS PADDING DATA (PICTURE USER DATA)
j + 1 TO n	FIRST PICTURE BASE LAYER DATA PLUS PADDING DATA
n + 1 TO m	SECOND PICTURE HEADER WITHOUT USER DATA PLUS PADDING DATA
m + 1 TO k	SECOND PICTURE ENHANCEMENT LAYER DATA PLUS PADDING DATA (PICTURE USER DATA)
k + 1 TO L	SECOND PICTURE BASE LAYER DATA PLUS PADDING DATA
...	

FIG. 4



**FIG. 5**



**FIG. 6**

**INTERNATIONAL SEARCH REPORT**

International application No.

PCT/US03/33889

**A. CLASSIFICATION OF SUBJECT MATTER**

IPC(7) : H04N 07/18  
US CL : 375/240.25

According to International Patent Classification (IPC) or to both national classification and IPC

**B. FIELDS SEARCHED**

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 375/240.10-240.17, 240.21, 240.25-240.29

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)  
APS: MPEG-2, MPEG-4 AVC, base layer, enhancement layer, HDTV

**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5,515,377 A (HORNE et al) 07 May 1996, column 6, lines 25-61; column 8, lines 40-67.	1-8

<input type="checkbox"/>	Further documents are listed in the continuation of Box C.	<input type="checkbox"/>	See patent family annex.
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"A"	document defining the general state of the art which is not considered to be of particular relevance	"X"	document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
"E"	earlier application or patent published on or after the international filing date	"Y"	document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
"L"	document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	"&"	document member of the same patent family
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Date of the actual completion of the international search <u>25 March 2004 (25.03.2004)</u>	Date of mailing of the international search report <u>09 APR 2004</u>
Name and mailing address of the ISA/US Mail Stop PCT, Attn: ISA/US Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450 Facsimile No. (703)305-3230	Authorized officer Andy S. Rao Telephone No. (703)205-4711 