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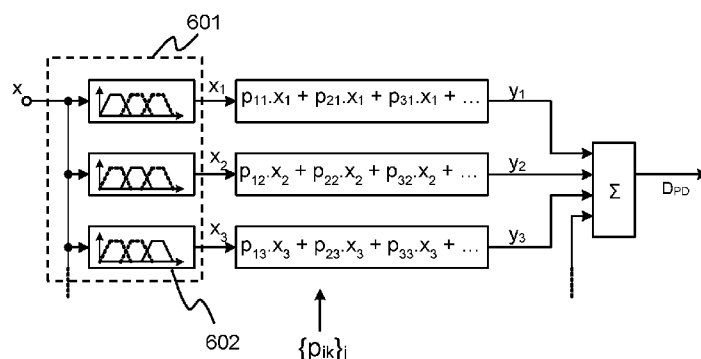
(54) **Title:** AUDIO AMPLIFIER WITH PRE-DISTORTION

Figure 6

(57) **Abstract:** This application relates to audio amplifier circuitry (100). An amplifier module (103) is located in a signal path between an input (101) and an output (102). A detection module (106) is configured to detect a characteristic of a load (104) electrically coupled, in use, to the output. A distortion setting controller (107) is provided for selecting one of a plurality of stored distortion settings $\{p_i\}$ based on the detected characteristic of the load; and a pre-distortion module (105) is configured to apply a first transfer function to a signal in the signal path prior to said amplifier module. The first transfer function is based on the selected distortion setting and for at least one of the stored distortion settings the corresponding first transfer function comprises a non-linear distortion function.



AUDIO AMPLIFIER WITH PRE-DISTORTION

This application relates to methods and apparatus for amplification of signals, and especially to audio amplifiers, and in particular to amplifiers that may be used to drive
5 different types of load.

Various electronic devices have audio driver, i.e. amplifier, circuitry for driving an audio signal to an audio load, e.g. a loudspeaker, and often devices may have a connector, in the form of a plug and socket for example, for outputting audio signals to an external
10 device. The external device could be a peripheral device such as a headset with relatively small loudspeakers, e.g. located in earphones or earbuds or the like. Such headset loudspeakers will typically have a relatively low impedance, say of the order of a few ohms or so. Additionally however it is often desired that the same audio driver circuitry can be used to drive higher impedance loudspeakers when connected, say of
15 the order of a few hundred ohms or so. It is also desirable for the same audio driver circuitry to be able to drive a line load of the order of a few kilo-ohms or more via the audio connector, e.g. when connected to a suitable audio system, which may for instance be part of a docking arrangement. Thus the audio driver circuitry should be capable of driving different types of load.

20

For high quality sound reproduction the distortion of the audio signal, especially THD (total harmonic distortion) and intermodulation distortion, should be low. Various factors may contribute to distortion in an amplifier circuit driving a load. Typically the distortion is worst for lower impedance loads. An amplifier may be designed to maintain
25 adequately low distortion into a low impedance load by increasing the output stage quiescent current, generally requiring larger output devices and higher current pre-driver stages. Distortion may also be managed by designing the amplifier circuitry with multiple amplifier stages to attempt to suppress output distortion by a high open loop amplifier gain. However these approaches generally increase both the power
30 consumption of the amplifier circuitry and the size and complexity, and hence cost, of the circuitry.

For many electronic devices, e.g. especially for battery powered devices where power demands impact on battery life, power efficiency of the components of the device may

be a concern. Higher power consumption may also have associated cooling requirements. The size and cost of components are also of concern for such applications.

- 5 Thus according to the present invention there is provided audio amplifier circuitry comprising:
- an amplifier module located in a signal path between an input and an output;
 - a detection module for detecting a characteristic of a load electrically coupled, in use, to the output;
 - 10 a distortion setting controller for selecting one of a plurality of stored distortion settings based on the detected characteristic of the load; and
 - a pre-distortion module configured to apply a first transfer function to a signal in the signal path prior to said amplifier module, wherein the first transfer function is based on the selected distortion setting;
 - 15 wherein for at least one of the stored distortion settings the corresponding first transfer function comprises a non-linear distortion function.

The first transfer function may be based on a polynomial function. Each of the stored distortion settings may comprise a set of one or more distortion coefficients for the polynomial function. Each distortion setting may comprise a set of distortion

20 coefficients characterised for a particular load characteristic. In some embodiments at least some distortion coefficients may have an associated phase delay component and the pre-distortion module may be configured to apply a distortion component corresponding to each distortion coefficient at a respective delay indicated by the

25 respective phase delay component.

For one of the stored distortion settings the corresponding first transfer function may correspond to substantially no distortion. For a plurality of the stored distortion settings the corresponding first transfer functions may comprise a non-linear distortion function,

30 wherein different distortion settings correspond to different non-linear distortion functions.

In some embodiments the audio amplifier circuitry may further comprise a storage module for storing the plurality of distortion settings. The storage module may comprise a look-up table.

- 5 The characteristic of the load that is determined may be the impedance of the load. In some embodiments the distortion setting controller may be configured to compare the impedance of the load with one or more threshold values to determine an impedance band for the load and to select one of the stored distortion settings that is appropriate for said impedance band.

10

The detection module may be coupled to the output of the amplifier circuitry. The detection module may be configured to determine the characteristic of the load through monitoring of the electrical properties at the output when a load is connected. The detection module may be configured to detect when a load is operably connected to the output and then to determine the characteristic of the load. In response to a
15 detection that a load has been operably connected to the output, the amplifier circuitry may be configured to apply a test stimulus to the output and the detection module may be configured to monitor of the electrical properties at the output in response to the test stimulus to determine the characteristic of the load.

20

The detection module may be configured to, in use, continuously or periodically monitor the characteristic of the load. When the detection module is monitoring the characteristic of the load, the amplifier circuitry may be configured to add a pilot tone at a pilot tone frequency to the signal in the signal path. The detection module may
25 monitor the electrical properties at the output at said pilot tone frequency. The pilot tone frequency may be below a low frequency threshold corresponding to an expected low frequency cut off for the possible loads.

In some embodiments the distortion setting controller is configured such that the
30 selection of the one of the stored plurality of distortion settings is also based on an indication of at least one operating condition of the amplifier circuitry. In some embodiments the distortion setting controller may be configured to modify the selected distortion setting based on an indication of at least one operating condition of the amplifier circuitry. The at least one operating condition of the amplifier circuitry

comprises temperature of the amplifier circuitry and/or the at least one operating condition of the amplifier circuitry may comprise at least one of: a supply voltage; a bias current; an amplifier operating mode.

- 5 In some embodiments the distortion setting controller is configured, such that, in the event that the selected distortion setting is changed in use, the distortion setting controller transitions from the old distortion setting to the new distortion setting via at least one intermediate setting value.
- 10 In some embodiments the pre-distortion module comprises a digital signal processor.

The audio amplifier circuitry may be implemented as an integrated circuit and aspects relate to an integrated circuit comprising audio amplifier circuitry in any of the variants described above.

15

- Aspects also relate to an electronic device comprising audio amplifier circuitry as described in any of the above variants. The output of the audio amplifier circuitry may be connected to an audio connector of the electronic device, the audio connector being configured for mating connection with a corresponding connector of a peripheral
- 20 device. The electronic device may be at least one of: a communications device; a computing device; a portable device; a battery powered device; a wearable device; a gaming device; a mobile telephone; a laptop, notebook or tablet computer.

- Aspects also relate to a method of operating an audio amplifier circuit comprising:
- 25 detecting a characteristic of a load electrically coupled, in use, to an output of the audio amplifying circuit;
- selecting one of a plurality of stored distortion settings based on the detected characteristic of the load; and
- applying a first transfer function to a signal received at an input to the audio
- 30 amplifier circuit, wherein the first transfer function is based on the selected distortion setting and for at least one of the stored distortion settings the corresponding first transfer function comprises a non-linear distortion function; and
- amplifying the signal to which the first transfer function has been applied and outputting the amplified signal to said output of the audio amplifying circuit.

The method may be implemented in any of the variants described above.

Aspects also relate to a method of determining a pre-distortion for compensating for
5 inherent distortion in an amplifier module, the method comprising:
 connecting the amplifier module to a defined impedance;
 supplying a predetermined test signal to an input of the amplifier module and
 monitoring an output signal from the amplifier module;
 analysing the output signal to determine the amplitude and polarity of the
10 harmonic coefficients for a plurality of the first harmonic components;
 transforming the harmonic coefficients into distortion coefficients of a polynomial
 function corresponding to a distortion transfer function of the amplifier module; and
 inverting the distortion coefficients to provide pre-distortion coefficients for a pre-
distortion for compensating for said distortion transfer function of the amplifier module.

15

In a further aspect there is provided an audio amplifier circuitry comprising:
 an amplifier module for receiving an input digital signal and producing an
 amplified analogue output signal; and
 a pre-distortion module for applying a first transfer function to the digital input
20 signal upstream of the amplifier module;
 wherein the first transfer function is variable based on an indication of the
impedance of a load coupled, in use, to an output of the amplifier module and for at
least some load impedances the first transfer function comprises a non-linear
distortion.

25

In a further aspect there is provided an audio amplifier circuitry for receiving an input
signal and outputting an output signal to an output, comprising:
 a storage module for storing a plurality of distortion settings;
 a controller for selecting one of the plurality of distortion settings based on an indication
30 of a characteristic of the load coupled, in use, to the output; and
 a pre-distort module applying a distortion function to the input signal based on the
selected distortion setting.

Embodiments will now be described by way of example only with reference to the accompanying drawings, of which:

Figure 1 illustrates an amplifier circuit according to an embodiment of the invention;

Figure 2 illustrates an embodiment of the invention in more detail;

Figure 3 illustrates one example of a pre-distortion module;

Figure 4 illustrates one example of a testing arrangement; and

Figure 5 illustrates the voice coil impedance and acoustic output power response of a typical low impedance .

Embodiments of the invention provide amplifier circuitry and methods of amplification that exhibit good distortion performance, across a range of different possible load impedances, but which do not require relatively large active devices or relatively high bias currents. In embodiments of the invention, for at least some loads, a first transfer function which implements a controlled distortion is applied to an input signal to compensate for the expected distortion when driving a particular load.

Different types of load may present different impedances, for example as illustrated in the following table.

Example Load Type	Example Load Device	Example Impedance Band
1	Earbuds	$Z < 100\Omega$
2	Headphones	$Z \geq 100 < 1000\Omega$
3	Lineout	$Z \geq 1000\Omega$

Table 1

The controlled distortion implemented by the first transfer function is thus a pre-distortion of the input signal and is tailored to the particular type of load which is being

driven. Embodiments therefore detect a characteristic of the load being driven and control at least one distortion setting of the first transfer function which is applied based on the detected characteristic of the load. Different distortion settings leads to different transfer functions, at least one of which is a non-linear distortion function.

5

Figure 1 illustrates generally an amplifier circuit 100 according to an embodiment. The amplifier circuit comprises an input 101 for receiving an input signal S_{IN} , which may typically be a digital audio signal. The amplifier circuit also has an output 102 for outputting the output audio signal S_{OUT} which will generally be an analogue audio
10 signal. Located in the signal path between the input 101 and the output 102 is an amplifier module 103 for amplifying the signal it receives. The amplifier module will comprise at least one amplifier stage and, depending on the type of amplifier, may typically include a DAC (digital-to-analogue converter) to convert the received digital signal into an analogue signal prior to amplification. In the embodiment illustrated in
15 figure 1 the output of the amplifier module 103 is the output signal S_{OUT} which is used to drive a load 104 which, in use, is electrically connected to the output 102 of the amplifier circuit. The load 104 may be a load which is external to the host device, i.e. the device in which the amplifier circuit is embodied, and which is removably connected via a suitable removable connector, e.g. a jack plug-and-socket or plug-and-receptacle
20 type arrangement. A load 104 that may be connected may, for instance, be a loudspeaker of a headset connected to the host device or a loudspeaker of a connected docking station.

The embodiment of figure 1 also has a pre-distortion module 105 configured to apply a
25 first transfer function to a signal in the signal path, i.e. between the input 101 and output 102. Conveniently the first transfer function is applied in the digital domain and may thus be applied in the signal path upstream of the amplifier module 103. The first transfer function may, for a particular load, apply a controlled distortion. As illustrated in figure 1 the controlled distortion may be applied to the input signal S_{IN} to generate a
30 pre-distorted version of the input signal S_{PD} which is passed to the amplifier module 103. The first transfer function may, for at least some loads, be a non-linear distortion and is based on at least one distortion setting, for example a set of one or more distortion coefficients $\{p_i\}$ for a polynomial function. The controlled distortion applied by the pre-distortion module 105 is chosen so as to compensate for at least some of the

downstream distortion that will be experienced when driving the load 104. The controlled distortion applied thus compensates or corrects for the inherent distortion of the downstream signal that will be experienced due to the non-linearity of the amplifier components. The controlled distortion distorts the original input signal S_{IN} in such a way that the subsequent inherent distortion experienced actually reduces the total distortion in the final output signal S_{OUT} . In other words the audio signal applied to the load will exhibit lower distortion than otherwise would be the case without the controlled distortion applied by the pre-distortion module 105.

However, the extent of the inherent distortion of the audio signal during amplification when driving a load may vary depending on the type of load connected. For example, for a given amplifier design, it may be worse when driving lower impedance loads. Thus any controlled distortion implemented by the first transfer function which is chosen so as to substantially cancel amplifier distortion with one type of load applied, i.e. when driving a load of a first impedance, may not correctly cancel the distortion when a different type of load is applied, i.e. when driving a load of a second, different, impedance. For applications where the load is detachably connected, for example in the case of a headset connection to a mobile phone or suchlike, the use of the same audio amplifier to drive different types of loads of different impedance, with resulting different distortions, is possible and quite likely.

Thus in order to correctly distort, i.e. pre-distort the input signal, so as to improve the distortion characteristic at the load, the first transfer function, and hence the controlled distortion, applied by the pre-distortion module 105 depends on at least one characteristic of the load, i.e. the applied pre-distortion is load dependent. Thus a detection module 106 may be configured to detect the relevant characteristic of the connected load, which may, for instance, be the impedance of the load Z_L or a characteristic indicative of impedance. The detection module may also detect the presence of the load.

In some embodiments the detection module 106 may be coupled to the output 102 of the amplifier circuitry and may be configured to determine the characteristic of the load through monitoring and/or testing the electrical properties at the output 102 when a load is connected. However in some embodiments the detection module 106 may

simply receive an indication of the relevant characteristic, for instance from the load 104 itself via some suitable communication pathway or from some other part of the host device that determines the characteristic of the load 104. In other words, the load 104 may comprise pre-programmed information about its characteristics that may be
5 read by the host device via the plug and socket interface 102.

As mentioned, the at least one distortion setting is based on the detected characteristic of the load 104. Thus a distortion setting controller may determine through monitoring and/or testing or receive an indication of the characteristic of the load and subsequently
10 determine an appropriate distortion setting. In some embodiments the distortion setting, which may comprise a set of distortion coefficients $\{p_i\}$ as will be explained in more detail later, may be based on a plurality of stored settings based on the detected characteristic. In other words there may be a plurality of stored distortions settings that have been characterised for different load characteristics. In use the characteristic of
15 the load may be detected and used to generate an appropriate distortion setting. In some embodiments this may comprise selecting the most appropriate stored distortion setting. Thus the distortion setting controller 107 may provide an appropriate set of pre-distortion coefficients $\{p_i\}$ to the pre-distortion module 105 based on the detected characteristic of the load.

20 The pre-distortion module 105 may apply the controlled distortion according to a polynomial function and the at least one distortion setting may therefore comprise a set of coefficients for polynomial distortion. In other words the first transfer function of the pre-distortion module may be a polynomial function.

25 Referring to figure 1 consider that the input value S_{IN} has an instantaneous value of x . The distortion inherent in the amplifier driver circuitry, e.g. the distortion introduced by amplifier module 103, effectively results in a non-linearity of amplification. The inherent distortion in the amplifier signal path can be reasonably modelled as a polynomial
30 distortion, and can be substantially cancelled by applying a suitable polynomial pre-distortion as the first transfer function.

For example consider that the amplifier transfer function $k(x)$ including distortion can be approximated as:

$$k(x) = x + k_2 \cdot x^2 + k_3 \cdot x^3 \quad \text{Eqn. (1)}$$

If the controlled pre-distortion is controlled according to:

5

$$p(x) = x + p_2 \cdot x^2 + p_3 \cdot x^3 \quad \text{Eqn. (2)}$$

then the output derived by cascading $k(x)$ after $p(x)$ is:

10

$$\begin{aligned} k(p(x)) &= p(x) + k_2 \cdot (p(x))^2 + k_3 \cdot (p(x))^3 \\ &= x + p_2 \cdot x^2 + p_3 \cdot x^3 + k_2 \cdot (x + p_2 \cdot x^2 + p_3 \cdot x^3)^2 + k_3 \cdot (x + p_2 \cdot x^2 + p_3 \cdot x^3)^3 \end{aligned} \quad \text{Eqn. (3)}$$

If p_2 and p_3 are chosen such that $p_2 = -k_2$ and $p_3 = 2 \cdot k_2^2 - k_3$, then:

15

$$\begin{aligned} k(p(x)) &= x - k_2 \cdot x^2 + (2 \cdot k_2^2 - k_3) \cdot x^3 + k_2 \cdot (x - k_2 \cdot x^2 + (2 \cdot k_2^2 - k_3) \cdot x^3)^2 \\ &\quad + k_3 \cdot (x - k_2 \cdot x^2 + (2 \cdot k_2^2 - k_3) \cdot x^3)^3 \\ &= x + x^2(-k_2 + k_2) + x^3(2 \cdot k_2^2 - k_3 + 2 \cdot k_2^2 + k_3) + O(x^4) \\ &= x + O(x^4) \end{aligned}$$

20

Eqn. (4)

where $O(x^4)$ indicates all terms of power x^4 or higher.

Thus it can be seen that all the second and third order effects of the distortion are
25 cancelled and only some small higher order terms remain.

In general this method can be expanded for higher order cancellation, i.e. the distortion may be modelled by:

30

$$k(x) = k_1 \cdot x + k_2 \cdot x^2 + k_3 \cdot x^3 + \dots + k_N \cdot x^N \quad \text{Eqn. (5)}$$

and the controlled pre-distortion, i.e. the first transfer function, may be applied according to:

$$p(x) = p_1.x + p_2.x^2 + p_3.x^3 + \dots + p_N.x^N \quad \text{Eqn. (6)}$$

Values of $p_1, p_2, \dots, p_N$ are summarised below for N up to 8.

p_1	$1/k_1$
p_2	$-(k_2.p_1^2)/k_1$
p_3	$-(k_3.p_1^3 + 2.k_2.p_2.p_1)/k_1$
p_4	$-(k_2.(p_2^2 + 2.p_1.p_3) + k_4.p_1^4 + 3.k_3.p_1^2.p_2)/k_1$
p_5	$-(k_5.p_1^5 + k_3.(p_1.(p_2^2 + 2.p_1.p_3) + 2.p_1.p_2^2 + p_1^2.p_3) + k_2.(2.p_1.p_4 + 2.p_2.p_3) + 4.k_4.p_1^3.p_2)/k_1$
p_6	$-(k_3.(p_2.(p_2^2 + 2.p_1.p_3) + p_1^2.p_4 + p_1.(2.p_1.p_4 + 2.p_2.p_3) + 2.p_1.p_2.p_3) + k_4.(p_1^3.p_3 + p_1.(p_1.(p_2^2 + 2.p_1.p_3) + 2.p_1.p_2^2 + p_1^2.p_3) + 3.p_1^2.p_2^2) + k_2.(p_3^2 + 2.p_1.p_5 + 2.p_2.p_4) + k_6.p_1^6 + 5.k_5.p_1^4.p_2)/k_1$
p_7	$-(k_7.p_1^7 + 6.k_6.p_1^5.p_2 + 5.k_5.p_1^4.p_3 + 10.k_5.p_1^3.p_2^2 + 4.k_4.p_4.p_1^3 + 12.k_4.p_1^2.p_2.p_3 + 3.k_3.p_5.p_1^2 + 4.k_4.p_1.p_2^3 + 6.k_3.p_4.p_1.p_2 + 3.k_3.p_1.p_3^2 + 2.k_2.p_6.p_1 + 3.k_3.p_2^2.p_3 + 2.k_2.p_5.p_2 + 2.k_2.p_4.p_3)/k_1$
p_8	$-(k_8.p_1^8 + 7.k_7.p_1^6.p_2 + 6.k_6.p_1^5.p_3 + 15.k_6.p_1^4.p_2^2 + 5.k_5.p_1^4.p_4 + 20.k_5.p_1^3.p_2.p_3 + 4.k_4.p_5.p_1^3 + 10.k_5.p_1^2.p_2^3 + 12.k_4.p_1^2.p_2.p_4 + 6.k_4.p_1^2.p_3^2 + 3.k_3.p_6.p_1^2 + 12.k_4.p_1.p_2^2.p_3 + 6.k_3.p_5.p_1.p_2 + 6.k_3.p_1.p_3.p_4 + 2.k_2.p_7.p_1 + k_4.p_2^4 + 3.k_3.p_2^2.p_4 + 3.k_3.p_2.p_3^2 + 2.k_3.p_6.p_2 + 2.k_3.p_5.p_3 + k_3.p_4^2)/k_1$

Table 2

5

In general k_1 will equal one, although other scaling factors may be used as appropriate and understood by those skilled in the art. Embodiments of the invention may therefore have a set of stored pre-distortion coefficients $\{p_i\}$, for each of a plurality of different inherent distortion characteristics $k(x)$. Detecting a characteristic of the load effectively identifies the inherent distortion characteristic $k(x)$ that will be expected in use and thus allows selection of the appropriate stored pre-distortion coefficients for reducing or cancelling that inherent distortion. The distortion setting controller 107 may therefore be seen as a distortion coefficient generator or selector module.

15

Figure 2 illustrates an embodiment of the invention in more detail than figure 1, where similar components are identified using the same reference numerals. Figure 2 again

illustrates that an amplifier module 103 may be located in a signal path between an input 101 and an output 102. The amplifier module may comprise a DAC 201 and an amplifier 202.

- 5 In use a load 104, such as a loudspeaker may be electrically connected to the output 102, possibly via a plug-and-socket type connection 203. The plug-and-socket arrangement may, for example be a conventional 3- or 4-pole 3.5mm or 2.5mm jack plug-and-socket and may have connections for driving stereo audio signals. The connection to the load 104 could however comprise any suitable connection for output
10 of analogue audio signals and may comprise a multi-pole connector such as a USB Type-C connector operating for example in Analog Audio Adapter Mode or similar.

The DAC 201 may be any suitable DAC and may for instance be a switched capacitor DAC or a current-steering DAC or a PWM (Pulse Width Modulation) DAC. The driver
15 amplifier 202 may, for example, be a Class AB or Class G amplifier with feedback such as would be well understood in the art. However embodiments may equally be implemented with other types of amplifier, for example Class D stages, either closed loop – with analogue feedback – or open-loop or all digital Class D. The DAC and driver amplifier functions may be combined into one circuit, for example an amplifier
20 powerful enough to drive specified loads with some switched-capacitor circuitry in feedback around it.

Each of the DAC 201 and the amplifier 202 may have non-linear properties that could lead to a distortion or voltage error in the output signal S_{OUT} . The level of the voltage
25 error will depend however on the impedance Z_L of the connected load 104. For low impedance loads such as low impedance loudspeakers, the distortion may be dominated by the non-linearity of the amplifier. For higher impedance loads, driving lower currents, the amplifier may be more linear, but the DAC may still contribute significant distortion, particularly as a higher signal voltage swing may be specified into
30 higher impedance loads such as line loads. The overall expected distortion characteristic $k(x)$ of the amplifier module is thus load dependent.

The detection module 106 in this example is an impedance detection module which may be coupled to the output 102 to determine the impedance of the connected load.

The detection module 106 may detect when a load is operably connected, for instance when a suitable connection to an external device is established, e.g. a jack plug is inserted into the socket or at power-up or reset of the audio amplifier circuit. The detection module may for instance detect that insertion of jack plug closes a normally
5 open switch 204 inside the jack socket, causing the input of a comparator 205 to go low. The comparator going low is signalled to detection module controller 206 indicating that a load has been connected.

It will be understood that there are various other ways in which connection of a load
10 may be detected and/or detection of the connection of a load could be performed elsewhere and signalled to an impedance detection module. Additionally the plug insert detection may involve extra steps or extra hardware: for instance in the case of USB Type-C, it may involve not only detection that a plug has been attached, but also measurement of various resistances in the attached accessory to determine that it is
15 operating in an analogue signal mode, rather than a digital signal transmission mode

Once it is signalled, by any means, that a load is connected, the detection module determines the impedance of the load connected to the output 102 via a relevant pole of the connector. In the example illustrated in figure 2 a known DC current I_{ZD} may be
20 applied to the relevant pole and the resultant voltage measured via an ADC 207. The output from the ADC 207 may be supplied to a selector 208 which determines which of a number of predefined impedance bands the presently connected load falls into. The selector 208 may for instance compare the output from the ADC 207 against one or more predefined thresholds to determine the relevant impedance band, i.e. the type of
25 load connected.

Standard nominal load impedance values for headsets include 8 ohms, 16 ohms, 32 ohms, 600 ohms and 10kohms. In some embodiments there may thus be five impedance bands each including or centred on one of these values. This may also
30 provide adequate performance for loads of intermediate values. In other embodiments there may be more or fewer impedance bands according to the distortion performance of the particular amplifier design and the amount of distortion cancellation required. At least some of the impedance bands may be logarithmically equally spaced, for example 8, 16 and 32 ohms.

In some embodiments there may be one or more impedance bands in which no pre-distortion is applied, equivalent to $p(x) = 1$. This may avoid needless manipulation of the signal, for example to save power or to avoid possible degradation of the signal. In
5 some embodiments there may be only two impedance bands, and pre-distortion is applied for impedances in a first band and no pre-distortion is applied for impedances in the second band. In other words for one of the stored distortion settings the corresponding first transfer function corresponds to substantially no distortion. There will however be at least one distortion setting for which the corresponding first transfer
10 function comprises a non-linear distortion function. In some embodiments there may be a plurality of different distortion settings that give rise different first transfer functions which correspond to different non-linear distortion functions characterised for a particular load characteristic.

15 For amplifier circuitry with stereo audio output drive circuitry, and thus two poles of a suitable connector driven by audio amplifiers, the impedance of each loudspeaker may be determined separately. However in some applications it may be sufficient to determine the impedance of a load connected to just one of the relevant poles, i.e. to determine the impedance of just one of the loudspeakers, with the assumption being
20 made that the other loudspeaker has a similar impedance value.

It will also be appreciated that there are a variety of ways in which the impedance of the loudspeaker may be detected. The applied current could be modulated in some way, e.g. ramped or stepped in a known way and the resultant voltage could monitored
25 using a multi-bit or one-bit, i.e. comparator, quantisation. The current could otherwise be modulated with time e.g. square wave or ultrasonic carrier amplitude. A test voltage may be applied (small and gradually to avoid audible artefacts) and the resulting current detected and digitised or compared. The test signal may be sourced from the amplifier module 103 or selected elements thereof such as high-side drive transistor.

30

In some embodiments the test signal may be applied in use. **Figure 5** illustrates, in a general way, the loudspeaker voice coil impedance and the electro-acoustic response versus frequency of a typical loudspeaker. As illustrated, the electrical input impedance of a loudspeaker, for example a small micro-speaker as used in ear-bud type headsets,

may be relatively constant over the audio band, whereas the electro-acoustic frequency response may drop off at low and high frequencies. Thus the impedance may be measured, either on initialisation or periodically (i.e. intermittently at regular or irregular intervals or when required) or continuously, by adding a pilot tone at some pilot tone
5 frequency f_{pilot} to the signal path. The pilot tone frequency may be below the acoustic low-frequency cut-off frequency. This pilot tone may provide an appropriate measurement of the coil impedance, but not produce any substantial audible effect. Similarly a pilot tone at a frequency above the audio band may be used, although at higher frequencies some allowance may need to be made for the inductance of the
10 voice coil. In more complex embodiments test signals of multiple frequencies across the audio band or possibly above or below the audio band may be used to characterise the attached load.

In some embodiments the impedance of the load may not be measured directly. For
15 instance the accessory device which is attached to the host device may be capable of indicating its identity via some signalling, which may be analogue or digital or some combination. For example a particular value of resistance may be attached between other poles of a suitable connector with the value of the resistance being chosen to signal the type of accessory and hence the general impedance band of the load. Some
20 accessory devices may provide a method of digital signalling, at least during an initialisation or discovery phase of operation, which may allow the nominal impedance to be communicated from the particular accessory attached to a host device. In such embodiments the detection module may instead be configured for suitable discovery of the type of connected accessory device and/or communication with the accessory
25 device.

It will be understood that detection of loudspeaker impedance may already be performed in some amplifier circuits for other reasons. For instance loudspeaker protection systems may determine an indication of the loudspeaker impedance as part
30 of ensuring that the loudspeaker is not thermally or mechanically over-loaded. The impedance detection module would therefore already be present in some amplifier circuits and thus does not represent any additional complexity. In any case the functions of the impedance detection module may therefore be shared with other control modules of the amplifier circuitry. For instance a determined value of load

impedance may be passed to a circuit controller 209 which controls at least one operating parameter of the amplifier circuitry, for instance the bias current and/or supply voltage supplied to the DAC 201 or amplifier 202.

- 5 The determined load characteristic, e.g. the relevant impedance band for the connected load, may be used by the distortion setting controller 107 to select one set $\{p_i\}_j$ of a plurality of possible stored sets $\{p_i\}_1$ to $\{p_i\}_N$ of coefficients. The sets of coefficients $\{p_i\}_1$ to $\{p_i\}_N$ may for instance be stored in a suitable storage module which may comprise a lookup table 210 or the like. The lookup table could for instance be
- 10 implemented as a bank of memory registers selected via a multiplexor although it will be appreciated that a number of other possibilities are available. The values of the coefficients could be stored in some suitable memory which may be form part of the amplifier circuitry or could be stored elsewhere in the host device.
- 15 The selected set of coefficients may then be latched into a register 211 or equivalent. The known current or other stimulus applied to the load by the detection module 106 may then be removed and the normal signal path enabled to allow playback of an audio signal. Enablement and disablement of the normal signal path is illustrated by a switch in series with the output driver amplifier 202, but other means may be employed
- 20 such as an output driver amplifier with an output stage which may be placed into a high-impedance state when the path is to be disabled. As mentioned above in some embodiments however the applied test signal may be applied via the output driver amplifier 202 or components thereof and thus the signal path would not be disabled.
- 25 In some embodiments the register may not be present as an actual separate component. For example the signal may be directly available from the look-up table, in which case the input to the look-up table may be latched in a register or equivalent instead.
- 30 In any case the selected set of coefficients are then used by the pre-distortion module to apply an appropriate polynomial first transfer function $p(x)$ to the input signal D_{IN} to provide the controlled distortion as described above to provide a pre-distorted digital signal D_{PD} .

In some embodiments the controlled distortion, i.e. the polynomial first transfer function $p(x)$, may be applied to the signal using dedicated hardware. For example figure 3 illustrates one example of a suitable pre-distortion module 105 for applying a controlled distortion which is polynomial function of the third order. The input signal, represented
5 by the term “ x ”, is split into three paths. In a first path the input signal x passes directly to summation block 301. In a second path the input signal x is multiplied by itself and then subsequently by the relevant coefficient p_2 before being input to the summation block 301. In the third path the x^2 term from the second path is multiplied by the input signal x again and then by the relevant coefficient p_3 . Equally however the pre-
10 distortion module 105 could be implemented by a suitable general-purpose digital signal processor (DSP) running appropriate code, which processor may already be performing other processing or pre-conditioning of the audio signal data stream. The appropriate code may be stored in a non-transitory fashion in memory in the host device.

15 In some embodiments the pre-distortion may be applied according to other transfer functions than a polynomial. For example the transfer function, or its deviation from a constant slope, may be defined in terms of a piecewise linear characteristic, characterised by coefficients denoting break points (abscissae and/or ordinates)
20 between which the transfer function is linearly interpolated. Other possibilities include various known spline methods of interpolating according to fixed parameters or coefficients.

The pre-distorted digital signal D_{PD} is then converted to an analogue signal V_{PD} by DAC
25 201 and amplified/buffered by driver amplifier 202 to drive the attached load 104. As discussed above, the transfer function from D_{PD} via the DAC 201 and driver amplifier 202 (and any other circuitry in this segment of the audio signal path) may be non-linear with signal according to a non-linear transfer function $k(x)$. The particular set of pre-
30 distortion polynomial coefficients $\{p_i\}_j$ are chosen on the basis of the load impedance and are designed to aim to substantially cancel, or reduce, at least the major components of distortion induced by the non-linearity of $k(x)$. In other words the pre-distortion components are chosen such that the overall transfer function $k(p(x))$ is constant, say unity, with signal amplitude.

The discussion above has discussed use of a scalar function, for example a real polynomial function, such as a scalar Taylor-type polynomial to compensate for the inherent distortion components. This will provide suitable compensation for a wide variety of cases. It does however assume that the inherent distortion components are
5 phase aligned with fundamental signal. In many cases this assumption will be valid and, as mentioned, such a polynomial function may provide adequate compensation. In at least some embodiments however the components of the amplifier module 103 may be such that at least some inherent distortion components may be out of phase with the fundamental by various amounts.

10

For instance, if the DAC 201 is a switched-capacitor DAC, distortion originating in the switches of the DAC is typically filtered by infinite-impulse-response (IIR) filtering using switched and un-switched feedback capacitors. This may result in a phase difference which may be sufficient in the audio band to cause imperfect cancellation. In other
15 cases, non-linearities in passive elements may cause distortion that becomes frequency-dependent in conjunction with other passive elements.

20

In some embodiments therefore at least some distortion coefficients may have an associated phase delay component (which may have a value zero for some coefficients of a given set of coefficients). For example the distortion coefficients could be stored as complex variables, rather than scalar variables, with the magnitude providing the amount of distortion, e.g. p_2 and the phase indicating the phase delay, e.g. Φ_2 . The pre-distortion module may thus be configured to apply the distortion component corresponding to each distortion coefficients so that the applied distortion component is
25 delayed with respect to the fundamental signal by the indicated phase shift.

30

Referring back to figure 3, a pre-distortion module 105 implemented by dedicated circuitry may therefore comprise delays 302 in the paths for deriving the distortion components. The delays 302 may provide a phase delay as a function of frequency and may, for example be provided by an all-pass filter arrangement as would be understood by one skilled in the art.

It should be noted that the operating point of the components of the amplifier module 103 and the various non-linearities may vary with temperature of the amplifier circuitry,

e.g. the chip temperature in an integrated circuit embodiment. The transfer function of the inherent distortion $k(x)$ may thus vary with temperature.

5 The controlled distortion of the first transfer function $p(x)$ applied by the pre-distortion module 105 may be defined according to the most likely operating temperature or mid-range temperature or some other chosen operating point. Such a controlled distortion may function best when the amplifier circuitry is near to the chosen operating point but may still offer improved performance at other operating temperatures compared to applying no controlled distortion. The distortion setting may be defined for each
10 impedance band as the distortion setting that gives the best overall performance across the expected temperature range.

In some embodiments however the at least one distortion setting, i.e. the pre-distortion coefficients, may be selected or generated based on the determined characteristic of
15 the load and also an indication of temperature of the amplifier circuitry. The temperature of the amplifier circuitry may be measured and the distortion coefficients $\{p_i\}$ adjusted, possibly according to some previous characterisation of the optimum coefficients versus temperature for the individual device or a representative sample of devices.

20 Referring back to figure 2 a circuit controller 209 may therefore determine an indication of temperature, for example receiving signals from an on-chip temperature sensor 212. The indication of temperature T may be provided to the distortion setting controller module 107 such that the set of coefficients $\{p_i\}$ provided to the pre-distortion module is
25 based on temperature as well as the detected impedance.

There are various ways that coefficients could be adjusted for temperature. For instance the lookup table may include previously determined coefficients for each impedance band at a range of different temperatures. As the temperature changes the
30 most appropriate set of coefficients could be selected based on the characteristic of the load and also the temperature. This would however involve changing the coefficients whilst the amplifier circuitry may be in use. To avoid any step changes in gain causing audible artefacts, the values of the coefficients could be interpolated between discrete entries in the lookup table and/or the coefficients could be varied in a gradual manner,

for instance by imposing coefficient ramp rate limitations to avoid artefacts when compensating for a varying chip temperature. Additionally or alternatively the coefficients could be processed to add various post-factors to vary the coefficients in a predetermined way with temperature.

5

The impedance of the loudspeaker may itself change in use, for example with temperature. In at least some cases the change in impedance of the loudspeaker in use may be over a relatively small range and thus the impedance may remain within the generally selected impedance band. Hence the distortion setting selected by an initial determination of load impedance will be adequate for any changes in impedance that occur in use. In some embodiments however the detection module 105 may be configured to continuously or periodically monitor the impedance of the load in use. If the impedance of the load changes sufficiently the distortion setting may be adjusted accordingly, i.e. the distortion coefficients may be altered in a similar fashion as discussed above in relation to changes in temperature.

As mentioned previously, detection of loudspeaker impedance may already be performed in some amplifier circuits for other reasons, for instance for loudspeaker protection systems. Such speaker protection systems may typically continuously monitor the impedance of the loudspeaker, for example by using an adaptive filter operating as a plant model, attempting to mimic the behaviour of the speaker, on the basis of an indication of the load voltage and load current. Referring back to figure 1 the detection module 105 may therefore be configured to continuously monitor the impedance of the connected load and in such a case the function of impedance monitoring may be shared with other functional modules, such as speaker protection system.

The amplifier circuit may in some instances be operable in a number of different operating modes and/or with configurable operating parameters, some of which may alter the inherent distortion characteristic $k(x)$ of the amplifier module 103. For example as described above a circuit controller 209 may adjust operating parameters of the amplifier circuitry, for instance the bias current and/or supply voltage supplied to the DAC 201 or amplifier 202, which may alter the inherent distortion characteristic $k(x)$. The distorting elements in the signal path may be configurable into different

operating modes. The operating mode or parameter may be selected based on the determined load impedance and/or may be selected according to other criteria, i.e. in response to other control signals.

- 5 For example the amplitude or signal envelope level and/or a volume control signal may influence some operating parameters or an operating mode. The supply voltage to the amplifier may be adjusted with envelope level in a class G type operating mode. Various operating parameters may be controlled in use and at least some changes in operating parameters may also impact the amplifier transfer function $k(x)$ and thus the
- 10 desired pre-distortion coefficients $\{p_i\}$. In some embodiments therefore a circuit controller 209 may provide an indication of one or more operating parameters, generally indicated as a mode signal M , to the distortion setting controller 107. As discussed previously the distortion setting, e.g. the set of pre-distortion coefficients $\{p_i\}$ may be selected or adjusted based on the detected load characteristic and also based
- 15 on the other operating parameters. Again the lookup table may have a plurality of entries for different operating parameters for each impedance band and/or the coefficients may be adjusted or modified in a predetermined manner based on the operating parameters.
- 20 The discussion above has focussed on selecting an appropriate set of coefficients to define the first transfer function with the first transfer function having the same form across the whole of the signal band of interest. For many applications this is sufficient. In many applications it may be possible to determine a general distortion characteristic that is appropriate across the whole or majority of the signal band of interest. However
- 25 in at least some instances the distortion characteristic of the amplifier, with a given load connected, may vary significantly across the signal band of interest.

- For example there is often a desire to reduce the quiescent power consumption, e.g. for an audio amplifier for driving audio signals to a connected accessory device, which
- 30 may result in a relatively low gain-bandwidth. In such a case the distortion experienced in use may have a different characteristic at different frequencies and may, for example, be significantly worse at higher frequencies as the open-loop gain is lower at higher frequencies and thus less effective at suppressing amplifier distortion sources. Additionally or alternatively some applications may include components for suppressing

electromagnetic interference (EMI), for example a ferrite bead in the path between the amplifier and the load. Such a component may have an impedance that varies with signal amplitude which results in distortion as the signal is attenuated by the signal-dependent ratio potential divider from amplifier output to load.

5

In such cases the distortion characteristic may vary relatively significantly over the signal band of interest. Thus if the first transfer function is applied to the signal with the same form (e.g. the same coefficients) across the whole signal band of interest there may still be significant distortion components in resulting signal at some frequencies.

10

In some embodiments therefore the first transfer function may have a form that varies with frequency, i.e. the effective transfer function for a signal band at one frequency may be different to that at a different frequency. For example the first transfer function may comprise a plurality of frequency band transfer functions, each frequency band transfer function being applied to a different frequency band of the signal of interest.

15

The overall distortion characteristic will still depend on the connected load, and possibly other factors such as temperature as described above, and thus an appropriate stored distortion setting may be selected as described above. In such an embodiment however the stored distortion setting defines the first transfer characteristic and how it varies with frequency. For example the stored distortion setting may comprise a set of coefficients as described above, with the set of coefficients comprising coefficients for defining a frequency band transfer function for a given frequency band. In some embodiments the set of coefficients may comprise a plurality of subsets of coefficients, each subset corresponding to the coefficients to be applied in a given frequency band.

20

25

Figure 6 illustrates this principle. The input signal x may be supplied to a band splitter 601 that splits the input signal into a plurality of different frequency bands. The band splitter may, for instance, comprise a plurality of filters 602, e.g. band pass filters. In some applications it may be sufficient to have just two frequency bands, effectively a high frequency band and a low frequency band. As noted above the gain-bandwidth effect may be greater for higher frequencies and the largest amplitude signals tend to be of low frequency. Thus dividing the signal band into just two frequency bands and addressing the distortion components in each band may give acceptable performance.

30

In some instances however it may be preferred to split the signal into more bands to allow the first transfer function to be tailored for each band. This may improve the performance at the expense of additional components and processing.

- 5 It will be understood however that there may be other reasons to split the signal into different frequencies bands to apply different processing in each band, for instance for applying compression, equalisation etc. In such a case the audio signal path may have a band splitter provided for other purposes, which may influence the frequency bands chosen. Thus band splitter 601 may be a component that also provides band splitting
- 10 for some other signal processing reason. In which case the signal may be split into more bands than would be needed for applying the pre-distortion, in which case two or more frequency bands that are separated for some other signal processing reason may be combined for the purposes of applying a frequency band function of the first transfer function or the same frequency band function may be applied to two or more of the
- 15 frequency bands.

- In the example illustrated in figure 6 the input signal x is split into at least three signal components x_1 to x_3 , each in its own frequency band. A relevant distortion setting, index j say, from a set of J possible distortion settings is selected based on at least the
- 20 characteristics of the load as discussed above. In this example each of the J distortion settings comprises a set of coefficients $\{p_{ik}\}_j$ which comprises subsets of coefficients for each of the k frequency bands and defining individual frequency band transfer functions, the first transfer function being the combination of the individual frequency band transfer functions. The relevant frequency band transfer functions are applied to
- 25 the relevant signal components and the pre-distorted frequency bands may be combined to provide a suitably pre-distorted signal D_{PD} . Each individual frequency band transfer function may be implemented as described above on the relevant signal component.

- 30 It should be noted that the stored distortion settings may vary from one another in only some of the frequency bands. For instance a first and second distortion setting may have the same coefficients for a given frequency band k but vary for another frequency band. It will also be appreciated that the coefficients for at least some of the frequency

bands may lead to no distortion in that band, i.e. p_{1k} is set to one and p_{2k} and higher order coefficients are set to zero.

It will be noted that various functions have been explained with reference to a circuit controller 209 illustrated in figure 2. This controller has been illustrated as a single element in figure 2 for clarity but in practice the described functionality may be provided by various different functional blocks. It will also be appreciated that at least some of the functionality of the circuit controller may be included as part of the distortion setting controller.

Note that as used herein the term 'module' shall be used to refer to a functional unit for performing the stated function. A module may be implemented at least partly by dedicated hardware components such as custom defined circuitry and/or at least partly be implemented by one or more software processors or appropriate code running on a suitable general purpose processor or the like. The appropriate code may be stored in memory in the host device. It should also be noted that any circuitry components forming part of one module may be shared with another module and/or the same processor may implement multiple modules and may already be performing other control or data processing, which include other processing of the audio signal, e.g. pre-conditioning of the audio signal data stream.

As described above in various embodiments an appropriate set of distortion coefficients may be generated or selected based on a characteristic of a load connected to the amplifier circuit and used to apply a first transfer function in the signal path, which for at least some loads results in a controlled distortion. The controlled distortion is tailored to at least partly compensate or correct for the inherent distortion of the amplifier circuitry. This improves the distortion characteristics of the resultant output signal, i.e. provides an output signal with lower distortion than otherwise would be the case. This means that relatively small and/or low power components may be used for the amplifier module and the circuit still provides acceptable quality.

As mentioned at least some relevant pre-distortion coefficients may be stored in a suitable store, e.g. a lookup table or the like. This may comprise a dedicated register

circuitry or may be some addressable space of memory circuitry also usable for other purposes, e.g. for storing other operational parameters of the host device.

The value of the coefficients which are stored may be generated in a variety of ways.

- 5 At least some distortion coefficients may be determined by simulating the inherent distortion characteristic, $k(x)$, for the particular amplifier module to be used. The relevant pre-distortion coefficients could then be determined based on the simulated distortion characteristic.
- 10 In practice however simulation using normal device models and parasitic extraction methods may, in some cases, not be adequately accurate to achieve desired low levels of distortion in the output signal. In at least some cases therefore the coefficients $\{p_i\}$ may thus be derived empirically as part of a testing or calibration process. For example the coefficients $\{p_i\}$ may be derived empirically by applying test signals to a
- 15 selection of loads.

Figure 4 illustrates one example of a test arrangement. An amplifier module 103 under test is connected to a defined impedance Z_{Li} 401. This known impedance may be centred in a desired band of impedance, which may be chosen with regard to the likely

20 types of loads that may be used in practice.

To start the test, the pre-distortion module 105 may be disabled or bypassed so that it doesn't apply any controlled distortion to the input signal D_{IN} . Alternatively the set of coefficients $\{p_i\}$ could be set to apply no distortion, e.g. set coefficient p_1 to have a

25 value equal to 1 and the higher order coefficients to zero.

A defined input test signal is then applied. The test signal may have a form designed to allow characterisation of the response of the amplifier module. The test signal may for instance be a sine wave at an amplitude level corresponding to the level at which it

30 is optimally desired to cancel any distortion, which may for instance be at, or close to, full scale. In some instances the test signal may have a defined frequency modulation or be a multi-tone test signal.

The output signal, i.e. the voltage, at the output of the amplifier module may then be input to a calibration module 402. The output voltage may be sampled by an ADC 403. The ADC 403 may be a high performance ADC to allow accurate capture of the inherent distortion components. In practice the calibration module may be part of an

5 ATE (Automated Test Equipment) machine that is used to calibrate an amplifier circuit, e.g. as part of a factory calibration. In some embodiments however the calibration module 402 may be provided as part of the host device, say as part of an on-chip calibration module for the amplifier circuit. In which case the ADC 403 may be an ADC that forms part of the detection module, say ADC 207. To reduce the requirements of

10 the ADC 403 the ideal or non-distorted output signal may be subtracted from the monitored signal before conversion to leave only the distortion components requiring to be digitised.

With no pre-distortion $p(x)$ applied, the output from the ADC 403 for a certain period of

15 the test signal is passed to a harmonic analyser 404 which may, for example, perform a FFT (Fast Fourier Transform) or Goertzel algorithm to measure the amplitude and polarity of the first few harmonics.

A processor 405 may be configured to transform the amplitude coefficients of the first

20 few (e.g. N) harmonics $\{a_i\}$ of the output signal with no pre-distortion into order N output path polynomial model coefficients $\{k_i\}$ and then invert the polynomial $\sum k_i x^i$ to obtain coefficients $\{p_i\}$ of an order N polynomial $p(x)$ that cancels the first N harmonics.

For instance, for a system designed to cancel the second and third harmonics, a signal

25 y may be defined as:

$$\begin{aligned}
 y &= k(A \sin \omega t) \\
 &= k_1(A \sin \omega t) + k_2(A \sin \omega t)^2 + k_3(A \sin \omega t)^3 \\
 &= \frac{k_2 A^2}{2} + \left(k_1 A - \frac{k_3 A^3}{4}\right) \sin \omega t + \frac{k_2 A^2}{2} \cos 2\omega t - \frac{k_3 A^3}{4} \sin 3\omega t
 \end{aligned}$$

30 Eqn. (7)

By comparing to the coefficients of respective bins in an FFT:

$$= a_0 + a_1 \sin \omega t + a_2 \cos 2\omega t - a_3 \sin 3\omega t = a_0 + a_1 \sin \omega t + a_2 \cos 2\omega t - a_3 \sin 3\omega t$$

Eqn. (8)

Then it can be determined that:

$$k_3 = 4 \frac{a_3}{A^3}$$

5

$$k_2 = 4 \frac{a_2}{A^2}$$

$$k_1 = \frac{(a_1 + a_3)}{A}$$

Eqn. (9)

10

One can then calculate $p_2 = -k_2$ and $p_3 = 2.k_2^2 - k_3$ as discussed with respect to Eqn. (4) above.

The process would then be repeated for each of a plurality of different characteristic impedances to generate a set of coefficients $\{p_i\}$ for each impedance band. These sets of coefficients could then be written into some suitable device memory.

If desired the test process could also be conducted at each of a plurality of controlled temperatures of the amplifier module 103 and/or with a plurality of different operating parameters to provide suitable coefficients for a plurality of temperatures and/or operating parameters.

The testing process could be applied to each part produced, i.e. each amplifier circuit, during a manufacturing test. Alternatively a representative sample of parts may be characterised, and suitable coefficients derived from statistical analysis. This would avoid the need for time-consuming, and hence costly, individual calibration. In either case the coefficients may be written to and stored in some programmable non-volatile storage, e.g. EEPROM or OTP. The coefficients may be stored in storage on the same integrated circuit as the amplifier circuitry or elsewhere in a host device.

30

The calibration process described with reference to figure 4 uses an electrical feedback signal from the amplifier module 103. The resulting distortion coefficients will thus compensate for any distortion components in the voltage waveform at the amplifier

module when connected to a load of the relevant impedance. In practice however there may however be other distortion downstream of the amplifier module 103, for instance arising from any downstream components, such as perhaps ESD protection components or series switches in the host device. It may therefore be preferred to
5 perform the calibration process and determine the coefficients by monitoring the signal at a downstream point in the host device or even with a typical accessory device connected and by monitoring the audio signal from a point adjacent to the loudspeaker.

No matter how the coefficients are derived, storing the plurality of sets of coefficients
10 allows the amplifier circuitry to be used with a variety of different types of load, e.g. different types of loudspeaker with different impedances, and to apply an appropriate controlled distortion for the circuit when driving that load. This is particularly useful for audio amplifiers that may be used to drive loads that may be removably connected and thus the amplifier circuit may encounter different loads, i.e. impedances, in use. The
15 embodiments described herein may also be useful however even if the amplifier circuitry is actually, in use, embodied in a host device with a non-removable load. The amplifier circuit may be manufactured and calibrated with respect to each of a specified set of ranges of load by one manufacturer, e.g. a chip manufacturer, and then supplied to a different manufacturer for assembly into a final device. For example a headset
20 manufacturer may manufacture at least some headsets with audio driver circuitry in the headset itself, for instance in a dongle. The headset manufacturer may wish to use the same type of amplifier circuit in a range of products with different types of loudspeaker and may not want to have to calibrate the amplifier circuitry or otherwise specify a particular distortion setting. Thus there may be benefit in the amplifier circuitry being
25 able to determine the type of load used. Alternatively the manufactured headset device may include a stored digital code which is provided to the audio driver circuitry to indicate what type of loudspeaker has been used for each particular batch of devices.

Also as mentioned above in some applications the amplifier circuit may respond to any
30 variation in impedance of a given loudspeaker in use.

It will be appreciated that the embodiments described above have been described with reference to a single-ended load with a return to ground. It will of course be appreciated that the output could be an H-bridge type arrangement. The principles of

operation would be the as described, except that any sensing of the load voltage may involve differential sensing and any load current measurement may be adapted according to which quadrant of operation is active.

5 The skilled person will thus recognise that some aspects of the above described apparatus and methods may be embodied as processor control code, for example on a non-volatile carrier medium such as a disk, CD- or DVD-ROM, programmed memory such as read only memory (Firmware), or on a data carrier such as an optical or electrical signal carrier. For many applications, embodiments of the present invention will be
10 implemented on a DSP (Digital Signal Processor), ASIC (Application Specific Integrated Circuit) or FPGA (Field Programmable Gate Array). Thus the code may comprise conventional program code or microcode or, for example, code for setting up controlling an ASIC or FPGA. The code may also comprise code for dynamically configuring re-configurable apparatus such as re-programmable logic gate arrays. Similarly, the code
15 may comprise code for a hardware description language such as Verilog TM or VHDL. AS the skilled person will appreciate, the code may be distributed between a plurality of coupled components in communications with one another. Where appropriate, the embodiments may also be implemented using code running on a field-(re)programmable analogue array or similar device in order to configure analogue hardware.

20

Some embodiments of the present invention may be arranged as part of an audio processing circuit, for instance an audio circuit which may be provided in a host device. A circuit according to an embodiment of the present invention may be implemented as an integrated circuit. One or more speakers may be connected to the integrated circuit
25 in use.

Embodiments of the present invention may be implemented in a host device, especially a portable and/or battery powered host device such as a mobile telephone, an audio player, a video player, a PDA, a mobile computing platform such as a laptop computer
30 or tablet and/or a games device, for example. Embodiments of the invention may also be implemented wholly or partially in accessories attachable to a host device, for example, in active speakers or the like.

It should be noted that the above-mentioned embodiments illustrate rather than limit the invention, and that those skilled in the art will be able to design many alternative embodiments without departing from the scope of the appended claims. The word “comprising” does not exclude the presence of elements or steps other than those listed
5 in the claim, “a” or “an” does not exclude a plurality, and a single feature or other unit may fulfil the functions of several units recited in the claims. Any reference numerals or labels in the claims shall not be construed so as to limit their scope. Terms such as amplify or gain include possible applying a scaling factor or less than unity to a signal.

CLAIMS

1. Audio amplifier circuitry comprising:
an amplifier module located in a signal path between an input and an output;
5 a detection module for detecting a characteristic of a load electrically coupled, in use, to the output;
a distortion setting controller for selecting one of a plurality of stored distortion settings based on the detected characteristic of the load; and
a pre-distortion module configured to apply a first transfer function to a signal in
10 the signal path prior to said amplifier module, wherein the first transfer function is based on the selected distortion setting;
wherein for at least one of the stored distortion settings the corresponding first transfer function comprises a non-linear distortion function.
- 15 2. Audio amplifier circuitry as claimed in claim 1 wherein the first transfer function is based on a polynomial function and each of the stored distortion settings comprises a set of one or more distortion coefficients for said polynomial function.
- 20 3. Audio amplifier circuitry as claimed in claim 2 wherein each distortion setting comprises a set of distortion coefficients characterised for a particular load characteristic.
4. Audio amplifier circuitry as claimed in claim 2 or claim 3 wherein at least some
25 distortion coefficients have an associated phase delay component and wherein the pre-distortion module is configured to apply a distortion component corresponding to each distortion coefficient at a respective delay indicated by the respective phase delay component.
- 30 5. Audio amplifier circuitry as claimed in any preceding claim wherein for one of the stored distortion settings the corresponding first transfer function corresponds to substantially no distortion.

6. Audio amplifier circuitry as claimed in any preceding claim wherein for a plurality of the stored distortion settings the corresponding first transfer functions comprises a non-linear distortion function, wherein different distortion settings correspond to different non-linear distortion functions.
- 5
7. Audio amplifier circuitry as claimed in any preceding claim further comprising a storage module for storing the plurality of distortion settings.
8. Audio amplifier circuitry as claimed in claim 7 wherein said storage module
- 10 comprises a look-up table.
9. Audio amplifier circuitry as claimed in any preceding claim wherein said characteristic of the load is impedance of the load.
- 15 10. Audio amplifier circuitry as claimed in claim 9 wherein the distortion setting controller is configured to compare the impedance of the load with one or more threshold values to determine an impedance band for the load and to select one of the stored distortion settings that is appropriate for said impedance band.
- 20 11. Audio amplifier circuitry as claimed in any preceding claim wherein the detection module is coupled to the output of the amplifier circuitry and is configured to determine the characteristic of the load through monitoring of the electrical properties at the output when a load is connected.
- 25 12. Audio amplifier circuitry as claimed in claim 11 wherein the detection module is configured to detect when a load is operably connected to the output and then to determine the characteristic of the load.
- 30 13. Audio amplifier circuitry as claimed in claim 12 wherein, in response to a detection that a load has been operably connected to the output, the amplifier circuitry is configured to apply a test stimulus to the output and the detection module is configured to monitoring of the electrical properties at the output in response to the test stimulus to determine the characteristic of the load.

14. Audio amplifier circuitry as claimed in any preceding claim wherein the detection module is configured to, in use, continuously or periodically monitor the characteristic of the load.
- 5 15. Audio amplifier circuitry as claimed in 14 wherein, when the detection module is monitoring the characteristic of the load, the amplifier circuitry is configured to add a pilot tone at a pilot tone frequency to the signal in the signal path and the detection module monitors the electrical properties at the output at said pilot tone frequency.
- 10 16. Audio amplifier circuitry as claimed in claim 15 wherein said pilot tone frequency is below a low frequency threshold corresponding to an expected low frequency cut off for the possible loads.
- 15 17. Audio amplifier circuitry as claimed in any preceding claim wherein the distortion setting controller is configured such that the selection of the one of the stored plurality of distortion settings is also based on an indication of at least one operating condition of the amplifier circuitry.
- 20 18. Audio amplifier circuitry as claimed in any of claims 1 to 16 wherein the distortion setting controller is configured to modify the selected distortion setting based on an indication of at least one operating condition of the amplifier circuitry.
- 25 19. Audio amplifier circuitry as claimed in claim 17 or claim 18 wherein the at least one operating condition of the amplifier circuitry comprises temperature of the amplifier circuitry.
- 30 20. Audio amplifier circuitry as claimed in any of claims 17 to 19 wherein the at least one operating condition of the amplifier circuitry comprises at least one of: a supply voltage; a bias current; an amplifier operating mode.
21. Audio amplifier circuitry as claimed in any preceding claim wherein the distortion setting controller is configured, such that, in the event that the selected distortion

setting is changed in use, to transition from the old distortion setting to the new distortion setting via at least one intermediate setting value.

22. Audio amplifier circuitry as claimed in any preceding claim wherein the first
5 transfer function has a form that varies with frequency within a signal band of interest.
23. Audio amplifier circuitry as claimed in any preceding claim wherein the first
10 transfer function comprises a plurality of frequency band transfer functions and the pre-distortion module is configured to apply each frequency band transfer function to a component of the signal in a respective frequency band.
24. Audio amplifier circuitry as claimed in claim 23 when dependent directly or
15 indirectly on claim 2 wherein said set of one or more distortion coefficients comprises a subset of distortion coefficients for each of said frequency band transfer functions.
25. Audio amplifier circuitry as claimed in claim 23 or claim 24 further comprising a
20 band splitter for splitting the signal in the signal path into said respective frequency bands.
26. Audio amplifier circuitry as claimed in any preceding claim wherein said pre-distortion module comprises a digital signal processor.
- 25 27. Audio amplifier circuitry as claimed in any preceding claim implemented as an integrated circuit.
28. An electronic device comprising audio amplifier circuitry as claimed in any
30 preceding claim.
29. An electronic device as claimed in claim 28 wherein the output of the audio amplifier circuitry is connected to a audio connector of the electronic device, the audio connector being configured for mating connection with a corresponding connector of a peripheral device.

30. An electronic device as claimed in claim 28 or claim 29 wherein the electronic device is at least one of: a communications device; a computing device; a portable device; a battery powered device; a wearable device; a gaming device;
5 a mobile telephone; a laptop, notebook or tablet computer.
31. Audio amplifier circuitry as described hereinbefore with reference to figures 1 to 5 of the accompanying drawings.
- 10 32. A method of operating an audio amplifier circuit comprising:
detecting a characteristic of a load electrically coupled, in use, to an output of the audio amplifying circuit;
selecting one of a plurality of stored distortion settings based on the detected characteristic of the load; and
15 applying a first transfer function to a signal received at an input to the audio amplifier circuit, wherein the first transfer function is based on the selected distortion setting and for at least one of the stored distortion settings the corresponding first transfer function comprises a non-linear distortion function; and
20 amplifying the signal to which the first transfer function has been applied and outputting the amplified signal to said output of the audio amplifying circuit.
33. A method of determining a pre-distortion for compensating for inherent distortion in an amplifier module, the method comprising:
25 connecting the amplifier module to a defined impedance;
supplying a predetermined test signal to an input of the amplifier module and monitoring an output signal from the amplifier module;
analysing the output signal to determine the amplitude and polarity of the harmonic coefficients for a plurality of the first harmonic components;
30 transforming the harmonic coefficients into distortion coefficients of a polynomial function corresponding to a distortion transfer function of the amplifier module; and

inverting the distortion coefficients to provide pre-distortion coefficients for a pre-distortion for compensating for said distortion transfer function of the amplifier module.

- 5 34. Audio amplifier circuitry comprising:
 an amplifier module for receiving an input digital signal and producing an
 amplified analogue output signal; and
 a pre-distortion module for applying a first transfer function to the digital input
 signal upstream of the amplifier module;
- 10 wherein the first transfer function is variable based on an indication of the
 impedance of a load coupled, in use, to an output of the amplifier module
 and for at least some load impedances the first transfer function comprises
 a non-linear distortion.
- 15 35. Audio amplifier circuitry for receiving an input signal and outputting an output
 signal to an output, comprising:
 a storage module for storing a plurality of distortion settings;
 a controller for selecting one of the plurality of distortion settings based on an
 indication of a characteristic of the load coupled, in use, to the output;
- 20 and
 a pre-distort module applying a distortion function to the input signal based on
 the selected distortion setting.

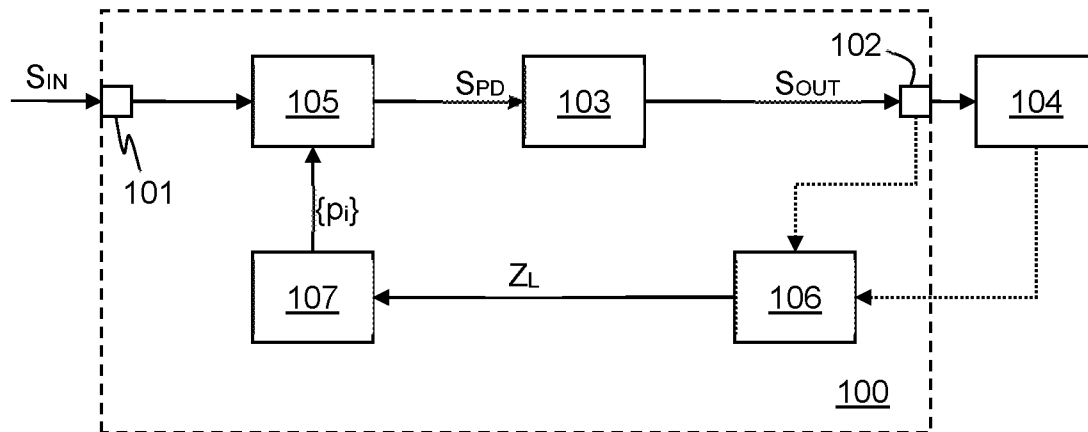


Figure 1

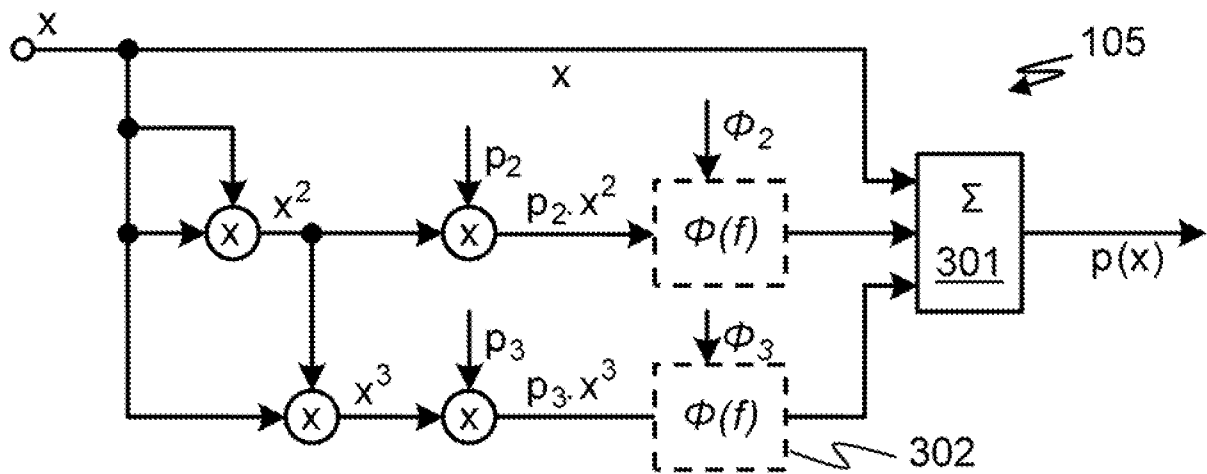


Figure 3

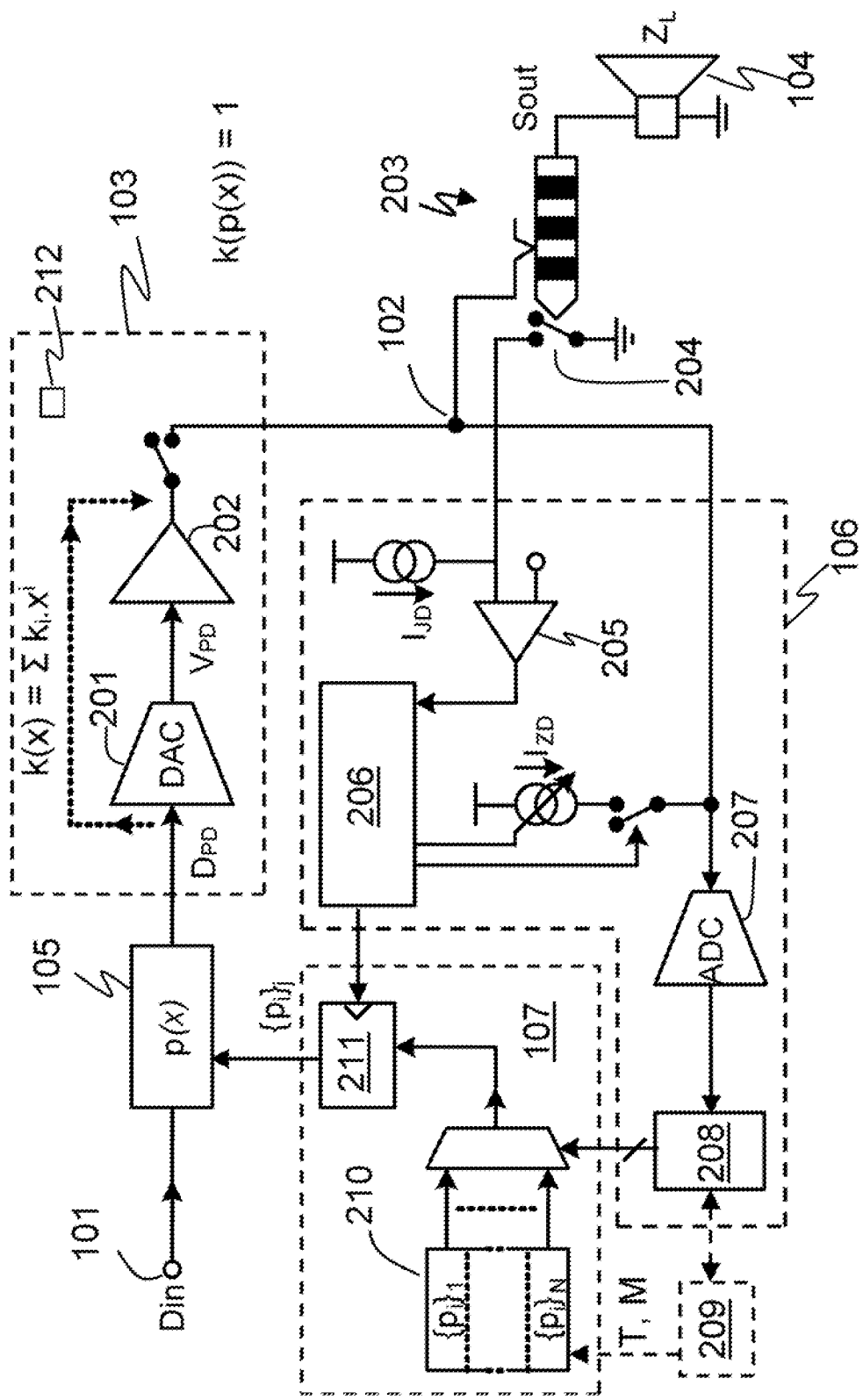


Figure 2

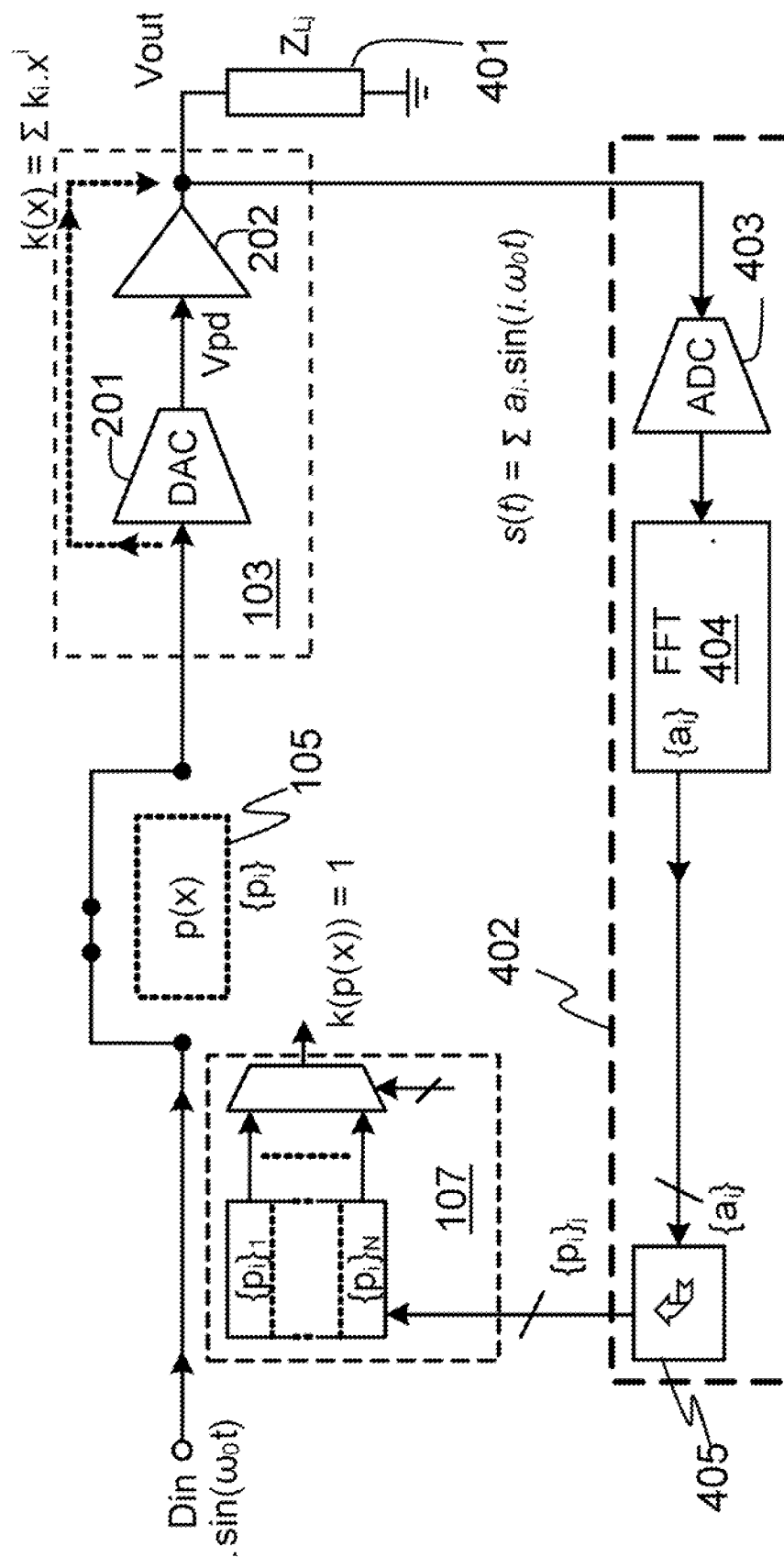


Figure 4

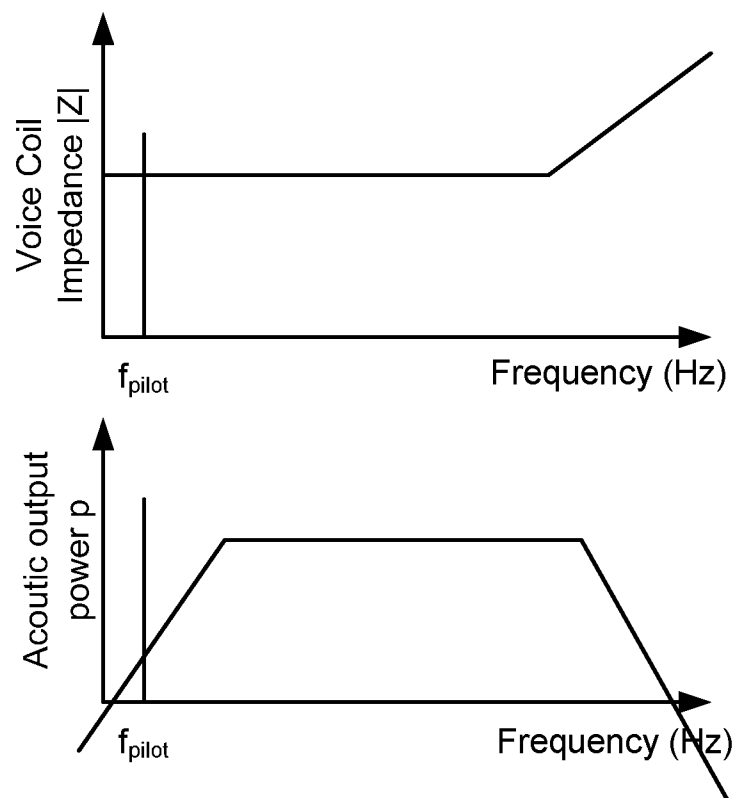


Figure 5

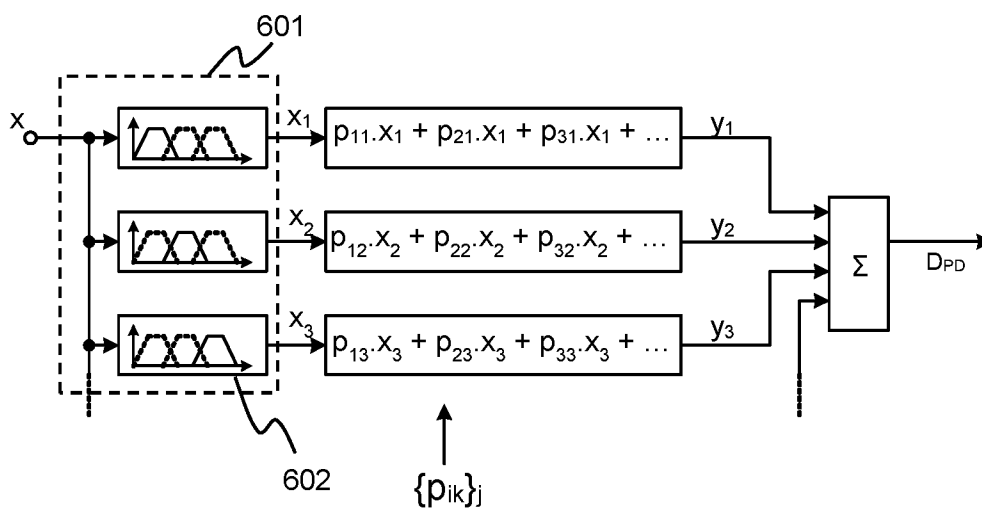


Figure 6

INTERNATIONAL SEARCH REPORT

International application No
PCT/GB2016/051272

A. CLASSIFICATION OF SUBJECT MATTER
INV. H03F1/32 H04R3/00
ADD.

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
H03F H04R

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EPO-Internal, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2006/104451 A1 (BROWNING RAYMOND [US] ET AL) 18 May 2006 (2006-05-18) paragraph [0151] - paragraph [0208]; figure 8	1-30, 32, 34, 35
X	----- EP 2 453 670 A1 (NXP BV [NL]) 16 May 2012 (2012-05-16) paragraph [0067] - paragraph [0074]; figure 1	1-6, 9-12, 21, 26-32, 34, 35
X	----- EP 1 523 218 A1 (SONY ERICSSON MOBILE COMM AB [SE]) 13 April 2005 (2005-04-13) paragraph [0032]; figure 1 ----- -/-	1-6, 9-12, 21, 23-32, 34, 35



Further documents are listed in the continuation of Box C.



See patent family annex.

* Special categories of cited documents :

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier application or patent but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

28 July 2016

Date of mailing of the international search report

05/10/2016

Name and mailing address of the ISA/

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Lorenzo, Carlos

INTERNATIONAL SEARCH REPORT

International application No

PCT/GB2016/051272

C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	EP 0 340 762 A2 (YAMAHA CORP [JP]) 8 November 1989 (1989-11-08) column 4, line 14 - column 5, line 33; figure 1 -----	1-6, 9-12,21, 26-32, 34,35

INTERNATIONAL SEARCH REPORT

International application No.
PCT/GB2016/051272

Box No. II Observations where certain claims were found unsearchable (Continuation of item 2 of first sheet)

This international search report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons:

1. ☐ Claims Nos.:
because they relate to subject matter not required to be searched by this Authority, namely:
2. ☐ Claims Nos.:
because they relate to parts of the international application that do not comply with the prescribed requirements to such an extent that no meaningful international search can be carried out, specifically:
3. ☐ Claims Nos.:
because they are dependent claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a).

Box No. III Observations where unity of invention is lacking (Continuation of item 3 of first sheet)

This International Searching Authority found multiple inventions in this international application, as follows:

see additional sheet

1. ☐ As all required additional search fees were timely paid by the applicant, this international search report covers all searchable claims.
2. ☐ As all searchable claims could be searched without effort justifying an additional fees, this Authority did not invite payment of additional fees.
3. ☐ As only some of the required additional search fees were timely paid by the applicant, this international search report covers only those claims for which fees were paid, specifically claims Nos.:
4. ☒ No required additional search fees were timely paid by the applicant. Consequently, this international search report is restricted to the invention first mentioned in the claims; it is covered by claims Nos.:

1-32, 34, 35

Remark on Protest

- ☐ The additional search fees were accompanied by the applicant's protest and, where applicable, the payment of a protest fee.
- ☐ The additional search fees were accompanied by the applicant's protest but the applicable protest fee was not paid within the time limit specified in the invitation.
- ☐ No protest accompanied the payment of additional search fees.

FURTHER INFORMATION CONTINUED FROM PCT/ISA/ 210

This International Searching Authority found multiple (groups of) inventions in this international application, as follows:

1. claims: 1-32, 34, 35

Audio amplifier circuitry comprising: an amplifier module located in a signal path between an input and an output; a detection module for detecting a characteristic of a load electrically coupled, in use, to the output; a distortion setting controller for selecting one of a plurality of stored distortion settings based on the detected characteristic of the load; and a pre-distortion module configured to apply a first transfer function to a signal in the signal path prior to said amplifier module, wherein the first transfer function is based on the selected distortion setting; wherein for at least one of the stored distortion settings the corresponding first transfer function comprises a non-linear distortion function

2. claim: 33

A method of determining a pre-distortion for compensating for inherent distortion in an amplifier module, the method comprising: connecting the amplifier module to a defined impedance; supplying a predetermined test signal to an input of the amplifier module and monitoring an output signal from the amplifier module; analysing the output signal to determine the amplitude and polarity of the harmonic coefficients for a plurality of the first harmonic components; transforming the harmonic coefficients into distortion coefficients of a polynomial function corresponding to a distortion transfer function of the amplifier module; and inverting the distortion coefficients to provide pre-distortion coefficients for a pre-distortion for compensating for said distortion transfer function of the amplifier module.

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No

PCT/GB2016/051272

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 2006104451 A1	18-05-2006	CN 1998262 A	11-07-2007
		US 2006104451 A1	18-05-2006
EP 2453670 A1	16-05-2012	CN 102469382 A	23-05-2012
		EP 2453669 A1	16-05-2012
		EP 2453670 A1	16-05-2012
		US 2012121098 A1	17-05-2012
EP 1523218 A1	13-04-2005	NONE	
EP 0340762 A2	08-11-1989	EP 0340762 A2	08-11-1989
		JP H0728473 B2	29-03-1995
		JP H01280998 A	13-11-1989
		US 4969195 A	06-11-1990