

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization

International Bureau



(10) International Publication Number

WO 2012/106213 A1

(43) International Publication Date
9 August 2012 (09.08.2012)

(51) International Patent Classification:
G01D 18/00 (2006.01)

(21) International Application Number:
PCT/US2012/022991

(22) International Filing Date:
27 January 2012 (27.01.2012)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
61/439,704 4 February 2011 (04.02.2011) US
13/114,935 24 May 2011 (24.05.2011) US

(71) Applicant (for all designated States except US): SEMI-CONDUCTOR COMPONENTS INDUSTRIES, LLC [US/US]; 5005 East McDowell Road, Phoenix, AZ 85008 (US).

(72) Inventor; and

(75) Inventor/Applicant (for US only): WARNECK, Timothy, J. [US/US]; 5005 East McDowell Road, Phoenix, AZ 85008 (US).

(74) Agents: LEE, Adrian, J. et al.; Workman Nydegger, 1000 Eagle Gate Tower, 60 East South Temple, Salt Lake City, UT 84111 (US).

(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LT, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

— with international search report (Art. 21(3))

(54) Title: ELECTRONIC DEVICE WITH FLEXIBLE DATA AND POWER INTERFACE

100

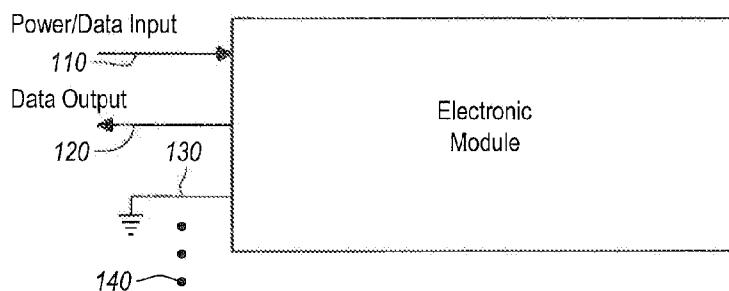


Fig. 1

(57) Abstract: Electronic modules with small and flexible interfaces are disclosed. One example electronic module includes a power supply terminal configured to receive power for the electronic module and circuitry configured to carry out various functions. The functions carried out by the electronic module circuitry include simultaneously receiving both of the following via the power supply terminal: a power signal for carrying out a mission mode operation of the electronic module, and a data signal.

WO 2012/106213 A1

**ELECTRONIC DEVICE WITH FLEXIBLE DATA
AND POWER INTERFACE**

CROSS-REFERENCE TO RELATED APPLICATIONS

5 This patent application claims the benefit of United States provisional patent application serial number 61/439,704, filed February 4, 2011, and United States patent application serial number 13/114,935, filed May 24, 2011 both of which are incorporated herein by reference in its entirety.

BACKGROUND

10 Electronic devices (also referred to herein as modules) typically include a relatively small number of terminals and each terminal is typically dedicated to a specific purpose. For example, certain electronic modules have only a terminal dedicated for providing power to the module, a terminal dedicated for providing ground, and a terminal dedicated for providing output data. No terminal is provided
15 for inputting data to such modules. A small number of dedicated terminals minimizes overall module size and simplifies the interface. Therefore, adding a new terminal for an additional purpose is undesirable, particularly if the additional purpose is not needed during mission mode operations. Including a terminal that is useful only during a configuration mode operation is undesirable for a variety of reasons. For
20 example, an additional terminal consumes valuable space and materials and increases interface complexity, which in turn increases the risk of confusion as to which terminal is which.

Programming an electronic sensor module with sensor calibration data is one example of a function for which an additional terminal might be needed during only a

limited portion of a module's lifetime. Electronic sensor modules and other similar electronic devices typically require some form of calibration of their output to compensate for incidental design variations that occur during manufacturing or other changes that occur during operational use. Electrical calibration is performed, for 5 example, by first manufacturing the sensor module, stimulating the completed sensor module with a known stimulus, comparing the module's output with an expected output corresponding to the known stimulus, and recording in a memory a table of calibration data that is thereafter referenced by the sensor module when outputting sensor readings. Consequently, the sensor module is able to compensate for any 10 variations detected with respect to the expected output corresponding to the known stimulus. After the electronic sensor module is calibrated and performing its sensing function, additional data input is rarely, if ever, required. Therefore, use of a dedicated terminal for entering calibration data is undesirable because the dedicated terminal would have little use relative to other terminals while consuming valuable 15 space and materials and increasing interface complexity.

A variety of techniques exist for loading calibration data into an electronic sensor module or other electronic module without using a dedicated terminal. However, certain existing techniques require integration of overdriving circuitry into the electronic module, which consumes a large area and large amounts of power. 20 Moreover, many existing techniques preclude or prevent any data from being output by the electronic module while calibration data is loaded.

SUMMARY

In general, embodiments of the proposed invention relate to an electronic module with a small and flexible interface and methods of configuring and operating such electronic modules using the small, flexible interface.

In a first embodiment, an electronic module includes a power supply terminal 5 configured to receive power for the electronic module and circuitry configured to carry out various functions. The functions carried out by the electronic module circuitry include simultaneously receiving both of the following via the power supply terminal: a power signal for carrying out a mission mode operation of the electronic module, and a data signal.

10 In a second example embodiment, a sensor module includes a power supply terminal configured to receive power for the sensor module and circuitry configured to carry out various functions. The functions carried out by the sensor module circuitry include receiving an uncalibrated sensor signal from a sensor and, via the power supply terminal, receiving both power and calibration data from a source 15 external to the sensor module. The sensor module circuitry functions also include calibrating the uncalibrated sensor signal based on the received calibration data.

In a third example embodiment, an electronic module includes a data input terminal, a data output terminal, and circuitry configured to carry out various functions. The functions carried out by the sensor module circuitry include receiving 20 a mode selection signal via the data input terminal and, in response to receiving the mode selection signal, begin receiving input data via the data input terminal. The sensor module circuitry functions also include performing an operation use the input data and outputting a result of the operation performed using the input data via the data output terminal.

In a fourth example embodiment, a method of configuring and operating a sensor module includes, via a multi-use terminal on the sensor module, receiving both power and calibration data from a source external to the sensor module. The method further includes receiving an uncalibrated sensor signal from a sensor, calibrating the 5 uncalibrated sensor signal based on the received calibration data, and outputting the calibrated sensor signal via a data output terminal on the sensor module.

Additional features of the invention will be set forth in the description which follows, and in part will be obvious from the description, or may be learned by the practice of the invention. The features of the invention may be realized and obtained 10 by means of the instruments and combinations particularly pointed out in the appended claims. These and other features of the present invention will become more fully apparent from the following description and appended claims, or may be learned by the practice of the invention as set forth hereinafter.

BRIEF DESCRIPTION OF THE DRAWINGS

15 To further clarify the above and other features of the present invention, a more particular description of the invention will be rendered by reference to specific embodiments thereof which are illustrated in the appended drawings. It is appreciated that these drawings depict only typical embodiments of the invention and are therefore not to be considered limiting of its scope. The invention will be described 20 and explained with additional specificity and detail through the use of the accompanying drawings in which:

Figure 1 depicts a general embodiment of an electronic module with a small and flexible interface;

Figure 2 depicts a method of using the small and flexible interface of the electronic module in Figure 1 to configure the electronic module;

Figure 3 depicts a second embodiment of an electronic module that is more detailed than the general embodiment of Figure 1;

5 Figure 4 depicts a method of using the small and flexible interface of the electronic module in Figure 3 to configure the electronic module;

Figures 5A and 5B depict graphs of signaling levels at input and output terminals of the electronic module of Figure 3 during different modes of operation;

10 Figure 6 depicts a third embodiment of an electronic module that is able to transition modes without use of a timer;

Figure 7 depicts graphs of signaling levels at input and output terminals of the electronic module of Figure 6 during different modes of operation;

Figure 8 depicts a fourth embodiment of an electronic module that conserves more power than the third embodiment of Figure 6 with use of a detect enable signal;

15 Figure 9 depicts a method of using the small and flexible interface of the electronic module in Figure 8 to configure the electronic module;

Figure 10 depicts graphs of signaling levels within and at input and output terminals of the electronic module of Figure 8 during different modes of operation; and

20 Figure 11 depicts graphs of signaling levels within and at input and output terminals of an alternative timerless embodiment of the electronic module of Figure 8 during different modes of operation.

DETAILED DESCRIPTION

Reference will now be made to the figures wherein like structures will be provided with like reference designations. It is understood that the figures are diagrammatic and schematic representations of presently preferred embodiments of the invention, and are not limiting of the present invention, nor are they necessarily 5 drawn to scale.

Embodiments of electronic modules and methods of using such modules described herein provide, among other things, efficient use of input and output terminals, thereby decreasing interface complexity and circuit footprint size. Moreover, certain embodiments provide full duplex bidirectional data 10 communications with the module during a configuration or programming mode.

Although the description provided herein is primarily directed to embodiments of electronic sensor modules that output calibrated sensor readings, application of the invention is not limited to use with or in electronic sensor modules only. The sensor module embodiments of the invention described herein are merely illustrative of 15 certain benefits of the invention. Those skilled in the art will appreciate that other types of electronic modules, including controllers, data loggers, or any other device for which a remote control interface is desired can also benefit from the principles of the invention.

Figure 1 is a schematic block diagram of an electronic module 100 in 20 accordance with a general embodiment of the present invention. The module 100, which may be a single integrated circuit, a circuit board assembly with discrete components, or a combination thereof, has an interface to external circuitry comprising three terminals: a power supply and data input (i.e., multi-use) terminal 110, an output terminal 120, and a ground or reference voltage terminal 130.

Although the module 100 is illustrated as having only terminals 110, 120, and 130, the vertical ellipses 140 represent that the module may include any positive integer (one or more) of terminals which may be configured as digital only, analog only, or switchable between digital and analog, depending on the module's design and/or 5 operation mode. In certain embodiments, however, the terminals do not include a dedicated data input terminal and/or the number of terminals is limited to three to simplify the interface and make a highly self-contained module. In this description and in the claims, a terminal means any input terminal, output terminal, or input/output terminal of the module, regardless of the physical form of that terminal.

10 Thus, a terminal can be a conductive pin that protrudes, a contact pad, an inductive terminal, a radio frequency terminal, or a terminal of any other type that is capable of receiving and/or transmitting control, data, or power as a digital or analog signal. The principles of the present invention are not limited in any way to the form of the terminals, and thus the term "terminal" should be broadly construed. Moreover, 15 where reference is made herein to voltage signals, equivalent signals, such as current signals, inductive signals, radio frequency signals, etc., can be used instead.

As with the other circuit drawings provided herein, Figure 1 is only a circuit block diagram that is used to introduce some basic components that may be used to practice embodiments of the present invention. Accordingly, the figure is not drawn to 20 scale, nor does the placing of the various components imply any sort of actual physical position or connectivity on a circuit.

In one embodiment of the present invention, the power supply and data input terminal 110 is a multi-use terminal that can be used differently in different modes of the module 100. In a normal mode, the multi-use terminal is used for a single

purpose—receiving power, such as a constant voltage level to power components within the module 100. In contrast, in a configuration mode, the multi-use terminal 110 is used for two purposes—receiving power, as in the normal mode, and receiving data. The data input over the multi-use terminal 110 may be executable instructions and/or reference data intended for use during the configuration mode and/or the normal mode. For example, the data received over the multi-use terminal 110 may be calibration data used to calibrate raw sensor data output from a sensor integrated with or communicatively coupled to the electronic module 100. An electronic sensor module may be configured to reference the calibration data when processing the raw sensor data and output appropriately calibrated sensor data via the output terminal 120 in both the normal mode and the configuration mode.

Control circuitry (not shown) in the module 100 controls the operation mode of the module 100. Figure 2 illustrates a flowchart of a method 200 that may be implemented by control circuitry in the module 100 to thereby control the operation modes of the module 100 in accordance with one embodiment of the present invention. The method 200 may be implemented when the control circuitry is first placed in a known state. For instance, the control circuitry may be configured to automatically enter a known reset state in response to receiving a reset signal from a power-on-reset component within the module 100 or a reset pin external to the module 100. Once the module is placed in the known state, an initialization routine may be carried out and the module 100 may then enter a normal mode (stage 210).

While operating in the normal mode, the sensor module 100 carries out its normal or mission mode function(s), e.g., reading, processing, and outputting sensor data. If, during normal mode operations, a first predetermined criterion or set of

criteria is satisfied (decision stage 220), the module 100 enters a configuration mode (stage 230). Otherwise, the module 100 remains operating in the normal mode (stage 210).

While operating in the configuration mode, the multi-use terminal 110
5 continues to supply a power signal and the module 100 listens for data to be received
over the power signal on the multi-use terminal 110. The received data may be, for
example, calibration data used to calibrate raw sensor data output from a sensor. In
certain embodiments, the module 100 may also continue to carry out its normal
function(s), e.g., reading, processing, and outputting sensor data, when operating in
10 the configuration mode. If, during configuration mode operations, a second
predetermined criterion or set of criteria is satisfied (decision stage 220), the module
100 returns to the normal mode (stage 210). Otherwise, the module remains operating
in the configuration mode (stage 230).

The first predetermined criteria that causes the module 100 to enter the
15 configuration mode may be a first predetermined signal level that is applied from a
source external to the module 100 for a first predetermined amount of time. Circuitry
in the module 100 may be configured to recognize the first predetermined signal level
applied to the multi-use terminal 110 (stage 220) to trigger entry into the
configuration mode (stage 230) after the first predetermined amount of time. To
20 avoid a false triggering of the configuration mode due to noise on the multi-use
terminal 110, the first predetermined amount of time may exceed the duration of noise
signals that are expected to reach or exceed the first predetermined signal level.

The second predetermined criteria that causes the module 100 to return to the
normal mode (decision stage 240) may include a second predetermined signal level

that is applied from the external source to the multi-use terminal 110. The second predetermined criteria may be the same as or similar to the first predetermined criteria. For example, the second predetermined criteria may include detection of the first predetermined signal level applied to the multi-use terminal 110 for the first 5 predetermined amount of time or for a second predetermined amount of time that differs from the first predetermined amount of time. Alternatively, the second predetermined criteria may include expiration of a countdown timer onboard the module 100 that begins counting down after the configuration mode is entered. If desired, the module 100 may also be configured to enter additional modes in response 10 to other predetermined signaling levels or bit sequences input over the multi-use terminal 110. Examples of additional modes include, but are not limited to, a testing mode, a calibration mode, a programming mode, a start up mode, a verification mode, etc.

Figure 3 illustrates an electronic sensor module 300 that represents a more 15 specific embodiment of the electronic module 100 of Figure 1. Figure 4 illustrates a flowchart of a method 400 for controlling the various operation modes of the module 300 of Figure 3 and Figures 5A and 5B illustrate graphs 500a and 500b, respectively, of signaling levels versus time at the input and output terminals of the module 300 during different modes of operation. Accordingly, the module 300 of Figure 3 will 20 now be described with frequent reference to the method 400 of Figure 4 and the graphs 500a and 500b of Figures 5A and 5B.

Referring to Figure 3, the module 300 includes terminals 310, 320, and 330 (similar to corresponding terminals 110, 120, and 130 of module 100 in Figure 1) and vertical ellipses 340 representing additional non-data-receiving terminals that are

optionally included. The module 300 also includes circuitry comprising a controller 340, detectors 344 and 346, a sensor 348, and a timer 350. The detectors 344 and 346 are configured to receive an input voltage from the multi-use terminal 310 and to output detection signals to the controller 340. As will be discussed in more detail 5 below, the signal detected by the detector 344 is a mode selection signal and the signal detected by the detector 346 is a configuration data carrying signal, which the controller 340 receives from the detector 346 and stores in a data storage unit 352, such as a nonvolatile memory unit, that is communicatively coupled to the controller 340.

10 As shown in Figure 3, the detectors 344 and 346 may include logic comparators that can compare a received input with a reference voltage V_{ref} . Comparators often require a level shifting of input voltage levels, which can be performed by a high impedance voltage divider network or any similar voltage level shifting circuitry. For example, a network of voltage dividing resistors 354 are 15 optionally included in the module 300 and the values of the resistors are selected to shift the expected voltage from the multi-use terminal 310 to levels that the comparators can accurately detect. One or both of the comparators 344 and 345 and the voltage dividing resistors 354 of the voltage dividing network collectively comprise circuitry referred to herein as data detection circuitry.

20 Referring now to Figure 4, after power up of the module 300 and a start-up or initialization routine, the module 300 enters a normal mode (stage 410). While operating in the normal mode, the sensor module 300 carries out its normal function(s), e.g., reading, processing, and outputting sensor data. At any time during normal operations, if the controller 340 receives an indication from the detector 344

that a voltage level V_{in} detected on the multi-use terminal 310 is greater than a first predetermined level V_{config} for a predetermined amount of time (decision stage 420), the controller 340 causes the module 100 to enter a configuration mode (stage 430). Otherwise, the module 300 continues to operate in the normal mode (stage 410).

5 Referring now to Figure 5A, the first predetermined level of voltage V_{config} and a first predetermined amount of time T_1 comprise a first set of predetermined criteria that governs whether the module 300 enters the configuration mode. For example, as indicated by the vertical dashed lines and the modes labeled along the horizontal time axis, the module 300 enters the configuration mode if a voltage on the multi-use 10 terminal 310 remains at or above the first predetermined voltage level V_{config} for the predetermined time period T_1 or longer. The first time period T_1 may be measured by the timer 350 and/or a time constant and/or hysteresis lag value, which may be designed into or inherent in the particular detector used. To avoid a false triggering of the configuration mode due to noise on the multi-use terminal 310, the time period T_1 15 may exceed the duration of noise signals that are expected to reach or exceed the first voltage level V_{config} .

With continued reference to the graph 500a of Figure 5A, while operating in the configuration mode, the sensor module 300 listens for data 510 to be received on the multi-use terminal 110. The received data may be calibration data used to 20 calibrate raw sensor data output from the sensor 348. The configuration data 510 is detected by the detector 346. The configuration data may be communicated by modulating the voltage level on the multi-use terminal 310 between the first voltage level V_{config} and a second voltage level V_{data} that is higher (as shown in the graph 500a) or lower than the first voltage level V_{config} .

As shown in the graph 500a, the first voltage level V_{config} and the second voltage level V_{data} are both higher than a power supply voltage V_{dd} , but they may instead be lower than the power supply voltage V_{dd} . One reason for setting the first voltage level V_{config} and the second voltage level V_{data} higher than the power supply 5 voltage V_{dd} is to enable the module 300 to remain powered while operating in the configuration mode. Alternatively, one or both of first voltage level V_{config} and the second voltage level V_{data} are lower than the power supply voltage V_{dd} but high enough to continue to provide sufficient power to the module 300. Consequently, while in the configuration mode the module 300 may be able to not only write 10 received data to the data storage unit 352 but also carry out normal mode operation(s), such as reading, processing, and outputting sensor data 520 via the output terminal 320. Thus, certain embodiments of the module 300 are capable of full duplex bidirectional communications in the configuration mode and the controller 340 can carry out the normal functions of the sensor module 300 to demonstrate whether the 15 module 300 is operating in a calibrated fashion. If the module 300 is not operating properly, a new set of calibration data can be transmitted to the module 300 as many times as necessary until the data from the output terminal 320 satisfies an operator or external automatic test device that the module 300 has been properly calibrated before entering the normal mode.

20 Referring again to the method 400 of Figure 4, if a second predetermined criterion or set of criteria is satisfied (decision stage 440), the module 100 returns to the normal mode (stage 410). Otherwise, the module remains operating in the configuration mode (stage 430). The second predetermined criteria may include a predetermined data sequence 530 detected by the detector 346, which the controller

340 receives and, in response thereto, causes the module 300 to return to the normal mode (stage 410). Alternatively, the second predetermined criteria may include a predetermined voltage level, such as the first voltage level V_{config} or another predetermined voltage level, e.g., between the power supply level V_{dd} and the first 5 voltage level V_{config} . As another alternative, the second predetermined criteria may include expiration of a second predetermined time period T_2 , as illustrated in the graph 500b of Figure 5B. The second predetermined time period T_2 may be measured by the timer 350. For example, the controller 340 can cause the timer 350 to begin counting down the predetermined time period T_2 upon entering the configuration 10 mode.

Referring again to Figure 3, additional components that may be present in the sensor module 300 include a voltage regulator 360 and an output buffer 362. The voltage regulator 360 may be present if there is a risk that the overvoltage levels input over the multi-use terminal 310 will overdrive and therefore damage certain 15 components of the module 300 that derive power from the V_{dd} voltage on the multi-use terminal 310. Otherwise, the voltage regulator may be omitted to conserve space. Moreover, various components, such as the sensor 348, the timer 350, and the data storage unit 352 may be external to the sensor module 300. For example, in certain 20 applications the sensor 348 is a relatively large unit and/or is located in a remote region with environmental conditions that are hostile to electronic circuitry and therefore cannot easily be included in an integrated circuit or circuit board assembly.

Figure 6 illustrates an electronic sensor module 600 in accordance with another embodiment of the present invention. The module 600 primarily differs from the module 300 in its omission of the timer 350 and the detector 346. Thus, the

module 600 is configured to enter the configuration mode without the use of a timer to measure a period of time that the multi-use terminal 310 is set at or above a predetermined voltage level, such as V_{config} . Instead, the module 600 is configured to enter the configuration mode when a unique configuration mode code is 5 asynchronously detected on the multi-use terminal 310. Similarly, another unique code can be designated to signal return to the normal mode after configuration is complete.

Figure 7 illustrates a graph 700, of signaling levels versus time at the input and output terminals of the module 600 during different modes of operation. As shown in 10 the graph 700, a unique code 710 is transmitted by an external source (not shown), such as a programming module, on multi-use terminal 310, which the controller 340 receives and recognizes as a signal to enter the configuration mode. Because a unique code is used to trigger the configuration mode instead of a predetermined voltage level, such as the first voltage level V_{config} , the calibration data may be received by 15 detecting modulation of the multi-use line between the power supply level V_{dd} and the second voltage level V_{data} . Moreover, the second voltage level V_{data} may be lower, e.g., at the level of the first voltage level V_{config} .

To implement asynchronous detection of a unique code, an asynchronous receiver 610, such as a universal asynchronous receiver/transmitter, is included in the 20 module 600 to receive the output of the detector 344 and transmit a serial data stream to the controller 340. The asynchronous receiver 610 detects a serial data stream input over the multi-use terminal 310. The controller 340 later processes and decodes the serial data stream to determine if it matches a unique identification code that is designated to trigger the configuration mode in the module 600. It will be appreciated

by those of skill in the art, that a protocol and/or handshaking that is established after the configuration mode is entered may be implemented in any of various ways depending on an overall system architecture or how an external data source and the module 600 are configured to handle communications.

5 Figure 8 illustrates an electronic sensor module 800 in accordance with another embodiment of the present invention. The module 800 includes all of the circuitry in the module 300 and also includes a set of switches 810 that are controlled by a detect enable signal 820 from the controller 340. The voltage divider network of resistors 354 consumes power even when not being used. Therefore, in the module 10 800, the timer 350 controls a window of time in which the controller 340 asserts the detect enable signal 820 and listens to the multi-use terminal 310 for configuration data to be received. Assertion of the detect enable signal 820 opens the switches 810 to disable the voltage divider network when not in use. Accordingly, power that would otherwise be dissipated by the voltage divider network is conserved.

15 Figure 9 illustrates a flowchart of a method 900 for controlling the various operation modes of the module 800 of Figure 8 and Figure 10 illustrates a graph 1000, of signaling levels versus time at the input and output terminals of the module 800 and of the detect enable signal 820 during different modes of operation. Accordingly, the module 800 of Figure 8 will now be described with frequent reference to the 20 method 900 of Figure 9 and the graph 1000 of Figure 10.

 After a power-up or a reset of the module 800, the module 800 enters an initialization mode in which a start-up or initialization routine is executed and the detect enable signal 820 is asserted (stage 910). During the initialization mode, if the controller 340 receives an indication from the detector 344 that a voltage level V_{in}

detected on the multi-use terminal 310 is greater than a first predetermined level V_{config} for a predetermined amount of time (decision stage 920), the controller 340 causes the module 800 to enter a configuration mode (stage 930). In addition to determining whether to enter the configuration mode, the controller 340 determines 5 whether a third predetermined time period (i.e., a configuration time period) T_3 is expired (stage 922). For example, the controller 340 may instruct the timer to begin counting down the configuration time period T_3 upon entering the initialization mode and the timer 350 may indicate to the controller when the configuration time period expires. If the configuration time period T_3 has not expired and the voltage level V_{in} 10 detected on the multi-use terminal 310 is not greater than the first predetermined level V_{config} , the module 800 continues to operate in the initialization mode (stage 910). If, however, the configuration time period T_3 does expire before the configuration mode is entered, the controller 340 causes the module 800 to enter the normal mode and de-assert the detect enable signal 620 (stage 950) without entering the configuration mode. 15 Implementing a limited configuration time period will save power that would otherwise be dissipated by the network of voltage dividing resistors 354.

With reference to the graph 1000 of Figure 10, while operating in the configuration mode, the sensor module 800 listens for data 510 to be received on the multi-use terminal 310. The data 510 is detected by the detector 346 and may be, for 20 example, calibration data used to calibrate the sensor 348.

Referring again to the method 900 of Figure 9, if, while operating in the configuration mode, a second predetermined criterion or set of criteria is satisfied (decision stage 940), the controller 340 causes the module 800 to enter the normal mode (stage 950). In the normal mode (stage 950), the controller 340 de-asserts the

detect enable signal 720 to conserve power and causes the sensor module 100 to carry out its normal function(s), e.g., reading, processing, and outputting sensor data. Otherwise, the module remains operating in the configuration mode (stage 930). The second predetermined criteria may include a predetermined data sequence 530 5 detected by the detector 346, which the controller 340 receives and, in response thereto, causes the module 800 to enter the normal mode (stage 950). Alternatively, the second predetermined criteria may include a predetermined voltage level or expiration of a fourth predetermined time period T_4 , as illustrated in the graph 1000 of Figure 10.

10 In an alternative embodiment of the module 800 of Figure 8, the timer 350 is omitted and mode selection is performed without the use of the time periods T_3 and T_4 . Thus, the module 800 can enter and exit the configuration mode at any time, including during normal operations. For example, as discussed above, a non-timer-based criteria, such as the predetermined data sequence 530, may trigger transition 15 from the configuration mode to the normal mode.

Moreover, to enable entry to the configuration mode, the detect enable signal 820 is pulsed or strobed at a periodic interval and a mode selection signal, such as the first voltage level V_{config} , or a unique code is detected on the multi-use terminal 310. By strobing the detect enable signal 820 the module 800 can enter the configuration 20 mode at any time, including during normal operations. Although strobing the detect enable signal 820 would still dissipate some power by periodically activating the voltage divider network of resistors 354, some power is also conserved when the detect enable signal 820 is de-asserted.

In an example implementation of the alternative embodiment of the module 800, the detect enable signal 820 may be asserted for 10ms of every 100ms period and the output of the detector 344 is fed to a counter (not shown) that is integrated with or communicatively coupled to the controller 340. If after the controller 340 recognizes 5 that a mode selection signal, such as the first voltage level V_{config} , is detected on the multi-use terminal 310 a predetermined number of times, the controller 340 causes the module 800 to exit whatever mode it happens to be in and enter the configuration mode to listen for data from the detector 346. The controller 340 is configured to reset the counter to zero if the mode selection signal is not detected when the detect 10 enable signal 820 is asserted to avoid inadvertently triggering the configuration mode when noise events coincide with assertion of the detect enable signal 820. This inexpensive implementation of mode transition simulates a low pass filter of the signal on the multi-use terminal 310. The analog of this digital simulation of low pass filtering is an RC filter, which would typically consume much more chip area.

15 Figure 11 depicts a graph 1100 of signaling levels versus time at input and output terminals and of the detect enable signal 820 during different modes of operation of the timerless embodiment of the module 800. As shown in the graph 1100, the detect enable signal 820 is strobed at regular intervals depicted by a series of pulses 1110 and 1120. At time T_5 an external source applies the first voltage level 20 V_{config} to initiate transition to the configuration mode. The controller 340 detects the first voltage level V_{config} at each of a series of consecutive pulses 1120 of the detect enable signal 820, which briefly enable detection by the detectors 344 and 346. If the first voltage level V_{config} is detected a sufficient number of consecutive times (e.g., about three times to about twenty times depending on expected noise levels) the

controller 340 causes the module 800 to enter the configuration mode. As shown in the graph 1100, the controller 340 asserts the detect enable signal 820 for the duration of the configuration mode to enable receipt of data by the detector 346.

By using a multi-use terminal to receive calibration data and power on a 5 sensor module, the relatively small interface of the sensor module is used effectively for multiple operations in multiple modes and calibration can be performed quickly. Moreover, preserving the data output terminal for the sole purpose of outputting data minimizes the amount of extra hardware needed on the module to receive calibration data, enables full duplex bidirectional communications during the configuration 10 mode, and increases communication speeds. Furthermore, the sensor modules described herein add only a few small, low-power consuming components to standard sensor modules to perform calibration functions. For example, the sensor modules described herein do not require integration of high power / low speed driving 15 circuitry, which is included in certain other implementations to output data over the power supply terminal due to the significant amount of capacitance typically implemented on the power supply terminal. Instead, high power driving circuitry may only be needed in a test or programming system that is external to sensor modules of the present invention.

The foregoing detailed description of various embodiments is provided by way 20 of example and not limitation. Accordingly, the present invention may be embodied in other specific forms without departing from its spirit or essential characteristics. For example, although the foregoing description is directed to sensor modules, any module or circuit that has a limited number of terminals (or for which a reduced numbers of terminals is desired to decrease interface complexity, for example) and a

need to receive data for configuration, testing, upgrading firmware, or any other purpose for which input data or instructions is needed or expected to be needed at startup only and/or at relatively infrequent intervals during operation can benefit from the principles of the invention. Examples of such alternative modules include, among 5 other things, a motor speed controller module, which may require an input at startup or during normal operation of a target speed to be maintained, an environmental parameter controller module, which may require an input at startup or during normal operation of a target environmental parameter to be maintained, or a data logger, which may require an input at startup or during normal operation of a desired 10 parameter to be logged, an enable/disable setting, or the like.

Moreover, one of ordinary skill in the art will appreciate that the notation of V_{dd} for the power supply voltage is exemplary and is not intended to limit implementation of the invention to one in which power is supplied to drain terminals of field effect transistors. Implementations that use bipolar junction transistors, for 15 example, are also contemplated.

The described embodiments are to be considered in all respects only as illustrative and not restrictive. The scope of the invention is, therefore, indicated by the appended claims rather than by the foregoing description. All changes which come within the meaning and range of equivalency of the claims are to be embraced 20 within their scope.

CLAIMS

What is claimed is:

1. An electronic module comprising:

a power supply terminal; and

5 circuitry configured to simultaneously receive both of the following via the power supply terminal:

a power signal for carrying out a mission mode operation of the electronic module; and

a data signal.

10 2. The electronic module of claim 1, wherein the electronic module does not include a dedicated data input terminal and the circuitry is configured to receive the data signal via the power supply terminal only during a configuration mode of the module.

3. The electronic module of claim 1, wherein data carried by the data signal received via the power supply terminal includes one or more of:

calibration data for a sensor in or coupled to the electronic module;

executable instructions for a processor in the electronic module;

a control parameter for a control operation carried out by the electronic module; and

20 a configuration setting for a data logging operation carried out by the electronic module.

4. A sensor module comprising:
 - a power supply terminal configured to receive power for the sensor module;
 - and
 - circuitry coupled to the power supply terminal and configured to:
 - 5 receive an uncalibrated sensor signal from a sensor;
 - via the power supply terminal, receive calibration data from a source external to the sensor module and receive power; and
 - calibrate the uncalibrated sensor signal based on the received calibration data.
- 10 5. The sensor module of claim 4, wherein the circuitry includes a comparator configured to facilitate receipt of the calibration data from the external source by comparing a signal on the power supply terminal to a reference level.
6. The sensor module of claim 4, further comprising the sensor.
7. The sensor module of claim 4, further comprising an output terminal, the 15 circuitry being further configured to output the calibrated sensor signal via the output terminal to verify calibration accuracy.
8. An electronic module comprising:
 - a power supply terminal;
 - data detection circuitry coupled to detect signal levels at the power supply terminal; and
 - 20 control circuitry coupled to the data detection circuitry, the control circuitry being configured to:
 - receive a first mode selection signal via the power supply terminal based on the signal levels detected by the data detection circuitry;

in response to receiving the first mode selection signal, begin receiving input data via the power supply terminal based on the signal levels detected by the data detection circuitry; and

perform an operation using the input data.

- 5 9. The electronic module of claim 8, further comprising:
 - a data output terminal coupled to the control circuitry,
wherein the control circuitry is further configured to output, via the data output terminal, a result of the operation performed using the input data.
10. The electronic module of claim 9, further comprising a ground terminal
coupled to the control circuitry.
11. The electronic module of claim 8, wherein the control circuitry is further configured to receive power via the power supply terminal while detecting the mode selection signal and while receiving the input data.
12. The electronic module of claim 8, wherein the control circuitry is further
15 configured to recognize a first predetermined signal level on the power supply terminal as the mode selection signal and to recognize modulation on the power supply terminal between the first predetermined signal level and a second predetermined signal level as the input data.
13. The electronic module of claim 12, wherein the first predetermined signal
20 level and the second predetermined signal level are both larger in magnitude than a signal level of the power received via the power supply terminal.
14. The electronic module of claim 8, wherein the operation performed using the input data includes calibrating a sensor reading from a sensor.

15. The electronic module of claim 8, wherein the control circuitry is configured to enable the data detection circuitry in preparation for receiving the first mode selection signal and to disable the data detection circuitry in response to a predetermined criterion being satisfied, the predetermined criterion including one or
5 more of:

expiration of a first predetermined period of time; and
detection, via the power supply terminal, of a second mode selection signal that differs from the first mode selection signal.

16. The electronic module of claim 15, wherein the second mode selection signal
10 is one of a predetermined data sequence and a predetermined signal level asserted for at least a second predetermined period of time.

17. The electronic module of claim 8, wherein the data detection circuitry includes:

15 a voltage divider network coupled to the power supply terminal at a first end and to ground at a second end;

a first comparator configured to detect the first mode selection signal by comparing a first voltage-divided signal received via the voltage divider network with a first predetermined signal level; and

20 a second comparator configured to detect the input data on the power supply terminal by comparing a second voltage-divided signal received via the voltage divider network with a second predetermined signal level.

18. The electronic module of claim 17, wherein the control circuitry is configured to:

periodically enable the data detection circuitry until the first mode selection signal is received;

enable the data detection circuitry during receipt of the input data; and
after the input data is received, periodically enable the data detection circuitry

5 to detect a subsequent occurrence of the first mode selection signal.

19. The electronic module of claim 8, wherein the data detection circuitry includes:

a voltage divider network coupled to the power supply terminal at a first end and to ground at a second end;

10 a comparator configured to compare a voltage-divided signal received via the voltage divider network with a predetermined signal level; and

an asynchronous receiver configured to asynchronously detect the first mode selection signal and the input data based on an output of the comparator.

20. A method of configuring and operating a sensor module, comprising:

15 via a multi-use terminal on the sensor module, receiving calibration data from a source external to the sensor module and receiving power;

receiving an uncalibrated sensor signal from a sensor;

calibrating the uncalibrated sensor signal based on the received calibration data; and

20 outputting the calibrated sensor signal via a data output terminal on the sensor module.

21. The method of claim 20, wherein the calibration data and the power are received simultaneously.

22. The method of claim 20, further comprising:

when a first predetermined criterion is satisfied, causing the sensor module to enter a configuration mode in which the sensor module listens for the calibration data on the multi-use terminal; and

when a second predetermined criterion is satisfied, causing the sensor module

5 to enter a normal mode in which the sensor module performs normal operations and does not listen for calibration data on the multi-use terminal.

23. The method of claim 22, wherein the first predetermined criterion includes detection of a first mode selection signal on the multi-use terminal, the first mode selection signal including one of a first predetermined data sequence and a first predetermined signal level asserted for at least a first predetermined period of time.

10

24. The method of claim 23, wherein the second predetermined criterion includes one of:

expiration of a second predetermined period of time; and

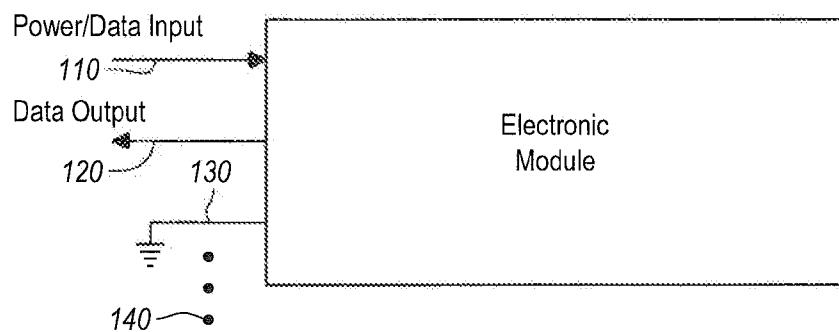
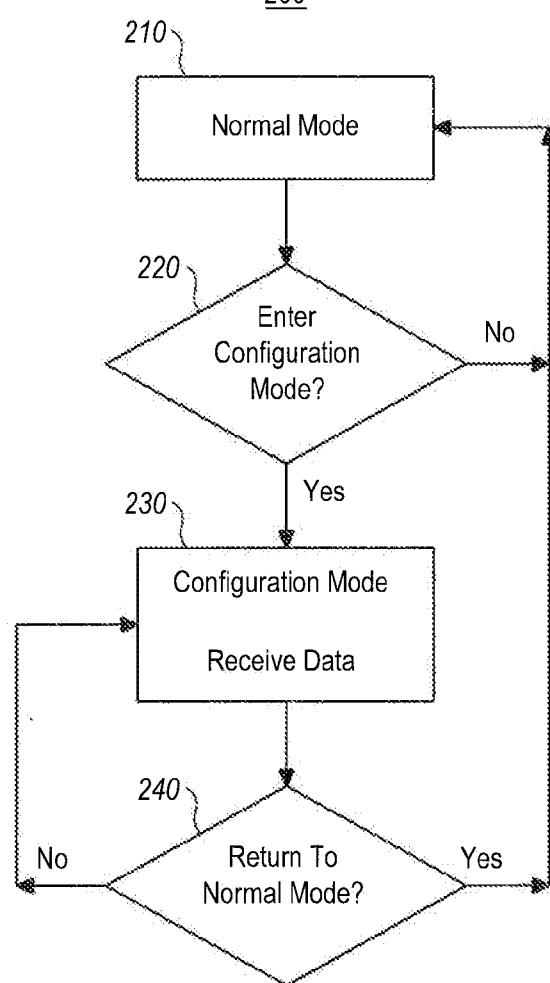
15

detection by the data detection circuitry of a second mode selection signal that differs from the first mode selection signal, the second mode selection signal including one of a second predetermined data sequence and a second predetermined signal level asserted for at least a third predetermined period of time.

25. The method of claim 24, wherein the first predetermined signal level is the same as the second predetermined signal level.

20 26. The method of claim 22, wherein the acts of receiving the uncalibrated sensor signal and outputting the calibrated sensor signal are performed in both the configuration mode and the normal mode.

1 / 10

100*Fig. 1*200*Fig. 2*

2 / 10

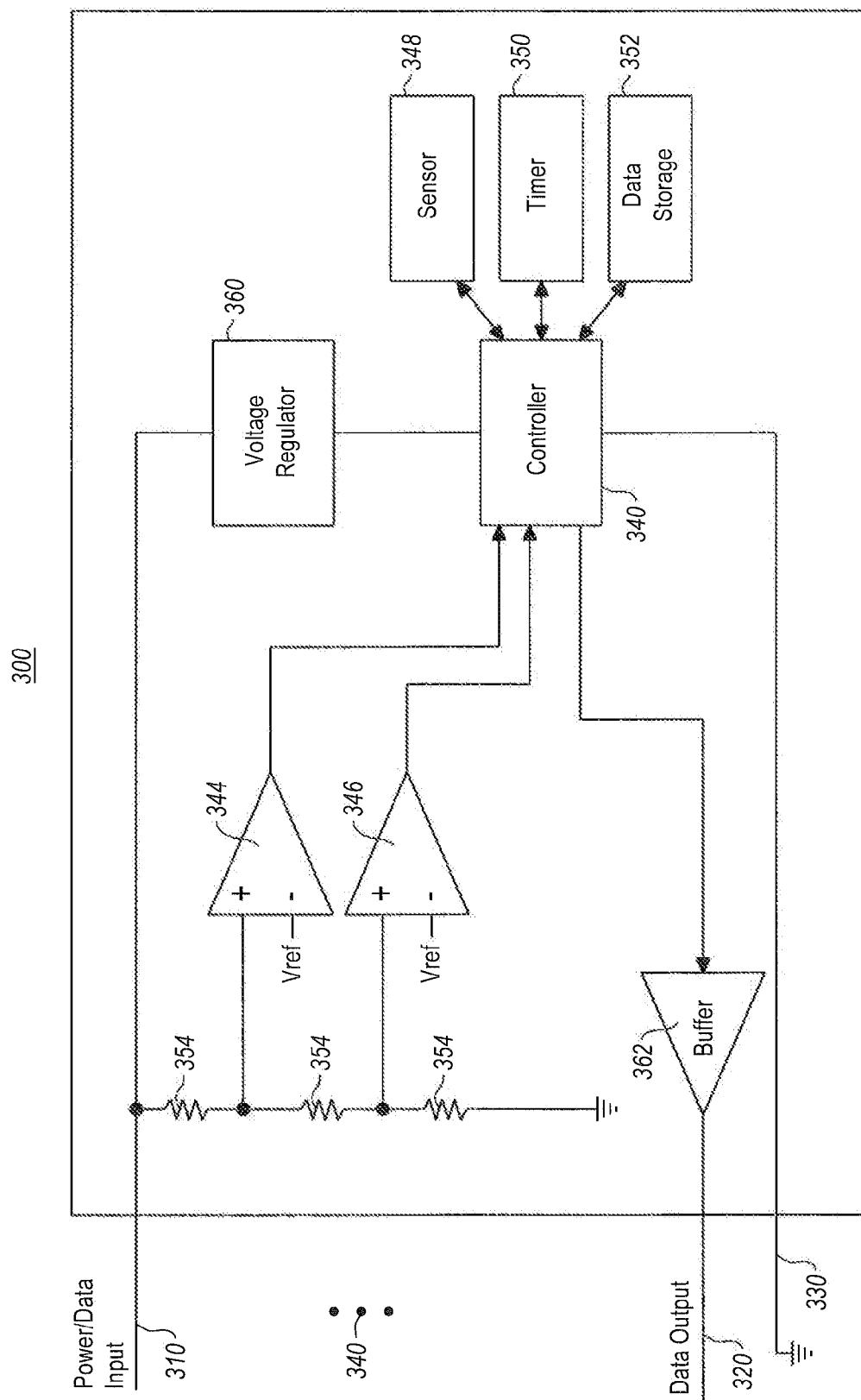


Fig. 3

3 / 10

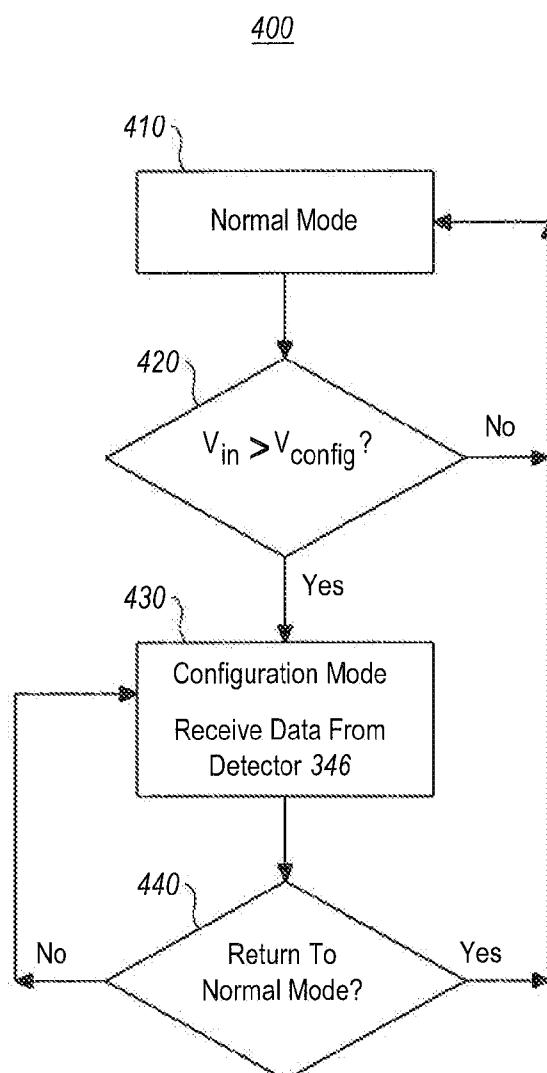


Fig. 4

4 / 10

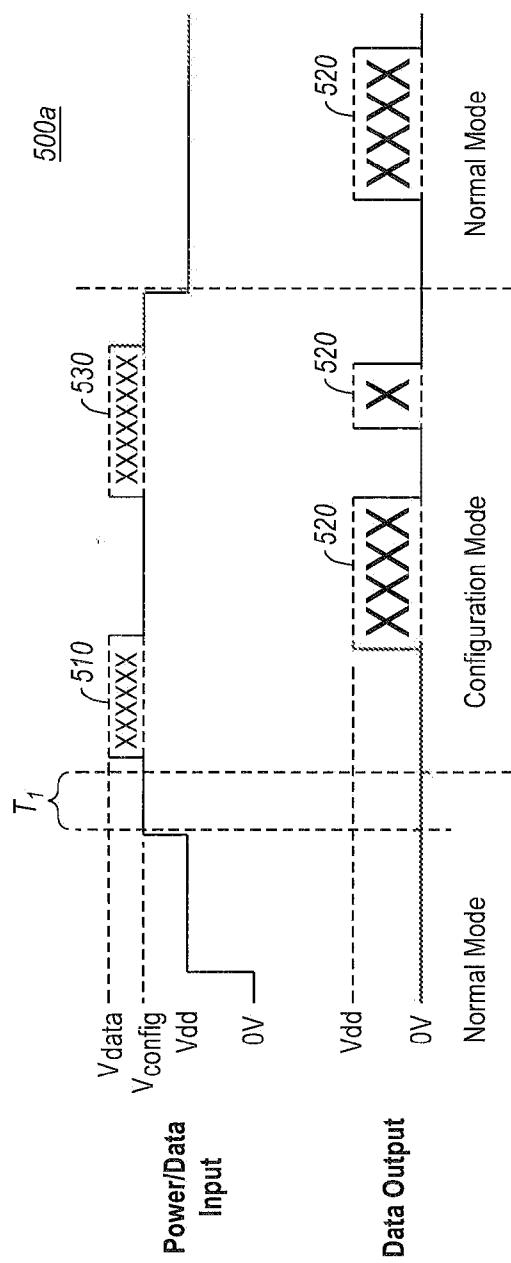


Fig. 5A

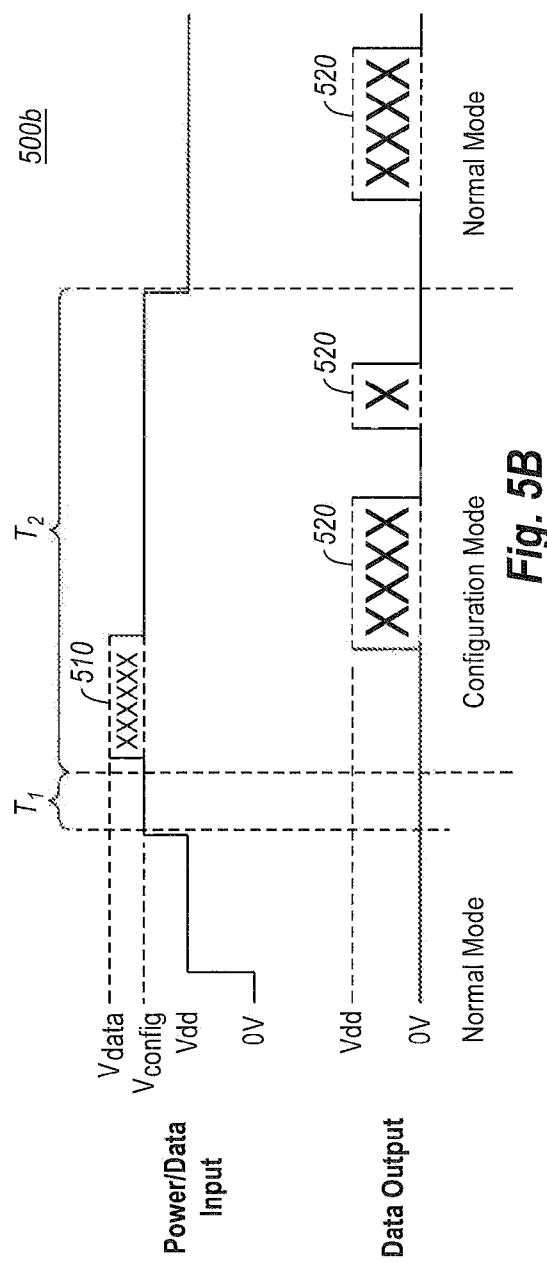


Fig. 5B

5 / 10

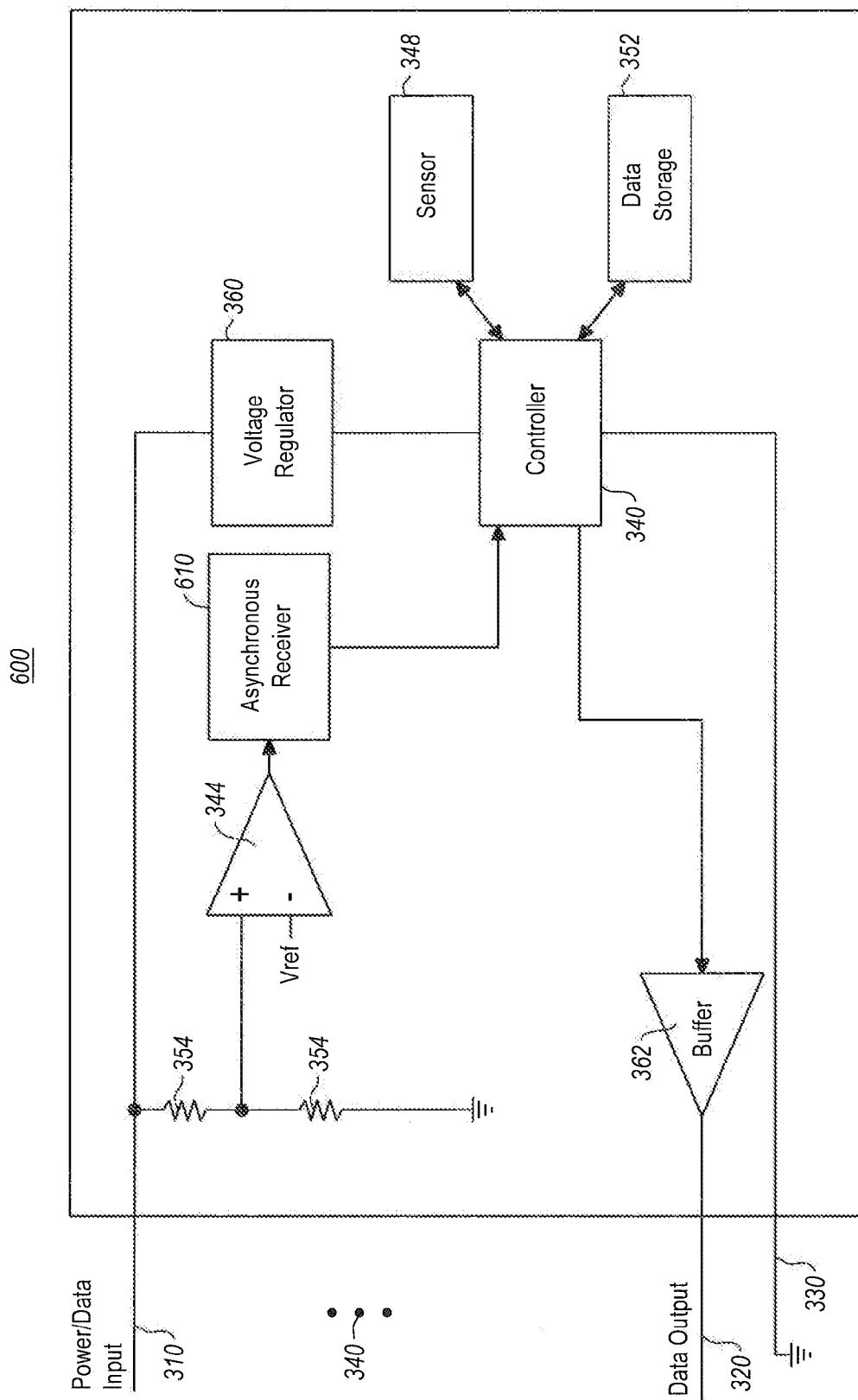


Fig. 6

6 / 10

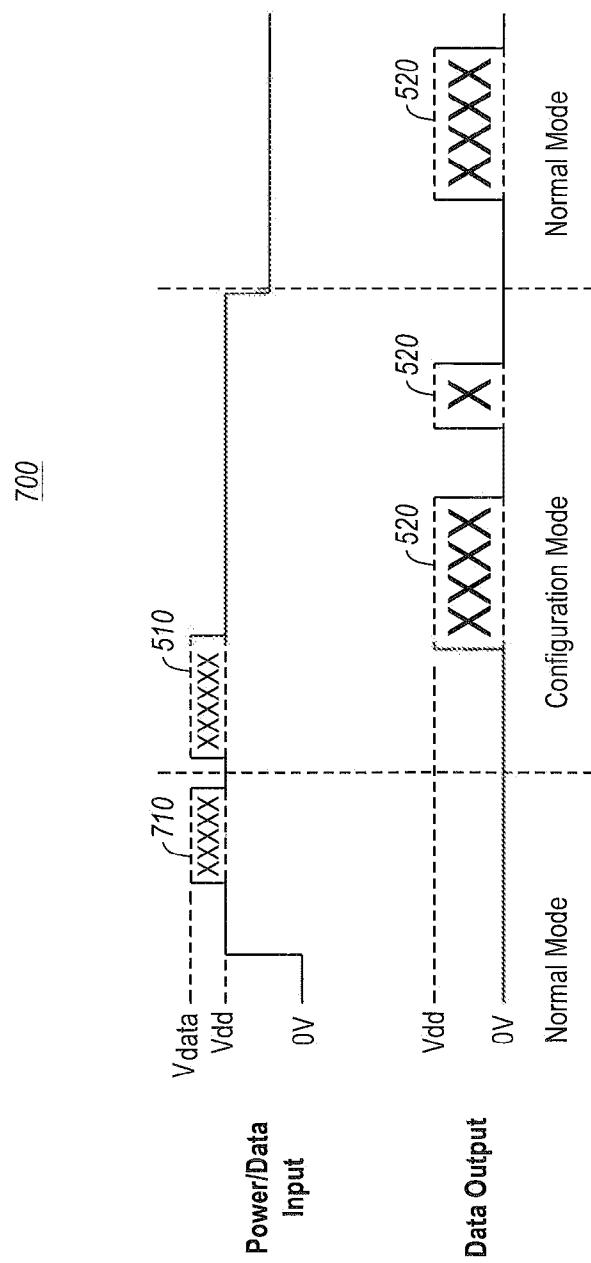


Fig.7

7 / 10

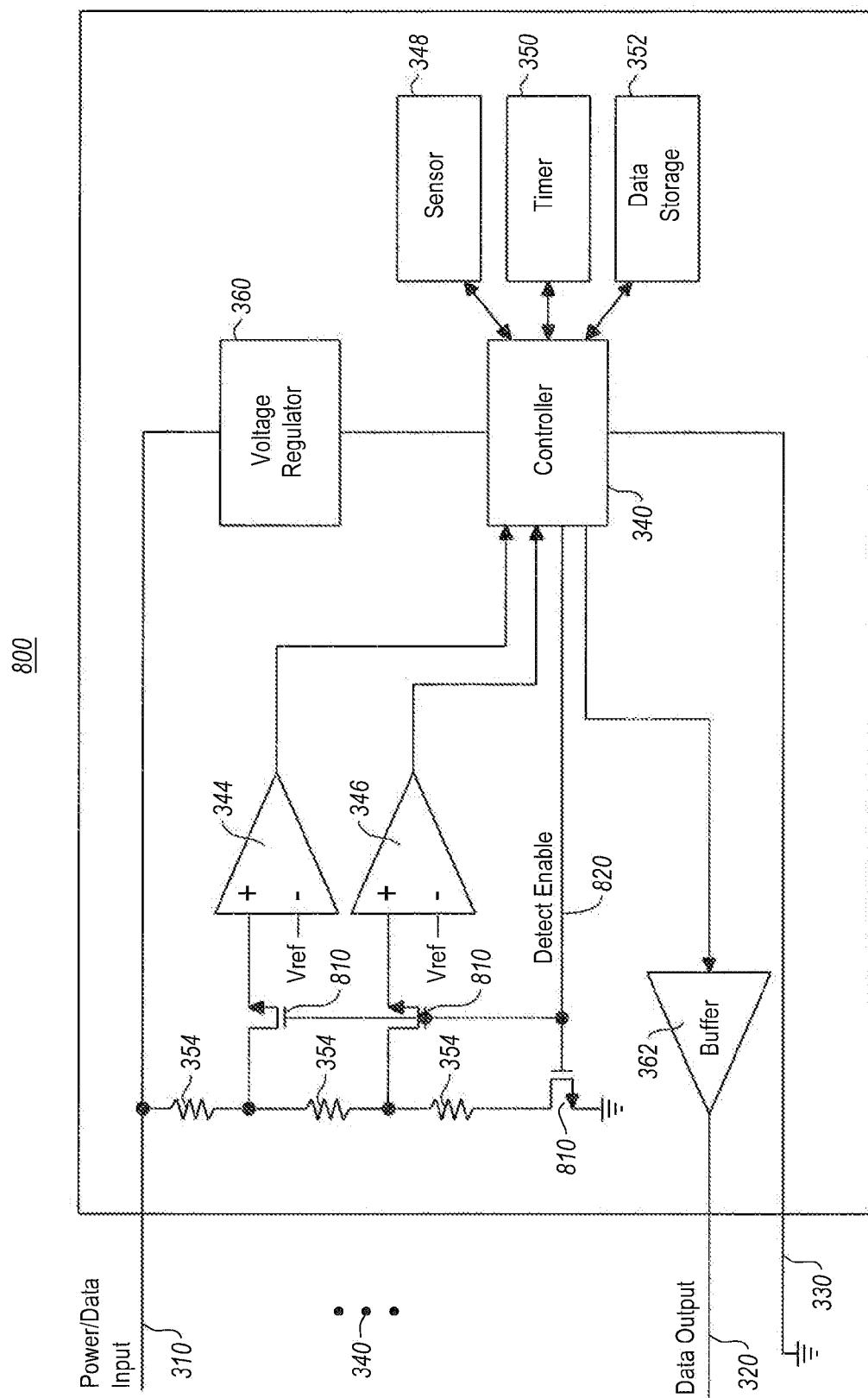
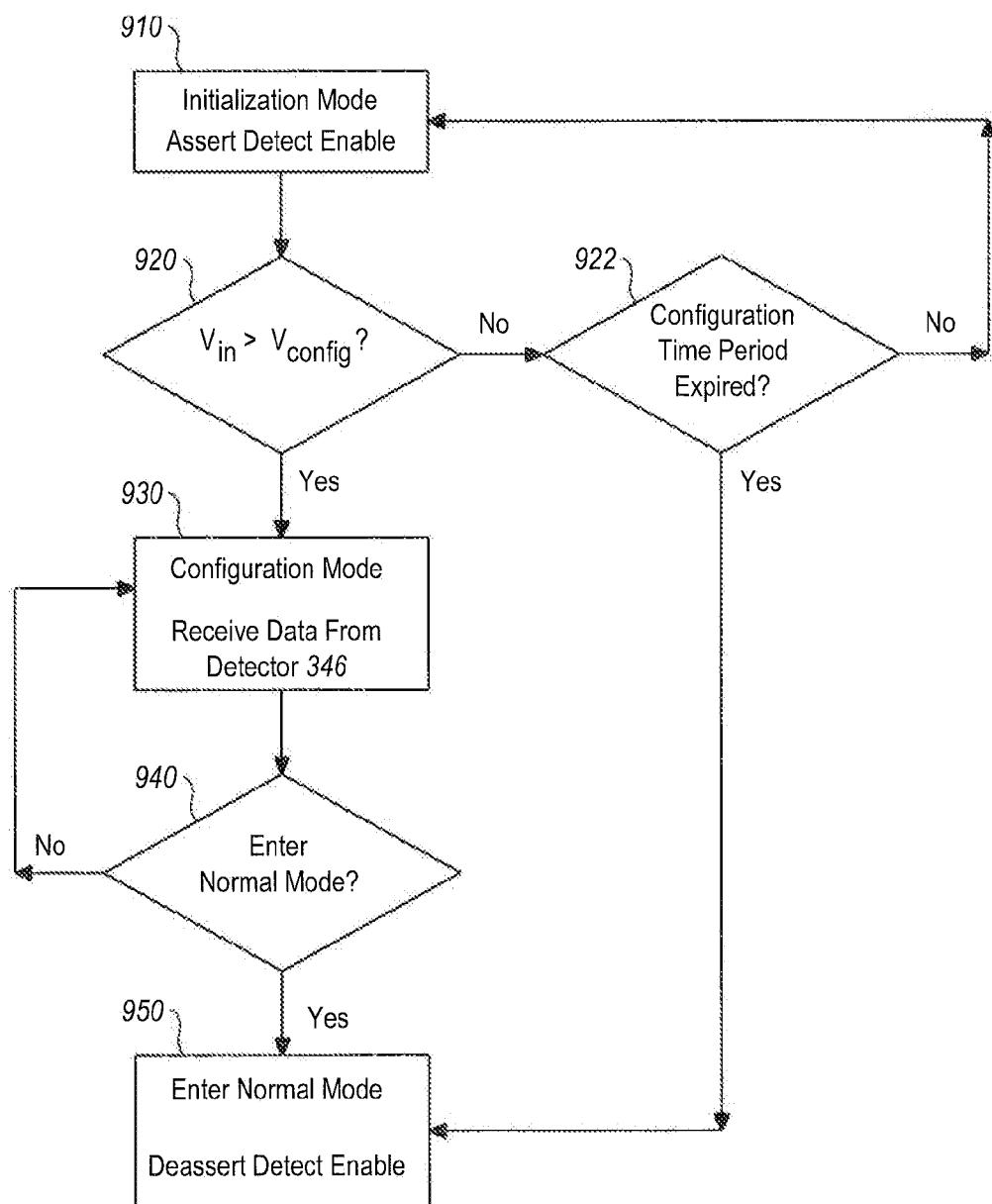


Fig. 8

8 / 10

900*Fig. 9*

9 / 10

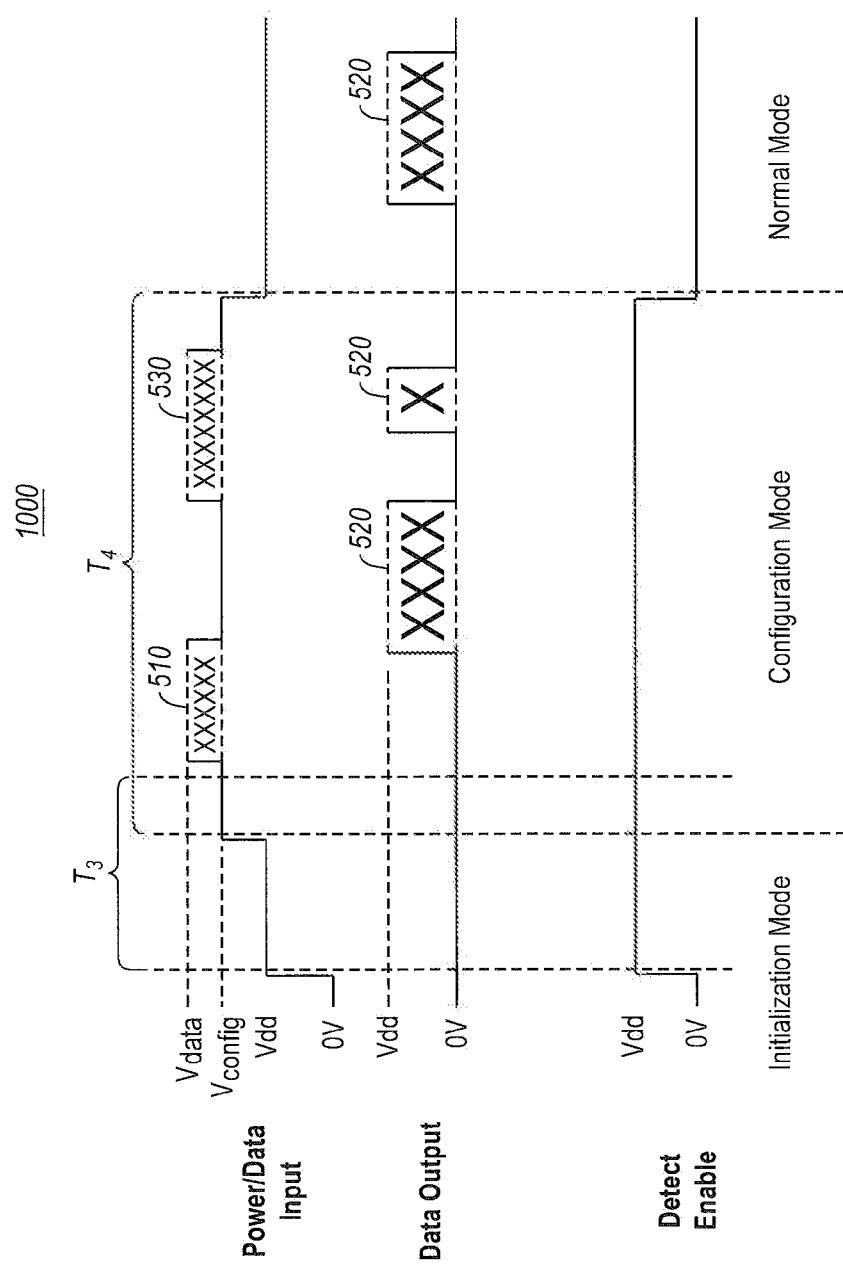


Fig. 10

10 / 10

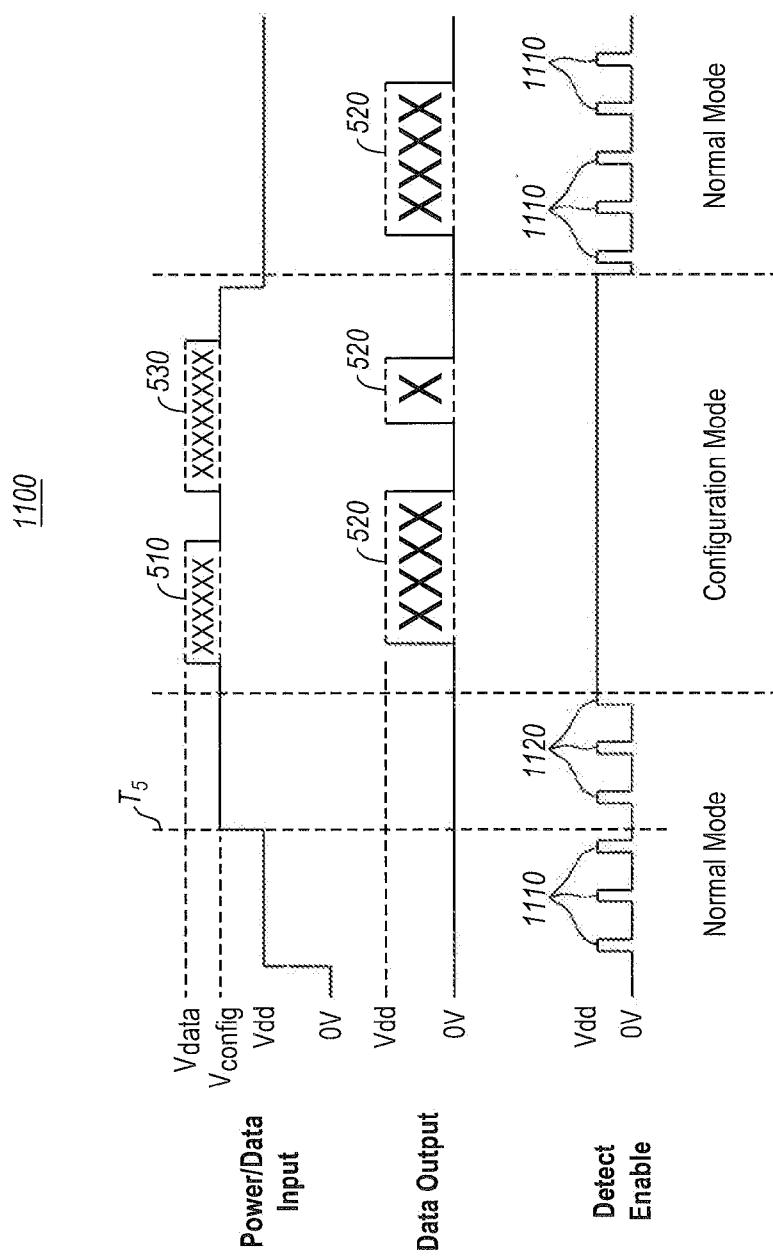


Fig. 11

INTERNATIONAL SEARCH REPORT

International application No
PCT/US2012/022991

A. CLASSIFICATION OF SUBJECT MATTER
INV. G01D18/00
ADD.

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
G01D

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	EP 0 845 660 A1 (MOTOROLA INC [US]) 3 June 1998 (1998-06-03)	1-6, 8-14, 21-23
A	column 2, line 18 - column 3, line 10; figure 1 column 3, line 11 - line 49; figure 2 -----	7,15,16, 26
X	EP 0 926 470 A1 (MATSUSHITA ELECTRIC IND CO LTD [JP]) 30 June 1999 (1999-06-30)	1-6,8, 11,14, 20-23,26
A	paragraph [0012] - paragraph [0021]; figures 1,2 paragraph [0030] - paragraph [0032]; figure 5 -----	7,9,10, 12,13

Further documents are listed in the continuation of Box C.

See patent family annex.

* Special categories of cited documents :

- "A" document defining the general state of the art which is not considered to be of particular relevance
- "E" earlier document but published on or after the international filing date
- "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- "O" document referring to an oral disclosure, use, exhibition or other means
- "P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

"&" document member of the same patent family

Date of the actual completion of the international search

26 March 2012

Date of mailing of the international search report

12/04/2012

Name and mailing address of the ISA/

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040,
Fax: (+31-70) 340-3016

Authorized officer

Chapple, Ian

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No

PCT/US2012/022991

Patent document cited in search report	Publication date	Patent family member(s)			Publication date
EP 0845660	A1	03-06-1998	DE	69728239 D1	29-04-2004
			DE	69728239 T2	19-08-2004
			EP	0845660 A1	03-06-1998
			US	5805466 A	08-09-1998
<hr/>					
EP 0926470	A1	30-06-1999	AU	745163 B2	14-03-2002
			AU	8130098 A	10-02-1999
			CA	2266485 A1	28-01-1999
			EP	0926470 A1	30-06-1999
			NZ	334610 A	27-10-2000
			US	RE41847 E1	19-10-2010
			US	6324482 B1	27-11-2001
			WO	9904226 A1	28-01-1999
<hr/>					