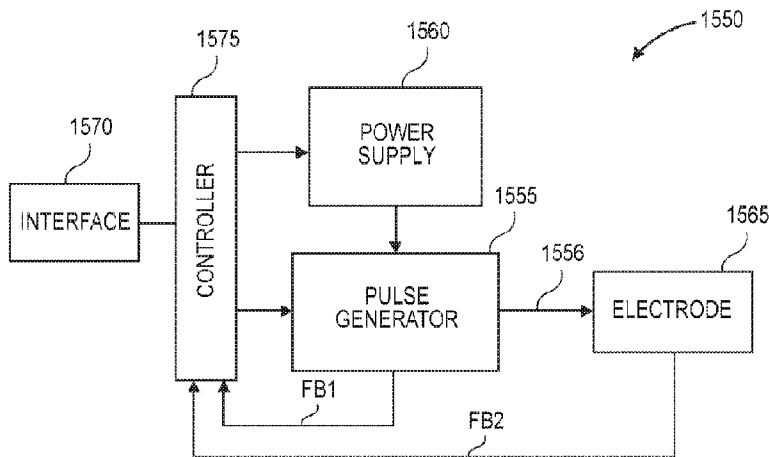




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(54) Titre : PULSEUR DE CIRCUIT ANALOGIQUE HAUTE TENSION AYANT UNE COMMANDE DE RETROACTION
 (54) Title: HIGH-VOLTAGE ANALOG CIRCUIT PULSER WITH FEEDBACK CONTROL



(57) **Abrégé/Abstract:**

A sub-microsecond pulsed electric field generator is disclosed. The field generator includes a controller, which generates a power supply control signal and generates a pulse generator control signal, and a power supply, which receives the power supply control signal and generates one or more power voltages based on the received power supply control signal. The field generator also includes a pulse generator which receives the power voltages and the pulse generator control signal, and generates one or more pulses based on the power voltages and based on the pulse generator control signal. The controller receives feedback signals representing a value of a characteristic of or a result of the pulses and generates at least one of the power supply control signal and the pulse generator control signal based on the received feedback signals.

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(54) Title: HIGH-VOLTAGE ANALOG CIRCUIT PULSER WITH FEEDBACK CONTROL

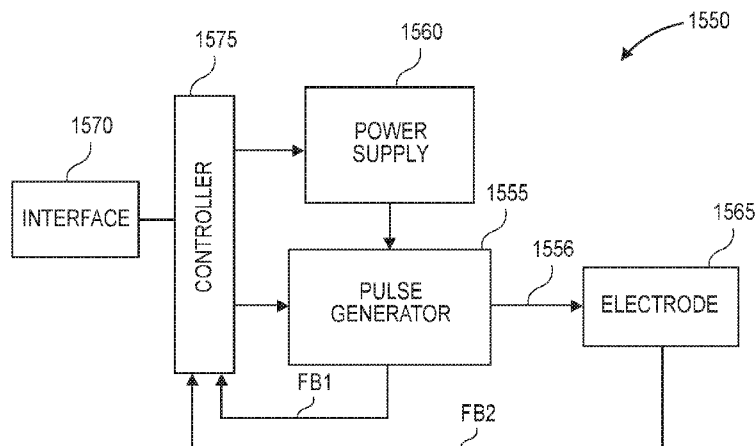


FIG. 15

(57) Abstract: A sub-microsecond pulsed electric field generator is disclosed. The field generator includes a controller, which generates a power supply control signal and generates a pulse generator control signal, and a power supply, which receives the power supply control signal and generates one or more power supply voltages based on the received power supply control signal. The field generator also includes a pulse generator which receives the power voltages and the pulse generator control signal, and generates one or more pulses based on the power voltages and based on the pulse generator control signal. The controller receives feedback signals representing a value of a characteristic of or a result of the pulses and generates at least one of the power supply control signal and the pulse generator control signal based on the received feedback signals.

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HIGH-VOLTAGE ANALOG CIRCUIT PULSER WITH FEEDBACK CONTROL

[0001] BACKGROUND

[0002] 1. Field of the Invention

[0003] The present application generally relates to electrical pulse techniques including circuits and systems for generating electric pulses, including the use of an energy-accumulating element discharged through a load by a relatively low voltage transistor and for controlling the discharge. Specifically, the pulse techniques are used for generating variable duration nanosecond pulsed electric fields (nsPEF) for electrotherapy.

[0004] 2. Description of the Related Art

[0005] Surgical excision of a tumor can result in an infection and leave a scar. Furthermore, if there are more tumors, every cancerous tumor should be identified and individually excised by a surgeon. This can be time consuming and expensive, not to mention uncomfortable for patients.

[0006] Cancerous tumors that are internal to a patient may be especially difficult to remove, let alone detect and treat. Many patients' lives are turned upside down by the discovery of cancer in their bodies, sometimes which have formed relatively large tumors before being detected.

[0007] A "nanosecond pulsed electric field," sometimes abbreviated as nsPEF, includes an electric field with a sub-microsecond pulse width of between 0.1 nanoseconds (ns) and 1000 nanoseconds, or as otherwise known in the art. It is sometimes referred to as sub-microsecond pulsed electric field. nsPEFs often have high peak voltages, such as 10 kilovolts per centimeter (kV/cm), 20 kV/cm, to 500 kV/cm. Treatment of biological cells with nsPEF technology often uses a multitude of periodic pulses at a frequency ranging from 0.1 per second (Hz) to 10,000 Hz.

[0008] NsPEFs have been found to trigger apoptosis in cancerous tumors. Selective treatment of such tumors with nsPEFs can induce apoptosis within the tumor cells without substantially affecting normal cells in the surrounding tissue due to its non-thermal nature.

[0009] An example of nsPEF applied to biological cells is shown and described in U.S. Patent No. 6,326,177 (to Schoenbach et al.).

[0010] The use of nsPEF for the treatment of tumors is a relatively new field. There exists a need for a device with better control over electrical characteristics for safe and effective studies and treatments of cancer in human subjects.

BRIEF SUMMARY

[0011] Generally, a nanosecond pulsed electric field (nsPEF) generator is disclosed that incorporates a feedback control system for compensating or adjusting one or more characteristics of the nsPEF pulses generated. For example, one or more of supply voltage, pulse width, number of pulses, frequency of pulses, duty cycle, or another characteristic may be adjusted in response to a measured parameter. For example, one or more characteristics may be adjusted in response to a measurement of one or more of current, voltage, temperature, or another parameter.

[0012] To generate high voltage short duration pulses for nsPEF treatments a nanosecond pulsed electric field (nsPEF) generator is used. The pulses generated are preferably controllable at least in one or more aspects, such as duration, amplitude, rise/fall time, and affect on temperature of the treated tissue.

[0013] One inventive aspect is a sub-microsecond pulsed electric field generator. The field generator includes a controller, configured to generate a power supply control signal and to generate a pulse generator control signal, and a power supply, configured to receive the power supply control signal and configured to generate one or more power voltages based in part on the received power supply control signal. The field generator also includes a pulse generator configured to receive the one or more power voltages and the pulse generator control signal, and to generate one or more pulses based in part on the one or more power

voltages received from the power supply and based in part on the pulse generator control signal received from the controller. The controller is configured to receive one or more feedback signals representing a value of a characteristic of or a result of the pulses and to generate at least one of the power supply control signal and the pulse generator control signal based partly on the received one or more feedback signals.

[0014] Another inventive aspect is a method of generating one or more sub-microsecond pulsed electric field pulses. The method includes generating a power supply control signal with a controller, generating a pulse generator control signal with the controller, and receiving the one or more power supply control signals at a power supply. The method also includes generating one or more power voltages based in part on the power supply control signal with the power supply, receiving the one or more power voltages and the pulse generator control signal at a pulse generator, and, with the pulse generator, generating one or more pulses based in part on the one or more power voltages received from the power supply and based in part on the pulse generator control signal received from the controller. The method also includes receiving, at the controller, one or more feedback signals representing a value of a characteristic of or a result of the pulses. In addition, at least one of the power supply control signal and the pulse generator control signal is generated by the controller based partly on the received one or more feedback signals.

[0015] Another inventive aspect is a sub-microsecond pulsed electric field generator. The field generator includes a controller, configured to generate a power supply control signal and to generate a pulse generator control signal at a controller output, and a power supply, including a power supply input configured to receive the power supply control signal, where the power supply is configured to generate one or more power voltages at one or more corresponding power supply outputs, and where the one or more power voltages is generated based in part on the received power supply control signal. The field generator also includes a pulse generator, including a plurality of pulse generator inputs configured to receive the one or more power voltages and the pulse generator control signal, where the pulse generator is configured to generate one or more pulses based in part on the one or more power voltages received from the power supply and based in part on the pulse generator control signal received from the controller. In addition, the controller includes a feedback input configured to receive one or more feedback signals representing a value of a characteristic of or a result of the pulses, and the controller is configured to generate at least one of the power supply

control signal and the pulse generator control signal based partly on the received one or more feedback signals.

BRIEF DESCRIPTION OF THE DRAWINGS

- 5 [0016] FIG. 1 illustrates a nanosecond pulse generator apparatus in accordance with an embodiment.
- [0017] FIG. 2 illustrates a pulse profile for both voltage and current in accordance with an embodiment.
- [0018] FIG. 3 illustrates a perspective view of a seven-needle electrode in accordance with an embodiment.
- 10 [0019] FIG. 4 illustrates a perspective view of a two-pole electrode in accordance with an embodiment.
- [0020] FIG. 5 is an electrical schematic of a pulse generator in accordance with an embodiment.
- 15 [0021] FIG. 6A is a schematic illustrating the pulse generator shown in FIG. 5 during charge mode.
- [0022] FIG. 6B is a schematic illustrating the pulse generator shown in FIG. 5 during discharge mode.
- [0023] FIG. 7 is an electrical schematic of an assembly of pulse generator circuits.
- 20 [0024] FIG. 8 is an electrical schematic of one of the pulse generator circuits shown in FIG. 7.
- [0025] FIG. 9 is an electrical schematic of one of the pulse generator stages shown in FIG. 8.
- [0026] FIG. 10 is an electrical schematic of one of the switch drivers shown in FIG. 9.
- [0027] FIG. 11 is an electrical schematic of an alternative switch element.
- 25 [0028] FIG. 12 is a waveform diagram illustrating the operation of a transformer and a control voltage to a MOSFET gate.
- [0029] FIG. 13 is an alternative electrical schematic of a pulse generator shown in FIG. 1.
- [0030] FIG. 14 is an alternative electrical schematic of a pulse generator shown in FIG. 1.

- [0031] FIG. 15 is a block diagram of a nsPEF treatment system.
- [0032] FIG. 16 is a schematic illustration of an alternative pulse generator.
- [0033] FIG. 17 is a schematic illustration of an electrode which may be used in the nsPEF treatment system of FIG. 15.
- 5 [0034] FIG. 18 is a flowchart illustration of methods of using an nsPEF treatment system.
- [0035] FIG. 19 is a flowchart illustration of methods of using an nsPEF treatment system.
- [0036] FIG. 20 is a flowchart illustration of methods of using an nsPEF treatment system.
- [0037] FIG. 21 is a flowchart illustration of methods of using an nsPEF treatment system.
- [0038] FIG. 22 is a flowchart illustration of a method of using an nsPEF treatment system.
- 10 [0039] FIG. 23 is a flowchart illustration of a method of using an nsPEF treatment system.
- [0040] FIG. 24 is a flowchart illustration of a method of using an nsPEF treatment system.
- [0041] FIG. 25 is a flowchart illustration of a method of using an nsPEF treatment system.

DETAILED DESCRIPTION

[0042] It has been shown that nsPEF treatments can be used to cause cancerous tumor cells
 15 to undergo apoptosis, a programmed cell death. Tests have shown that tumors can shrink to nonexistence after treatment. No drugs may be necessary. It has also been shown that the subject's immune system may be stimulated to attack all similar tumor cells, including those of tumors that are not within the nsPEF-treated tumor.

[0043] A "tumor" includes any neoplasm or abnormal, unwanted growth of tissue on or
 20 within a subject, or as otherwise known in the art. A tumor can include a collection of one or more cells exhibiting abnormal growth. There are many types of tumors. A malignant tumor is cancerous, a pre-malignant tumor is precancerous, and a benign tumor is noncancerous. Examples of tumors include a benign prostatic hyperplasia (BPH), uterine fibroid, pancreatic carcinoma, liver carcinoma, kidney carcinoma, colon carcinoma, pre-basal cell carcinoma,
 25 and tissue associated with Barrett's esophagus.

[0044] A "disease" includes any abnormal condition in or on a subject that is associated with abnormal, uncontrolled growths of tissue, including those that are cancerous, precancerous, and benign, or other diseases as known in the art.

[0045] “Apoptosis” of a tumor or cell includes an orderly, programmed cell death, or as otherwise known in the art.

[0046] “Immunogenic apoptosis” of a tumor or cell includes a programmed cell death that is followed by an immune system response, or as otherwise known in the art. The immune system response is thought to be engaged when the apoptotic cells express calreticulin or another antigen on their surfaces, which stimulates dendritic cells to engulf, consume, or otherwise commit phagocytosis of the targeted cells leading to the consequent activation of a specific T cell response against the target tumor or cell.

[0047] Pulse lengths of between 10 and 900 nanoseconds for nsPEF have been particularly studied to be effective in stimulating an immune response. Pulse lengths of about 100 nanoseconds are of particular interest in that they are long enough to carry sufficient energy to be effective at low pulse numbers but short enough to be effective in the manner desired.

[0048] A time of “about” a certain number of nanoseconds includes times within a tolerance of $\pm 1\%$, 2% , 3% , 4% , 5% , 7.5% , 10% , 15% , 20% , 25% or other percentages, or fixed tolerances, such as ± 0.1 , ± 0.2 , ± 0.3 , ± 0.4 , ± 0.5 , ± 0.7 , ± 1.0 , ± 2.0 , ± 3.0 , ± 4.0 , ± 5.0 , ± 7.0 , ± 10 , ± 15 , ± 20 , ± 25 , ± 30 , ± 40 , ± 50 , ± 75 ns, or other tolerances as acceptable in the art in conformance with the effectivity of the time period.

[0049] Immune system biomarkers can be measured before and/or after nsPEF treatment in order to confirm that the immune response has been triggered in a patient. Further, nsPEF treatment can be paired with a CD47-blocking antibody treatment to better train CD8+T cells (i.e., cytotoxic T cells) for attacking the cancer.

[0050] FIG. 1 illustrates a nanosecond pulse generator system in accordance with an embodiment. NsPEF system 100 includes electrode 102, footswitch 103, and interface 104. Footswitch 103 is connected to housing 105 and the electronic components therein through connector 106. Electrode 102 is connected to housing 105 and the electronic components therein through high voltage connector 112. NsPEF system 100 also includes a handle 110 and storage drawer 108. As shown in DETAIL A portion of FIG. 1, nsPEF system 100 also includes holster 116, which is configured to hold electrode 102 at its handle portion 114.

[0051] A human operator inputs a number of pulses, amplitude, pulse duration, and frequency information, for example, into a numeric keypad or a touch screen of interface 104. In some embodiments, the pulse width can be varied. A microcontroller sends signals to

pulse control elements within nsPEF system 100. In some embodiments, fiber optic cables allow control signaling while also electrically isolating the contents of the metal cabinet with nsPEF generation system 100, the high voltage circuit, from the outside. In order to further isolate the system, system 100 may be battery powered instead of from a wall outlet.

5 [0052] FIG. 2 illustrates a pulse profile for both voltage and current in accordance with an embodiment. Output from the nsPEF system 100 with voltage on the top of the figure and current on the bottom for a first and second pulses. The first pulse has an amplitude of about 15 kV, a current of about 50 A, and a duration of about 15 ns. The second pulse has an amplitude of about 15 kV, a current of about 50 A, and a duration of about 30 ns. If such a
10 pulse had been delivered on suction electrodes having 4 mm between the plates, the pulse generator would have delivered a pulse of about 50 A and 37.5 kV/cm. Given a voltage, current depends heavily on the electrode type and tissue resistance.

[0053] While FIG. 2 illustrates a specific example, other pulse profiles may also be generated. For example, in some embodiments, rise and/or fall times for pulses may be less
15 than 20 ns, about 20 ns, about 25 ns, about 30 ns, about 40 ns, about 50 ns, about 60 ns, about 75 ns, or greater than 75 ns. In some embodiments, the pulse voltage may be less than 5 kV, about 5 kV, about 10 kV, about 15 kV, about 20 kV, about 25 kV, about 30 kV, or greater than 30 kV. In some embodiments, the current may be less than 10 A, about 10 A, about 25 A, about 40 A, about 50 A, about 60 A, about 75 A, about 100 A, about 125 A, about 150 A,
20 about 175 A, about 200 A, or more than 200 A. In some embodiments, the pulse duration may be less than 10 ns, about 10 ns, about 15 ns, about 20 ns, about 25 ns, about 30 ns, about 40 ns, about 50 ns, about 60 ns, about 75 ns, about 100 ns, about 125 ns, about 150 ns, about 175 ns, about 200 ns, about 300 ns, about 400 ns, about 500 ns, about 750 ns, about 1 μ s, about 2 μ s, about 3 μ s, about 4 μ s, about 5 μ s, or greater than 5 μ s.

25 [0054] FIG. 3 illustrates a perspective view of a seven-needle suction electrode in accordance with an embodiment. In electrode 300, sheath 301 surrounds seven sharp electrodes 302 with an broad opening at a distal end. When the open end is placed against a tumor, air is evacuated from the resulting chamber through vacuum holes 304 to draw the entire tumor or a portion thereof into the chamber. The tumor is drawn so that one or more of
30 the electrodes preferably penetrates the tumor. Sharp ends of the electrodes are configured to pierce the tumor. The center electrode may be at one polarity, and the outer six electrodes

may be at the opposite polarity. Nanopulses electric fields can then be precisely applied to the tumor using nsPEF system 100 (see FIG. 1).

[0055] The electrodes can be apposed, one of each positive and negative pair of electrodes on one side of a tumor and the other electrode of the pair on an opposing side of the tumor. Opposing sides of a tumor can include areas outside or within a tumor, such as if a needle electrode pierces a portion of the tumor.

[0056] FIGS. 4 illustrates a two-pole suction electrode in accordance with an embodiment. In electrode device 400, sheath 401 surrounds two broad electrodes 402 on opposite sides of a chamber. When air is evacuated through vacuum holes 404 and a tumor is pulled within the chamber, the opposing electrodes apply nsPEF pulses to the tumor.

[0057] The nature of the electrode used mainly depends upon the shape of the tumor. Its physical size and stiffness can also be taken into account in selection of a particular electrode type.

[0058] U.S. Patent No. 8,688,227 B2 (to Nuccitelli et al.) discloses other suction electrode-based medical instruments and systems for therapeutic electrotherapy.

[0059] If there are multiple tumors in a subject, a surgeon can select a single tumor to treat based on the tumor's compatibility with electrodes. For example, a tumor that is adjacent to a stomach wall may be more easily accessible than one adjacent a spine or the brain. Because a nsPEF pulse is preferably applied so that the electric field transits through as much tumor mass as possible while minimizing the mass of non-tumor cells that are affected, a clear path to two opposed 'poles' of a tumor may also be a selection criterion.

[0060] For tumors on or just underneath the skin of subject, needle electrodes can be used percutaneously. For locations deeper within a subject, a retractable electrode can fit into a gastroscope, bronchoscope, colonoscope, or other endoscope or laparoscope. For example, a tumor in a patient's colon can be accessed and treated using an electrode within a colonoscope.

[0061] Barrett's esophagus, in which portions of tissue lining a patient's esophagus are damaged, may be treated using an electrode placed on an inflatable balloon.

[0062] Embodiments of nanosecond pulsed power generators produce electric pulses in the range of single nanoseconds to single microseconds. The pulses are created by rapid release of energy stored in, for example, a capacitive or inductive energy reservoir to a load in a period that is generally much shorter than the charging time of the energy reservoir.

5 [0063] Conventional capacitive-type pulsed generators include pulse forming networks, which provide fixed pulse duration and impedance. With prior knowledge of a load's resistance, a pulse forming network with impedance that matches the load can be used. But for broader applications, especially when the load resistance is unknown, it is desirable to have a pulse generator with a flexibility of impedance matching and variation of pulse
10 duration. Such flexibility can be implemented by switching a capacitor with a controllable switch. In this case, the capacitor can be regarded as a "voltage source" and can adapt to various load resistance. The switched pulse amplitude can then have the same voltage as the voltage of the capacitor. The pulse width is accordingly determined by the switch "on" time.

[0064] The selection of switches in nanosecond pulse generators is limited because of the
15 high voltages, high currents, and fast switching times involved.

[0065] Spark gap switches, typically used in pulsed power technology, are capable of switching high voltages and conducting high currents. But they can only be turned on, and stopping the current flow in the middle of conduction is impossible. Besides spark gaps, other types of high voltage, high power switches are available, such as: magnetic switches, vacuum
20 switches (Thyratrons for example), and certain high-voltage semiconductor switches.

[0066] Magnetic switches, relying on the saturation of magnetic core, change from high impedance to low impedance in the circuit. They can be turned on above a certain current threshold but will not be turned off until all the current is depleted by the load.

[0067] Vacuum switches are a good option for high voltage and high repetition rate
25 operation, but similar to magnetic switches, they also can be only turned on, but cannot be turned off at a predetermined time.

[0068] Some types of high-voltage semi-conductor switches may also be considered. Thyristors and insulated gate bipolar transistors (IGBTs) may, in some embodiments be used. However, the turn-on times of Thyristors and IGBTs limit their usefulness.

30 [0069] Metal-oxide-semiconductor field-effect transistors (MOSFETs) have insufficient maximum drain to source voltage ratings (e.g. < 1kV) and insufficient maximum drain to

source current ratings (e.g. < 50A) to be used in conventional pulse generator architectures to produce the voltage and current necessary for the applications discussed herein. If they were used, a large number of stages would be needed in order to produce high-amplitude output voltages. However, in conventional Marx generator architectures with a large number of stages, the Marx generator goes into an underdamped mode instead of a critically damped mode, resulting in loss in overshoot. As a result, the overall voltage efficiency decreases. For example, a voltage efficiency of a Marx generator may be 80% at 5 stages but decrease to 50% at 20 stages.

[0070] Furthermore, as the number of stages is increased, the impedance of the Marx generator also increases. This reduces the total energy deliverable to the load. This is particularly unfavorable for driving low impedance loads and long pulses.

[0071] In addition, the charging losses in the charging resistors also increases with the increased number of stages. As a result, such Marx generators are unsuitable for high repetition rate operation.

[0072] Therefore, in order to produce high voltage pulses, simply increasing the number of stages will cause a series of problems, including low efficiency, high impedance, etc. Because there is a tradeoff between the number of the stages and the actual output voltage, using conventional Marx generators cannot produce high voltage pulses which are sufficient for the applications discussed herein.

[0073] Some embodiments of this disclosure include a tunable, high voltage, nanosecond pulse generator. The switches may be power MOSFETs, which may, for example, be rated for a voltage of 1 kV and current of up to 30A. In some embodiments, the switches power MOSFETs rated for a voltage of 1 kV and current of up to continuous 90A and more than 200A peak. Voltage is scaled up by a Marx-switch stack hybrid circuit. In each Marx generator stage, a particularly configured stack of MOSFETs is used. As a result, the charging voltage for each stage is greater than the rated maximum for a single switch.

[0074] A technical advantage of the configuration is that the overall output voltage can be increased with just a few stages (e.g. ≤ 5). As a result, the problems discussed above with Marx generators having a large number of stages are avoided and high efficiency, low impedance, and large variability in the pulse duration can be achieved.

[0075] Such an architecture also allows much easier control as only one trigger circuit may be needed for each stage. One additional benefit is that the pulse generator has low impedance, so it will be able to drive various loads with high current and extended pulse duration. The scaling up of the current is implemented by combining multiple Marx-switch stack circuits in parallel. The pulse duration is controlled by the closing and opening of the switch stack switches.

[0076] FIG. 5 illustrates a pulse generator circuit 500 which may be used inside nsPEF system 100 of FIG. 1. Pulse generator circuit 500 illustrates a panel comprising a Marx generator switched by three switch stacks. The nsPEF system can have a single pulse generator circuit panel. In some embodiments, a nsPEF system includes multiple panels in parallel.

[0077] Circuit 500 includes three stages – 510, 520, and 530. In some embodiments, another number of stages is used. For example, in some embodiments, 2, 4, 5, 6, 7, 8, 9, or 10 stages are used. Stage 510 includes resistors 512 and 514, capacitor 515, and switch stack 516. Likewise, stage 520 includes resistors 522 and 524, capacitor 525, and switch stack 526, and stage 530 includes resistors 532 and 534, capacitor 535, and switch stack 536. Each of these elements have structure and functionality which is similar to the corresponding elements of stage 510.

[0078] Stage 510 has first and second input voltage input terminals 511 and 513 and first and second voltage output terminals 517 and 518. Stage 520 has first and second input voltage input terminals 521 and 523, and first and second voltage output terminals 527 and 528. Stage 530 has first and second input voltage input terminals 531 and 533, and first and second voltage output terminals 537 and 538.

[0079] The first and second voltage input terminals 511 and 513 of stage 510 are respectively connected to first and second power supply input terminals V1 and V2. The first and second voltage output terminals 517 and 518 of stage 510 are respectively connected to the first and second voltage input terminals 521 and 523 of stage 520. The first and second voltage output terminals 527 and 528 of stage 520 are respectively connected to the first and second voltage input terminals 531 and 533 of stage 530. The second voltage output terminal 538 of stage 530 and second voltage input terminal 513 of stage 510 are respectively connected to first and second power output terminals VO1 and VO2.

[0080] Pulse generator circuit 500 operates in a charge mode, and in a discharge mode. During the charge mode, described below with reference to FIG. 6A in more detail, capacitors 515, 525, and 535 are charged by current received from the first and second power supply input terminals V1 and V2. During the discharge mode, described below with
5 reference to FIG. 6B in more detail, capacitors 515, 525, and 535 are discharged to provide a current to a load (not shown) connected across first and second power output terminals VO1 and VO2.

[0081] FIG. 6A illustrates pulse generator circuit 500 during charge mode. First and second input voltages are respectively applied to first and second power supply input terminals V1
10 and V2 while each of switch stacks 516, 526, and 536 are nonconductive or open, and while first and second power output terminals may be disconnected from the load (not shown). Because each of switch stacks 516, 526, and 536 are open, substantially no current flows therethrough, and they are represented as open circuits in FIG. 6A. During the charge mode,
15 each of capacitors 515, 525, and 535 are charged by current flowing through resistors 512, 522, 532, 534, 524, and 514 to or toward a voltage equal to the difference between the first and second input voltages.

[0082] Each of the switches of switch stacks 516, 526, and 536 has a breakdown voltage rating which should not be exceeded. However, because the switches are serially connected,
20 the capacitors 515, 525, and 535 may be charged to a voltage significantly greater than the breakdown voltage of the individual switches. For example, the breakdown voltage of the switches may be 1 kV, and the capacitors 515, 525, and 535 may be charged to a voltage of 5 kV, when 5 or more switches are used in each switch stack.

[0083] For example, the first and second input voltages may respectively be 5kV and 0V. In such an example, each of the capacitors 515, 525, and 535 is charged to or toward a
25 voltage equal to 5kV. In some embodiments, the difference between the first and second input voltages is limited to be less than 10kV.

[0084] FIG. 6B illustrates pulse generator circuit 500 during discharge mode. First power supply input terminal V1 may be disconnected from the first input voltage. In some
30 embodiments, first power supply input terminal V1 remains connected to the first input voltage. Second power supply input terminal V2 remains connected to the second input voltage. In addition, each of switch stacks 516, 526, and 536 are conductive or closed. Because each of switch stacks 516, 526, and 536 are closed, current flows therethrough, and

they are represented as conductive wires in FIG. 6B. As a result, a low impedance electrical path from power supply input terminal V2 to power output terminal VO1 is formed by switch stack 516, capacitor 515, switch stack 526, capacitor 525, switch stack 536, and capacitor 535. Consequently, the difference between the voltages at the power output terminals VO1 and VO2 is equal to the number of stages (in this example, 3) times the difference between the first and second input voltages.

[0085] Where the first and second input voltages are respectively 5kV and 0V, a voltage difference of 15kV is developed across the power output terminals VO1 and VO2.

[0086] FIG. 7 illustrates an alternative pulse generator circuit 700 which may be used inside nsPEF system 100 of FIG. 1. This pulse generator includes panels in parallel. The number of panels can be adjusted to allow the system to generate different amounts of current and power.

[0087] Pulse generator circuit 700 receives input pulses across input port Vin, and generates output pulses across output port Vout in response to the received input pulses.

[0088] Pulse generator circuit 700 includes multiple panels or pulse generator circuits 710, 720, 730, and 740. Pulse generator circuit 700 also includes driver 750. In this embodiment, four pulse generator circuits are used. In alternative embodiments, fewer or more pulse generator circuits are used. For example, in some embodiments, 2, 3, 5, 6, 7, 8, 9, 10 or another number of pulse generator circuits are used.

[0089] Each of the pulse generator circuits 710, 720, 730, and 740 may have characteristics similar to other pulse generator circuits discussed herein. For example, each the pulse generator circuits 710, 720, 730, and 740 may have characteristics similar to pulse generator circuit 500 discussed above with reference to FIGs. 5, 6A, and 6B.

[0090] Each of pulse generator circuits 710, 720, 730, and 740 has positive and negative DC input terminals, positive and negative control input terminals, and positive and negative output terminals, and is configured to generate output voltage pulses across the positive and negative output terminals in response to driving signal pulses applied across the positive and negative control input terminals. The output voltage pulses are also based on power voltages received across positive and negative DC power input terminals.

[0091] The driving signal pulses are generated across conductors 756 and 758 by driver 750, which includes amplifier circuit 751, capacitor 752, and transformer 753. In some embodiments, driver 750 also includes clamp circuits 754.

[0092] Driver 750 receives an input signal pulse at input port V_{in} and generates a driving signal pulse across conductors 756 and 758 in response to the input signal pulse. Amplifier circuit 751 receives the input signal pulse and drives transformer 753 through capacitor 752, which blocks low frequency and DC signals. In response to being driven by amplifier circuit 751, transformer 753 generates an output voltage pulse across conductors 756 and 758, such that the duration of the output voltage pulse is equal to or substantially equal (e.g. within 10% or 1%) to the duration of the input signal pulse at input port V_{in} .

[0093] In some embodiments, clamp circuits 754 are included at least to dampen potential signals, which may otherwise be caused by resonance. Clamp circuits 754 include parallel diodes, which provide a short-circuit path for any current reversal, and also clamp the maximum voltage across the components connected to the clamp circuits 754.

[0094] In some embodiments, transformer 753 has a 1:1 turns ratio. In alternative embodiments, a different turns ratio is used.

[0095] Each of pulse generator circuits 710, 720, 730, and 740 receives the voltage pulses from driver 750 across the positive and negative control input terminals and generates corresponding voltage pulses across the positive and negative output terminals in response to the received voltage pulses from driver 750. The voltage pulses generated across the positive and negative output terminals have durations which are equal to or substantially equal (e.g. within 10% or 1%) to the durations of the voltage pulses received from driver 750.

[0096] In this embodiment, the negative output terminals of pulse generator circuits 710, 720, 730, and 740 are directly connected to the negative V_{out} terminal of the output port V_{out} of pulse generator circuit 700. In addition, in this embodiment, the positive output terminals of pulse generator circuits 710, 720, 730, and 740 are respectively connected to the positive V_{out} terminal of the output port V_{out} of pulse generator circuit 700 through diodes 715, 725, 735, and 745. Diodes 715, 725, 735, and 745 decouple pulse generator circuits 710, 720, 730, and 740 from one another. As a consequence, interference and the associated pulse distortion that would otherwise occur is substantially eliminated. For example, diodes 715, 725, 735, and 745 prevent current from one of pulse generator circuits 710, 720, 730, and 740 to another of pulse generator circuits 710, 720, 730, and 740 if the switching is not perfectly

synchronous. Diodes 715, 725, 735, and 745 also prevent current from flowing from the pulse generator circuits 710, 720, 730, and 740 while they are charging.

[0097] In this embodiment, diodes 715, 725, 735, and 745 each include a single diode. In alternative embodiments, diodes 715, 725, 735, and 745 each include multiple diodes
5 connected serially based at least upon voltage ratings of the serially connected diodes.

[0098] In this embodiment, diodes 715, 725, 735, and 745 are connected so as to conduct current from the positive terminal of output port V_{out} toward pulse generator circuits 710, 720, 730, and 740, as pulse generator circuits 710, 720, 730, and 740 in this embodiment are configured to generate negative pulses. In alternative embodiments, where pulse generator
10 circuits are configured to generate positive pulses, diodes may be similarly connected so as to conduct current from the pulse generator circuits to the positive terminal of the output port.

[0099] FIG. 8 illustrates a pulse generator circuit 800 which may be used for pulse generator circuits 710, 720, 730, and 740 of pulse generator circuit 1000 of FIG. 7.

[0100] Pulse generator circuit 800 receives input pulses across input port V_{in} , and
15 generates output pulses across output port V_{out} in response to the received input pulses.

[0101] Pulse generator circuit 800 includes multiple pulse generator stages 810, 820, and 830. In this embodiment, pulse generator circuit 700 also includes driver 850, and optional common mode chokes 815, 825, and 835.

[0102] Each of the pulse generator stages 810, 820, and 830 may have characteristics
20 similar to other pulse generator stages discussed herein. For example, each the pulse generator stages 810, 820, and 830 may have characteristics similar to stages 510, 520, and 530 of pulse generator circuit 500 discussed above with reference to FIGs. 5, 6A, and 6B. In some embodiments, fewer or more pulse generator stages may be used.

[0103] Each of pulse generator stages 810, 820, and 830 has positive and negative trigger
25 input terminals, power positive and negative DC input terminals, and positive and negative V_o output terminals, and is configured to generate output voltage pulses across the positive and negative V_o output terminals in response to driving signal pulses applied across the positive and negative trigger input terminals. The output voltage pulses are also based on power voltages V_1 and V_2 respectively received at power positive and negative DC input
30 terminals.

[0104] In this embodiment, the negative Vi input terminal of pulse generator stage 830 is connected with the negative terminal of the output port Vout of pulse generator circuit 800. In addition, in this embodiment, the negative Vo output terminal of pulse generator stage 810 is connected with the positive terminal of the output port Vout of pulse generator circuit 800.

5 [0105] In addition, as shown, the positive Vo output terminal of pulse generator 830 is connected with the positive Vi input terminal of pulse generator 820, and the negative Vo output terminal of pulse generator 830 is connected with the negative Vi input terminal of pulse generator 820. Furthermore, the positive Vo output terminal of pulse generator 820 is connected with the positive Vi input terminal of pulse generator 810, and the negative Vo
10 output terminal of pulse generator 820 is connected with the negative Vi input terminal of pulse generator 810.

[0106] The driving signal pulses for pulse generator stages 810, 820, and 830 are generated across conductors 856 and 858 by driver 850, which includes amplifier circuit 851, capacitor 852, and transformer 853. In some embodiments, driver 850 also includes clamp circuits 854.

15 [0107] Driver 850 receives an input signal pulse at input port Vin, which is connected to conductors 756 and 758, as shown in FIG. 7 discussed above. Driver 850 generates a driving signal pulse across conductors 856 and 858 in response to the input signal pulse. Amplifier circuit 851 receives the input signal pulse, and drives transformer 853 through capacitor 852, which reduces or blocks low frequency and DC signals. In response to being driven by
20 amplifier circuit 851, transformer 853 generates an output voltage pulse across conductors 756 and 758, such that the duration of the output voltage pulse is equal to or substantially equal (e.g. within 10% or 1%) to the duration of the input signal pulse at input port Vin.

[0108] In some embodiments, clamp circuits 854 are included at least to dampen potential signals, which may otherwise be caused by resonance. Clamp circuits 854 include parallel
25 diodes, which provide a short-circuit path for any current reversal, and also clamp the maximum voltage across the components connected to the clamp circuits 854.

[0109] In some embodiments, transformer 853 has a 1:1 turns ratio. In alternative embodiments, a different turns ratio is used.

[0110] Each of pulse generator stages 810, 820, and 830 receives the voltage pulses from
30 driver 850 through a corresponding choke 815, 825, or 835, which blocks high frequency signals, for example, from coupling from the high voltage pulse generator stages 810, 820,

and 830. The voltage pulses are received at the positive and negative trigger input terminals and the pulse generator stages 810, 820, and 830 each generate corresponding voltage pulses across the positive and negative V_o output terminals in response to the received voltage pulses from driver 850. The voltage pulses generated across the positive and negative V_o output terminals have durations which are equal to or substantially equal (e.g. within 10% or 1%) to the durations of the voltage pulses received from driver 850.

[0111] FIG. 9 illustrates a pulse generator stage 900 which may be used as one of the pulse generator stages 810, 820, and 830 of pulse generator circuit 800 shown in FIG. 8.

[0112] Pulse generator stage 900 receives trigger pulses across input port trigger input, and generates output voltages at output port V_{out} in response to the received trigger pulses. The output voltages are also generated based on power voltages received at power input terminals V_1 and V_2 . Pulse generator stage 900 includes multiple switch drivers 950. Pulse generator stage 900 also includes switch stack 910, capacitor 920, and resistors 930 and 940.

[0113] Switch drivers 950 are configured to receive the trigger pulses, and to generate control signals for the switches of switch stack 910 in response to the received trigger pulses, as discussed in further detail below. Each of the control signals is referenced to a voltage specific to the switch being driven. Accordingly, a first switch receives a control signal pulse between first and second voltages, and a second switch receives a control signal pulse between third and fourth voltages, where each of the first, second, third, and fourth voltages are different. In some embodiments, the difference between the first and second voltages is substantially the same as the difference between the third and fourth voltages.

[0114] Switch stack 910, capacitor 920, and resistors 930 and 940 cooperatively function with corresponding elements in the other pulse generator stages of pulse generator circuit 800, discussed above with reference to FIG. 8, to generate the voltage pulses across the positive and negative V_o output terminals of pulse generator circuit 800. These elements may, for example, cooperatively function as the corresponding elements discussed above with reference to pulse generator circuit 500 shown in FIGs. 5, 6A, and 6B. For example, these elements may cooperate to generate the voltage pulses across the positive and negative V_o output terminals of pulse generator circuit 800 in response to the power voltages applied to power input terminals V_1 and V_2 and to the control signals applied to the switches of switch stack 910.

[0115] Because the control signals are generated in response to the input pulses received across input port V_{in} of pulse generator circuit 700 illustrated in FIG. 7 through multiple stages of driving, the control signals cause all of the switches of the switch stacks of pulse generator circuit 700 to be turned on and to be turned off substantially simultaneously. For example, a 15V input pulse having a duration of, for example 100 ns, received at input port V_{in} of pulse generator circuit 700 may cause the pulse generator circuit 700 to generate a high-voltage (e.g. ~15 kV) output pulse having a duration of about 100 ns. Similarly, a 15V input pulse having a duration of, for example 5 μ s, received at input port V_{in} of pulse generator circuit 700 may cause the pulse generator circuit 700 to generate a high-voltage (e.g. ~15 kV) output pulse having a duration of about 5 μ s. Accordingly, the duration of the high-voltage output pulse is substantially the same as a selected duration of an input pulse.

[0116] FIG. 10 illustrates a switch driver 1000 which may be used as one of the switch drivers shown in FIG. 9.

[0117] Switch driver 1000 receives trigger pulses across input port V_{in} , and generates control signal pulses at output port V_{out} in response to the received trigger pulses. Switch driver 1000 includes amplifier circuit 1010, capacitor 1020, and transformer 1030. In some embodiments, switch driver 1000 also includes clamps circuits 1070.

[0118] Amplifier circuit 1010 receives the trigger pulses, and drives transformer 1030 through capacitor 1020, which reduces or blocks low frequency and DC signals. In response to being driven by amplifier circuit 1010, transformer 1030 generates control signal pulses at output port V_{out} , such that the duration of the control signal pulses is equal to or substantially equal (e.g. within 10% or 1%) to the duration of the trigger pulses at input port V_{in} .

[0119] In some embodiments, amplifier circuit 1010 includes multiple amplifier integrated circuits. For example, for increased current driving capability, multiple amplifier integrated circuits may be connected in parallel to form amplifier circuit 1010. For example, 2, 3, 4, 5, 6, 7, 8 or another number of amplifier integrated circuits may be used.

[0120] In some embodiments, clamp circuits 1070 are included at least to dampen potential signals, which may otherwise be caused by resonance. Clamp circuits 1070 include parallel diodes, which provide a short-circuit path for any current reversal, and also clamp the maximum voltage across the components connected to the clamp circuits 1070.

[0121] In some embodiments, the drivers 750, 850, and 1000 receive power from a DC-DC power module which is isolated from the power supply for the Marx generator. This ensures the cutoff of ground coupling.

5 [0122] In some embodiments, transformer 1030 has a 1:1 turns ratio. In alternative embodiments, a different turns ratio is used.

[0123] In some embodiments, in order to obtain very fast switching, the transformers 1030 has fewer than 5 turns in the primary winding and fewer than 5 turns in the secondary winding. For example, in some embodiments, the transformer 1030 has 1, 2, 3, or 4 turns in each of the primary and secondary windings. In some embodiments, the transformer 1030 has
10 less than a complete turn, for example, $\frac{1}{2}$ turn in the primary and secondary windings. The low number of turns in each of the primary and secondary windings allows for a low inductance loop and increases the current risetime in the secondary winding, which charges the input capacitance of the MOSFET switches.

[0124] Transformers for triggering MOSFETs in conventional applications require high
15 coupling, high permeability, and a low-loss core in order to ensure current transfer efficiency. From pulse to pulse, the residual flux in the core needs to be cleared in order to avoid saturation when the transformer is operated at high frequency. Conventionally, a resetting circuit, which involves a third winding, to dissipate the core energy is used.

[0125] In some embodiments, lossy transformers, such as that typically used as an
20 electromagnetic interference (EMI) choke to confine high frequency signals and dissipate their energy as heat are used to trigger the switches. For example, the transformers may have a voltage time constant less than $100\text{V}\mu\text{s}$. In some embodiments, the Transformers have a voltage time constant less than $50\text{V}\mu\text{s}$, $30\text{V}\mu\text{s}$, $20\text{V}\mu\text{s}$, $10\text{V}\mu\text{s}$, or $5\text{V}\mu\text{s}$. The use of the lossy transformer is contrary to the common practice in power electronics.

25 [0126] Although the high frequency flux is dampened due to the loss of the core (eddy loss, hysteresis loss, and resistive loss), the lossy transformers still allow sufficient confinement of the magnetic flux and provides sufficient coupling. In addition, the flux also decreases quickly in response to the signal on the primary winding being removed. The flux decay process usually takes approximately several microseconds.

30 [0127] Having such a transformer conventionally seems disadvantageous, but for coupling nanosecond to a few microsecond pulses, such a transformer is preferably used.

Consequently, the following benefits are achieved: 1) high voltage, high frequency transient coupling from the high-voltage Marx generators to the low-voltage drivers is suppressed; 2) because of the loss in the transformer cores, the residual flux from previous pulses are dissipated faster than common low-loss transformer cores, such that the resetting winding is not needed and is not present.

[0128] A benefit of the switch driver 1000 is that it limits the output pulse duration. Because the switch control signals are generated by transformer 1030, even if circuitry generating the input trigger signals at input port V_{in} were to generate a pulse of indefinite length, the transformer would saturate, causing the control signals to turn off the switches.

10 [0129] FIG. 11 illustrates an example of a switch element 1100 comprising components which may be used in the switch stacks discussed here. Switch element 1100 includes switch 1110, and selectively forms a conductive or low resistance path between terminals VA and VB in response to a control voltage applied to input port V_{in} .

[0130] In some embodiments, switch 1110 is a transistor, such as a MOSFET. In some 15 embodiments, switch 1110 is another type of switch. In some embodiments, switch 1110 has a turn on time of less than 5 ns, about 5 ns, about 10 ns, about 25 ns, about 15 ns, about 75 ns, about 100 ns, or greater than 100 ns.

[0131] In some embodiments, switch element 1100 also includes snubber circuit 1120. In some embodiments, the turn on times of the switches of the switch stacks are not identical. In 20 order to prevent voltages greater than that which switch 1110 can tolerate, snubber circuit 1120 provides a current shunt path bypassing switch 1110. Diodes 1122 provide a low-frequency current path, and the combination of the capacitor 1126 and resistor 1124 provide a high-frequency current path.

[0132] In some embodiments, switch element 1100 also includes optional overcurrent 25 protection circuit 1140. Overcurrent protection circuit 1140 includes switch 1142 and sense resistor 1144.

[0133] Current flowing from terminal VA to terminal VB is conducted through sense resistor 1144. Accordingly, a voltage is generated across sense resistor 1144 when the current flows from terminal VA to terminal VB. The generated voltage controls a conductive state of 30 switch 1142. If the current flowing from terminal VA to terminal VB is greater than a threshold, the generated voltage causes the switch 1142 to conduct. As a result, switch 1142

reduces the control voltage of switch 1110. In response to the reduced control voltage, switch 1110 becomes less conductive or turns off. Consequently, the current which may be conducted from terminal VA to terminal VB is limited by overcurrent protection circuit 1140.

5 [0134] In some embodiments, a current limiting resistor is placed between the gate of switch 1110 and the drain of switch 1142 to prevent switch 1142 from experiencing current greater than that which would cause damage.

[0135] In the embodiments discussed herein, MOSFET switches are used. In alternative embodiments, other switches are used. For example, in some embodiments, thyristors, IGBTs or other semiconductor switches are used.

10 [0136] An example of the operation of the transformer is illustrated in FIG. 12. The voltage at the input primary inductor is substantially a square waveform, but the voltage at the secondary inductor, which is the MOSFET's gate-source voltage, tapers as the voltage magnitude decreases toward zero, for example, within a period of several microseconds. After a reduction in voltage at the secondary inductor due to transformer saturation, the
15 switch receiving the voltage enters a linear region of operation from a saturation region of operation when the voltage is lower than the fully enhanced V_{gs} . As a result, the resistance of the switch increases and the output voltage across the load also shows a tapered profile. When the voltage at the secondary inductor decreases to a value less than the turn-on threshold of a MOSFET (V_{th}), the MOSFET will be shut off. Once the MOSFET is off, even
20 if the duration of the trigger signal is extended, the switch no longer conducts and can be considered an open circuit. The waveform of the voltage at the secondary inductor therefore limits the duration of high voltage output pulses from each panel, for example, to be several microseconds or less.

[0137] In some embodiments, the duration of the trigger signal is short enough that the
25 switches remain in saturation because the reduction in voltage at the secondary inductor is insufficient to cause the switches to enter linear region operation. In such embodiments, the load voltage pulses do not exhibit the tapering illustrated in FIG. 12. For example, in such embodiments the load voltage pulses may be substantially square.

[0138] In some embodiments, the switch stacks discussed herein include switches, as
30 discussed above, as well as other components.

[0139] In some embodiments, when generating pulses of a duration less than a threshold, the shape of the pulses are substantially square. In some embodiments, when generating pulses of the duration greater than a threshold, the shape of the pulses are substantially square for a duration substantially equal (e.g. within 10% or 1%) to the threshold. During the time
5 after the threshold, the voltage of such long pulses drops toward 0 V. In some embodiments, the drop toward 0 V is substantially linear. In some embodiments, the drop toward 0 V is substantially exponential.

[0140] FIG. 13 illustrates an alternative pulse generator circuit 1300 which may be used inside nsPEF system 100 of FIG. 1.

10 [0141] Pulse generator circuit 1300 receives input pulses across input port V_{in} and DC voltages at input ports V_{DC1} and V_{DC2} , and generates output pulses across output port V_{out} in response to the received input pulses and DC voltages.

[0142] Pulse generator circuit 1300 includes multiple pulse generator circuits 1310 and 1320. In this embodiment, two pulse generator circuits are used. In alternative embodiments,
15 more pulse generator circuits are used. For example, in some embodiments, 3, 4, 5, 10 or another number of pulse generator circuits having their output ports serially connected, as discussed below with reference to pulse generator circuit 1300, are used.

[0143] Each of pulse generator circuits 1310 and 1320 may be similar to the other pulse generator circuits discussed herein. For example pulse generator circuits 1310 and 1320 may
20 be similar to or may be substantially identical to pulse generator circuit 700 discussed above with reference to FIG. 7.

[0144] Each of pulse generator circuits 1310 and 1320 receive the same input pulse signal across their respective Control In input ports. In response, each of pulse generator circuits 1310 and 1320 generate high voltage pulses across their respective V_{out} output ports.
25 Because the V_{out} output ports of pulse generator circuits 1310 1320 are serially connected, the voltage pulse generated by pulse generator circuits 1310 and 1320 across output port V_{out} of pulse generator circuit 1300 is substantially equal (e.g. within 10% or 1%) to the sum of the voltages of the pulses respectively generated by pulse generator circuits 1310 and 1320.

[0145] FIG. 14 illustrates an alternative pulse generator circuit 1400 which may be used
30 inside nsPEF system 100 of FIG. 1, and which has characteristics similar to the pulse

generator 1300 of FIG. 13. Pulse generator circuit 1400 includes pulse generators 1410 and 1420, drivers 1415 and 1425, and power supplies 1412 and 1422.

[0146] Pulse generator circuit 1400 includes multiple pulse generator circuits 1410 and 1420. In this embodiment, two pulse generator circuits are used. In alternative embodiments, 5 more pulse generator circuits are used. Each of pulse generator circuits 1410 and 1420 may be similar to the other pulse generator circuits discussed herein.

[0147] Pulse generator circuit 1400 receives input pulses at each of drivers 1415 and 1425, which may be similar to driver 850 discussed above with reference to FIG. 8. Pulse generator circuit 1400 generates output pulses across output port Vout in response to the received input 10 pulses. The output voltage pulses are also based on power voltages received from power supplies 1412 and 1422.

[0148] Each of drivers 1415 and 1425 receive an input pulse signal. In response to the received input signals, drivers 1415 and 1425 respectively generate driving signal pulses for pulse generator circuits 1410 and 1420. In response to the driving signal pulses, each of pulse 15 generator circuits 1410 and 1420 generate high voltage pulses across their respective output ports Vo1 and Vo2. Because the Vo1 and Vo2 output ports of pulse generator circuits 1410 and 1420 are serially connected, the voltage pulse generated by pulse generator circuits 1410 and 1420 across output port Vout of pulse generator circuit 1400 is substantially equal (e.g. within 10% or 1%) to the sum of the voltages of the pulses respectively generated by pulse 20 generator circuits 1410 and 1420.

[0149] In this embodiment, pulse generator circuit 1410 generates a high voltage pulse across its output port Vo1 which is substantially equal (e.g. within 10% or 1%) to three times the voltage of power supply 1412, $(-3 \times [V_1 - V_2])$. In addition, pulse generator circuit 1420 generates a high voltage pulse across its output port Vo2 which is substantially equal (e.g. 25 within 10% or 1%) to three times the voltage of power supply 1414 $(3 \times [V_1 - V_2])$. As a result, pulse generator circuit 1400 generates a voltage of $(3 \times [V_1 - V_2]) - (-3 \times [V_1 - V_2])$ across its output port Vout.

[0150] In some embodiments, a single driver circuit connected to both pulse generator circuit 1410 and 1420 is used instead of drivers 1415 and 1425. In such embodiments, the 30 single driver circuit generates driving signal pulses for both pulse generator circuits 1410 and 1420 in response to an input pulse signal.

[0151] FIG. 15 is a block diagram of a nsPEF treatment system 1550, which has characteristics similar to or identical to those of nsPEF system 100 illustrated in FIG. 1. NsPEF treatment system 1550 includes pulse generator 1555, power supply 1560, electrode 1565, interface 1570, and controller 1575.

5 [0152] Pulse generator 1555 may be similar or identical to any of the pulse generator circuits discussed herein. For example, pulse generator 1555 may be configured to generate pulses having a voltage magnitude corresponding with power voltages received from power supply 1560 and having pulse widths and other characteristics corresponding with control signals received from controller 1575. In alternative embodiments, other
10 pulse generator circuits may be used.

[0153] Electrode 1565 may be similar or identical to any of the electrodes discussed herein. For example, electrode 1565 may be similar or identical to electrodes 300 and 400 discussed above with reference to FIGs. 3 and 4. Electrode 1565 is configured to receive nsPEF pulses generated by pulse generator 1555 from conductor 1556 and is configured to deliver nsPEF
15 pulses to a patient undergoing therapeutic nsPEF treatment. In alternative embodiments, other therapeutic electrodes may be used.

[0154] Sensor 1566 may include one or more of a thermocouple, a voltage probe, a current probe, an impedance probe, a capacitance probe, a light sensor, a humidity sensor, a tissue monitoring probe, and a chemical analysis probe. Sensor 1566 may be configured to sense
20 one or more characteristics of the patient, the electrode 1565, the nsPEF pulses delivered by the electrode 1565, and effects of the nsPEF pulses delivered by the electrode 1565.

[0155] Power supply 1560 is configured to provide power voltages to pulse generator 1555. For example, in embodiments where pulse generator 1555 is similar to pulse generator circuit 700 illustrated in FIG. 7, power supply 1560 may be configured to provide power voltages
25 corresponding with power voltages V1 and V2 of pulse generator circuit 700. In some embodiments, power supply 1560 generates and provides power voltages which have a voltage level corresponding with a control signal from controller 1575.

[0156] Interface 1570 is configured to receive input from a user identifying various parameters and characteristics of the nsPEF pulses to be applied to the patient. For example,
30 interface 1570 may be configured to receive input identifying or specifying values for one or more characteristics of one or more nsPEF pulses to be applied to the patient. For example,

the characteristics may include one or more of an amplitude, a polarity, a width, a rise time, and a fall time of one or more nsPEF pulses to be applied to the patient. Additionally or alternatively, the characteristics may include one or more of a frequency and a pulse quantity of a sequence of nsPEF pulses to be applied to the patient. Furthermore, the characteristics
5 may additionally or alternatively include a result of the nsPEF pulses to be applied to the patient, such as a maximum temperature for the treated tissue of the patient. Other characteristics may additionally or alternatively be identified or specified by the received input.

[0157] In addition, interface 1570 is configured to communicate the characteristics
10 identified or specified by the received input to controller 1575.

[0158] Controller 1575 is configured to generate and provide one or more control signals to pulse generator 1555 and to power supply 1560 based at least partly on the communicated characteristics received from interface 1570. Additionally, pulse generator 1555, power supply 1560, and electrode 1565 are collectively configured to, in response to the control
15 signals from controller 1575, generate nsPEF pulses having characteristics corresponding with the control signals.

[0159] In this embodiment, one or more of pulse generator 1555, electrode 1565, and sensor 1566 are configured to generate corresponding feedback signals FB1, FB2, and FB3 representing measured parametric characteristics of the nsPEF pulses applied to the patient or
20 other signals of nsPEF treatment system 1550. In some embodiments, the parametric characteristics of the nsPEF pulses represented by the feedback signals FB1, FB2, and FB3 include one or more of an amplitude, a polarity, a width, a rise time, and a fall time of the nsPEF pulses. In some embodiments, the parametric characteristics of the nsPEF pulses represented by the feedback signals, FB2, and FB3 additionally or alternatively include one
25 or more of current and voltage applied to the tissue so that one or more of tissue impedance, tissue inductance, tissue capacitance, instantaneous power applied to the tissue, and energy applied to the tissue may be calculated. In some embodiments, the parametric characteristics represented by the feedback signal FB1 may additionally or alternatively include one or more
30 of a voltage at a capacitor being charged during a charge mode of pulse generator 1555, voltage and/or current characteristics of a control signal of pulse generator 1555, voltage and/or current characteristics of a power supply signal of pulse generator 1555, voltage and/or current characteristics of a pulse generated by pulse generator 1555, and voltage

and/or current characteristics of another input, output, or internal signal of pulse generator 1555. Additionally or alternatively, the parametric characteristics may include a frequency of a sequence of nsPEF pulses. Furthermore, the parametric characteristics may additionally or alternatively include a temperature of the treated tissue of the patient. The feedback signals, FB2, and FB3 may correspond or represent other measured parametric characteristics of one or more of the nsPEF pulses applied to the patient, the patient, the environment, and the nsPEF treatment system 1550.

[0160] In some embodiments, controller 1575, power supply 1560, pulse generator 1555, electrode 1565, and optionally sensor 1566 collectively form a feedback loop which causes one or more parametric characteristics of the nsPEF pulses applied to the patient to have measured values substantially equal (e.g. within 10% or 1%) to the values of corresponding characteristics identified in the input received by interface 1570.

[0161] For example, interface 1570 may receive input specifying a value of 15kV for an amplitude of the nsPEF pulses applied to the patient. In addition, the controller 1575 may be configured to, in response to a feedback signal FB2 from electrode 1565, a feedback signal FB1 from pulse generator 1555, or a feedback signal FB3 from sensor 1566 indicating that the measured amplitude of the nsPEF pulses applied to the patient is less than (or greater than) 15kV, change a control signal provided to power supply 1560. In response to the changed control signal, power supply 1560 may be configured to increase (or decrease) the voltage of power signals provided to pulse generator 1555 such that the amplitude of the nsPEF pulses generated and applied to the patient increases (or decreases) to or toward 15kV.

[0162] Similarly, interface 1570 may receive input specifying a value of 150ns for a pulse width of the nsPEF pulses applied to the patient. The controller 1575 may be configured to, in response to a feedback signal FB3 from sensor 1566, a feedback signal FB2 from electrode 1565, or a feedback signal FB1 from pulse generator 1555 indicating that the measured pulse width of the nsPEF pulses applied to the patient is greater than (or less than) 150ns, change a control signal provided to pulse generator 1555. In response to the changed control signal, pulse generator 1555 may be configured to generate and apply to the patient nsPEF pulses having decreased (or increased) pulse width. As a result, one or more of the feedback signals FB1, FB2, and FB3 causes the controller 1575 to generate control signals which cause the pulse generator 1555 to generate and apply nsPEF pulses having pulse widths decreased (or increased) to or toward 150ns.

[0163] In some embodiments, the feedback loop is controlled using a Proportional-Integral-Derivative (PID) method. For example, using the PID method, controller 1575 may be configured to continuously or substantially continuously calculate an error value as the difference between a desired value perceived at interface 1570 and a corresponding measured parameter. In addition, using the PID method, controller 1575 may be configured to continuously or substantially continuously calculate the control signals as a sum of one or more of: a first constant times the error signal, a second constant times an integral of the error signal, and a third constant times a derivative of the error signal, where the first, second, and third constants may be positive, negative, or equal to zero. Other custom or standard control methods may additionally or alternatively be used.

[0164] In some embodiments, the feedback loop is controlled using a lookup table to determine a next value based on a measured value. In some embodiments, the feedback loop is controlled by reducing or increasing a value by a fixed amount or step size based on a determination of whether a measured value is greater than or less than a threshold.

[0165] FIG. 16 illustrates an alternative pulse generator 1600 which may be used as pulse generator 1555 of nsPEF treatment system 1550 illustrated in FIG. 15. Pulse generator 1600 may have features similar to or identical to other pulse generator circuits discussed herein. For example, pulse generator circuit 1600 may have features similar to or identical to pulse generator circuit 700 of FIG. 7.

[0166] For example, pulse generator 1600 includes the driver circuit 1650 which may be similar to or identical to driver 750 of pulse generator circuit 700. In addition, pulse generator 1600 includes pulse generator circuits 1610, 1620, 1630, and 1640, which may respectively be similar or identical to pulse generator circuits 710, 720, 730, and 740.

[0167] Pulse generator 1600 also includes, or in some embodiments is connected to, analog-to-digital converter 1660. Furthermore, pulse generator 1600 additionally or alternatively includes, or in some embodiments is connected to, current monitors 1670 and 1680.

[0168] In this embodiment, analog-to-digital (A/D) converter 1660 includes a first channel having inputs which are respectively connected to the positive (+) and negative (-) voltage output terminals of pulse generator 1600. In some embodiments, a first low input impedance differential buffer (not shown) is connected to the positive (+) and negative (-) voltage output terminals of pulse generator 1600, and drives the inputs of analog-to-digital converter 1660.

In some embodiments, a probe, such as a Tektronix P6015A Passive High Voltage Probe (not shown) is connected to the positive (+) and negative (-) voltage output terminals of pulse generator 1600, and drives the inputs of analog-to-digital converter 1660.

[0169] In some embodiments, only the positive (+) voltage output terminal is connected to analog-to-digital converter 1660. In some embodiments, the positive (+) voltage output terminal is connected to analog-to-digital converter 1660 through a voltage divider. In such embodiments, the voltage at the positive (+) voltage output terminal is ground referenced, and the ground is also connected to analog-to-digital converter 1660. For example, the positive (+) voltage output terminal is ground referenced if the negative (-) voltage output terminal of pulse generator 1600 is at the ground voltage.

[0170] In addition, analog-to-digital converter 1660 is configured to generate a first digital output representing the voltage difference between the positive (+) and negative (-) voltage output terminals of pulse generator 1600. When used in the nsPEF treatment system 1650 of FIG. 15, the first digital output may be used as a feedback signal for controller 1675. In some embodiments, analog-to-digital converter 1660 generates the first digital output based on either, but not both, of the voltages at the positive (+) and negative (-) voltage output terminals.

[0171] In this embodiment, analog-to-digital converter 1660 also includes a second channel having inputs which are respectively connected to the current monitors 1670 and 1680, and the current monitors 1670 and 1680 are respectively connected to the positive (+) and negative (-) voltage output terminals of pulse generator 1600. In some embodiments, a second low input impedance differential buffer (not shown) is connected to the current monitors 1670 and 1680, and drives the inputs of analog-to-digital converter 1660.

[0172] In addition, analog-to-digital converter 1660 is configured to generate a second digital output representing the current difference between the currents flowing through positive (+) and negative (-) voltage output terminals of pulse generator 1600. When used in the nsPEF treatment system 1550 of FIG. 15, the second digital output may be used as a feedback signal for controller 1575. In some embodiments, analog-to-digital converter 1660 generates the second digital output based on either, but not both, of inputs from the current monitors 1670 and 1680.

[0173] In some embodiments, current monitors 1670 and 1680 each include a sense resistor and an amplifier. The sense resistor is configured to generate a voltage response of the

current flowing therethrough, and the amplifier generates an input for the analog-to-digital converter based on the voltage across the sense resistor.

5 [0174] In some embodiments, current monitors 1670 and 1680 include a current monitor, such as Pearson Current Monitor 2878, which generates a voltage in response to a sensed current.

[0175] In some embodiments, pulse generator 1600 generates either, but not both, of the first and second digital outputs. In some embodiments, one or more single channel analog-to-digital converters are used instead of or in addition to analog-to-digital converter 1660.

10 [0176] In some embodiments, only single current monitor is used. The single current monitor may monitor the current of either of the positive (+) and negative (-) voltage output terminals of pulse generator 1600.

[0177] FIG. 17 is a schematic illustration of an electrode 1700 which may, for example, be used as electrode 1565 in nsPEF treatment system 1550 of FIG. 15. Electrode 1700 may be similar or identical to any of the electrodes discussed herein. For example, electrode 1700
15 may be similar or identical to electrodes 300 and 400 discussed above with reference to FIGs. 3 and 4.

[0178] Electrode 1700 is configured to receive nsPEF pulses across input terminals 1710 and 1720 and to deliver nsPEF pulses to a patient undergoing therapeutic nsPEF treatment through positive (+) and negative (-) output therapeutic electrodes 1730 and 1740.

20 [0179] Electrode 1700 includes, or in some embodiments is connected to, analog-to-digital converter 1750. Furthermore, electrode 1700 additionally or alternatively includes, or in some embodiments is connected to, current monitors 1760 and 1770. In addition, electrode 1700 includes thermal sensors 1780 and 1790. In some embodiments, electrode 1700 includes either but not both of thermal sensors 1780 and 1790.

25 [0180] In this embodiment, analog-to-digital converter 1750 includes a first channel having inputs which are respectively connected to the positive (+) and negative (-) voltage output therapeutic electrodes 1730 and 1740. In some embodiments, a first low input impedance differential buffer (not shown) is connected to the positive (+) and negative (-) voltage output therapeutic electrodes 1730 and 1740 and drives the inputs of the first channel of analog-to-
30 digital converter 1750. In some embodiments, a probe, such as a Tektronix P6015A Passive High Voltage Probe (not shown) is connected to the positive (+) and negative (-) voltage

output therapeutic electrodes 1730 and 1740, and drives the inputs of analog-to-digital converter 1750.

[0181] In addition, analog-to-digital converter 1750 is configured to generate a first digital output at output terminal 1735 representing the voltage difference between the positive (+) and negative (-) voltage output therapeutic electrodes 1730 and 1740. When used in the nsPEF treatment system 1650 of FIG. 15, the first digital output may be used as a feedback signal for controller 1575. In some embodiments, analog-to-digital converter 1750 generates the first digital output based on either, but not both, of the voltages at the positive (+) and negative (-) voltage output therapeutic electrodes 1730 and 1740.

10 [0182] In this embodiment, analog-to-digital converter 1750 also includes a second channel having inputs which are respectively connected to the current monitors 1760 and 1770, and the current monitors 1760 and 1770 are respectively connected to the positive (+) and negative (-) voltage output therapeutic electrodes 1730 and 1740. In some embodiments, a second low input impedance differential buffer (not shown) is connected to the current
15 monitors 1760 and 1770 and drives the inputs of the second channel of analog-to-digital converter 1750.

[0183] In addition, analog-to-digital converter 1750 is configured to generate a second digital output at output terminal 1765 representing the current difference between the currents flowing through positive (+) and negative (-) voltage output therapeutic electrodes 1730 and
20 1740. When used in the nsPEF treatment system 1550 of FIG. 15, the second digital output may be used as a feedback signal for controller 1575. In some embodiments, analog-to-digital converter 1750 generates the second digital output based on either, but not both, of inputs from the current monitors 1760 and 1770.

[0184] In this embodiment, analog-to-digital converter 1750 also includes a third channel
25 having inputs which are respectively connected to the thermal sensors 1780 and 1790, and the thermal sensors 1780 and 1790 are respectively thermally coupled to the positive (+) and negative (-) voltage output therapeutic electrodes 1730 and 1740.

[0185] In some embodiments, a third low input impedance differential buffer (not shown) is connected to the thermal sensors 1780 and 1790, and drives the inputs of the third channel
30 of analog-to-digital converter 1750.

[0186] Analog-to-digital converter 1750 may be configured to generate a third digital output at output terminal 1785 representing a temperature of at least one of positive (+) and negative (-) voltage output therapeutic electrodes 1730 and 1740. When used in the nsPEF treatment system 1550 of FIG. 15, the third digital output may be used as a feedback signal
5 for controller 1575. In some embodiments, analog-to-digital converter 1750 generates the third digital output based on either, but not both, of inputs from the thermal sensors 1780 and 1790.

[0187] In some embodiments, the thermal sensors 1780 and 1790 are not coupled to the positive (+) and negative (-) voltage output therapeutic electrodes 1730 and 1740, but are,
10 instead, coupled to first and second pins which contact the patient. In such embodiments, the first and second pins may contact the patient to sense tissue temperature, and the therapeutic electrodes 1730 and 1740 may contact the patient to deliver nsPEF pulses.

[0188] In some embodiments, one or more thermal sensors separate from electrode 1565 contact the patient and have a cable providing thermal information to controller 1575, where
15 at least part of the cable is different from the cable connecting electrode 1565 and controller 1575.

[0189] In some embodiments, electrode 1565 includes at least one laser thermometer, such as an IR laser thermometer, which provides thermal information corresponding with that of thermal sensors 1780 and 1790.

[0190] In various embodiments, pulse generator 1700 generates any one, two, or all of the first, second, and third digital outputs. In some embodiments, one or more single channel analog-to-digital converters are used instead of or in addition to analog-to-digital converter
20 1750.

[0191] FIG. 18 is a flowchart illustration of a method 1800 of using an nsPEF treatment
25 system, such as nsPEF treatment system 1550 of FIG. 15. In the method, the nsPEF treatment system implements a feedback loop to control a parameter of the treatment. Because of one or more factors including, but not limited to, manufacturing variation, temperature, and system age, realized or measured parameters during treatment tend to have values somewhat different from the corresponding values with which the system was programmed. To increase
30 accuracy of the system, the feedback loop actively measures and controls realized parameters so that the measured parameters more closely match the desired or programmed values.

[0192] At 1810, information representing one or more desired characteristics of a patient or of nsPEF pulses to be applied to the patient is received at an interface, such as interface 1570 of nsPEF treatment system 1550.

[0193] At 1820, a controller, such as controller 1575 of nsPEF treatment system 1550,
5 generates control values corresponding with the values of the desired characteristics received at the interface.

[0194] At 1830, a power supply, such as power supply 1560 of nsPEF treatment system 1550, charges a pulse generator, such as pulse generator 1555 of nsPEF treatment system 1550. The power supply charges the pulse generator with a voltage value determined based
10 on one or more control signals received from the controller, where the received one or more control signals correspond with one or more control values generated at 1820.

[0195] At 1840, at least one nsPEF pulse is generated. In some embodiments, the at least one generated nsPEF pulse is applied to the patient. For example, in response to one or more control signals from the controller, the pulse generator may generate the nsPEF pulse. In
15 addition, an electrode, such as electrode 1565, may apply the nsPEF pulse to the patient. In some embodiments, the nsPEF pulse is applied to the patient as part of a treatment regimen. In some embodiments, the nsPEF pulse is applied to the patient as part of a characterization, set up, or calibration of the nsPEF treatment system. In some embodiments, the nsPEF pulse is not applied to the patient.

[0196] At 1850, one or more electrical characteristics of the nsPEF pulse or characteristics
20 of the patient are measured or sensed, for example, while the nsPEF pulse is applied to the patient.

[0197] At 1860, a value of the measured or sensed characteristic is compared with the value of a corresponding desired characteristic as represented by the received information at
25 1810.

[0198] Returning to 1820, the controller modifies the control values corresponding with the values of the desired characteristics received at the interface according to the results of the comparison performed at 1860. The controller is configured to modify the control values so that, because of the modification to the control value, the value of a next measured or sensed
30 characteristic is expected to be closer to the desired value of the characteristic than the value of the previously measured or sensed characteristic.

[0199] In some embodiments, the measured or sensed characteristic may include electrical characteristics of the nsPEF pulse, such as amplitude, pulse width, frequency, current, pulse shape, power, and energy. In some embodiments, a statistical value of one or more of amplitude, pulse width, frequency, current, pulse shape, power, and energy, such as average, standard deviation, median, minimum, and maximum is additionally or alternatively used. In some embodiments, an instantaneous value of one or more of amplitude, pulse width, frequency, current, pulse shape, power, and energy, such as average, standard deviation, median, minimum, and maximum is additionally or alternatively used. Other measured or sensed characteristics may be used.

10 [0200] In some embodiments, the measured or sensed characteristic may additionally or alternatively include an effect of the nsPEF pulse, such as a temperature of the tissue of the patient, a conductivity of the tissue of the patient, and arcing at the nsPEF pulse delivery electrode.

15 [0201] In some embodiments, the measured or sensed characteristic may additionally or alternatively include an environmental characteristic, such as a temperature, a humidity, and a chemical concentration.

[0202] FIG. 19 is a flowchart illustration of a method 1900 of using an nsPEF treatment system, such as nsPEF treatment system 1550 of FIG. 15.

20 [0203] At 1910, information representing a current or voltage amplitude of nsPEF pulses to be applied to the patient is received at an interface, such as interface 1570 of nsPEF treatment system 1550.

[0204] At 1920, a controller, such as controller 1575 of nsPEF treatment system 1550, generates control values corresponding with the desired amplitude.

25 [0205] At 1930, a power supply, such as power supply 1560 of nsPEF treatment system 1550, charges a pulse generator, such as pulse generator 1555 of nsPEF treatment system 1550. The power supply charges the pulse generator with a voltage value determined based on one or more control signals received from the controller, where the received one or more control signals correspond with one or more control values generated at 1920.

30 [0206] At 1940, at least one nsPEF pulse is generated. In some embodiments, the at least one generated nsPEF pulse is applied to the patient. For example, in response to one or more control signals from the controller, the pulse generator may generate an nsPEF pulse. In

addition, an electrode, such as electrode 1565, may apply the nsPEF pulse to the patient. In some embodiments, the nsPEF pulse is applied to the patient as part of a treatment regimen. In some embodiments, the nsPEF pulse is applied to the patient as part of a characterization, set up, or calibration of the nsPEF treatment system. In some embodiments, the nsPEF pulse
5 is not applied to the patient.

[0207] At 1950, the amplitude of the nsPEF pulse is measured or sensed, for example, while the nsPEF pulse is applied to the patient.

[0208] At 1960, a value of the measured or sensed amplitude is compared with the amplitude as represented by the received information at 1910.

10 [0209] Returning to 1920, the controller modifies the control values corresponding with the values of the desired amplitude received at the interface according to the results of the comparison performed at 1960. The controller is configured to modify the control values so that if the measured or sensed value of the amplitude is less than the desired amplitude, the modified control values will cause the power supply to charge the pulse generator with a
15 voltage of greater value than previously used. Likewise, the controller is additionally configured to modify the control values so that if the measured or sensed value of the amplitude is greater than the desired amplitude, the modified control values will cause the power supply to charge the pulse generator with a voltage of less value than previously used.

[0210] FIG. 20 is a flowchart illustration of a method 2000 of using an nsPEF treatment
20 system, such as nsPEF treatment system 1550 of FIG. 15.

[0211] At 2010, information representing a pulse width of nsPEF pulses to be applied to the patient is received at an interface, such as interface 1570 of nsPEF treatment system 1550.

[0212] At 2020, a controller, such as controller 1575 of nsPEF treatment system 1550, generates control values corresponding with the desired pulse width.

25 [0213] At 2030, a power supply, such as power supply 1560 of nsPEF treatment system 1550, charges a pulse generator, such as pulse generator 1555 of nsPEF treatment system 1550. The power supply charges the pulse generator with a voltage value determined based on one or more control signals received from the controller.

[0214] At 2040, at least one nsPEF pulse is generated. In some embodiments, the at least
30 one generated nsPEF pulse is applied to the patient. For example, in response to one or more

control signals from the controller, the pulse generator may generate an nsPEF pulse. In addition, an electrode, such as electrode 1565, may apply the nsPEF pulse to the patient. In some embodiments, the nsPEF pulse is applied to the patient as part of a treatment regimen. In some embodiments, the nsPEF pulse is applied to the patient as part of a characterization, set up, or calibration of the nsPEF treatment system. In some embodiments, the nsPEF pulse is not applied to the patient.

[0215] At 2050, the pulse width of the nsPEF pulse is measured or sensed, for example, while the nsPEF pulse is applied to the patient.

[0216] At 2060, a value of the measured or sensed pulse width is compared with the pulse width as represented by the received information at 2010.

[0217] Returning to 2020, the controller modifies the control values corresponding with the values of the desired pulse width received at the interface according to the results of the comparison performed at 2060. The controller is configured to modify the control values so that if the measured or sensed value of the pulse width is less than the desired pulse width, the modified control values will cause the pulse generator to generate further nsPEF pulses with a pulse width of greater value than previously generated. Likewise, the controller is configured to modify the control values so that if the measured or sensed value of the pulse width is greater than the desired pulse width, the modified control values will cause the pulse generator to generate further nsPEF pulses having a pulse width of less value than previously generated.

[0218] FIG. 21 is a flowchart illustration of a method 2100 of using an nsPEF treatment system, such as nsPEF treatment system 1550 of FIG. 15.

[0219] At 2110, information representing a maximum tissue temperature of the patient being treated with nsPEF pulses is received at an interface, such as interface 1570 of nsPEF treatment system 1550.

[0220] At 2120, a controller, such as controller 1575 of nsPEF treatment system 1550, generates control values corresponding with the desired maximum tissue temperature.

[0221] At 2130, a power supply, such as power supply 1560 of nsPEF treatment system 1550, charges a pulse generator, such as pulse generator 1555 of nsPEF treatment system 1550. The power supply charges the pulse generator with a voltage value determined based on one or more control signals received from the controller.

[0222] At 2140, one or more nsPEF pulses are generated. In some embodiments, the generated nsPEF pulses are applied to the patient. For example, in response to one or more control signals from the controller, the pulse generator may generate the nsPEF pulses. In addition, an electrode, such as electrode 1565, may apply the nsPEF pulses to the patient. In some embodiments, the nsPEF pulses are applied to the patient as part of a treatment regimen. In some embodiments, the nsPEF pulses are applied to the patient as part of a characterization, set up, or calibration of the nsPEF treatment system. In some embodiments, the nsPEF pulses are not applied to the patient.

[0223] At 2150, the temperature of the patient is measured or sensed with a temperature sensor, for example, while the nsPEF pulses are applied to the patient.

[0224] At 2160, a value of the measured or sensed temperature is compared with the maximum temperature as represented by the received information at 2110.

[0225] Returning to 2120, the controller modifies the control values corresponding with the values of the desired maximum temperature received at the interface according to the results of the comparison performed at 2160. The controller is configured to modify the control values so that if the measured or sensed value of the temperature is greater than the maximum temperature or is greater than a threshold less than the maximum temperature, the modified control values will cause the nsPEF treatment system to deliver less power to the patient. For example, the modified control values may cause nsPEF pulses having less pulse width to be generated. Alternatively or additionally, the modified control values may cause nsPEF pulses with lower frequency to be generated. Alternatively or additionally, the modified control values may cause nsPEF pulses with lower voltage to be generated.

[0226] FIG. 22 is a flowchart illustration of a method 2200 of using an nsPEF treatment system, such as nsPEF treatment system 1550 of FIG. 15.

[0227] At 2210, information representing a desired pulse generator charge voltage is received, for example, at an interface, such as interface 1570 of nsPEF treatment system 1550. In some embodiments, the desired pulse generator charge voltage is received at a controller, such as controller 1575 of nsPEF treatment system 1550.

[0228] At 2220, a controller, such as controller 1575 of nsPEF treatment system 1550, generates control values corresponding with the desired pulse generator charge voltage.

[0229] At 2230, a power supply, such as power supply 1560 of nsPEF treatment system 1550, charges a pulse generator, such as pulse generator 1555 of nsPEF treatment system 1550. The power supply charges the pulse generator with a voltage value determined based on one or more control signals received from the controller.

5 [0230] At 2235, the charge of the pulse generator is sensed and compared with the desired pulse generator charge voltage. For example, the controller may sense the voltage of the charged pulse generator and compare the sensed voltage with the desired pulse generator charge voltage.

[0231] If the difference between the sensed voltage and the desired pulse generator voltage
10 falls outside an acceptable limit window, the method returns to 2220, where the controller generates new control values based on the desired pulse generator charge voltage and on the sensed voltage.

[0232] If the difference between the sensed voltage and the desired pulse generator voltage falls within an acceptable window, at 2140, one or more nsPEF pulses are generated. In some
15 embodiments, the generated nsPEF pulses are applied to the patient. For example, in response to one or more control signals from the controller, the pulse generator may generate the nsPEF pulses. In addition, an electrode, such as electrode 1565, may apply the nsPEF pulses to the patient. In some embodiments, the nsPEF pulses are applied to the patient as part of a treatment regimen. In some embodiments, the nsPEF pulses are applied to the patient as part
20 of a characterization, set up, or calibration of the nsPEF treatment system. In some embodiments, the nsPEF pulses are not applied to the patient.

[0233] FIG. 22 is a flowchart illustration of a method 2200 of using an nsPEF treatment system, such as nsPEF treatment system 1550 of FIG. 15. FIG. 23 is a flowchart illustration of a method 2300 of using an nsPEF treatment system, such as nsPEF treatment system 1550 of
25 FIG. 15.

[0234] At 2310, information representing at least one of a maximum energy and a maximum average power to be delivered to the patient being treated with nsPEF pulses is received at an interface, such as interface 1570 of nsPEF treatment system 1550.

[0235] At 2320, a controller, such as controller 1575 of nsPEF treatment system 1550,
30 generates control values corresponding with at least one of the desired maximum energy, the

desired maximum power, one or more desired characteristics of a patient, and one or more desired characteristics of nsPEF pulses to be applied to the patient.

[0236] At 2330, a power supply, such as power supply 1560 of nsPEF treatment system 1550, charges a pulse generator, such as pulse generator 1555 of nsPEF treatment system 5 1550. The power supply charges the pulse generator with a voltage value determined based on one or more control signals received from the controller.

[0237] At 2340, one or more nsPEF pulses are generated. In some embodiments, the generated nsPEF pulses are applied to the patient. For example, in response to one or more control signals from the controller, the pulse generator may generate the nsPEF pulses. In 10 addition, an electrode, such as electrode 1565, may apply the nsPEF pulses to the patient. In some embodiments, the nsPEF pulses are applied to the patient as part of a treatment regimen. In some embodiments, the nsPEF pulses are applied to the patient as part of a characterization, set up, or calibration of the nsPEF treatment system. In some embodiments, the nsPEF pulses are not applied to the patient.

[0238] At 2350, the energy of the nsPEF pulses is determined, for example, by measuring and integrating the product of instantaneous current and voltage values of the nsPEF pulses 15 applied to the patient. The measured or sensed energy may be added to previously determined energy values to determine a total energy applied to the patient. Alternatively or additionally, an average power for a time duration may be determined, for example, by dividing the total 20 energy delivered during the duration by the time of the duration.

[0239] At 2360, the processor may compare the total energy applied to the patient and the received maximum energy. Additionally or alternatively, the processor may compare the average power applied to the patient and the received maximum average power.

[0240] Returning to 2320, the controller modifies the control values according to the results 25 of the comparison performed at 2360. The controller is configured to modify the control values so that if the measured or sensed value of the energy or average power is greater than the maximum desired energy or desired average power, or is greater than a threshold less than the maximum desired energy or desired average power, the modified control values will cause the nsPEF treatment system to deliver less power to the patient. For example, the 30 modified control values may cause nsPEF pulses having less pulse width to be generated. Alternatively or additionally, the modified control values may cause nsPEF pulses with lower

frequency to be generated. Alternatively or additionally, the modified control values may cause nsPEF pulses with lower voltage to be generated.

[0241] FIG. 24 is a flowchart illustration of a method 2400 of using an nsPEF treatment system, such as nsPEF treatment system 1550 of FIG. 15.

5 [0242] At 2410, information representing a maximum tissue temperature of the patient being treated with nsPEF pulses is received at an interface, such as interface 1570 of nsPEF treatment system 1550.

[0243] At 2420, a controller, such as controller 1575 of nsPEF treatment system 1550, generates control values corresponding with the desired maximum tissue temperature.

10 [0244] At 2430, a power supply, such as power supply 1560 of nsPEF treatment system 1550, charges a pulse generator, such as pulse generator 1555 of nsPEF treatment system 1550. The power supply charges the pulse generator with a voltage value determined based on one or more control signals received from the controller.

[0245] At 2440, one or more nsPEF pulses are generated. In some embodiments, the
15 generated nsPEF pulses are applied to the patient. For example, in response to one or more control signals from the controller, the pulse generator may generate the nsPEF pulses. In addition, an electrode, such as electrode 1565, may apply the nsPEF pulses to the patient. In some embodiments, the nsPEF pulses are applied to the patient as part of a treatment regimen. In some embodiments, the nsPEF pulses are applied to the patient as part of a
20 characterization, set up, or calibration of the nsPEF treatment system. In some embodiments, the nsPEF pulses are not applied to the patient.

[0246] At 2450, the energy of the nsPEF pulses is determined, for example, by measuring and integrating instantaneous current and voltage values of the nsPEF pulses applied to the patient. The measured or sensed energy may be added to previously determined energy
25 values to determine a total energy applied to the patient. Alternatively or additionally, an average power for a time duration may be determined, for example, by dividing the total energy delivered during the duration by the time of the duration.

[0247] Based on one or more of the determined total energy and average power, a tissue temperature may be calculated. For example, temperature and thermal conductivity
30 characteristics of the surrounding tissue and environment may be known and used to calculate a tissue temperature based on these parameters and the determined total energy

and/or average power. Alternatively, a temperature measurement may be made, for example, using a thermocouple or a thermometer.

5 [0248] At 2460, the processor may compare the value of the calculated or measured temperature with the maximum temperature as represented by the received information at 2410.

10 [0249] Returning to 2420, the controller modifies the control values corresponding with the values of the desired maximum temperature received at the interface according to the results of the comparison performed at 2460. The controller is configured to modify the control values so that if the calculated or measured value of the temperature is greater than the maximum temperature or is greater than a threshold less than the maximum temperature, the modified control values will cause the nsPEF treatment system to deliver less power to the patient. For example, the modified control values may cause nsPEF pulses having less pulse width to be generated. Alternatively or additionally, the modified control values may cause nsPEF pulses with lower frequency to be generated. Alternatively or additionally, the modified control values may cause nsPEF pulses with lower voltage to be generated. In some embodiments of method 2400, tissue temperature corresponding with the calculated tissue temperature is not determined using a thermal sensor.

[0250] FIG. 25 is a flowchart illustration of a method 2500 of using an nsPEF treatment system, such as nsPEF treatment system 1550 of FIG. 15.

20 [0251] At 2510, information representing one or more desired characteristics of a patient or of nsPEF pulses to be applied to the patient is received at an interface, such as interface 1570 of nsPEF treatment system 1550.

25 [0252] At 2520, a controller, such as controller 1575 of nsPEF treatment system 1550, generates control values corresponding with the values of the desired characteristics received at the interface.

[0253] At 2530, a power supply, such as power supply 1560 of nsPEF treatment system 1550, charges a pulse generator, such as pulse generator 1555 of nsPEF treatment system 1550. The power supply charges the pulse generator with a voltage value determined based on one or more control signals received from the controller, where the received one or more control signals correspond with one or more control values generated at 2520.

[0254] At 2540, at least one nsPEF pulse is generated. In some embodiments, the at least one generated nsPEF pulse is applied to the patient. For example, in response to one or more control signals from the controller, the pulse generator may generate the nsPEF pulse. In addition, an electrode, such as electrode 1565, may apply the nsPEF pulse to the patient. In some embodiments, the nsPEF pulse is applied to the patient as part of a treatment regimen. In some embodiments, the nsPEF pulse is applied to the patient as part of a characterization, set up, or calibration of the nsPEF treatment system. In some embodiments, the nsPEF pulse is not applied to the patient.

[0255] At 2550, one or more electrical characteristics of the nsPEF pulse or of the patient are measured or sensed, for example, while the nsPEF pulse is applied to the patient to determine a load impedance. For example, the nsPEF pulse voltage and current may be measured to determine the load impedance.

[0256] At 2560, a value of the measured or sensed load impedance is compared with the value of a corresponding expected load impedance.

[0257] Returning to 2520, the controller conditionally modifies the control values corresponding with the values of the desired characteristics received at the interface according to the results of the comparison performed at 2560. The controller is configured to modify control values so that, for example, if the measured impedance is greater than a threshold, the control values are modified such that the nsPEF treatment system stops generating nsPEF pulses. The high measured impedance may be an indication that the nsPEF treatment system should not continue generating nsPEF pulses because, for example, the nsPEF pulse delivery electrodes are no longer connected to the patient.

[0258] In some embodiments, the controller is configured to modify control values so that, for example, if the measured impedance is outside an expected range, the control values are set such that the nsPEF treatment system generates low voltage nsPEF pulses. The measured impedance being outside the expected range may be an indication that the nsPEF pulse delivery electrodes are not properly connected to the patient. The low voltage nsPEF pulses may be used until the measured load impedance is within the expected range.

[0259] In some embodiments, the nsPEF treatment system is configured to simultaneously perform more than one of the methods described above or other methods. For example, the nsPEF treatment system may be configured to modify one or more control values to realize multiple measured characteristics in the generated nsPEF pulses. For example, during a

treatment session, the nsPEF treatment system may be configured to simultaneously measure and modify control parameters such that both the pulse width and the amplitude of the nsPEF pulses are actively controlled through feedback. In some embodiments, to ensure proper electrode connection, load impedance is measured based on one or more nsPEF pulses applied to the patient being treated, while the characteristics of the nsPEF pulses are actively controlled through feedback. In some embodiments, the pulse generator is charged using a feedback method having characteristics of method 2200 and the control values determining nsPEF characteristics are calculated based on measurements of one or more other methods.

[0260] Applying nsPEF to a tumor sufficient to stimulate apoptosis includes at least the electrical characteristics found experimentally. For example, a 100 ns long pulse with a 20 ns rise time to 30 kV/cm (kilovolts per centimeter) at 1 to 7 pulses per second (pps) for 500 to 2000 pulses has been found to be sufficient to stimulate apoptosis, depending on the tumor type. Pulsed electric fields of at least 20 kV/cm have been shown to be effective. A number of pulses greater than 50 pulses has also been shown to be effective. Current values between 12 A and 60 A resulted, depending on the electrode type and skin resistance.

[0261] The embodiments of pulse generators described herein have many uses. Cancer that has metastasized through a subject's bloodstream may be treated using nsPEF's immune stimulation properties. For treatment, circulating tumor cells (CTCs) are isolated from the bloodstream and amassed in vial, test tube, or other suitable in vitro environment. In some cases, there may only be a few (e.g., 5, 10), tumor cells that are collected and amassed. Through this mass, an nsPEF electric field is applied in order to treat the cells. This may cause calreticulin or one or more other damage-associated molecular patterns (DAMPs) to be expressed on the surface membranes of the tumor cells. The tumor cells may then be introduced back into the subject's bloodstream by injection, infusion, or otherwise.

[0262] In an alternative embodiment, single CTCs may also be isolated from the bloodstream, and each tumor cell treated individually. An automated system that captures CTCs in whole blood using iron nanoparticles coated with a polymer layer carrying biotin analogues and conjugated with antibodies for capturing CTCs can automatically capture the tumor cells, and a magnet and or centrifuge can separate them. After separation from the antibodies, the CTCs may be treated with nsPEF through a small capillary and then reintroduced to the patient's bloodstream.

[0263] While examples in the application discuss human and murine subjects, the treatment of other animals is contemplated. Agricultural animals, such as horses and cows, or racing animals, such as horses, may be treated. Companion animals, such as cats and dogs, may find special use with the treatments described herein. It may be difficult for a veterinarian to
5 remove many tumors from a small animal, and cancers may be caught relatively late because the animals cannot communicate their advancing pain. Further, the risk inherent in reinjecting tumor cells—albeit treated tumor cells—may be worth the potential benefits of potentially halting a metastasized cancer in a loved pet.

[0264] The methods of the present invention can be used for the treatment of any type of
10 cancer, whether characterized as malignant, benign, soft tissue, or solid, and cancers of all stages and grades including pre- and post-metastatic cancers. Examples of different types of cancer include, but are not limited to, digestive and gastrointestinal cancers such as gastric cancer (e.g., stomach cancer), colorectal cancer, gastrointestinal stromal tumors, gastrointestinal carcinoid tumors, colon cancer, rectal cancer, anal cancer, bile duct cancer,
15 small intestine cancer, and esophageal cancer; breast cancer; lung cancer; gallbladder cancer; liver cancer; pancreatic cancer; appendix cancer; prostate cancer, ovarian cancer; renal cancer (e.g., renal cell carcinoma); cancer of the central nervous system; skin cancer (e.g., melanoma); lymphomas; gliomas; choriocarcinomas; head and neck cancers; osteogenic sarcomas; and blood cancers.

[0265] Electrical characteristics of nsPEF treatments can be adjusted based on a size and/or
20 a type of a tumor. Types of tumors may include tumors of different regions of the body, such as the cancerous tumors described above.

[0266] It is understood that the various embodiments described herein are by way of
25 example only, and are not intended to limit the scope of the invention. For example, many of the materials and structures described herein may be substituted with other materials and structures without deviating from the spirit of the invention. The present invention as claimed may therefore include variations from the particular examples and preferred embodiments described herein, as will be apparent to one of skill in the art. It is understood that various theories as to why the invention works are not intended to be limiting.

[0267] The above description is illustrative and is not restrictive. Many variations of the
30 invention will become apparent to those skilled in the art upon review of the disclosure. The scope of the invention should, therefore, be determined not with reference to the above

description, but instead should be determined with reference to the pending claims along with their full scope or equivalents.

[0268] As noted previously, all measurements, dimensions, and materials provided herein within the specification or within the figures are by way of example only.

- 5 [0269] A recitation of “a,” “an,” or “the” is intended to mean “one or more” unless specifically indicated to the contrary. Reference to a “first” component does not necessarily require that a second component be provided. Moreover reference to a “first” or a “second” component does not limit the referenced component to a particular location unless expressly stated.
- 10 [0270] All publications mentioned herein are incorporated herein by reference to disclose and describe the methods and/or materials in connection with which the publications are cited. The publications discussed herein are provided solely for their disclosure prior to the filing date of the present application. Nothing herein is to be construed as an admission that the present invention is not entitled to antedate such publication by virtue of prior invention.
- 15 Further, the dates of publication provided may be different from the actual publication dates, which may need to be independently confirmed.

WHAT IS CLAIMED IS:

1. A sub-microsecond pulsed electric field generator system, comprising:
 - a controller configured to generate a power supply control signal and to generate a pulse generator control signal;
 - a power supply configured to receive the power supply control signal and generate one or more power voltages based in part on the received power supply control signal; and
 - a pulse generator configured to receive the one or more power voltages and the pulse generator control signal, and to generate one or more pulses based in part on the one or more power voltages received from the power supply and based in part on the pulse generator control signal received from the controller,wherein the controller is configured to receive one or more feedback signals representing a value of a characteristic of or a result of the one or more pulses and to generate at least one of the power supply control signal or the pulse generator control signal, or both, based partly on the received one or more feedback signals, and
 - wherein the pulse generator comprising a plurality of pulse generator stages connected in series, at least one pulse generator stage of the plurality of pulse generator stages comprises:
 - a switch stack comprising a plurality of switches connected in series;
 - one or more switch drivers comprising a lossy transformer, the one or more switch drivers connecting to the plurality of switches and configured to generate switch control signal pulses for switching various switches of the plurality of switches; and
 - a capacitive element coupled to the switch stack.
2. The generator system of claim 1, wherein the one or more feedback signals comprises a signal representing a measured voltage or a signal representing a measured current, or both, and wherein the controller calculates one or more of energy applied to tissue, tissue impedance, tissue inductance, tissue capacitance, or instantaneous power applied to the tissue.
3. The generator system of claims 1 or 2, wherein the one or more feedback signals comprises a signal representing a measured temperature.

4. The generator system of claim 2, the generator system comprising a sensor, wherein the sensor comprises one or more of a thermocouple, a voltage probe, a current probe, an impedance probe, a capacitance probe, a light sensor, a humidity sensor, a tissue monitoring probe, or a chemical analysis probe.

5. The generator system of any one of claims 1 to 4, the generator system comprising an interface configured to receive information indicating a desired value for the characteristic of or the result of the one or more pulses, wherein the controller is configured to generate at least one of the power supply control signal or the pulse generator control signal, or both, based partly on the desired value.

6. The generator system of claim 5, wherein the power supply, the controller, and the pulse generator collectively form a feedback loop which causes the characteristic of or the result of the one or more pulses to have a value substantially equal to the desired value indicated by the information received by the interface.

7. The generator system of claim 6, wherein the feedback loop is controlled using a Proportional-Integral-Derivative (PID) method.

8. The generator system of claim 6, wherein the feedback loop is controlled using a lookup table to determine a next control signal value or a method comprising increasing or decreasing a next control signal value by a fixed amount.

9. The generator system of any of claims 1 to 8, wherein the pulse generator is configured to generate at least one of the one or more feedback signals.

10. The generator system of claim 4, wherein the sensor comprises a voltage sensor configured to generate a signal representing the measured voltage, and wherein the at least one feedback signal represents the measured voltage.

11. The generator system of claim 4, wherein the sensor comprises a current sensor configured to generate the signal representing the measured current.

12. The generator system of any of claims 1 to 4 and 9 to 11, the generator system comprising an electrode configured to receive pulses from the pulse generator and apply the pulses to a target, wherein the electrode is configured to generate at least one of the one or more feedback signals.

13. The generator system of claim 12, wherein the electrode comprises at least one of:
a current sensor configured to generate the signal representing the measured current,
or
a temperature sensor configured to generate a signal representing the measured temperature, and wherein the at least one feedback signal represents the measured temperature.

14. The generator system of claim 12, further comprising an interface configured to receive information indicating a desired value for the characteristic of or the result of the one or more pulses, wherein the controller is configured to generate at least one of the power supply control signal or pulse generator control signal, or both, based partly on the desired value, and wherein the power supply, the controller, and the electrode collectively form a feedback loop which causes the characteristic of or the result of the one or more pulses applied to the target to have a value substantially equal to the desired value indicated by the information received by the interface.

15. The generator system of any of claims 1 to 14, wherein the pulse generator comprises a driver circuit configured to generate driving signal pulses for the plurality of pulse generator stages in response to input signal pulses.

16. The generator system of claim 15, wherein an input port of each of the one or more switch drivers is coupled to an output port of the driver circuit; an output port of each of the one or more switch drivers is coupled to the respective switch; and the output port of each of the one or more switch drivers is coupled to the input port of the switch driver through the lossy transformer, and wherein the plurality of pulse generator stages comprises 5 or less stages.

17. The generator system of claim 16, wherein the lossy transformer comprises less than 5 primary turns and less than 5 secondary turns.

18. A method of generating one or more sub-microsecond pulsed electric field pulses, the method comprising:

- generating a power supply control signal with a controller;
- generating a pulse generator control signal with the controller;
- receiving the power supply control signals at a power supply;

generating one or more power voltages based in part on the power supply control signal with the power supply;

receiving the one or more power voltages and the pulse generator control signal at a pulse generator;

generating, with the pulse generator, one or more pulses based in part on the one or more power voltages received from the power supply and based in part on the pulse generator control signal received from the controller; and

receiving, at the controller, one or more feedback signals representing a value of a characteristic of or a result of the pulses, wherein at least one of the power supply control signal or the pulse generator control signal, or both, is generated by the controller based partly on the received one or more feedback signals,

wherein the pulse generator is sub-microsecond pulse generator comprising a plurality of pulse generator stages, wherein at least one pulse generator stage comprises:

a switch stack comprising a plurality of switches;

a capacitive element coupled to the switch stack; and

one or more switch drivers comprising a lossy transformer, the one or more switch drivers connecting to the plurality of switches and configured to generate a switch control signal for switching respective switches of the plurality of switches.

19. The method of claim 18, wherein the one or more feedback signals comprises a signal representing a measured voltage and wherein the controller calculates one or more of energy applied to tissue, tissue impedance, tissue inductance, tissue capacitance, or instantaneous power applied to the tissue.

20. The method of claims 18 or 19, the method comprising receiving information indicating a desired value for the characteristic of or the result of the one or more pulses at an interface, wherein at least one of the power supply control signal or the pulse generator control signal, or both, is generated by the controller based partly on the desired value.

21. The method of claim 20, wherein the power supply, the controller, and the pulse generator collectively form a feedback loop which causes the characteristic of or the result of the one or more pulses to have a value substantially equal to the desired value indicated by the information received by the interface.

22. The method of claim 21, wherein the feedback loop is controlled using at least one or a combination of:

a Proportional-Integral-Derivative (PID) method;

a lookup table to determine a next control signal value; or

a method comprising increasing or decreasing a next control signal value by a fixed amount.

23. The method of claims 18 to 22, the method comprising sending the one or more pulses from the pulse generator to at least one electrode and generating at least one of the one or more feedback signals with the pulse generator, the at least one electrode, or both.

24. The method of claim 23, the method comprising using a voltage sensor to measure voltage, wherein the voltage sensor is in the pulse generator or in the electrode.

25. The method of claim 23, the method comprising using a current sensor to measure current, wherein the current sensor is in the pulse generator or in the electrode.

26. The method of any of claims 18-25, the method comprising generating a signal representing a measured temperature with a temperature sensor, wherein the at least one feedback signal represents the measured temperature.

27. A sub-microsecond pulsed electric field generator system, comprising:
a controller, configured to generate a power supply control signal and a pulse generator control signal;
a power supply, configured to receive the power supply control signal and generate one or more power voltages based in part on the received power supply control signal; and
a pulse generator circuit configured to receive the one or more power voltages and the pulse generator control signal, and to generate one or more pulses based at least in part on the one or more power voltages received from the power supply and based at least in part on the pulse generator control signal received from the controller,
wherein the pulse generator circuit comprises a plurality of pulse generator stages, and
wherein at least one pulse generator stage of the plurality of pulse generator stages comprises:
a switch stack including a plurality of switches connected in series;

one or more switch drivers comprising a lossy transformer, the one or more switch drivers connecting to the plurality of switches and configured to generate switch control signal pulses for switching respective switches of the plurality of switches; and
a capacitive element coupled to the switch stack.

28. The system of claim 27, wherein the at least one pulse generator stage further comprises a plurality of resistive elements coupled to the switch stack and the capacitive element, and wherein the at least one pulse generator stage is configured to be connected in parallel to at least one other of the plurality of pulse generator stages through the plurality of resistive elements and configured to charge the capacitive element to the one or more power voltages.

29. The system of claims 27 or 28, wherein the at least one pulse generator stage is configured to be connected in series to at least one other of the plurality of pulse generator stages through the switch stack in response to the switch control signal pulses from the one or more switch drivers closing the plurality of switches to generate the one or more pulses.

30. The system of any one of claims 27-29, further comprising an interface configured to receive information indicating a desired value for a characteristic of or a result of the one or more pulses, wherein the controller is configured to generate at least one of the power supply control signal or the pulse generator control signal based at least partly on the desired value.

31. The system of any one of claims 27-30, further comprising an electrode configured to apply the one or more pulses to a patient.

32. The system of any one of claims 27-31, wherein the pulse generator circuit is a first pulse generator circuit of a plurality of pulse generator circuits connected in parallel.

33. The system of claim 32, wherein each pulse generator circuit further comprises a diode connected between an output of the respective pulse generator circuit and an output of the other pulse generator circuits, each respective diode configured to decouple the respective pulse generator circuit from the other pulse generator circuits.

34. The system of any one of claims 27-33, wherein the one or more switch drivers comprises a respective switch driver for each respective switch of the plurality of switches in the switch stack, and wherein the switch control signal pulse generated by each respective switch driver is referenced to a voltage specific to the respective switch being driven.

35. The system of any one of claims 27-34, wherein each of the plurality of switches has a breakdown voltage, wherein the one or more power voltages is greater than the breakdown voltage of each respective switch, and wherein the at least one pulse generator stage is configured to charge the capacitive element to the one or more power voltages.

36. The system of any one of claims 27-35, wherein each of the plurality of switches is a power metal-oxide-semiconductor field-effect transistor (MOSFET).

37. The system of any one of claims 27-36, wherein the pulse generator circuit comprises a driving circuit configured to generate a trigger pulse in response to the pulse generator control signal.

38. The system of any one of claims 27-37, wherein the trigger pulse closes the plurality of switches substantially simultaneously.

39. A method of generating sub-microsecond pulsed electric field pulses, the method comprising:

- generating a power supply control signal with a controller;
- generating a pulse generator control signal with the controller;
- receiving the one or more power supply control signals at a power supply;
- generating, with the power supply, one or more power voltages based in part on the power supply control signal;
- receiving the one or more power voltages and the pulse generator control signal at a pulse generator; and
- generating, with the pulse generator, one or more pulses based in part on the one or more power voltages received from the power supply and based in part on the pulse generator control signal received from the controller;

wherein the pulse generator is a sub-microsecond pulse generator comprising a plurality of pulse generator stages and at least one pulse generator stage of the plurality of pulse generator stages comprises:

a switch stack comprising a plurality of switches;

a capacitive element coupled to the switch stack; and

one or more switch drivers comprising a lossy transformer, the one or more switch drivers connecting to the plurality of switches and configured to generate switch control signal pulses for switching respective switches of the plurality of switches.

40. The method of claim 39, wherein generating the one or more pulses with the pulse generator comprises:

charging the capacitive element in the at least one pulse generator stage to the one or more power voltages;

generating, by a driving circuit, a trigger signal in response to receiving the pulse generator control signal; and

activating, by the one or more switch drivers of the pulse generator, the switch stack to discharge the capacitive element in each of the at least one pulse generator stage in series to generate the one or more pulses.

41. The method of claims 39 or 40, further comprising applying the one or more pulses to a patient via an electrode.

42. The method of any one of claims 39-41, further comprising:

receiving information indicating a desired value for a characteristic of or a result of the one or more pulses via an interface; and

generating at least one of the power supply control signal or the pulse generator control signal based at least partly on the desired value.

43. The method of claim 42, wherein the power supply, the controller, and the pulse generator collectively form a feedback loop which causes a characteristic of or a result of the one or more pulses to have a value substantially equal to the desired value.

44. The method of claim 43, wherein the feedback loop is controlled using at least one of:

- a Proportional-Integral-Derivative (PID) method;
- a lookup table to determine a next control signal value; and
- a method including increasing or decreasing a next control signal value by a fixed amount.

45. The method of any one of claims 39-41, further comprising generating one or more feedback signals with the pulse generator, an electrode, or both, and wherein the at least one of the one or more feedback signals represent measured voltage, measured current or measured temperature.

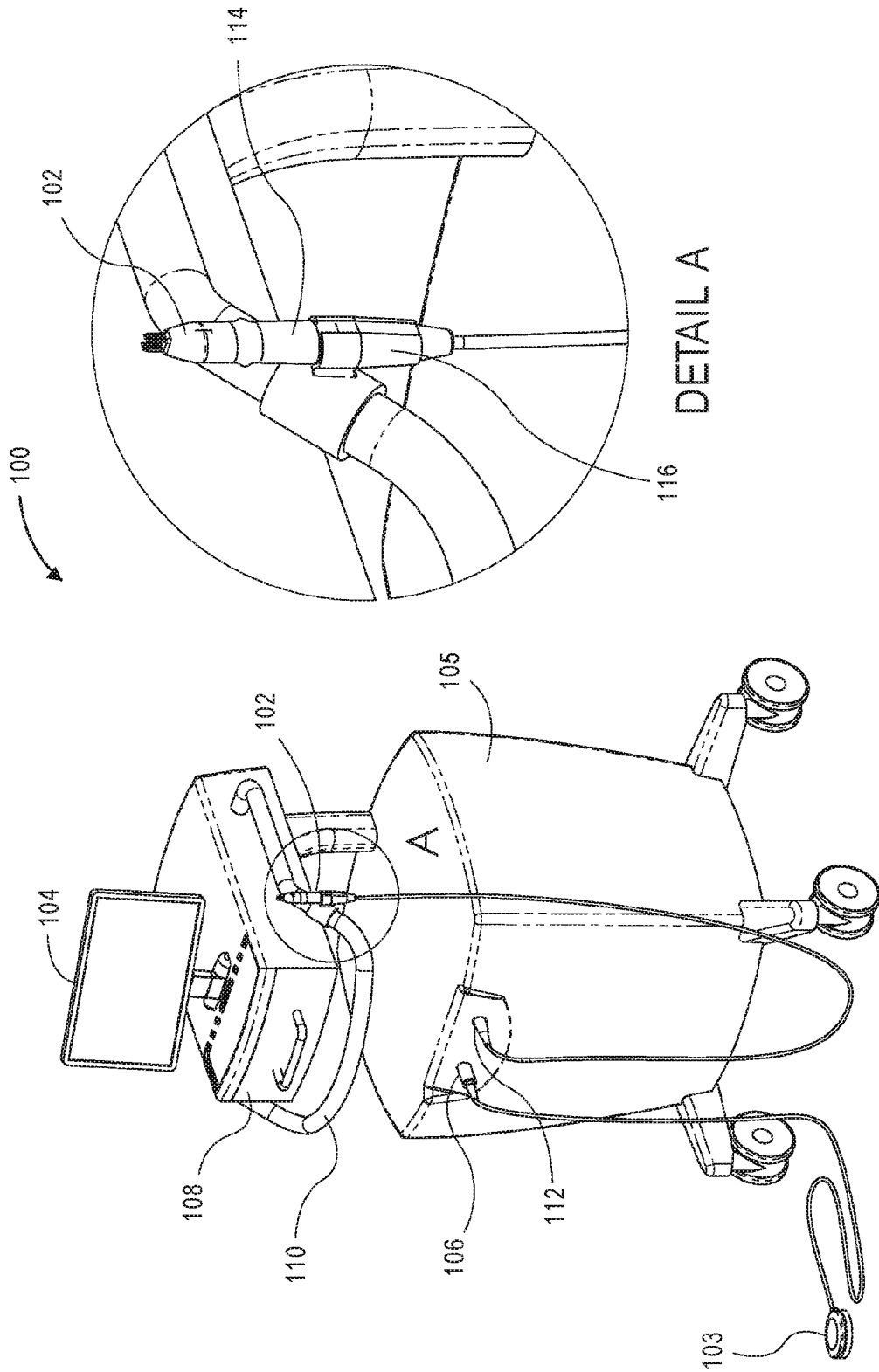


FIG. 1

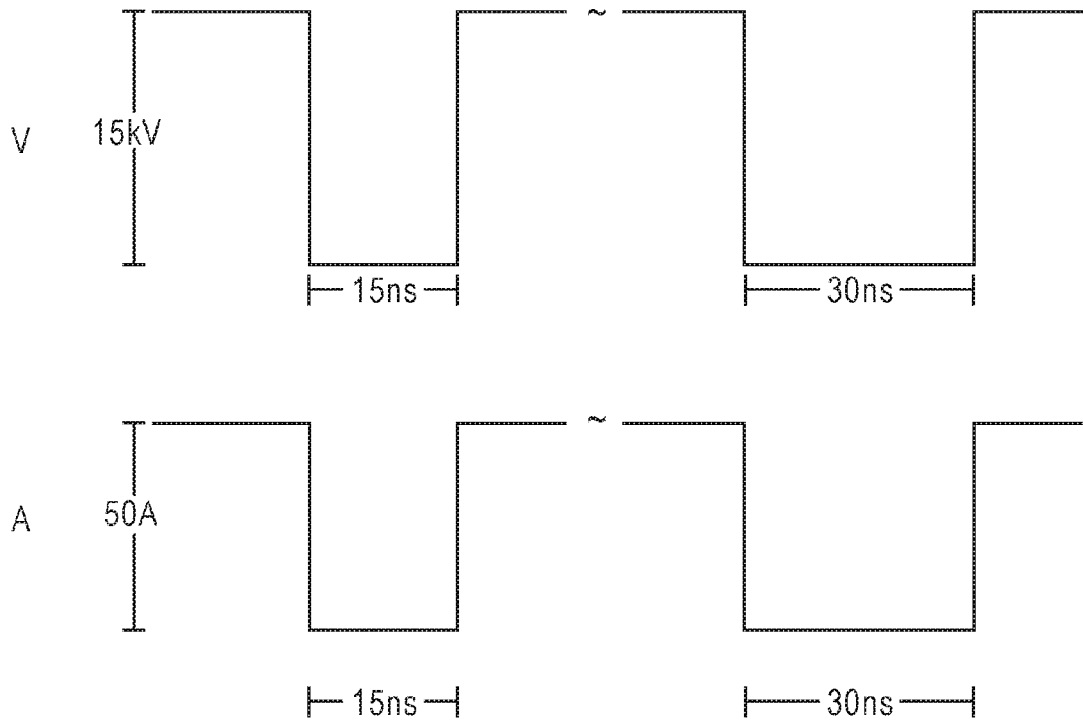


FIG. 2

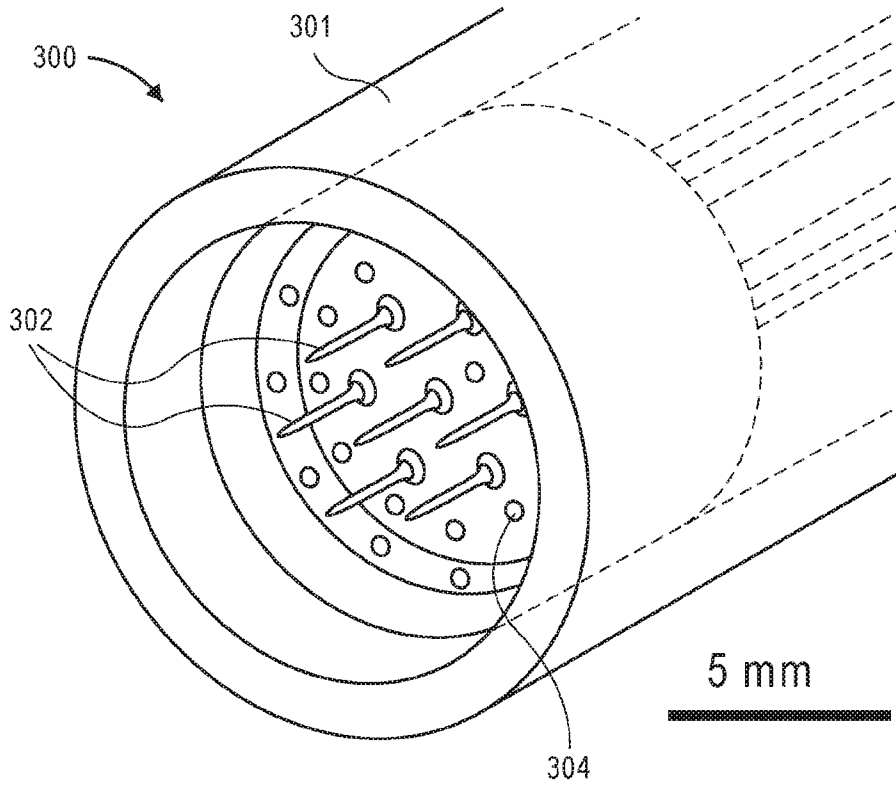


FIG. 3

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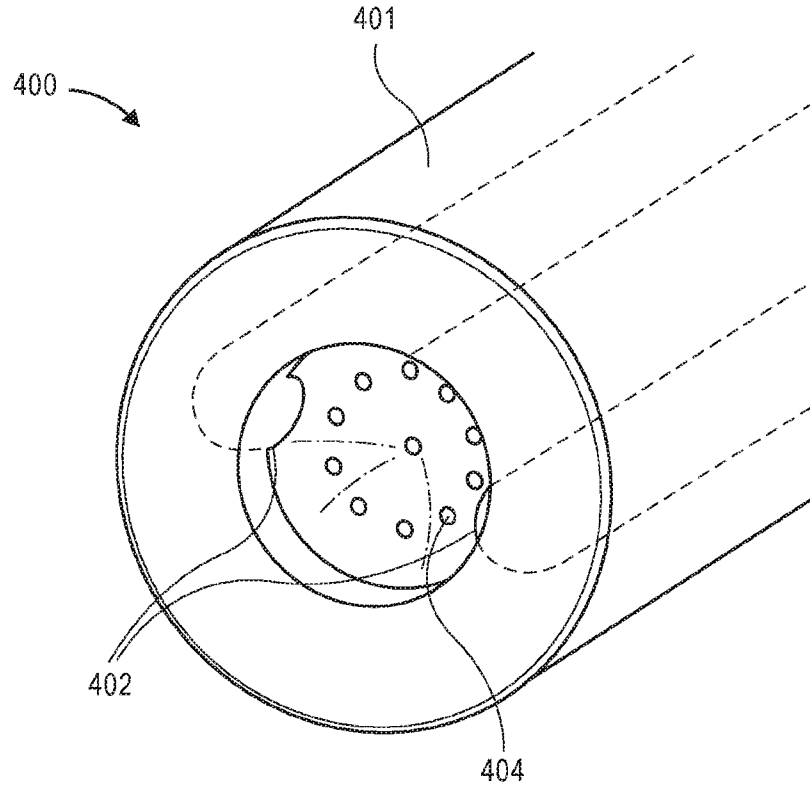


FIG. 4

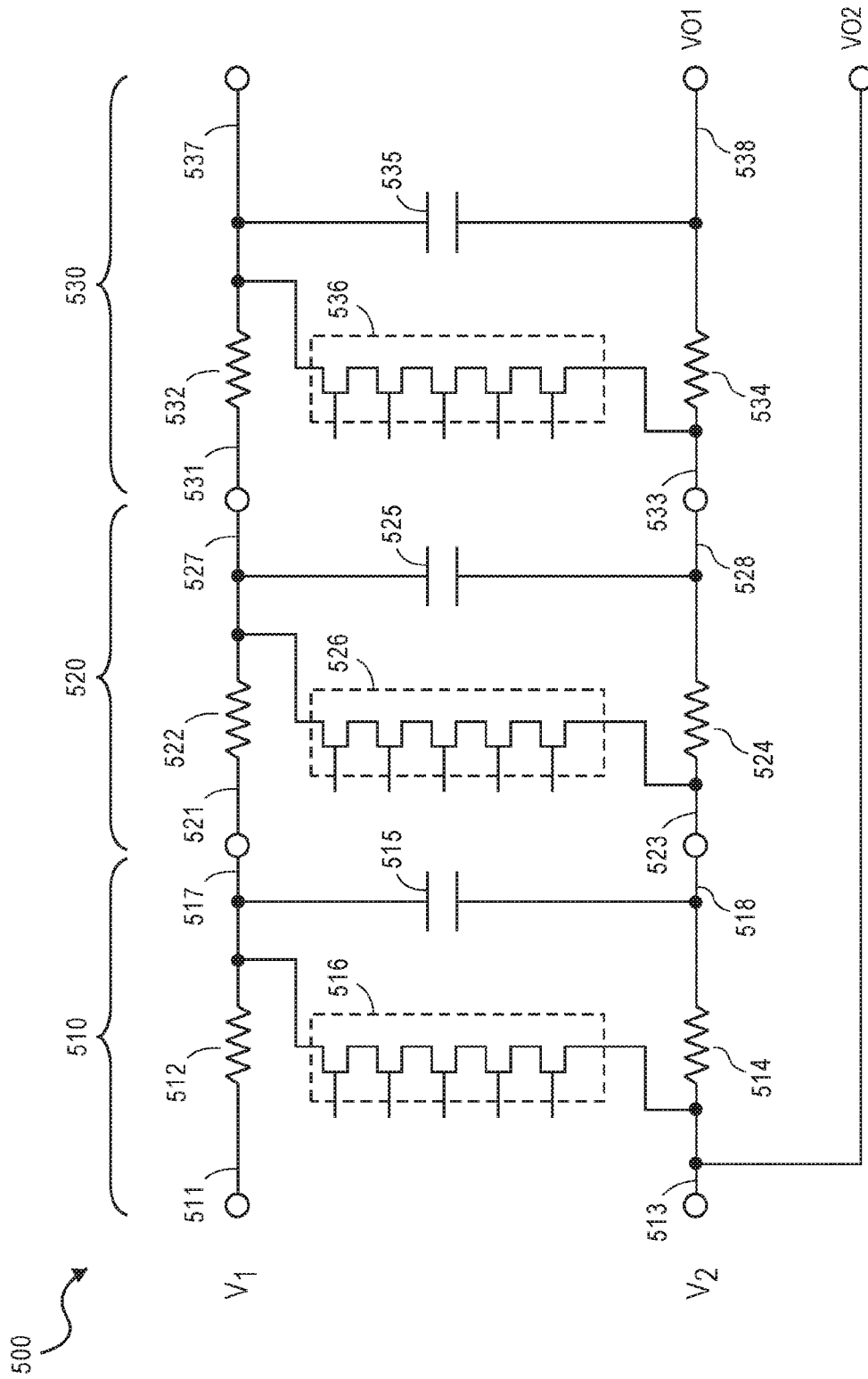


FIG. 5

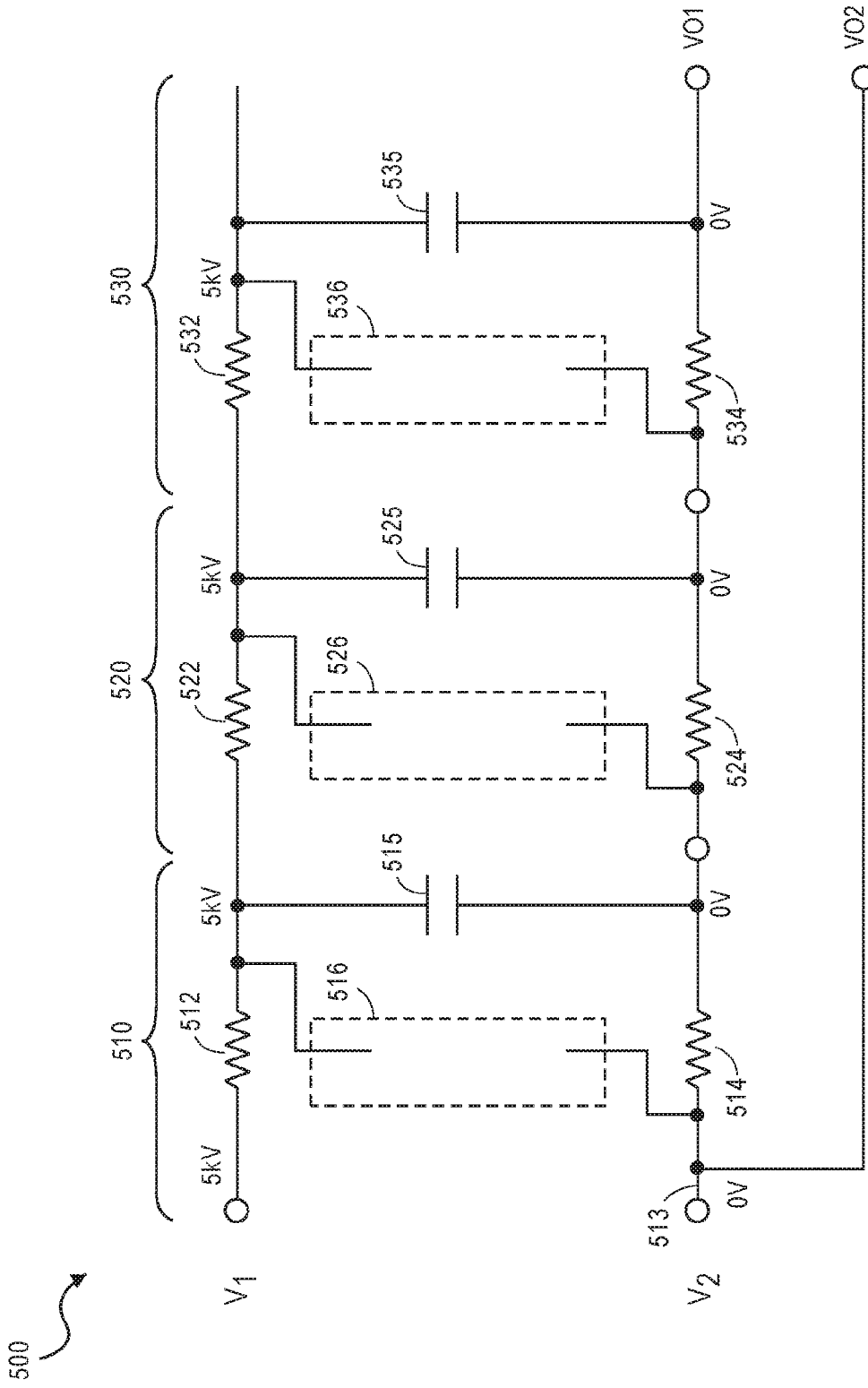


FIG. 6A

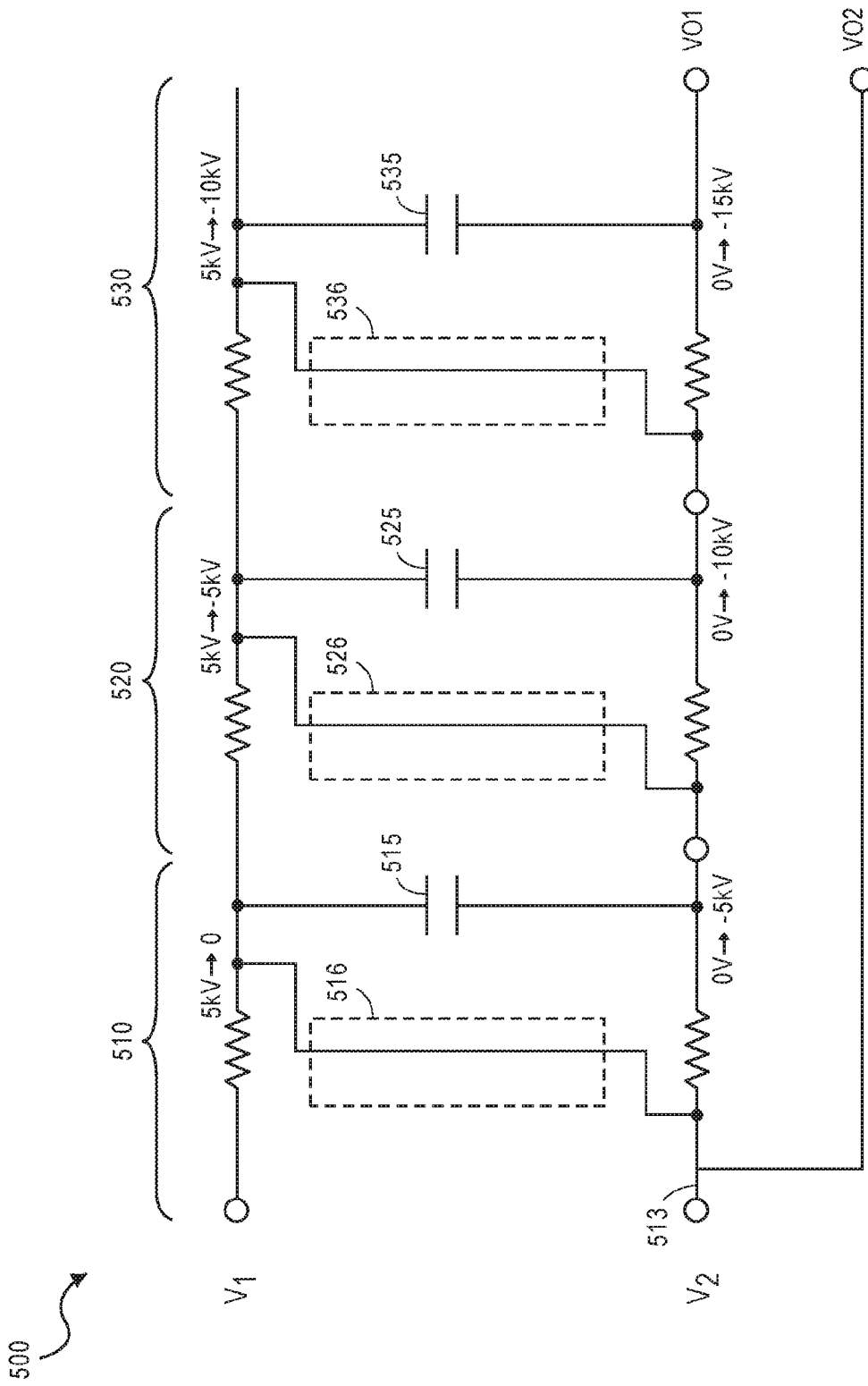


FIG. 6B

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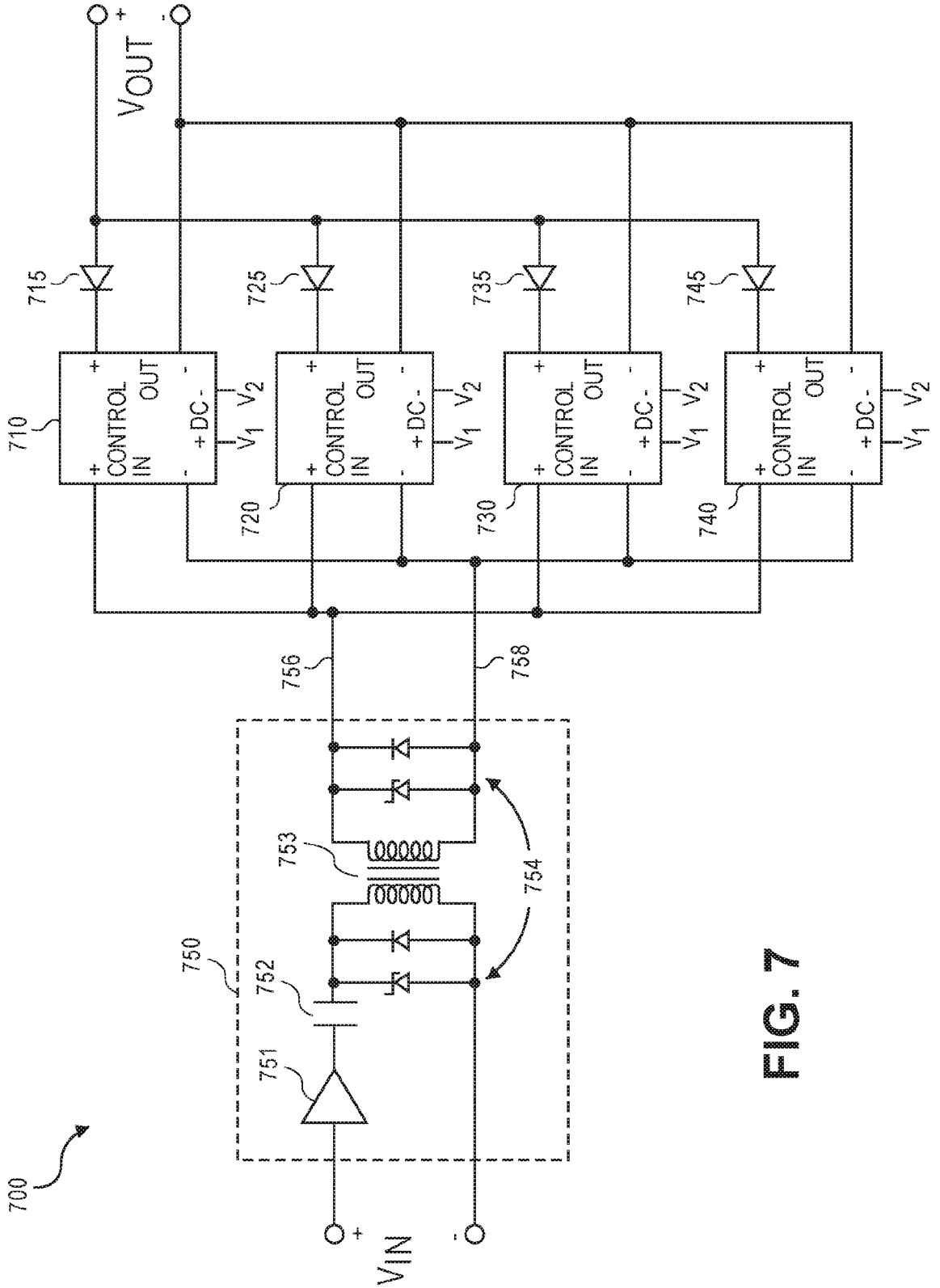


FIG. 7

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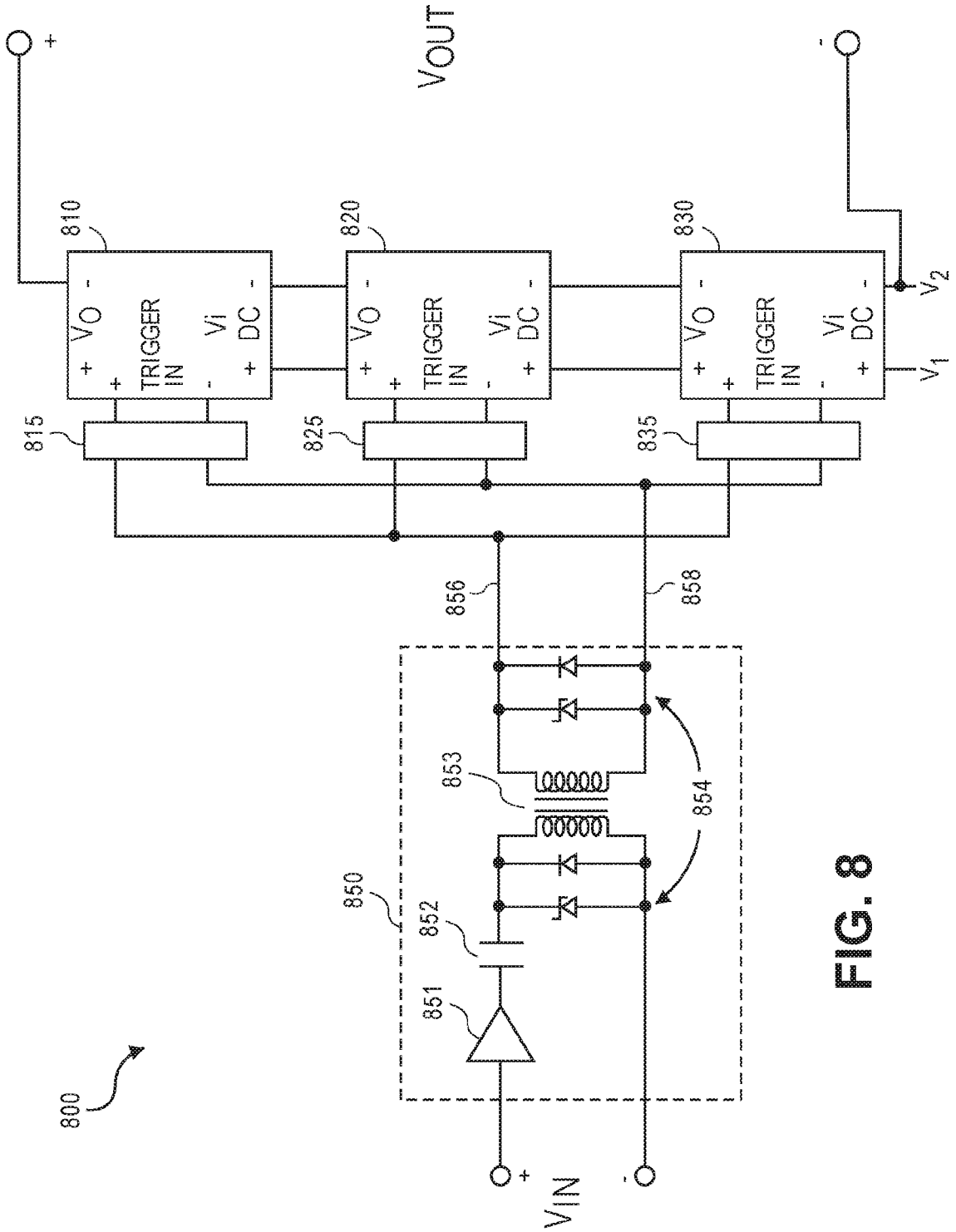


FIG. 8

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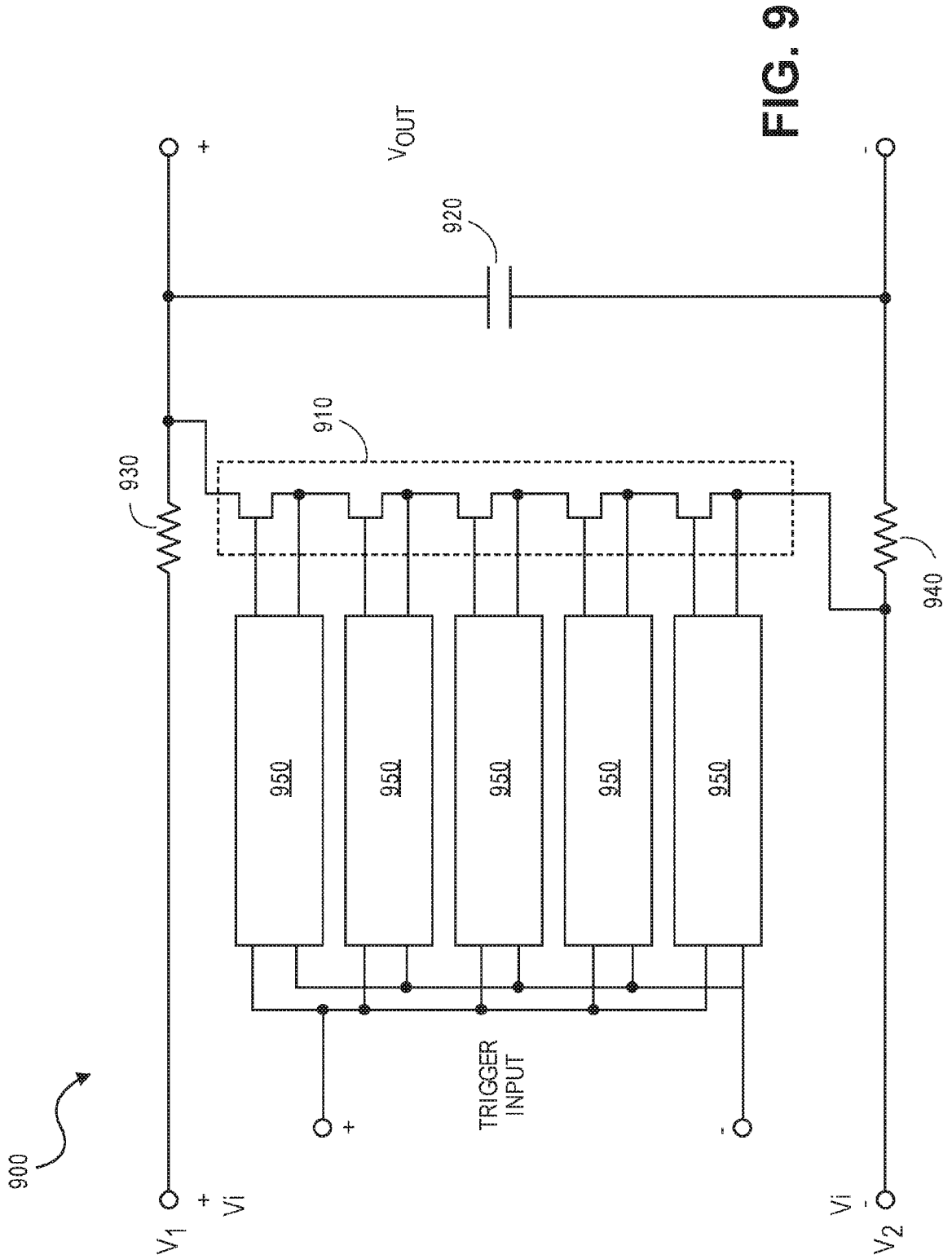


FIG. 9

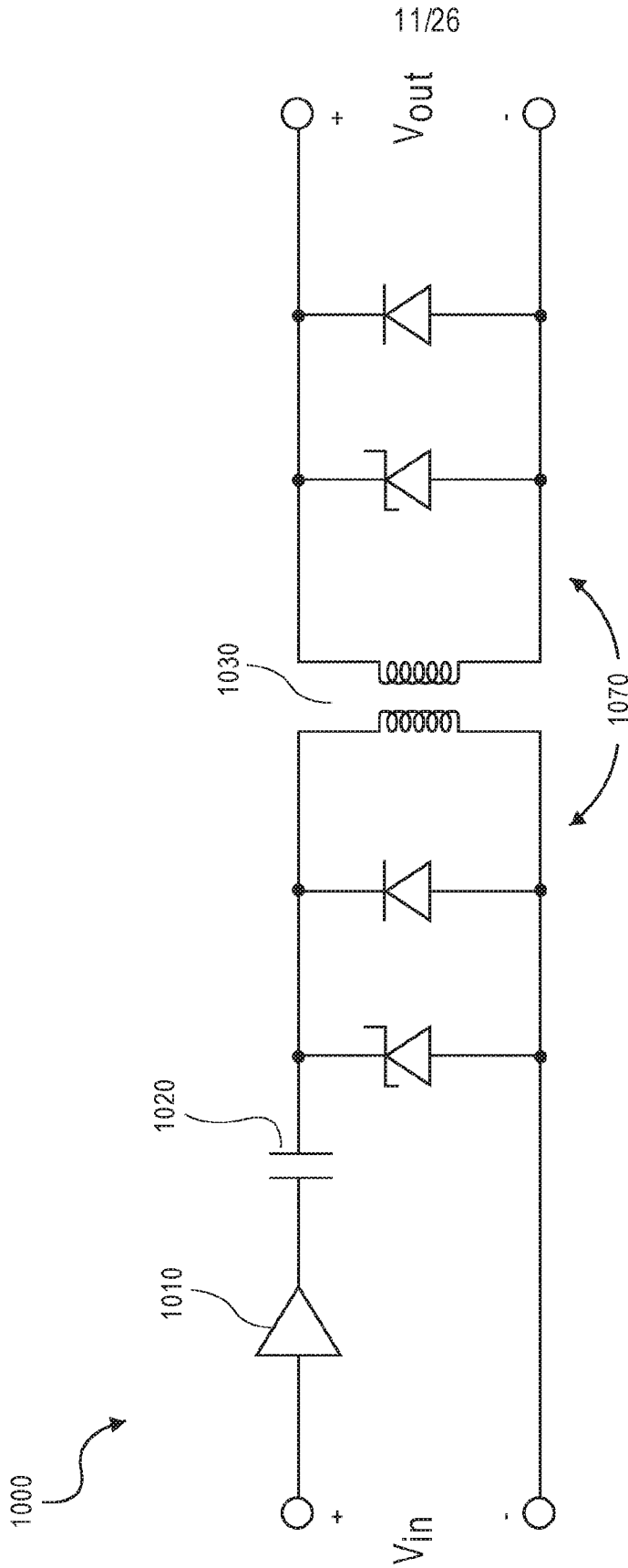


FIG. 10

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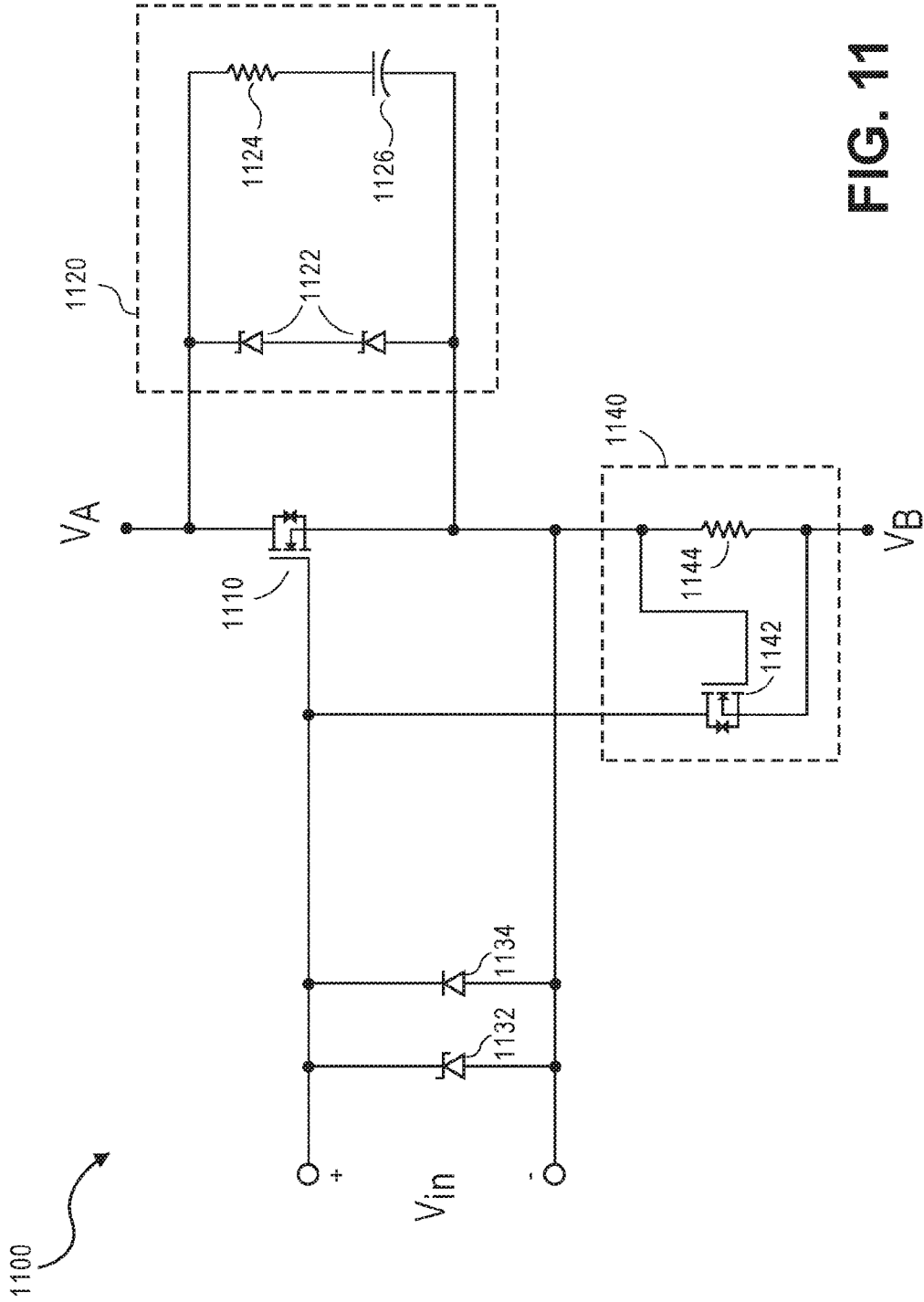


FIG. 11

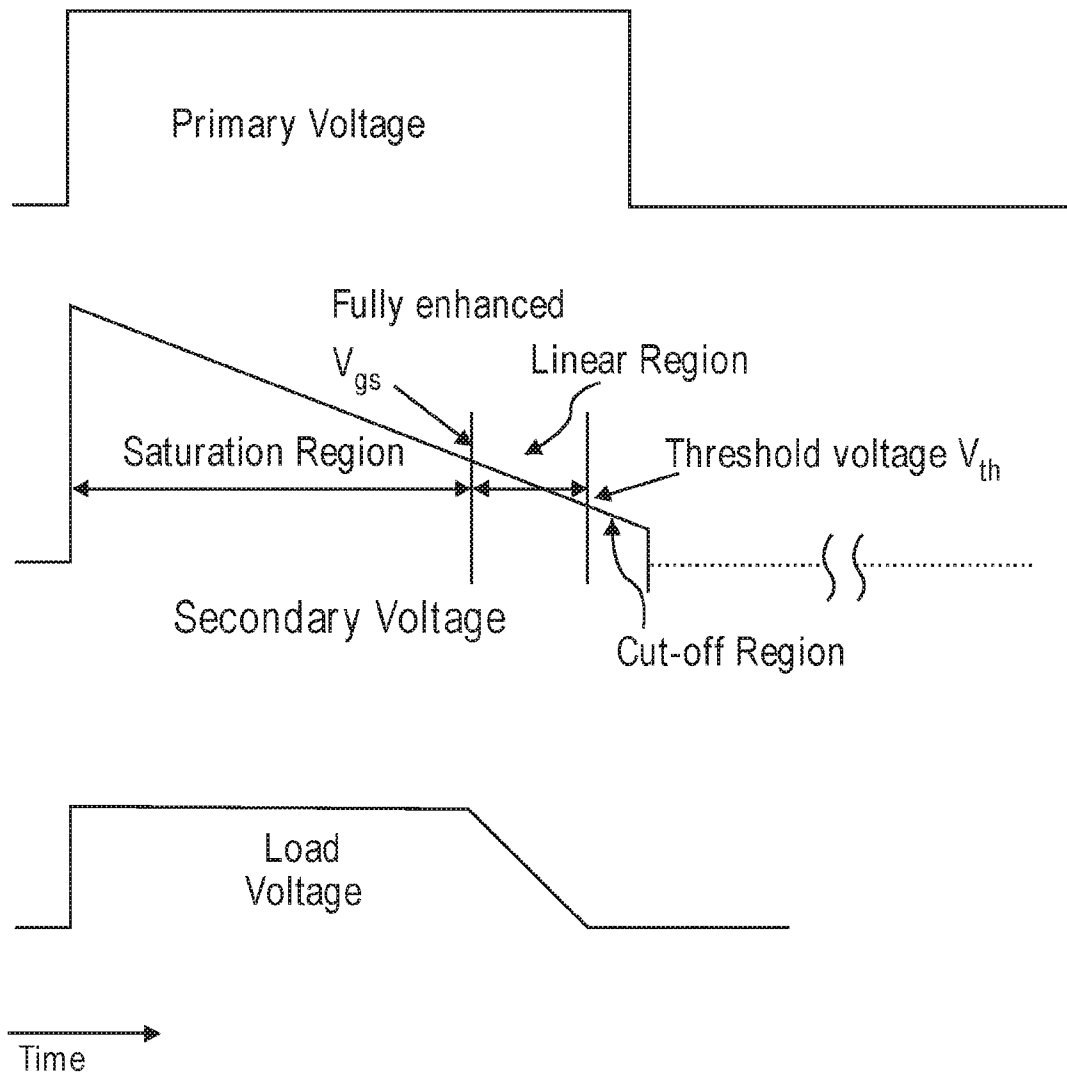


FIG. 12

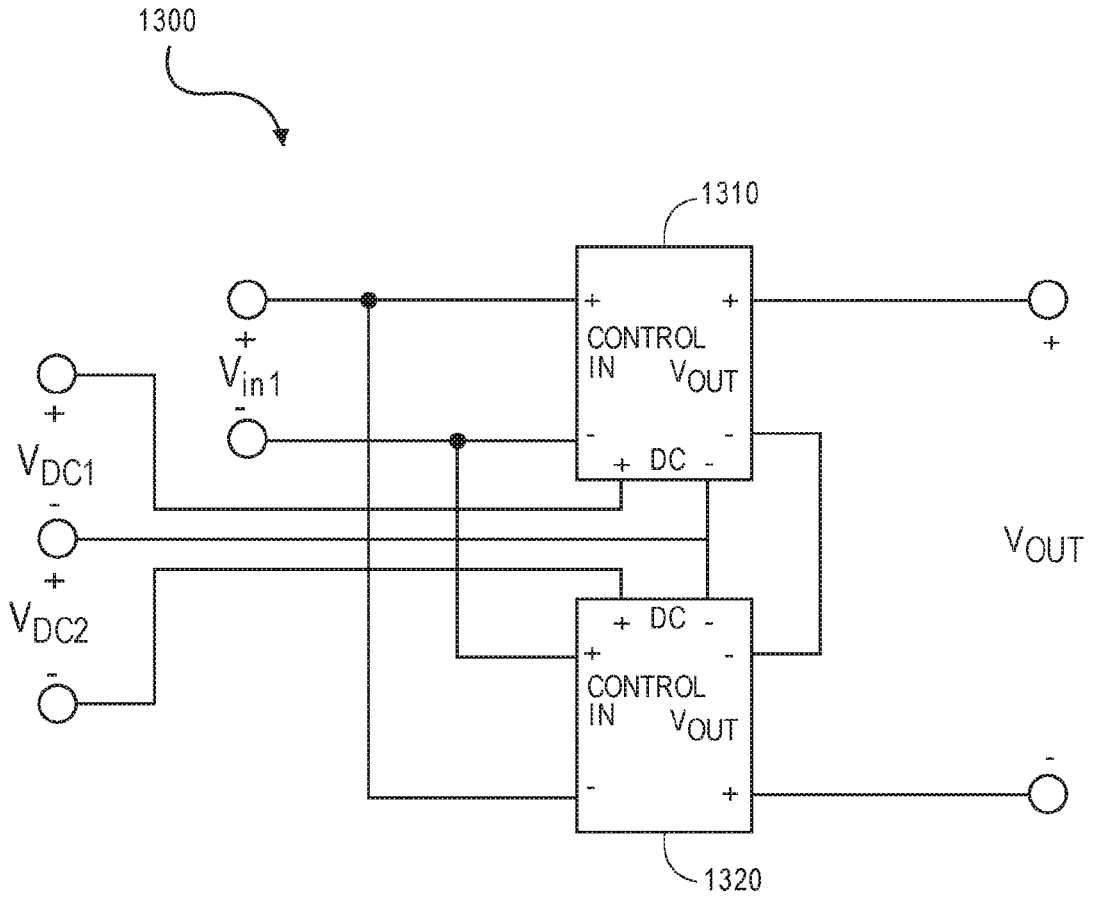


FIG. 13

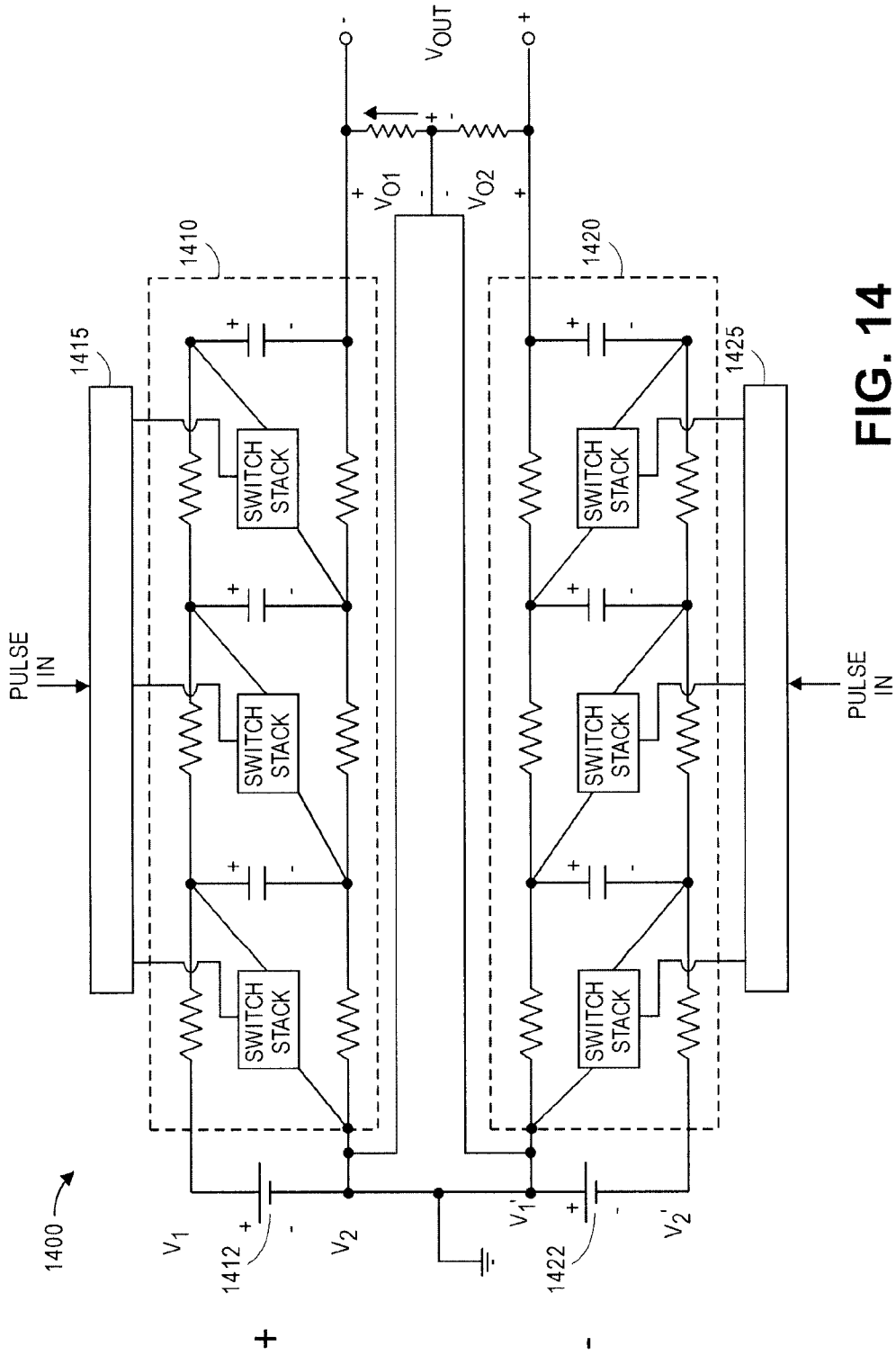


FIG. 14

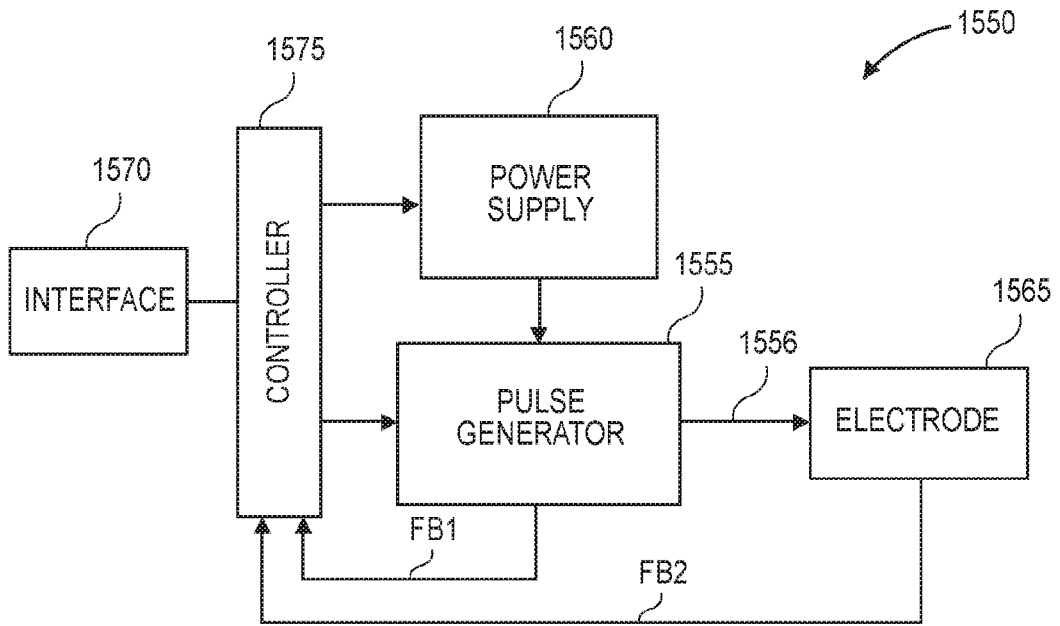


FIG. 15

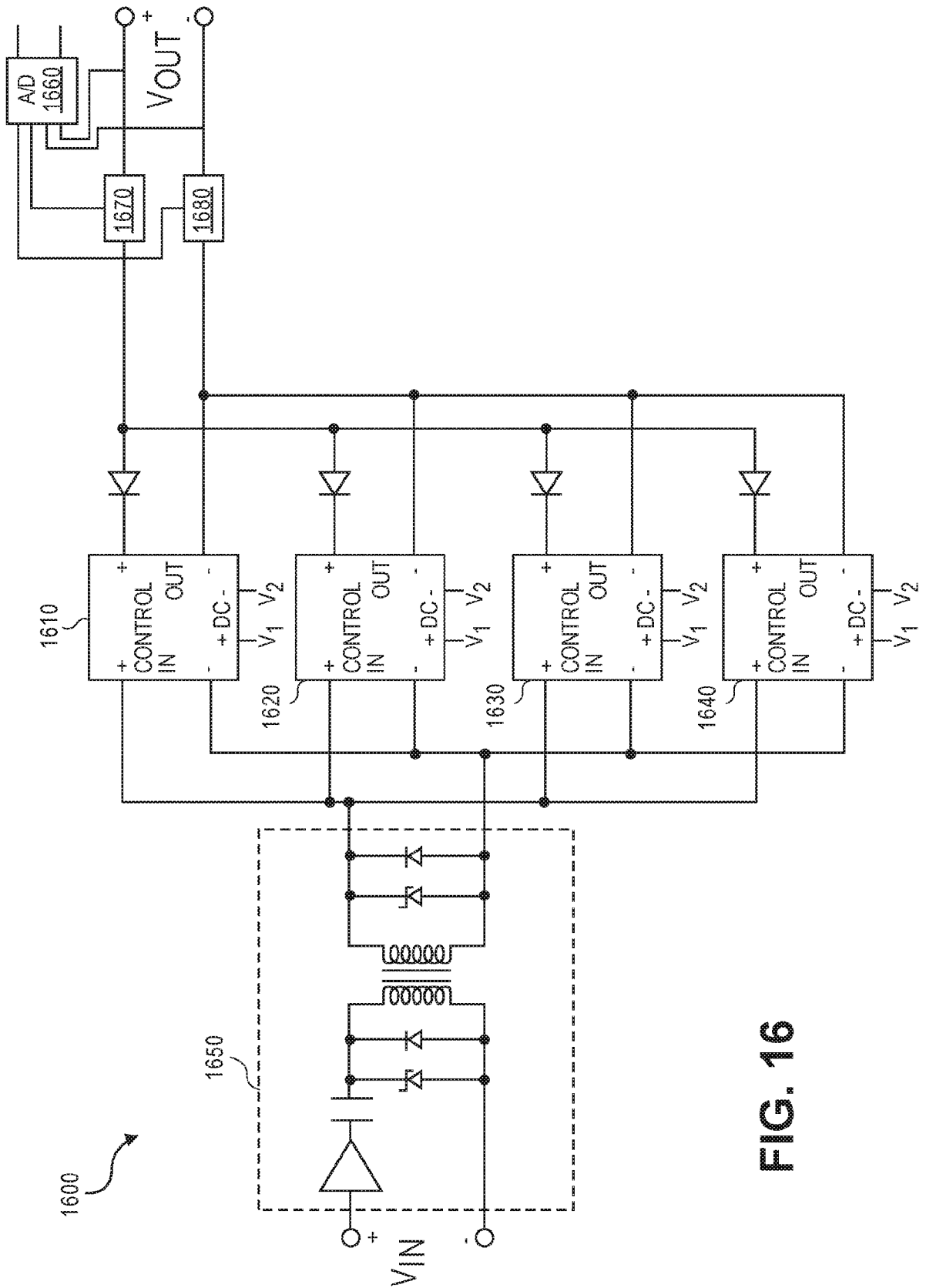


FIG. 16

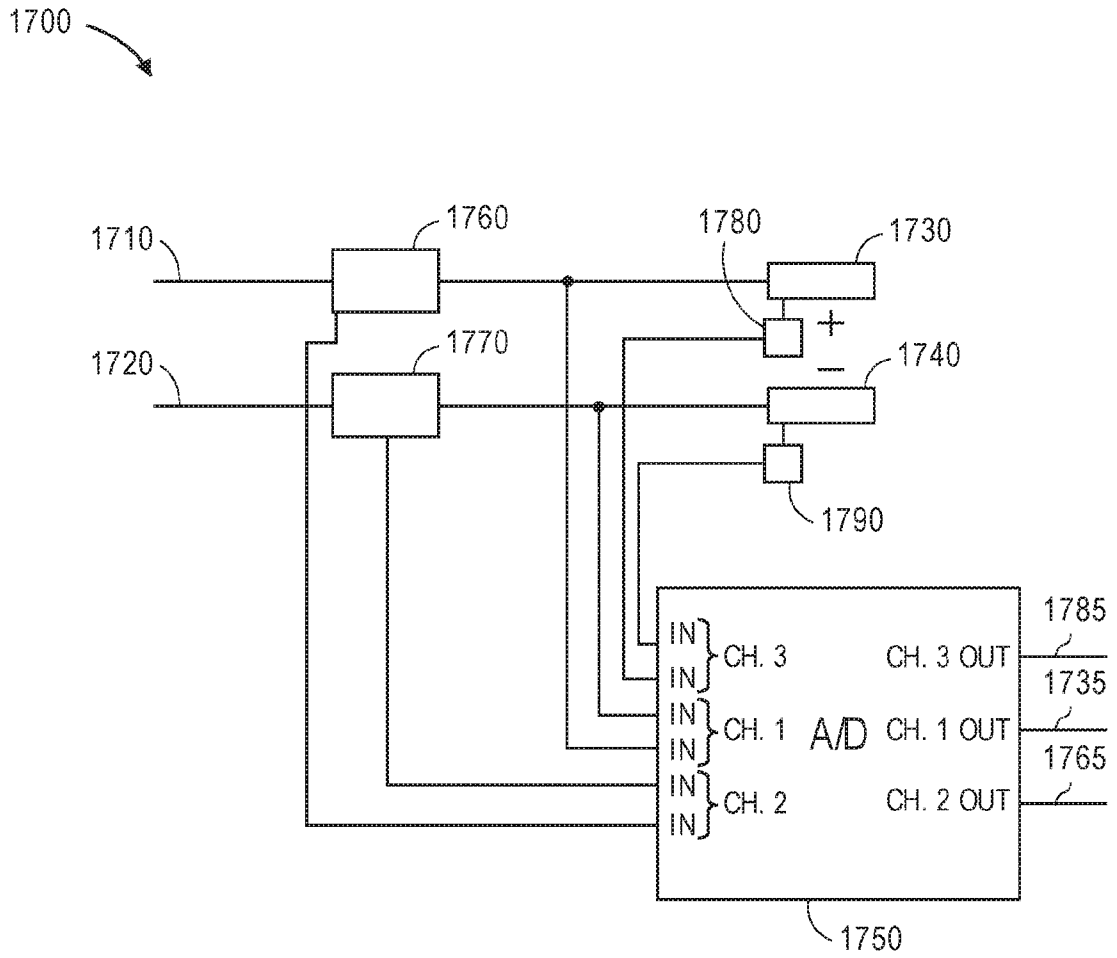


FIG. 17

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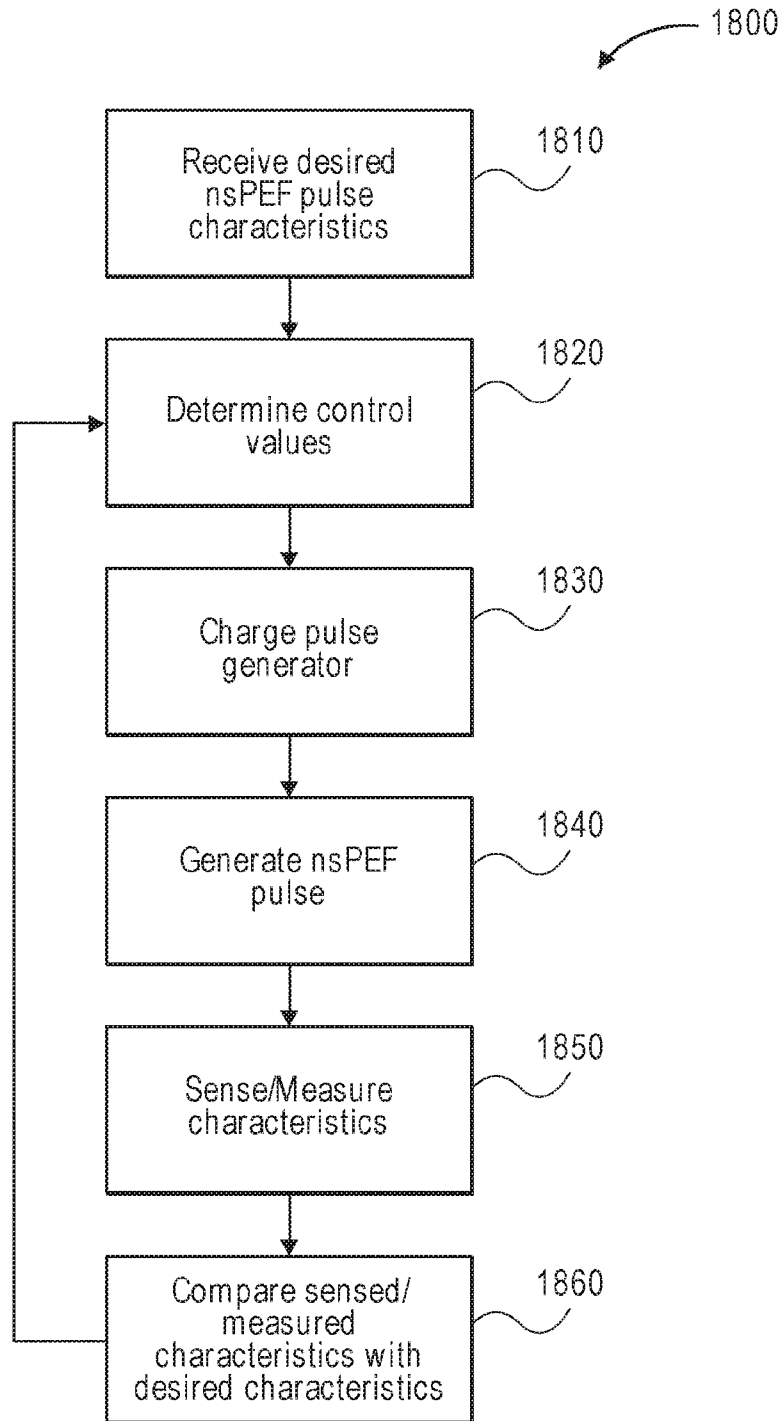


FIG. 18

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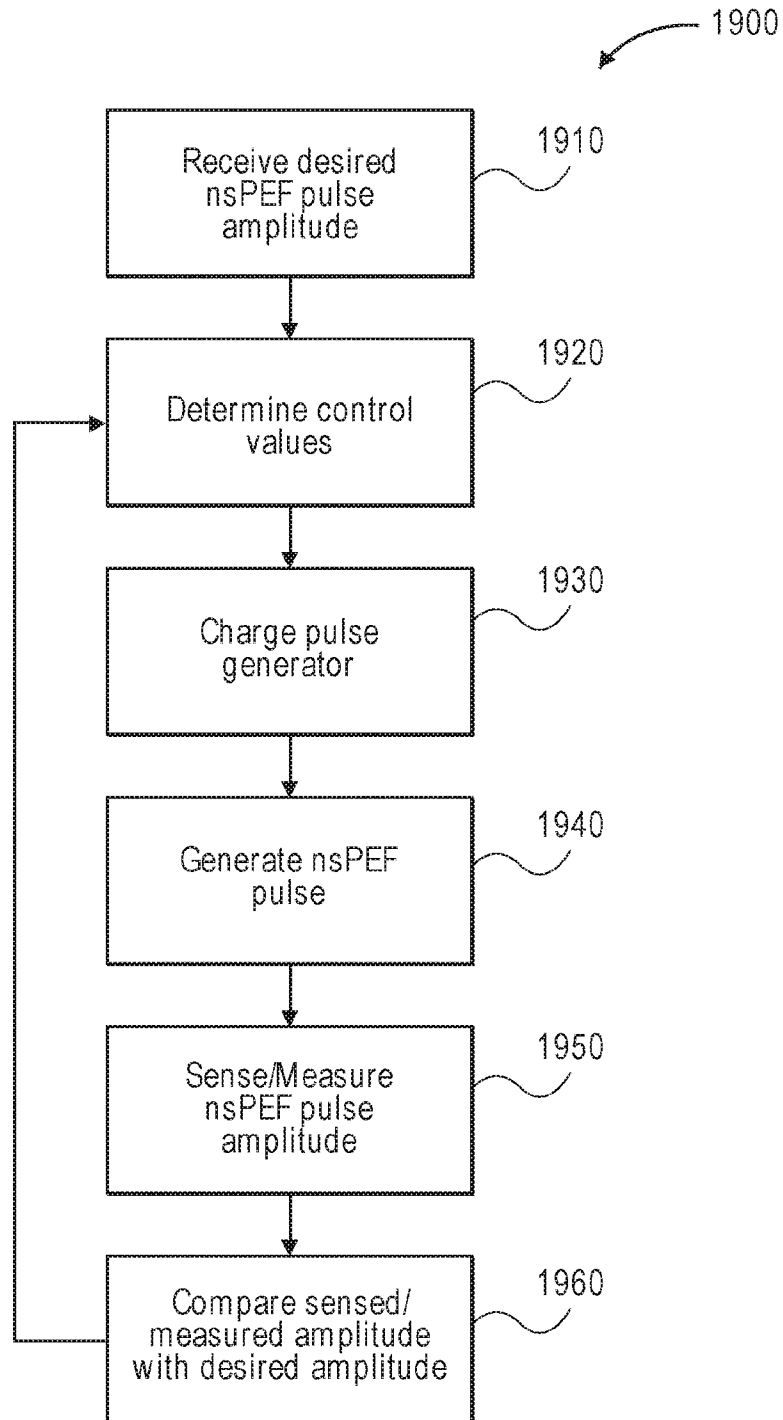


FIG. 19

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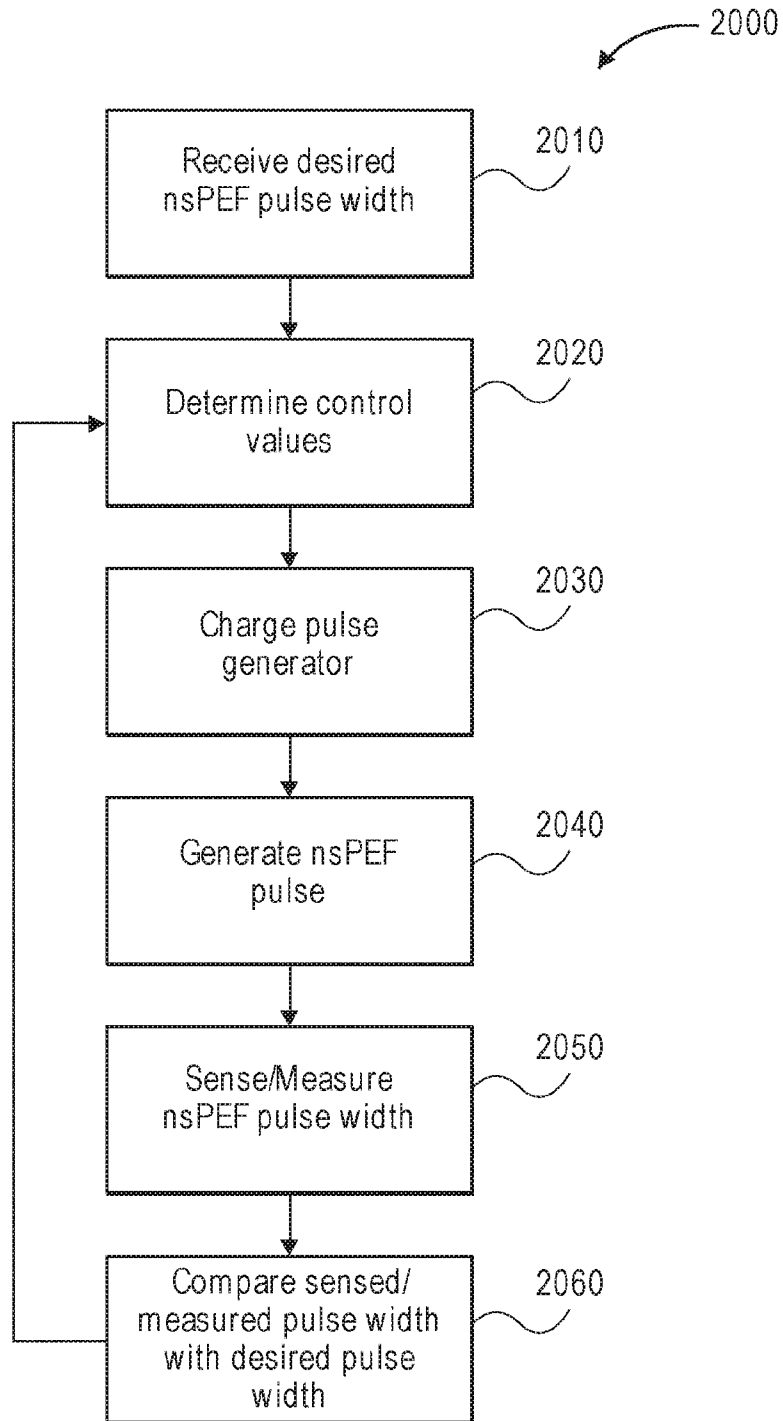


FIG. 20

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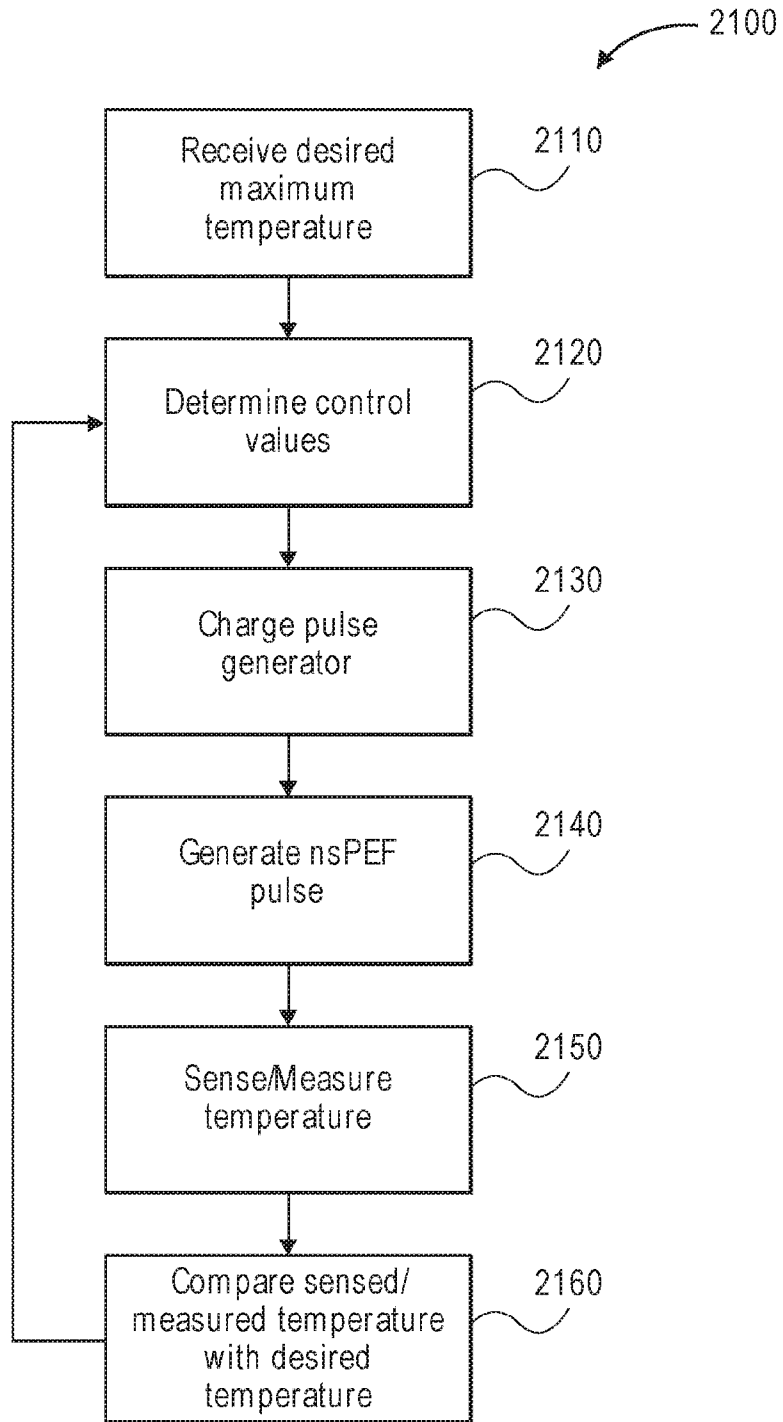


FIG. 21

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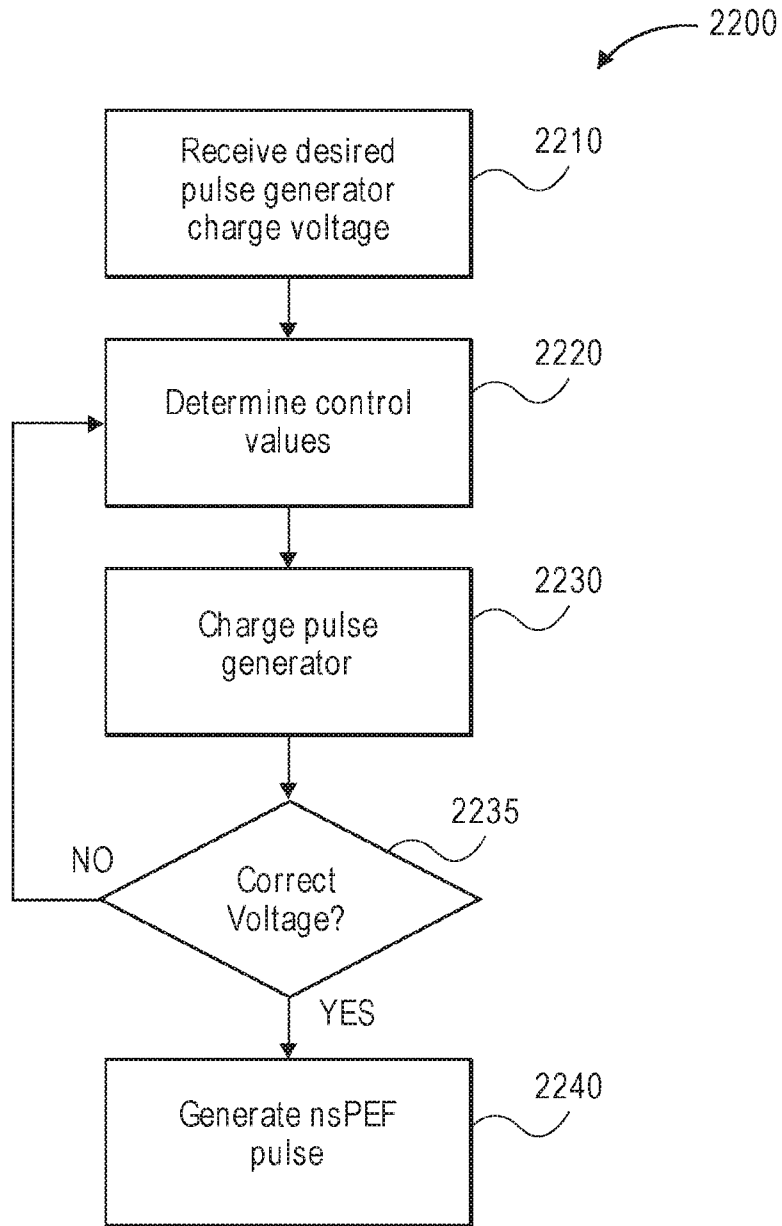


FIG. 22

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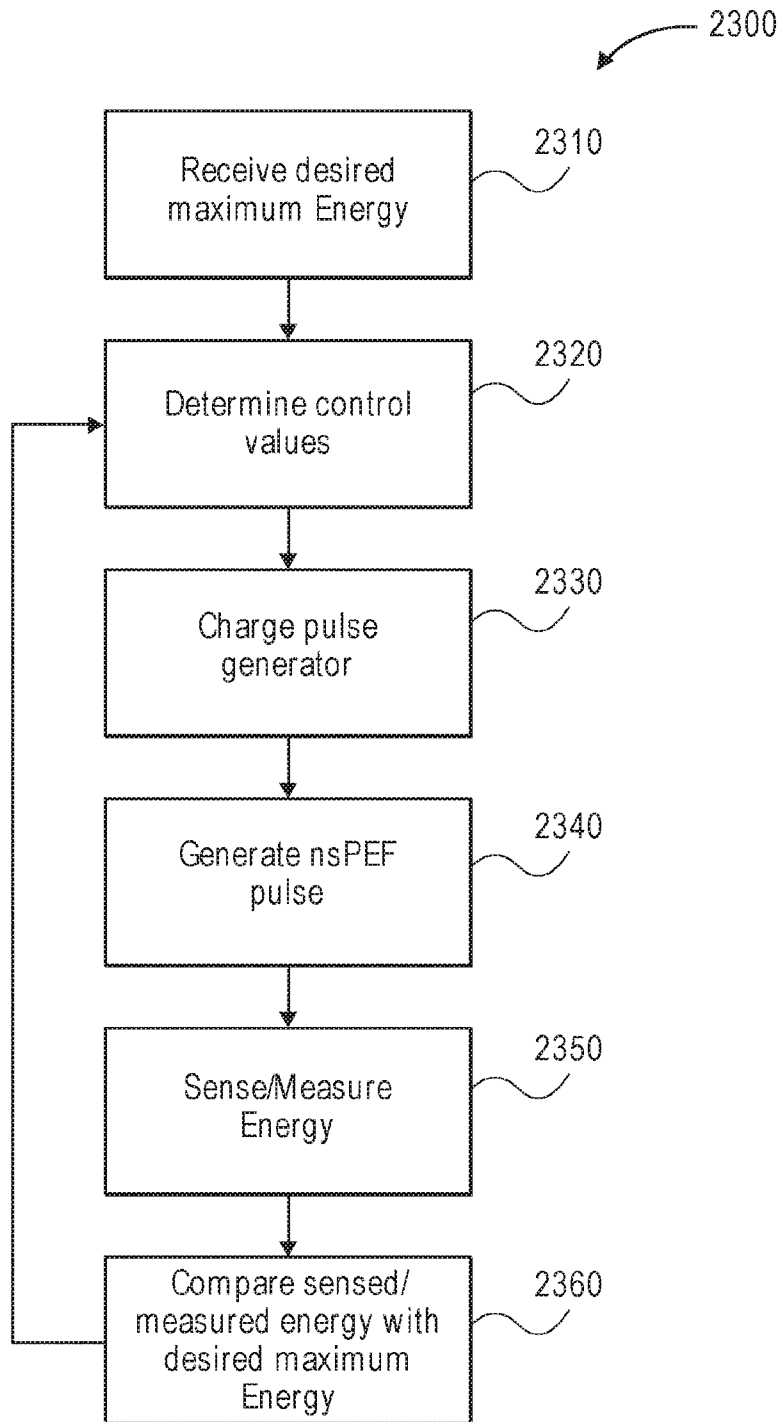


FIG. 23

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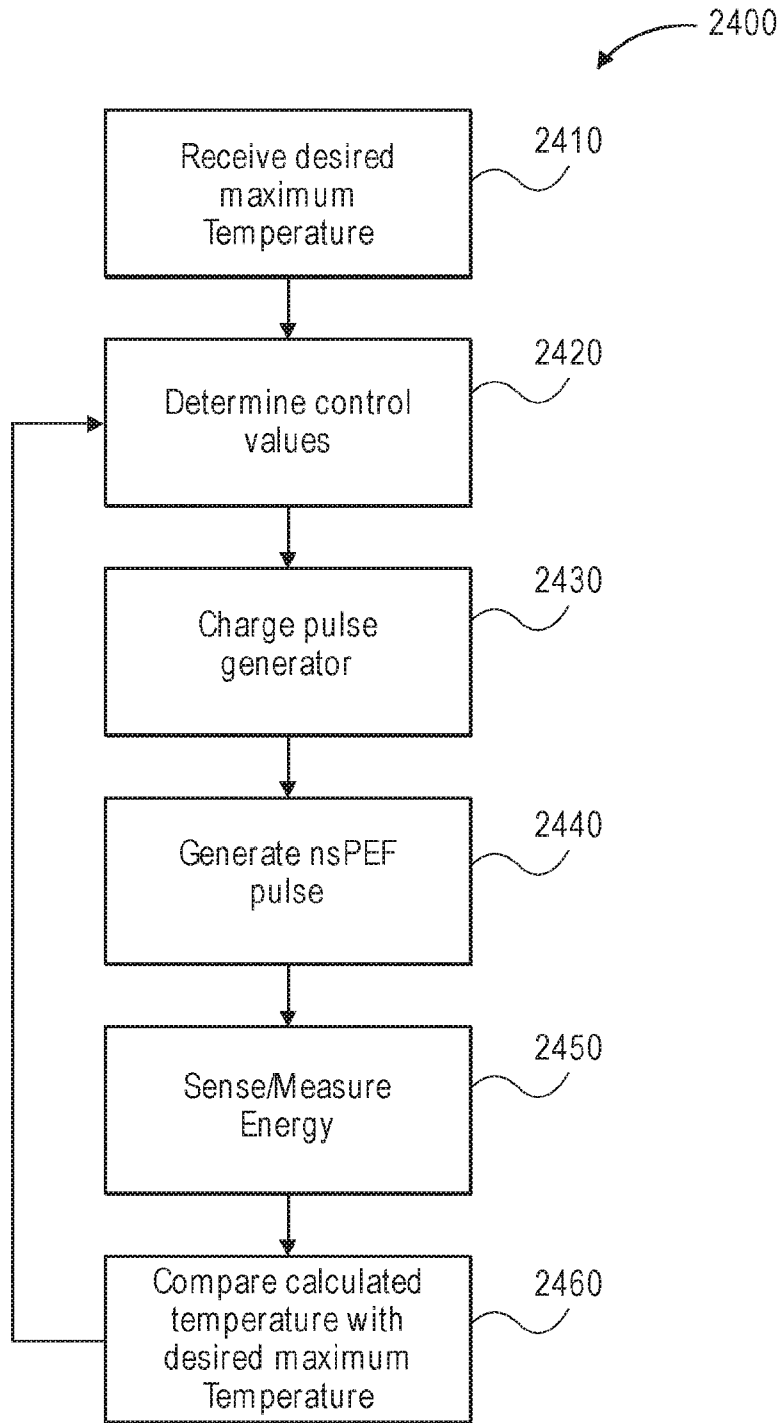


FIG. 24

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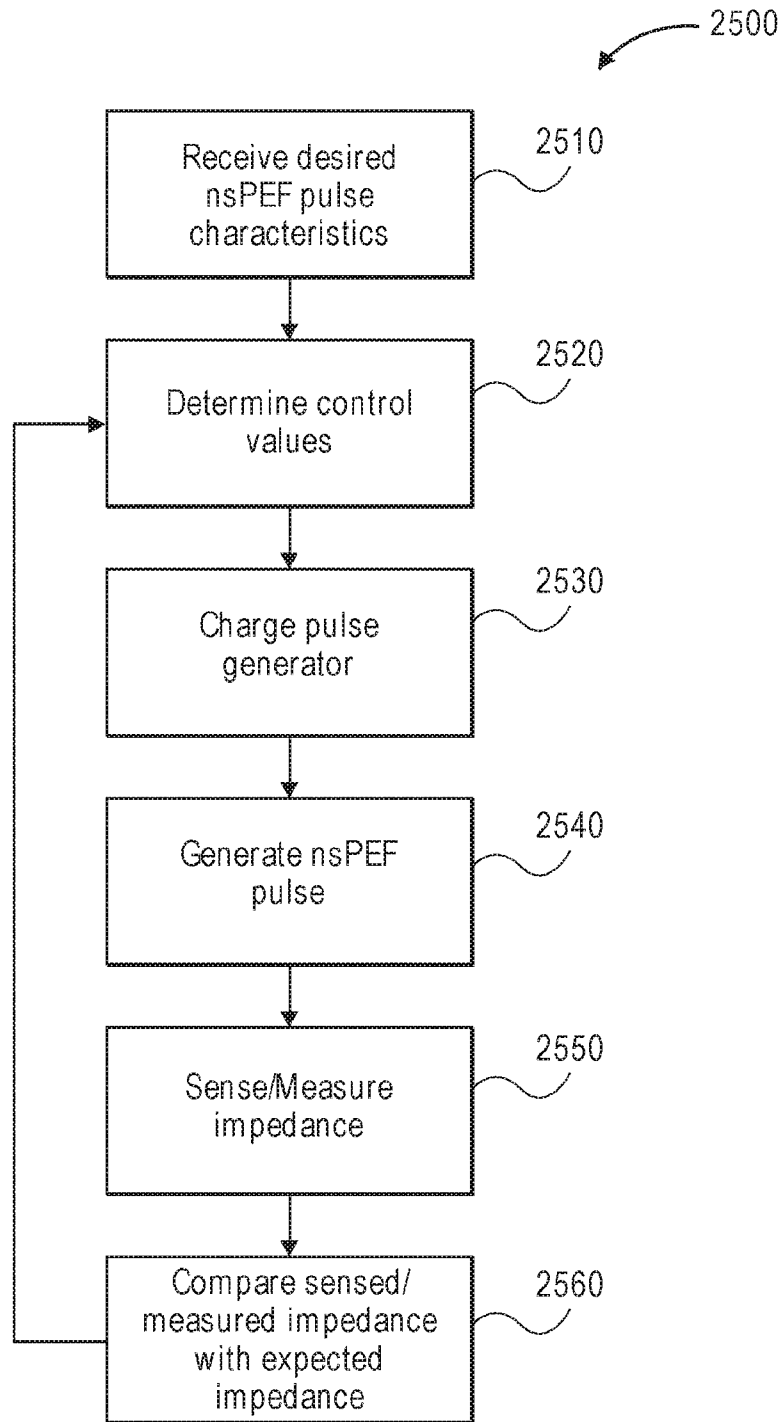


FIG. 25

