



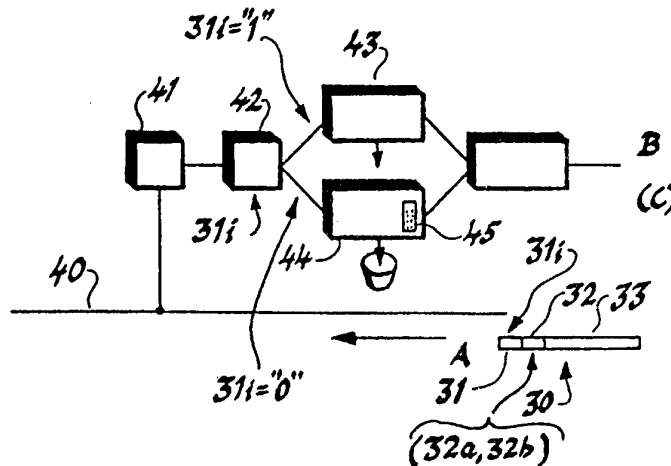
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(54) Title: PACKET DATA NETWORK

(57) Abstract

The present invention relates to a method and to a packet network in which a transmitting user unit (A) in the network is intended to transmit (40) an information sequence either to solely one selected receiver user unit (B) in the network or commonly to a selected number of receiving user units (B + C) in the network. Information sequences are comprised of a data cell (30) which is divided into a header (31, 32) and an information-carrying part (33). The header includes a bit configuration (32a, 32b) which, among other things, represents a destination address. The bit configuration in the data cell header shall be interpreted in accordance with the logic value of an indicated status bit (31i). The logic value of the status bit (31i) is category indicative and therewith determines (42) how a received destination address shall be interpreted, either as a directly coded address (43) pertaining to a single receiving user unit (B) or as a channel-number-coded address (44, 45) pertaining to a number of receiving user units (B + C).



The logic value of the status bit (31i) is category indicative and therewith determines (42) how a received destination address shall be interpreted, either as a directly coded address (43) pertaining to a single receiving user unit (B) or as a channel-number-coded address (44, 45) pertaining to a number of receiving user units (B + C).

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TITLE OF THE INVENTION:            PACKET DATA NETWORK

TECHNICAL FIELD

5        The present invention relates to and is intended for  
application within a packet data network in which a  
transmitting user unit in the network is intended to  
transmit an information sequence either to solely one  
selected receiving user unit in the network or to a  
10       commonly receiving selected number of user units in  
said network.

The invention primarily relates to a method of carry-  
ing out the aforesaid procedures and secondarily, to a  
15       packet data network adapted to carry out the method.

The invention is also based on the requisite exchange  
of information between the user units of the packet  
network being effected in the form of data cells or  
20       data packets, where each data cell may advantageously  
be a standardized data cell with regard to the number  
of bit positions within a bit field and the distribu-  
tion of these bit positions, and that the data cell is  
therewith divided into a specified header and a speci-  
25       fied information-carrying part.

The header includes distributed bit fields and is  
allocated bit configurations which represent, among  
other things, a destination address, while the infor-  
30       mation part includes distributed bit fields and is  
allocated bit configurations which represent relevant  
information, among other things.

When a user unit forming part of the packet network  
35       and belonging to the system wishes to transmit one and  
the same information sequence in the form of a data  
cell to a plurality of selected receiving user units,  
the transmitting user unit allocates to the bit

configuration representative of the destination address in the header a common identity which is representative of and significant to each selected receiving user unit, wherein when receiving said data cell  
5 each selected receiving user unit will react to accept the whole of said data cell or at least its information part.

The invention is also a development of the procedure  
10 in which when a user unit that forms part of the packet network and belongs to the system wishes to transmit an information sequence in the form of a data cell to solely one receiving user unit, the transmitting user unit allocates to the bit configuration  
15 representative of the destination address in the header an identity which is significant solely to the selected receiving user unit, wherein when receiving the data cell the single selected receiving user unit will react to accept the whole of the data cell or at  
20 least its information-carrying part.

#### DESCRIPTION OF THE PRIOR ART

There are known to the art several different examples  
25 of packet networks which comprise a plurality of user units and in which the signal information or the information sequence is particular and standardized and is formed by one or more data cells, each having a CCITT standardized, constant length of 53 octettes, of  
30 which 5 octettes are used for a header which includes, among other things, a virtual destination address, and 48 octettes which are used to carry information, i.e a so-called information-carrying part.

35 A packet network which includes packet switches and which forms part of a telecommunication system is known per se and the present invention obtains particular suitable application when the packet switches used are constructed on an ATM system (Asynchronous

Transfer Mode) and where the data cells used are allocated internally in the switch equipment a special structure of extended length in relation to standardized data cells, for instance a length extended by 5  
5 octettes.

Also belonging to the earlier state of the art is thus a packet network in which each user unit is allocated a respective identity and in which the addressing  
10 procedure is based on the selection of the destination address in the data cell similar to the identity of the receiving user unit.

This application requires only one simple decoder for  
15 each user unit.

In this regard, it is known that it is impossible to address one and the same data packet or data cell to several receiving user units at one and the same time  
20 in a system of this kind.

It is also known to create in a packet network conditions which will enable one and the same packet to be transferred to several receiving user units simulta-  
25 neously.

In this regard, it has been proposed to use some form of channel number as the destination address.

30 In the case of earlier known packet networks, the use of channel numbers has made it necessary to provide each user unit with a table which contains information relating to the channel numbers that are intended precisely for a particular user unit.

35 It is therefore obvious that such a table must be updated each time the channel numbers in use are changed.

In the case of large systems, experience has shown that when a packet network requires a the provision of a table for each user unit, or at least for each receiving user unit, the tables become unmanageably  
5 large.

It can be mentioned here that each data cell or data packet has different bit fields, each representing different functions and carrying different information,  
10 tion, as a result of allotting the bit positions within respective fields a bit configuration corresponding to desired function and information.

The most relevant prior art is disclosed in WO-A1-  
15 92/10898. This publication discloses a system capable of providing a "multicast" function, i.e. a function in which one and the same information content can be sent by a transmitter to all receivers, and a "unicast" or a "direct" function, i.e. a function in which  
20 information content can be sent by a transmitter to a selected receiver, all in accordance with the basic concepts of the present invention.

According to the invention, a distinction is made  
25 between a packet intended for "multicast" and a packet intended for "unicast" by inserting additional bits.

Reference is made to a four-port-system 10, where each of four inputs shall be capable of connection to each  
30 of four outputs, with the aid of switch means 20.

To this end, the switch means 20 includes four ports 0-3, where each port coacts with a control unit 300-303 which functions as an interface between different  
35 data formats and the data format required by the switch means 20.

Each of the control units 300-303 has at least two connections to the switch means 20, i.e. a "multicast" addressing connection and a "unicast" addressing connection in addition to the unit 300, where both of these shall have the same physical line and are comprised of the port "0".

The control units 301-303 are connected to "unicast" addressable ports by connections 25 and to "multicast" addressable ports by connections 28.

Packets incoming to each of the units 300-303 are evaluated and an internal address part (13) for the switch means (20) is generated in the units 300-303 in accordance with the addressing part (Header) of the received packet.

This address part (13) is generated in accordance with a table look-up. This table indicates whether the address part (13) shall be provided with a "unicast" or "multicast" indication and a bit instruction corresponding to the choice made.

It shall be noted in this regard that the switch means 20 is constructed to point to a port 1, 2 and 3 corresponding to an indicated control unit 301-303 in the case of a "unicast" indication.

In the case of a "multicast" indication, a particularly adapted "multicast" port is indicated, namely the port "0".

Of necessity this arrangement requires the use of a selected port "0" each time the "multicast" function is selected, this port being connected to a separate (physical) communication path (28) which is adapted precisely for this function.

Each of the control units 300-302 notes the packet occurring on the "multicast" connection 28 and read this packet solely when coordination is found between the sum of the units and the selected bit positions in the "multicast" address.

Thus, all control units are connected to the "multicast" function through the separate, inserted communication path (28).

If no control unit wishes to take part in the "multicast" function, there is found in each control unit an internal table which must be read and when necessary block reception.

DISCLOSURE AND CONSIDERATIONS RELATED TO THE PRESENT INVENTION.

#### TECHNICAL PROBLEMS

When considering earlier known packet networks together with known telecommunication systems, and then in particular telecommunication systems of the kind defined in the introduction and provided with packet switches, it will be seen that a technical problem resides in the ability to create conditions with relatively simple measures that will reduce the necessary hardware and which will also simplify the necessary signalling procedure within the user units and the switching equipment respectively.

When considering the limited number of positions found in the bit fields in a data cell, and when considering the strict distribution with regard to the form of the different bit fields and the number of bit positions and in particular the structure of the addressing field, it will be seen that a technical problem is one of enabling the addressing possibilities to be increased with the aid of simple means.

Another technical problem is one of realizing the significance of using in a data cell which has already



strict distribution a selected bit position which is separate from those bit positions that are normally used for addressing purposes to point-out one of two addressing categories of which a first addressing category points to an interpretation of the address-indicating bit positions according to which said positions apply to a number of selected user units and indicate common transmission of information to these user units.

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In this regard, a technical problem is one of finding within a specific, clearly defined data cell having bit positions and bit fields which are well defined for different functions, a bit position and a bit field which are well suited for addressing category selection and which lie outside those bit positions that are normally used for addressing purposes and which therewith decisively increase the addressing possibilities within the standard format of the data cell.

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Another technical problem is one of creating conditions with the aid of simple means which will offer a table to respective user units, even in large systems, but with a limited, readily handled number of channel numbers inserted in the table.

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It will also be seen that a technical problem is one of realizing the significance of being able to re-structure in a special manner the bit configuration within the bit field of the data cell applicable to but located outside the actual destination address, so that a bit position selected by the transmitting user unit will indicate clearly that a transmitted information sequence or data cell is intended solely for one single selected receiving user unit belonging to the network, or that a transmitted information sequence is intended to be received commonly by a se-

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lected number of receiving user units belonging to said network.

Another technical problem in the present connection is one of realizing the significance of supplementing a destination address with a status bit which belongs to the destination address but which is separated from the destination-address bit field and points to a category, this status bit disclosing how the virtual destination address shall be interpreted. For instance, when the status bit is a logic "1", the address is interpreted as a direct coding of the identity of the receiver, whereas when the status bit is a logic "0", the address is interpreted as a general channel number which shall be translated with the aid of a table.

It will also be seen that a technical problem is one of realizing that the requisite tables belonging to the user units can be made much smaller than in the general case of the earlier known techniques, when the number of information transmissions to a selected user unit is much larger than the number of information transmissions common to a selected number of user units.

#### SOLUTION

With the intention of solving one or more of the aforesaid technical problems, the present invention provides a method for transmitting from a transmitting user unit belonging to a packet network, such as a packet network forming part of a telecommunication system, an information sequence in the form of a data cell, either solely to a single selected receiving user unit in the network or commonly to a selected number of receiving user units in said network.

The information content between transmitting user unit and receiving user unit is comprised of a data packet or a data cell which is comprised of a header and an information-carrying part, where the header includes one or more bit fields which represent a virtual destination address among other things, and the information part includes a bit field which represents relevant information among other things, wherein prior to transmitting said data cell solely to one receiving user unit a transmitting user unit allocates an identity, i.e. a direct coded address, to the bit configuration within the field representative for the destination address in said header, said identity being significant solely to the single selected receiving user unit, wherein upon receipt of said data cell the receiving, selected user unit reacts to accept the data cell or at least its information part.

The present invention expands upon this procedure insofar that prior to transmitting a data cell for common receipt in a selected number of receiving user units, a transmitting user unit allocates an identity which is common to said transmission, via direct addressing to each selected receiving user unit, whereafter the transmitting user unit transmits a data cell in which the bit configuration of the header within said field represents, among other things, the common destination address in the form of a channel number coded address, wherein upon receiving the data cell each receiving user unit selected by said transmitting user unit will react to accept the data cell as a common transmission and receive data cell or at least its information content in common with other receiving user units.

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According to the present invention, the address-dependent bit configuration within the addressing field of the data-cell header shall be interpreted in accordance with the logic value of an indicated status bit;

in that the indicated status bit is selected from a bit field which is separate from the bit field that includes said address-dependent bit configuration; and in that the logic value of the indicated status bit is comprised of a category indication and therewith determines how a received destination address shall be interpreted in the case of a first logic value, which is a direct coded address, and in the case of a second logic value, which is a channel number coded address.

The invention also relates to a packet network having a connection that can be used for a plurality of user units and including information exchange or information transmission between two or more user units with the aid of one or more data cells, each comprising a header and an information part among other things, where the header includes a bit configuration within the addressing field representing, among other things, a destination address, and an information part includes within the information field a bit configuration which represents relevant information, wherein each user unit is allocated an identity significant thereto.

According to the present invention, the destination-address-dependent bit configuration of the data cell header is intended to be interpreted by first means in accordance with an indicated status-bit logic value which at a given first value shall be interpreted so as to decode the given destination address as a direct coding of an identity allocated to a receiving user unit, and which at another specific value shall be interpreted so as to decode the given destination address as a channel number belonging to a plurality of receiving units.

The data cell shall be comprised of an ATM data cell.

The indicated status bit seizes one or more bit positions located outside the addressable section or field in the header.

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#### ADVANTAGES

Those advantages primarily afforded by an inventive method and an inventive packet network are that the destination address of the data cell has been structured so that in addition to the bit positions of the addressing field, the data cell also includes at least one category-selecting status bit which is separate from the addressing bit positions of the addressing field and which denotes how a relevant address shall be interpreted. When such a status bit is used, its value shall either be interpreted as a direct coding of the identity of the receiver or as a general channel number which shall be translated with the aid of a table belonging to respective user units.

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This results in an expansion of the addressing possibilities.

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The primary characteristic features of an inventive method are set forth in the characterizing clause of the following Claim 1, while the main characteristic features of an inventive packet network are set forth in the characterizing clause of the following Claim 2.

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#### BRIEF DESCRIPTION OF THE DRAWINGS

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A number of exemplifying embodiments at present preferred and having features significant to the present invention will now be described in more detail with reference to the accompanying drawings, in which

Figure 1 illustrates the principle construction of a packet network in which a plurality of user units are mutually connected in a ring structure;

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Figure 2 illustrates the principle construction of a packet switch included in a telecommunication system in which the characteristic features of the present invention can be well applied;

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Figure 3 illustrates an example of a bit configuration within different bit fields, among other things for the header of a data cell; and

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Figure 4 is a block schematic which illustrates in a highly simplified manner the means required in each user unit to provide the possibility of evaluating and interpreting the bit configuration of a selected bit position in the data cell.

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#### BEST MODES OF CARRYING OUT THE INVENTION

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Shown in Figure 1 is a packet network in which a plurality of user units A, B, C.....J are mutually connected to form a ring structure. It is assumed that each unit is able to transmit or to receive one or more information sequences in the form of data cells.

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The manner in which the packet network operates in accordance with the present invention will be described in more detail in the following.

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Figure 2 is a highly simplified illustration of ATM switch equipment 2 forming part of a telecommunication system 1.

The ATM technique is known to the art and consequently this technique will not be described in detail here, for the sake of simplicity.

5 A number of incoming links 4 are connected to the switch equipment 2 via first switch ports 3, while a number of outgoing links 6 are connected to the switch equipment 2 via second switch ports 5.

10 The switch equipment 2 includes control means 10 which includes a processor. The processor may alternatively be placed separate from the control means. The switch equipment further includes switching means 11 for  
15 connecting an incoming link 4 to an indicated outgoing link 6, where indication of the outgoing link 6 is initiated by signals in the form of one or more data cells appearing on the incoming link 4.

20 These switch-external data cells 30 have a CCITT standard, with 5 octettes in a header that contains a virtual destination address among other things, and 48 octettes in an information part that contains the actual information among other things, these data cells being dimensioned as standard ATM data cells.

25 The features or properties associated with the present invention are not limited solely to standardized switch-external data cells, but can also be readily applied to switch-internal data cells that derive from  
30 standardized data cells.

For this latter application, it is proposed in accordance with the invention that in a first port, additional information in the form of an additional bit  
35 configuration which presents different bit fields is supplied to a standardized switch-external data cell, and that these additional bits (5 octettes) are allocated a bit configuration, among other things in accordance with address information contained in the

data cell header, which results in said additional bits being used solely internally in the switch equipment to control selected functions. These additional bits shall be removed in the second port 5.

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Figure 3 illustrates schematically the structuring of the bit fields 31 applicable to said additional bits that have been supplied to the data cell 30, having a header 32 and an information part 33, and thereby form a switch-internal data cell 30' outgoing from the first port 3.

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A more detailed description of how these additional bit fields 31 that contain control information can be formed and utilized is given in Swedish Patent Application No. 92 03332-3 filed on the 9th November, 1992, and entitled "Distinguishing Connections".

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Figure 3 is intended to illustrate an example of how the bit positions can be distributed within a field or area 31 intended for the additional bits, and within a field or area 32 intended for the data cell header.

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The positions 31a represent RI (Routing Information), the positions 31b represent SEQ (Cell Sequence Number), the positions 31c represent MCI (Multicast Indication), the positions 31d represent ICLP (Implicit Cell Loss Priority), the positions 31e represent IDP (Implicit Delay Priority), the positions 31f represent CID (Cell Identifier), the positions 31g represent PLS (Plane Select), the positions 31h represent OAM (Operation and Maintenance) and the position 31i represents AM (Address Mode).

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The positions 32a represent VPI (Virtual Phase Identifier), the positions 32b represent VCI (Virtual Channel Identifier), the positions 32c represent PT (Payload Type) and the positions 32d represent CLP (Cell Loss Priority).

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A study of the bit field distribution illustrated in Figure 3 will show that the fields or the positions 32a and 32b primarily represent the addressing information and that the number of available addresses is limited by the specific number of available bit positions. Information stored in positions 32a and 32b is transformed to positions within the field 31.

In order to double the number of available addresses, there is used in accordance with the invention an additional bit position 31i (AM) whose value is intended to indicate the interpretive category of the address in the data cell.

The logic value of the bit positions 31i, or another indicated status bit, is thus category indicating and determines how a received destination address shall be interpreted. When the logic value has a first value, the address is interpreted as a directly coded address which belongs to one single receiving unit, whereas when the logic value has another value, the address is interpreted as being the same address as a channel-number-coded address belonging to a plurality of receiving units.

Figure 4 is intended to illustrate that each user unit shall be connected to a common information carrier 40 via a register 41, such as a buffer register, irrespective of whether the user unit is included in a packet network constructed in accordance with Figure 1 or in switch equipment 2 which is included in a telecommunication system according to Figure 2.

The buffer register 41 is connected to an address interpreter 42 which is intended to detect the logic value in the position 31i (Address Mode).

Should the interpretation in the unit 42 indicate that the data cell has a direct address, the address is

evaluated in a direct-address evaluating device 43 to establish whether or not the direct address is intended for the user unit B concerned.

5 If the device 43 ascertains that the direct address received is correct and applicable, a switch is made from the connection 40 to the user unit B which then receives the data cell. Otherwise, the data cell is rejected in an earlier known manner.

10

Should the address-interpreting unit 42 detect that the data cell carries a channel-number-coded address, this address is sent to a device 44 which evaluates channel-number-coded addresses and which looks-up a table 45 in order to establish whether or not the channel number is found in the table.

15

If the channel number in question is not stored in the table, the data cell is rejected in an earlier known manner. If the channel number is stored in the table 20 45, a switch is made so that the data cell can pass through the device 44 and to the user unit B.

20

The significant features of the inventive method and the manner in which the packet network operates will now be described with reference to Figures 1 and 2.

25

The invention is based on the concept of transmitting from a transmitting user unit A within a packet network or within switching equipment included in a telecommunication system an information sequence either to solely one selected receiving user unit B belonging to said network or to a selected number of commonly receiving user units B, C in said network.

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The information sequence is comprised of a data cell that is divided into a header and an information-carrying part, wherein the header includes a bit configuration within different bit fields which repre-

sents, among other things, a destination address, and the information-carrying part includes a bit configuration which represents relevant information among other things, wherein when transmitting said data cell  
5 to solely one receiving user unit, the transmitting user unit allocates to the destination-address bit configuration in the header an identity which is significant solely to the selected receiving user unit, and wherein when receiving said data cell the  
10 selected receiving user unit B reacts in the afore-described manner to accept the whole of the data cell or at least its information part.

When using in a packet network according to Figure 1  
15 data cells which lack an additional bit configuration 31, the requisite difference between a direct address and a common address, via a channel-number-coded address, resides in a selected bit position which is preferably separate from the bit configurations 32a  
20 and 32b, whereas in the Figure 2 embodiment the difference between a direct address and a channel-number-coded address will advantageously lie in solely the section 31i.

25 When transmitting data cells which are intended to be received commonly by a selected number of receiving user units B, C, the transmitting user unit A allocates to each selected receiving user unit B, C via a direct address, an identity which is common to all  
30 selected user units, this identity being received and stored in the device 44 which evaluates the channel-number-coded addresses, and particularly in the table 45 associated with the device 44.

35 The common identity is thus a channel-number-coded address.

When the transmitting user unit A then wishes to transmit a data cell which is common to each of the

receiving user units B and C, the data cell header will exhibit a unique bit configuration which, among other things, represents the common destination address or the channel-number-coded address, wherein  
5 upon receipt of said data cell each selected receiving user unit B, C will react to accept the data cell or at least its information-carrying part.

The exemplifying embodiment shown in Figures 2 and 3  
10 shows particularly that the address-dependent bit configuration pertaining to the data cell header shall be structured so that the logic value of an indicated status bit will show how a received destination address shall be interpreted.

15 In the case of a first logic value, the destination address shall be interpreted as a direct coded address, while in the case of a second logic value, the destination address shall be interpreted as a channel-  
20 number-coded address.

In each case, both with respect to the embodiment illustrated in Figure 1 and the embodiment illustrated in Figure 2, the bit configuration of the data cell,  
25 particularly with regard to the destination address, will be so structured that the logic value of an indicated status bit will determine how the destination address concerned shall be interpreted.

30 A decoded general channel number for each selected receiving user unit will require a transposition with the aid of a table associated with respective receiving user units, wherein the bit configuration representing the information-carrying part will be received  
35 by one or more user units, each adapted to receive the information over said general channel number.

In the case of a standardized data cell with bit positions solely within the regions 32 and 33, one (or

more) bit position within the field 32e is used as a status bit, this field normally being a control field.

5 The region or area 33 will also include free fields or parts thereof which can be used as additional addressing bits or status bits.

10 It should be mentioned that the use of an additional status bit outside the addressing field will theoretically double the number of available addresses. Naturally, the number of available addresses will be further increased when two status bits are used.

15 By "common reception" is meant primarily the reception of one and the same data cell in several user units, which need not necessarily be synchronous reception in time.

20 When two status bits are used, the second status bit may be used to initiate the transmission of signals to all receiving units or to preselected units without previous addressing via available addressing fields in the data cell.

25 It will be understood that the invention is not restricted to the aforescribed and illustrated exemplifying embodiments thereof, and that modifications and changes can be made within the scope of the inventive concept as illustrated in the following Claims.

### Claims :

1. A method of transmitting an information sequence from a transmitting user unit (A) in a packet network on a communication means or a connection (40) either to solely one single selected receiving user unit (B) in the network or commonly to a selected number of receiving user units (B+C) in said network, said transmitted information sequence being comprised of one or more data cells, each comprising a header and an information-carrying part, wherein the header includes within a bit field a bit configuration which represents a destination address, among other things, and the information-carrying part includes within a bit field a bit configuration which represents relevant information, among other things, wherein when transmitting a data cell which is to be received by solely one receiving user unit (B), the transmitting user unit allocates to the bit-configuration for the destination address in the header an identity which is significant solely to the single selected receiving user unit (B), wherein upon receipt of said data cell, the selected receiving user unit (B) reacts to accept the data cell or at least its information-carrying part, wherein when transmitting a data cell which is intended to be received by a plurality of selected receiving user units (B+C), the transmitting user unit allocates to the bit configuration representing the destination address in the header an identity which is common and significant to the selected receiving user units, wherein upon receiving said signal, each of said selected receiving user units (B+C) reacts to accept the data cell or at least its information-carrying part, characterized in that each user unit is programmed to interpret the logic value of an indicated status bit as a category indication; in that the address-dependent bit configuration (32a, 32b) pertaining to each data cell header appearing on the communication means or connection (40) is

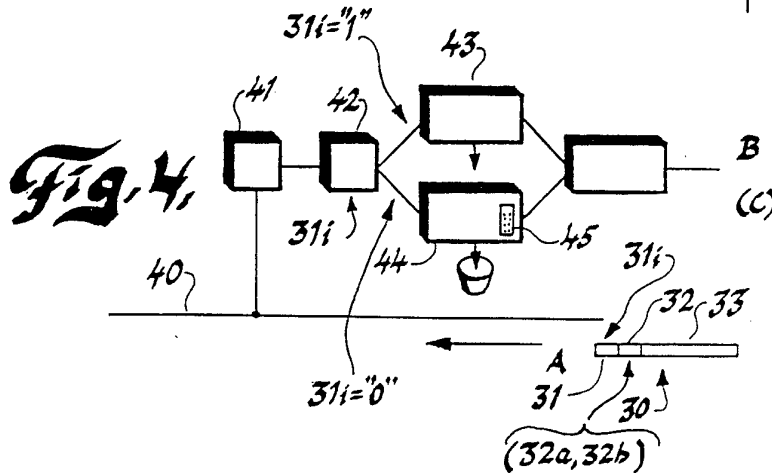
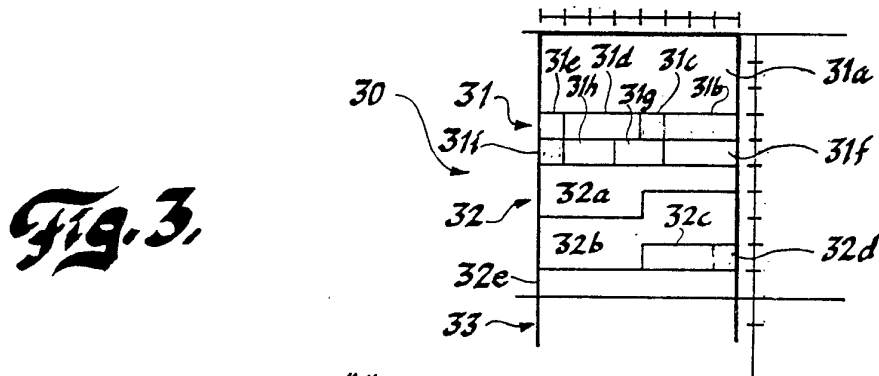
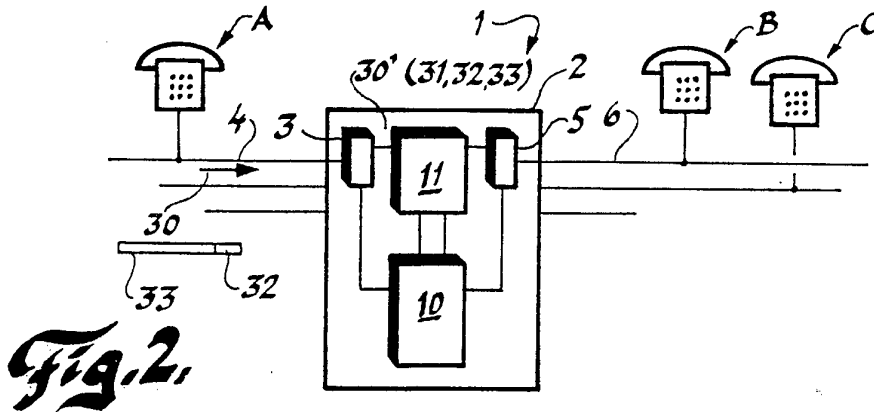
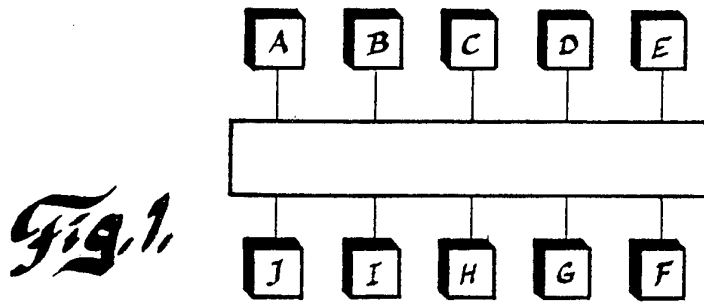
interpreted in each user unit in accordance with the logic value of an indicated status bit (31i) so as to determine therewith how a received destination address shall be interpreted, wherein, as a result of said programming of said units, in the case of a first logic value each user unit shall interpret the address as a direct coding of an address which is pertinent to solely one receiving user unit and the information sequence in said packet is therewith received solely in the user unit corresponding to the indicated address, and in the case of a second logic value the address is interpreted as a channel-number-coded address pertaining to a plurality of receiving user units and the information sequence pertaining to said packet is therewith received in each of the user units that correspond to the indicated address.

2. A packet network having a connection (40) which can be used by a plurality of user units and in which information is exchanged between two or more user units with the aid of one or more data cells, each comprising, among other things, a header and an information-carrying part, said header including within a bit field a bit configuration which is representative of a destination address, among other things, and said information-carrying part including within a bit field a bit configuration which is representative of relevant information, and wherein each user unit is allocated an identity, characterized in that, among other things, each user unit is able to receive a status bit (31i) which is separate from the destination-address dependent bit configuration (32a, 32b; 31a) belonging to the data cell; in that each user unit is programmed to interpret the value of the status bit (31i), which in the case of a given first value shall be interpreted as the given destination address decoded as a direct coding of an identity allocated to a single receiving user unit, and in the case of a second given value shall be interpreted so

as to decode the given destination address as a common identity or an associated channel number allocated to a plurality of receiving units.

- 5           3. A packet network according to Claim 2, c h a r -  
a c t e r i z e d that in the case of a coded channel  
number, there is required for each selected receiving  
user unit a transposition with the aid of a table  
10           belonging to respective receiving user units, wherein  
the bit configuration representing the information-  
carrying part is received by each of the receiver  
units adapted to receive the information over said  
general channel number.
- 15           4. A packet network according to Claim 2 or 3,  
c h a r a c t e r i z e d in that the data cell is an  
ATM data cell.
- 20           5. A packet network according to Claim 4, c h a r -  
a c t e r i z e d in that the indicated status bit  
seizes one or more bit positions situated outside the  
standardized address-carrying section or field (32a,  
32b) of the header (32) of an ATM data cell.





## INTERNATIONAL SEARCH REPORT

International application No.

PCT/SE 93/01044

<b>A. CLASSIFICATION OF SUBJECT MATTER</b>		
IPC5: H04L 12/56, H04L 12/18 According to International Patent Classification (IPC) or to both national classification and IPC		
<b>B. FIELDS SEARCHED</b>		
Minimum documentation searched (classification system followed by classification symbols)		
IPC5: H04J, H04L, H04M, H04Q		
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched		
SE,DK,FI,NO classes as above		
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)		
ORBIT: WPAT		
<b>C. DOCUMENTS CONSIDERED TO BE RELEVANT</b>		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	WO, A1, 9210898 (OTC LIMITED ET AL), 25 June 1992 (25.06.92), page 2, line 19 - page 3, line 26, figures 1-2, claims 1-3 --	1-5
X	EP, A2, 0255767 (AMERICAN TELEPHONE AND TELEGRAPH COMPANY), 10 February 1988 (10.02.88), column 1, line 12 - column 3, line 50 --	1-5
X	EP, A2, 0420493 (AMERICAN TELEPHONE AND TELEGRAPH COMPANY), 3 April 1991 (03.04.91), column 4, line 41 - column 5, line 19; column 9, line 56 - column 11, line 21, figures 2-4,10, abstract --	1-5
<input checked="" type="checkbox"/> Further documents are listed in the continuation of Box C. <input checked="" type="checkbox"/> See patent family annex.		
<p>* Special categories of cited documents:</p> <p>"A" document defining the general state of the art which is not considered to be of particular relevance</p> <p>"B" earlier document but published on or after the international filing date</p> <p>"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)</p> <p>"O" document referring to an oral disclosure, use, exhibition or other means</p> <p>"P" document published prior to the international filing date but later than the priority date claimed</p> <p>"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention</p> <p>"X" document of particular relevance: the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone</p> <p>"Y" document of particular relevance: the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art</p> <p>"&amp;" document member of the same patent family</p>		
Date of the actual completion of the international search		Date of mailing of the international search report
22 March 1994		28 -03- 1994
Name and mailing address of the ISA/ Swedish Patent Office Box 5055, S-102 42 STOCKHOLM Facsimile No. +46 8 666 02 86		Authorized officer Magnus Stiebe Telephone No. +46 8 782 25 00

## INTERNATIONAL SEARCH REPORT

International application No.

PCT/SE 93/01044

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US, A, 4566095 (MICHEL DEVAULT ET AL), 21 January 1986 (21.01.86), column 2, line 7 - line 47, claims 1-2  --	1-5
A	GB, A, 2254980 (ROKE MANOR RESEARCH LIMITED), 21 October 1992 (21.10.92), see the whole document  --	1,2
A	US, A, 5144619 (ERNST A. MUNTER), 1 Sept 1992 (01.09.92), abstract  -- -----	

**INTERNATIONAL SEARCH REPORT**  
Information on patent family members

26/02/94

International application No.  
PCT/SE 93/01044

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
WO-A1- 9210898	25/06/92	AU-A- 9071191 EP-A, A- 0561888	08/07/92 29/09/93
EP-A2- 0255767	10/02/88	AU-A- 7618987 JP-A- 63039231	04/02/88 19/02/88
EP-A2- 0420493	03/04/91	JP-A- 3119846 US-A- 4991171	22/05/91 05/02/91
US-A- 4566095	21/01/86	CA-A- 1211824 EP-A, B- 0126196	23/09/86 28/11/84
GB-A- 2254980	21/10/92	EP-A- 0509648	21/10/92
US-A- 5144619	01/09/92	CA-A- 2058816	12/07/92