Methods of forming metal carbide films are provided. In some embodiments, a substrate is exposed to alternating pulses of a transition metal species and an aluminum hydrocarbon compound, such as TMA, DMAH, or TEA. The aluminum hydrocarbon compound is selected to achieve the desired properties of the metal carbide film, such as aluminum concentration, resistivity, adhesion and oxidation resistance. In some embodiments, the methods are used to form a metal carbide layer that determines the work function of a control gate in a flash memory.
FIGURE 2
ATOMIC LAYER DEPOSITION OF METAL CARBIDE FILMS USING ALUMINUM HYDROCARBON COMPOUNDS

REFERENCE TO RELATED APPLICATIONS

[0001] The present application claims priority under 35 U.S.C. §119(e) to U.S. provisional application No. 61/045,554, filed Apr. 16, 2008, the disclosure of which is hereby incorporated by reference.

BACKGROUND OF THE INVENTION

[0002] 1. Field of the Invention
[0003] The present invention relates generally to vapor deposition processes and, more particularly, to deposition of metal carbide films by vapor deposition processes.

[0004] 2. Description of the Related Art
[0005] Metal carbides have found use in various applications in the electronics industry, from gate electrodes to diffusion barriers. For example, tantalum carbide (TaC) is a low resistivity metal that can be used as an n-type metal oxide semiconductor (N莫斯) gate electrode. Further, TaC has been found to be effective at inhibiting electromigration of noble metal atoms at the interface between metal interconnects and metal lines.

[0006] Generally, carbides of transition metal elements are in groups 4, 5, 6, 7, 8, 9, 10 and 11 of the periodic table. Transition metal carbides are relatively inert, have very high melting points, are extremely hard and wear resistant, and have high thermal conductivity and metal-like electrical conductivity.

[0007] Transition metal carbides can have a wide range of compositions. Ordered and disordered carbon deficient forms exist, of which the tungsten carbides, WC, are examples. In these forms, carbon resides in the interstitial cavities between metal atoms.

[0008] Metal carbide films have been formed by various methods including chemical vapor deposition (CVD), physical vapor deposition (PVD) and atomic layer deposition (ALD).

[0009] A “thermal” ALD method of forming metal carbide films, wherein the substrate is sequentially and alternately contacted with vapor phase pulses of two or more source chemicals, is described in, for example, U.S. Pat. No. 6,482,262. According to the methods described therein, a transition metal source chemical and carbon source gas are alternately and sequentially pulsed into a reaction space comprising a substrate, which is maintained at an elevated temperature. The pulsing sequence is repeated to form a metal carbide film (e.g., TaC) film of desired thickness. Due to the self-limiting nature of ALD films, growth rate is determined by the amount and composition of the carbide layer per deposition cycle.

[0010] A CVD method of depositing tungsten carbide from tungsten hexafluoride, hydrogen and a carbon-containing gas has been described in, for example, international patent application WO 00/47796. The carbon-containing compound is initially thermally activated. All of the gaseous source chemicals are introduced into a reaction space at the same time, resulting in the formation of volatile tungsten carbide on the substrate. A CVD reaction of WF6 with trimethylamine and H2 has been disclosed to yield WC films at 700°C.800°C and beta-WC1 films at 400°C.600°C. (Nakajima et al., J. Electrochem. Soc. 144 (1997) 2096-2100). The H2 flow rate was found to affect the deposition rate of the tungsten carbide films. A problem with the disclosed process is that the substrate temperature is rather high relative to thermal budgets for state-of-the-art semiconductor fabrication, particularly in metalization stages.

[0011] PVD processes generally deposit along a line-of-sight. One method of depositing tantalum carbide for a diffusion barrier layer by PVD has been described in U.S. Pat. No. 5,973,400. A tantalum carbide layer was formed by sputtering tantalum or tantalum carbide under an N2,CH3,Ar atmosphere. Line-of-sight deposition, however, means that complex substrate contours will have insufficient film coverage in shaded areas. Additionally, line-of-sight deposition means that low-volatility source material arriving directly from the source to the substrate will likely adhere to the first solid surface that it encounters, thus producing low-conformity coverage.

SUMMARY OF THE INVENTION

[0012] According to one aspect of the invention, methods for growing a metal carbide film over a substrate are provided. The methods generally comprise contacting a substrate in a reaction space with a first reactant that includes a metal source chemical and a second reactant that includes an aluminum hydrocarbon compound, thereby forming the metal carbide film over the substrate. The metal carbide film preferably comprises aluminum.

[0013] According to some embodiments of the invention, atomic layer deposition (ALD) processes for forming a metal carbide thin film on a substrate in a reaction space are provided. The methods comprising: alternately and sequentially contacting the substrate with vapor phase pulses of a first metal precursor and a first aluminum hydrocarbon compound, such that a metal carbide film comprising from about 6 to about 16% aluminum is formed. In some embodiments the aluminum hydrocarbon compound comprises one or more of trimethyl aluminum (TMA), triethyl aluminum (TEA), and dimethylaluminumhydride (DMAH).

[0014] In another aspect of the invention, metal carbide film comprising aluminum are deposited using aluminum hydrocarbon compounds. The amount of the aluminum can be controlled by selecting an appropriate aluminum hydrocarbon compound as the second reactant. Other reaction conditions, such as temperature pressure, pulse and purge length and plasma, can also be adjusted to achieve a desired aluminum concentration. In some embodiments the aluminum concentration is about 6%. In other embodiments the aluminum concentration is up to about 16%. However, higher concentrations are possible. By controlling the amount of aluminum, films with desirable characteristics can be formed, including low resistivity, good adhesion, and oxidation resistance.

[0015] In some embodiments the resistivity of the metal carbide film is controlled by selecting appropriate deposition conditions, including the aluminum hydrocarbon reactant, the deposition temperature and the deposition pressure.

[0016] In another aspect of the invention, methods of making metal carbides with good adhesion properties are provided. In some embodiments, tantalum carbide films are deposited by ALD using tantalum halide precursors and TEA.

[0017] In other embodiments, the oxidation resistance of a metal carbide film is controlled by controlling the amount of aluminum in the metal carbide film. The amount of aluminum can be controlled by selection of an aluminum hydrocarbon reactant for use in an ALD process as a carburizing agent and by adjusting other reaction conditions.
In some embodiments methods of forming a metal carbide thin film with a desired level of oxidation resistance are provided. The methods comprise: depositing a metal carbide thin film by alternately and sequentially contacting a substrate with vapor phase pulses of a metal precursor and an aluminum hydrocarbon compound, wherein one or more reaction conditions are selected to produce a desired concentration of aluminum in the metal carbide thin film, and wherein the concentration of aluminum in the metal carbide is from about 1 to about 30%. In other embodiments, the work function of a gate electrode is determined by controlling the amount of aluminum in a metal carbide film. The gate electrode may be, for example, a control gate in a flash memory structure or a gate electrode in a CMOS transistor. A gate stack can comprise a first gate electrode layer and a second gate electrode layer. The first gate electrode layer comprises a first metal carbide gate electrode material and the second gate electrode layer comprises a second gate electrode material, such as polysilicon, titanium or tantalum nitride or tungsten. Preferably the first and second gate electrode materials are conductive. In preferred embodiments, the first gate electrode material is different from the second gate electrode material. The work function of the gate electrode may be determined by the first metal carbide gate electrode material.

Methods for forming a flash memory comprising a metal carbide layer are also provided. In preferred embodiments a dielectric layer (tunnel oxide) is deposited over a substrate and a floating gate is deposited directly over the dielectric layer. The floating gate may comprise, for example, polysilicon. In some embodiments, such as for a TaNOS flash structure, a charge trap layer is used in place of the floating gate. The charge trap layer may be silicon nitride. A barrier oxide, such as AlO_x, is deposited over the floating gate or charge trap layer and a control gate is formed over the barrier oxide.

In some embodiments the methods for forming a flash memory on a substrate comprise: forming a dielectric layer on the substrate; forming a charge trap layer directly over and adjacent to the dielectric layer; forming a barrier oxide directly over and adjacent to the charge trap layer; forming a metal carbide control gate over the barrier oxide; etching the dielectric layer, charge trap layer, barrier oxide and control gate to form a flash structure; and passivating the flash structure by depositing SiO_x, wherein the metal carbide control gate comprises aluminum and during the deposition of SiO_x the aluminum in the metal carbide reacts with oxygen to self-passivate the control gate.

Forming the control gate preferably comprises depositing a metal carbide gate electrode layer by ALD using one or more aluminum hydrocarbon compounds, such that the metal carbide layer controls the work function of the control gate. The metal carbide layer is preferably deposited to a thickness of about 1 to 1000 Å, more preferably about 1 to 500 Å, and still more preferably about 25 to 200 Å. The deposition conditions, including temperature, pressure and reactant choice are adjusted to achieve a desired amount of aluminum in the metal carbide layer and thus produce the desired work function and other characteristics. For example, the aluminum content is preferably such that the film has good oxidation resistance and is able to self-passivate during subsequent patterning and/or deposition steps.

A further conductive layer, such as a polysilicon, metal or metal nitride layer, for example a titanium nitride or tungsten layer, may be deposited over the first gate electrode layer. The structure is then patterned, etched and passivated, for example with silicon oxide. During the passivation process, the edges of the metal carbide layer are exposed and the aluminum in the metal carbide reacts with oxygen to self-passivate the remaining metal film.

All of these embodiments are intended to be within the scope of the invention herein disclosed. These and other embodiments of the present invention will be readily apparent to those skilled in the art. From the following detailed description of the preferred embodiments having reference to the attached figure, the invention not being limited to any particular preferred embodiment(s) disclosed.

BRIEF DESCRIPTION OF THE DRAWINGS

The invention will be better understood from the Detailed Description of the Preferred Embodiments and from the appended drawings, which is meant to illustrate and not to limit the invention, and wherein:

FIG. 1 is a block diagram of a pulsing sequence in an ALD-type process according to some embodiment.
FIG. 2 is a schematic illustration of a flash memory structure formed according to some embodiments.
FIG. 3 is a schematic illustration of a gate electrode stack in a CMOS transistor.

DETAILED DESCRIPTION OF CERTAIN EMBODIMENTS

Metal carbide films, e.g. tantalum carbide (TaC) films, can be used to form various structures, such as control electrodes for flash memory structures. In such applications, it is desirable for the films to have good adhesion to underlying materials and also low resistivity and good oxidation resistance.

Metal carbide films with desirable properties can be formed by employing aluminum hydrocarbon compounds to carburnize a metal film on a substrate. In some embodiments of the present invention, metal carbide films are formed over a substrate in ALD-type processes by contacting the substrate with alternating and sequential pulses of a metal compound and a carbon-containing compound, where the carbon containing compound is an aluminum hydrocarbon compound.

Using the methods and compositions described herein, metal carbide films with a controlled aluminum content can be formed on a substrate. A substrate in a reaction space is contacted with a vapor phase metal source chemical (or metal compound) and an aluminum hydrocarbon compound. The films preferably have good adhesion, low resistivity and good oxidation resistance. The characteristics of the metal carbide films including aluminum content, adhesion, resistivity and/or oxidation resistance can be controlled by selecting the appropriate aluminum hydrocarbon reactant. The reaction conditions, such as the reaction temperature, pressure, pulse and purge times, pulsing sequence and post deposition annealing can also be adjusted to achieve films with the desired properties. In some embodiments, the desired film characteristics may be achieved by using a plasma enhanced ALD process.

By selecting an appropriate aluminum hydrocarbon compound and appropriate reaction conditions, a metal carbide film with properties that are advantageous to a particular situation can be formed. For example, in some embodiments a film with low resistivity is formed using TMA, TEA or
DMAH as the aluminum hydrocarbon compound. Films with good adhesion can be obtained in some embodiments using TEA and a metal halide reactant, such as TaCl₅. Oxidation resistant films can be formed in some embodiments by selecting reactants and conditions that provide a desired level of aluminum in a metal carbide film. For example, in some embodiments films with an aluminum concentration of about 1-30%, more preferably about 6-16% are deposited in order to obtain a desired level of oxidation resistance.

[0033] Although described herein primarily in the context of flash memory applications, the metal carbide films and deposition processes can find use in a variety of contexts, as will be recognized by the skilled artisan. For example, the metal carbide film formed can be a component of an integrated circuit (IC), such as, e.g., a conductive diffusion barrier forming a part of a line in a dual damascene structure, a metal gate electrode in a CMOS transistor, such as an NMOS or PMOS gate electrode (depending on the aluminum concentration), or an anti-reflective coating. In other embodiments, the metal carbide film may be also be used such as a corrosion protection layer. In still other embodiments, the metal carbide film may be used as part of a chemical reaction catalyst or as an etch stop barrier.

Definitions

[0034] In context of the present disclosure, an “ALD process” or “ALD type process” generally refers to a process for producing a film over a substrate monolayer (molecular layer) by monolayer using self-saturating chemical reactions. The general principles of ALD are disclosed, e.g., in T. Santola in, e.g., the Handbook of Crystal Growth 3, Thin Films and Epitaxy, Part B: Growth Mechanisms and Dynamics, Chapter 14, Atomic Layer Epitaxy, pp. 601-663, Elsevier Science B.V. 1994, and U.S. Pat. Nos. 4,058,430 and 5,711,811, the disclosures of which are incorporated herein by reference. In an ALD process, gaseous reactants, i.e., precursors or source materials are alternately and sequentially conducted into a reaction space where they contact a substrate to provide a surface reaction. Reaction conditions are selected such that generally only up to about one monolayer (i.e. an atomic layer or a molecular layer) of material is deposited at a time during each pulsing cycle. Gas phase reactions between precursors and any undesired reactions of byproducts are inhibited because precursor pulses are separated from each other and the reaction chamber is purged with an inactive gas (e.g. nitrogen, argon, or hydrogen) and/or evacuated using, e.g., a pumping system between precursor pulses to remove surplus gaseous reactants and reaction byproducts, if any, from the chamber. Thus, the concentration profiles of the reactants in the reaction space with respect to time are not overlapping. However, the skilled artisan will recognize that more than one monolayer may be deposited in one or more ALD cycles despite the separation of reactant pulses.

[0035] “Plasma-excited species” refers to radicals, ions or other excited species generated via application of energy to a gas. Plasma-excited species may be generated using a direct plasma generator (i.e., “in situ” or “direct” plasma generation) and/or a remote plasma generator (i.e., “ex situ” or “remote” plasma generation). Energy may be applied (or coupled) to 1 gas via a variety of methods, such as inductive coupling, ultraviolet radiation, microwaves, capacitive coupling, application of RF power, etc. In the absence of coupling energy, plasma generation is terminated. Plasma-excited species include, without limitation, hydrogen and nitrogen radicals.

[0036] “Plasma parameters” is used to designate one or more plasma generation variables, including, without limitation, plasma generator power, gas pressure, gas (or reactant) flow rate, and plasma pulse duration. As an example, for plasma generation using RF power, plasma parameters include, without limitation, radio frequency (RF) power on time, RF power amplitude, RF power frequency or frequencies (for dual frequency systems), reactant concentration, reactant flow rate, reaction space pressure, total gas flow rate, reactant pulse durations and separations, and RF electrode spacing.

[0037] “Reaction space” is used to designate a reactor or reaction chamber (“chamber”), or an arbitrarily defined volume therein, in which conditions can be adjusted to effect film growth. The reaction space can be, for example, in a single-wafer ALD reactor or a batch ALD reactor, where deposition on multiple substrates takes place at the same time.

[0038] “Adsorption” is used to designate a chemical attachment of atoms or molecules on a surface.

[0039] “Substrate” is any surface on which deposition is desired, and in preferred embodiments can include any workpiece that is suitable for integrated circuit (IC) fabrication. Typical substrates include, without limitation, silicon, silica, coated silicon and high k materials, such as metal oxides.

[0040] “Surface” is used to designate a boundary between the reaction space and a feature of the substrate.

[0041] “Film” means a film that is grown on a substrate from elements or compounds that are transported as separate ions, atoms or molecules from a source to the substrate. The thickness of the film will depend upon the application and may vary in a wide range, preferably from one atomic layer to 100 nanometers (nm) or more. In some embodiments, such as where the film serves to set the work function in a flash memory, the thickness may be about 25 Å to 200 Å, although in some embodiments it may be as high as 500 Å or even 1000 Å. In other embodiments the film is less than about 200 Å in thickness, even more preferably less than about 100 Å, and most preferably less than about 50 Å, such as for a CMOS gate application.

[0042] “Metal carbide film” designates a film comprising at least one metal and carbon. The metal may be a single elemental metal or a plurality of metals, such as a metal alloy. The metal carbide film may be stoichiometric, e.g., TaCₓ, or non-stoichiometric, e.g., TaCₓ₋ₓ where ‘x’ is greater than one if the film has excess carbon or less than one if the film is carbon deficient. In preferred embodiments, metal carbide films deposited according to the methods described herein comprise a first metal, carbon, and aluminum. The first metal is typically not aluminum.

ALD Methods

[0043] ALD is based on self-limiting reactions, whereby sequential and alternating pulses of reaction precursors are used to deposit about one atomic (or molecular) monolayer of material per deposition pulse. The deposition conditions and precursors are selected to provide self-saturating reactions, such that an adsorbed layer in one pulse leaves a surface termination that is non-reactive with the gas phase reactants of the same pulse. A subsequent pulse of different reactants reacts with the previous termination to enable continued deposition. Thus, each cycle of alternated pulses typically
leaves no more than about one monolayer of the desired material. However, as mentioned above, the skilled artisan will recognize that in one or more ALD cycles more than one monolayer of material may be deposited, for example if some gas phase reactions occur despite separate provisions of the reactants.

[0044] In a typical ALD-type process for depositing metal carbide films, one deposition cycle comprises exposing the substrate to a first reactant, removing any unreacted first reactant and reaction byproducts from the reaction space, exposing the substrate to a second reactant, followed by a second removal step. The first reactant is preferably a metal precursor and the second reactant is preferably a carburizing (or carbon-contributing) compound (although it is possible to begin the process with either reactant).

[0045] The metal compound preferably comprises one or more metals selected from the group consisting of titanium (Ti), zirconium (Zr), hafnium (Hf), vanadium (V), niobium (Nb), tantalum (Ta), chromium (Cr), molybdenum (Mo), tungsten (W), manganese (Mn), rhenium (Re), iron (Fe), cobalt (Co), nickel (Ni), copper (Cu), silver (Ag), gold (Au), palladium (Pd), platinum (Pt), rhodium (Rh), iridium (Ir), ruthenium (Ru) and osmium (Os).

[0046] Typically, halide reactants, such as, e.g., TaCl₅ and HCl₂, are used as metal precursors in ALD deposition because these precursors are inexpensive and relatively stable, but at the same time reactive towards different types of surface groups.

[0047] Carbon-contributing compounds are preferably aluminum hydrocarbon compounds. The aluminum hydrocarbon compound may be, for example, an alkane, alkene or alkyn. In some embodiments, the aluminum hydrocarbon compound is selected from the group consisting of trimethyl aluminum (TMA), triethyl aluminum (TEA) and dimethylaluminumhydride (DMAH).

[0048] The aluminum hydrocarbon compound is preferably selected to achieve desired characteristics in the metal carbide film. These include, without limitation, adhesion, resistivity, oxidation resistance and work function. In addition, by selecting an appropriate aluminum hydrocarbon compound and appropriate deposition conditions, the level of aluminum in the metal carbide film can be controlled. For example, to achieve a higher aluminum concentration in a particular film, TEA may be selected over TMA. In some embodiments, different aluminum hydrocarbon compounds may be used in different ALD deposition cycles to modify the aluminum incorporation in the metal carbide film. For example, in a deposition process to deposit a metal carbide layer a first ALD cycle may use a first aluminum compound and one or more ALD cycles may use a different aluminum compound.

[0049] The separation of precursors by inert gases, such as Ar, prevents gas-phase reactions between reactants and enables self-saturating surface reactions. Because the reactions self-saturate, strict temperature control of the substrates and precise dosage control of the precursors is not required. However, the substrate temperature is preferably such that an incident gas species does not condense into monolayers or decompose on the surface. Surplus chemicals and reaction byproducts, if any, are removed from the reaction space before the next reactive chemical pulse is introduced into the chamber. Undesired gaseous molecules can be effectively expelled from the reaction space with the help of an inert purging gas. The purging gas directs the superfluous molecules out of the chamber. A vacuum pump may be used to assist in the purging.

[0050] According to some embodiments of the invention, an ALD-type process is used to form metal carbide films on a substrate, such as an integrated circuit workpiece. Preferably, each ALD cycle comprises two distinct deposition steps or phases. In a first phase of the deposition cycle ("the metal phase"), a first reactant comprising a metal (i.e., metal source material or chemical) is pulsed to the reaction space and chemisorbs onto the substrate surface, forming no more than about one monolayer on the surface of the substrate. The metal source material in this phase is selected such that, under the preferred conditions, the amount of metal source material that can be bound to the surface is determined by the number of available binding sites and by the physical size of the chemisorbed species (including ligands). The chemisorbed layer left by a pulse of the metal source chemical is self-determined with a surface that is non-reactive with the remaining chemistry of that pulse. This phenomenon is referred to herein as "self-saturation." One of skill in the art will recognize that the self-limiting nature of this phase makes the entire ALD cycle self-limiting.

[0051] The metal source material preferably includes a metal species desired in the film being deposited. In some embodiments, the metal source chemical, also referred to herein as the "metal compound," is a halide and the adsorbed monolayer is terminated with halogen ligands. In some embodiments, the metal compound is selected from the group consisting of metal bromides, metal chlorides, and metal iodides. As an example, a tantalum-containing metal carbide film can be deposited using a metal compound selected from the group consisting of TaBr₅, TaCl₅, and TaI₅, where w, x, y, and z are numbers from 1 to 5. In some embodiments, where a tantalum-carbide film is desired, TaCl₅ is used as the metal compound.

[0052] Excess metal source material and reaction byproducts (if any) are removed from the reaction space, e.g., by purging with an inert gas. Excess metal source material and any reaction byproducts may be removed with the aid of a vacuum generated by a pumping system.

[0053] Maximum step coverage on the workpiece surface is typically obtained when the metal source material forms no more than about a single monolayer in each self-limiting phase. Due to the size of the chemisorbed species and the number of reactive sites, somewhat less than a monolayer may be deposited in each pulse of metal reactant. Thus, the maximum coverage of metal source material may be less than a monolayer. Depending on the process conditions in some embodiments it may be possible that more than one monolayer of first reaction is found on the substrate surface.

[0054] In a second phase of the deposition cycle ("carbon-contributing phase"), a second reactant, also referred to herein as a "second source chemical", is pulsed into the reaction space to react with the metal-containing molecules left on the substrate surface by the preceding pulse. The second source chemical is an aluminum hydrocarbon compound. Preferably, in the second phase carbon is incorporated into the film by the interaction of the second source chemical with the monolayer left by the metal source material. In preferred embodiments, reaction between the second source chemical and the chemisorbed metal species produces a metal carbide film over the substrate.
Aluminum may also be incorporated into the film in this second phase. Reaction conditions, including, without limitation, choice of reactant, temperature, pressure and pulse and purge times are adjusted to achieve a desired aluminum content in the film. In some embodiments the aluminum content may vary from about 0% to about 30%, more preferably from about 6% to about 16%. In other embodiments the aluminum content may be higher.

The aluminum hydrocarbon may be selected from the group consisting of alkanes, alkenes and alkynes. For example, the carbon-containing compound may be TMA, DMAH, or TEA. In some embodiments more than one aluminum hydrocarbon compound may be used. For example, two or more aluminum hydrocarbon compounds may be provided simultaneously in the same pulse. In other embodiments two or more different aluminum hydrocarbon compounds are provided in distinct ALD cycles with a single deposition process.

Excess second source chemical and reaction byproducts, if any, are removed from the reaction space by a purging gas pulse and/or vacuum generated by a pumping system. Purging gas is preferably any inert gas, such as, without limitation, argon (Ar) or helium (He). A phase is generally considered to immediately follow another phase if a purge (i.e., purging gas pulse) or other reactant removal step intervenes.

Additional reactants may be utilized in some embodiments, for example to reduce the deposited film or to incorporate a further species in the film. In some embodiments a third reactant may be a reducing agent, such as plasma-excited species of hydrogen generated by, e.g., an in situ or remote plasma generator. The reducing agent may be pulsed to the reaction space (or generated in the reaction space) after the metal phase and/or the carbon-contributing phase to reduce the deposited film. The reducing agent can be used, for example, to remove impurities, such as halogen atoms or oxidizing material (e.g., oxygen atoms) in the film and/or the substrate. It may also be used to control the incorporation of aluminum into the metal carbide film, thereby controlling the properties of the film. In some embodiments, thermal ALD and plasma ALD cycles are used in the same deposition process to control aluminum concentration in the deposited film. The ratio of thermal ALD cycles to plasma ALD cycles can be selected to achieve the desired aluminum concentration and/or concentration profile in the thin film.

In some embodiments, plasma parameters can be selected to modify the level of incorporation of aluminum into the metal carbide film and/or ratio of tantalum to carbon. That is, in some embodiments, film composition can be controlled as a function of plasma parameters. In addition to composition, other film characteristics such as crystallinity, crystal lattice constant, resistivity and crystal stress can be adjusted by selecting appropriate plasma parameters.

In some embodiments, plasma parameters are selected from relationships that have been established between plasma parameters and film composition and characteristics. “Plasma parameters” may include, for example, RF power and RF frequency. One plasma parameter, such as RF power, or multiple plasma parameters, i.e., a set of plasma parameters, such as RF power and RF frequency may be adjusted in one or more ALD cycles to achieve the desired film properties. Plasma parameters are preferably selected to yield a metal carbide film with a desired composition. In some cases plasma parameters are selected to form a gate electrode with a particular composition to yield a desired gate stack work function.

In some embodiments, deposition recipes for metal carbide films are determined or designed by selecting plasma parameters. As an example, the RF power may be selected to affect a stoichiometry as desired. As another example, a particular plasma pulse duration or RF power on time can be used to obtain a desired composition. As still another example, the desired composition may be achieved by selecting a combination of RF power, reactant pulse duration, and reactant flow rate.

Preferably, the plasma-excited species comprises hydrogen. Plasma-excited species of hydrogen may include, without limitation, hydrogen radicals (H⁺) and hydrogen cations (e.g., H⁺, H₂⁺). Plasma-excited species of hydrogen may be formed in situ or remotely, for example from molecular hydrogen (H₂) or hydrogen-containing compounds (e.g., silane, diborane, etc.). In some embodiments, one or more of the reactants described herein can be provided as a plasma.

Relationships between deposition parameters such as plasma, reactants, etc. and thin film composition can be established by selecting parameter(s) and depositing a metal carbide film by a particular atomic layer deposition process using the selected parameter(s) until a film of desired thickness is formed. The film composition and characteristics can then be determined and another film deposited using different parameters. This process can be repeated for different parameters to develop relationships between the parameters and film composition.

By selecting appropriate reaction conditions, a compound film with a composition as desired can be formed.

In one embodiment, formation of a metal carbide film via an ALD-type process comprises one or more deposition cycles, each comprising the steps of:

1. providing a metal compound to the reaction space;
2. purging and/or evacuating excess metal compound and reaction byproducts;
3. providing an aluminum hydrocarbon compound to the reaction space; and
4. purging and/or evacuating excess aluminum hydrocarbon compound and reaction byproducts from the reaction space.

Steps 1-4 can be referred to as a thermal ALD cycle. Steps 1-4 can be repeated as necessary to produce a metal carbide film of desired thickness and with a desired aluminum concentration. For example, steps 1-4 may be repeated up to 10, 100 or even 1000 or more times to produce metal carbide layers with uniform thicknesses ranging from one or several atomic layers to 100 nanometers (nm) or more. In some embodiments, steps 1-4 may be repeated until a metal carbide film is formed with a thickness of from about 1 to about 1000 Å, preferably less than about 1000 Å, more preferably less than about 500 Å. In some embodiments the film has a thickness of less than about 300 Å, and in other embodiments less than about 200 Å. In one embodiment, the thickness is preferably between about 100 Å and about 200 Å. In other embodiments the thickness is preferably from about 20 to about 200 Å. The skilled artisan will appreciate that the thickness of the metal carbide film can vary depending on the particular application. As an example, for NMOS gate applications, the thickness is typically from about 50 Å to about 500 Å. As another example, for MIM capacitor applications
(e.g., DRAM, eDRAM, etc.) the thickness range is typically from about 50 Å to about 200 Å. Further, for applications in which the metal carbide thin film serves to set the work function in a flash memory, the thickness is preferably between about 20 Å and about 200 Å.

[0071] In some embodiments, steps 1 and 2 are repeated a predetermined number of times prior to steps 3 and 4. For example, steps 1 and 2 may be repeated five times prior to steps 3 and 4. As another example, steps 1 and 2 may be repeated ten times prior to steps 3 and 4. It should be understood that if a metal carbide film with compositional uniformity is desired, the number of times steps 1 and 2 are repeated should not exceed that which will prevent substantial carburation of the metal film. In one embodiment, the metal compound has a low decomposition temperature and the number of times steps 1 and 2 are repeated does not exceed one.

[0072] As discussed herein, selection of hydrocarbon aluminum reactants can be used to achieve deposition of films with desired characteristics, such as adhesion, resistivity, oxidation resistance and/or work function. In some embodiments multiple aluminum hydrocarbon compounds are used. In addition, various reaction conditions can be manipulated to achieve the desired film qualities and composition. These reaction conditions include, but are not limited to, reaction temperature, source container temperature, pressure, flow rate, plasma parameters and pulse and purge times.

[0073] In one embodiment, formation of a metal carbide film via an ALD-type process comprises two or more deposition cycles, a first cycle comprising the steps of:

1. Providing a metal compound to the reaction space;
2. Purging and/or evacuating excess metal compound and reaction byproducts;
3. Providing a first aluminum hydrocarbon compound to the reaction space; and
4. Purging and/or evacuating excess aluminum hydrocarbon compound and reaction byproducts from the reaction space.

[0074] and a second deposition cycle comprising the steps of:

1. Providing a metal compound to the reaction space;
2. Purging and/or evacuating excess metal compound and reaction byproducts;
3. Providing a first aluminum hydrocarbon compound to the reaction space; and
4. Purging and/or evacuating excess aluminum hydrocarbon compound and reaction byproducts from the reaction space.

[0075] The first and second cycles need not be consecutive and the ratio of first cycles to second cycles can be selected to achieve the desired composition.

[0084] In some embodiments, the deposition cycles can begin with any of the reactants. Preferably the first and second aluminum hydrocarbon compounds are different compounds. Preferably the first and second aluminum hydrocarbon compounds comprise TMA, TEA, or DMAH. In some embodiments, the first aluminum hydrocarbon compound comprises TEA and the second hydrocarbon compound comprises TMA. In some embodiments, multiple deposition cycles using TEA are performed followed by multiple deposition cycles using TMA.

[0085] In some embodiments, the ratios between the first hydrocarbon pulses and second hydrocarbon pulses are between about 1:100 and 100:1. Preferably the ratio between first and second aluminum hydrocarbon pulses is about 5:1 to about 1:5. In some embodiments the ratio between first and second aluminum hydrocarbon pulses is about 1:1.

[0086] In some embodiments plasma can be used during the deposition of the metal carbide film. In one embodiment, formation of a metal carbide film via an ALD-type process comprises one or more plasma ALD deposition cycles, each comprising the steps of:

1. Providing a metal compound to the reaction space;
2. Purging and/or evacuating excess metal compound and reaction byproducts;
3. Providing a first aluminum hydrocarbon compound to the reaction space;
4. Purging and/or evacuating excess aluminum hydrocarbon compound and reaction byproducts from the reaction space;
5. Providing a plasma-excited species to the reaction space; and
6. Purging and/or evacuating excess plasma source and reaction byproducts from the reaction space.

[0093] Steps 1-6 can be referred to as the plasma deposition cycle. In some embodiments the plasma source can be provided after the metal compound and before the aluminum hydrocarbon compound. In some embodiments, the deposition cycle can begin with any of the reactants. Preferably, the plasma-excited species comprises hydrogen.

[0094] In some embodiments plasma ALD cycles and thermal ALD cycles are used in the same deposition process. The ratio between the thermal ALD cycles and plasma ALD cycles is typically between about 1:100 and 100:1. Preferably the ratio between first and second aluminum hydrocarbon pulses is about 5:1 to about 1:5. In some embodiments the ratio between thermal and plasma ALD cycles is about 1:1.

[0095] The following general conditions apply to any of the deposition cycles disclosed herein. The reaction temperature is preferably from about 200° C. to about 500° C., more preferably about 300° C. to about 400° C. In some embodiments the reaction temperature is about 350° C. to 375° C.

[0096] The reaction pressure is from about 0.5 to about 10 torr. In some embodiments the pressure is about 2 to about 7 torr. The pressure is preferably adjusted to achieve a desirable growth rate and acceptable uniformity.

[0097] In some embodiments the reactant temperature can be selected to provide films with a desired characteristic. In some embodiments, the halide reactant vessel temperature is about 40° C. to about 80° C. For example, for the deposition of tantalum containing films using a TaCl₅, as a metal precursor, the reactant vessel temperature may be from about 45° C. to about 70° C., more preferably about 65° C.

[0098] The metal reactant pulse time is preferably from about 0.1 to about 20 seconds, more preferably from about 1 to about 10 seconds.

[0099] The aluminum hydrocarbon compound pulse time is preferably from about 0.1 to about 20 seconds, more preferably from about 0.5 to about 2 seconds. In some embodiments TMA is used as the aluminum hydrocarbon reactant and a pulse time of longer than about 1 second is used, more preferably about 2 seconds. In other embodiments TEA is used as the aluminum hydrocarbon reactant with a pulse time of about 1 second. In other embodiments DMAH is used as the aluminum hydrocarbon reactant with a pulse time of about 1 second.
second. In other embodiments, two or more different aluminum hydrocarbon reactants can be used. In still other embodiments longer pulse times may be used. In some embodiments longer pulse times can be used for the aluminum hydrocarbon compound to affect the saturation of the compound on the substrate.

[0100] Purge times are generally from about 0.1 to about 10 seconds, more preferably about 2 to about 8 seconds. In some embodiments a purge time of about 6 seconds is used. However, in other embodiments longer purge times may be used. In some embodiments purge times are the same for purging the metal reactant and the aluminum hydrocarbon reactant, while in other embodiments the purge times are different for the different reactants.

[0101] Flow rates are generally from about 100 to about 400 sccm for the inert purge gas, such as Ar. The carrier flow for both metal precursors and aluminum hydrocarbons is preferably about 100 to about 400 sccm. The carrier gas is preferably an inert gas, such as Ar, and may be the same as or different from the purge gas. The flow rates of the purge and carrier gases can be determined based, in part, on the particular reactor, as will be appreciated by the skilled artisan.

[0102] With reference to FIG. 1, an exemplary embodiment for forming a carbide film by an ALD-type process is illustrated. After initial surface termination, if necessary, a first reactant or source material is supplied or pulsed to the substrate or workpiece. In accordance with a preferred embodiment, the first reactant pulse comprises a carrier gas flow and a metal precursor, preferably a volatile halide compound that is reactive with the workpiece surfaces of interest. The halide compound comprises a metal species that is to form part of the metal carbide film. Accordingly, a metal-containing species adsorbs upon the workpiece surfaces. The first reactant pulse self-saturates the workpiece surfaces such that any excess constituents of the first reactant pulse do not further react with the monolayer formed by this process. Self-saturation is due to ligands, such as halide tails terminating the monolayer, protecting the layer from further reaction. In some embodiments the first reactant is a tantalum halide compound, such as TaCl5.

[0103] Excess first reactant is then removed 20 from the reaction space. Preferably, step 104 merely entails stopping the flow of the first reactant or chemistry while continuing to flow a carrier gas (e.g., Ar or H2) for a sufficient time to diffuse or purge excess reactants and reactant byproducts from the reaction space, preferably with greater than about two reaction chamber volumes of the purge gas, more preferably with greater than about three chamber volumes. Preferably, the removal 20 comprises continuing to flow purge gas for between about 0.1 seconds and 20 seconds after stopping the flow of the first reactant pulse. Inter-pulse purging is described in a co-pending U.S. patent application, having Ser. No. 09/392,371, filed Sep. 8, 1999 and entitled IMPROVED APPARATUS AND METHOD FOR GROWTH OF A THIN FILM, the disclosure of which is incorporated herein by reference. In other arrangements, the chamber may be pumped down between alternating chemistries. See, for example, PCT publication number WO 96/17107, published Jun. 6, 1996, entitled METHOD AND APPARATUS FOR GROWING THIN FILMS, the disclosure of which is incorporated herein by reference. Together, the adsorption 10 and reactant removal 20 represent the first phase 50 in the deposition cycle. The first phase 50 in the illustrated deposition cycle is thus the metal phase.

[0104] With continued reference to FIG. 1, a second reactant or source chemical is pulsed 30 to the workpiece. The second chemistry reacts with or adsorbs upon the monolayer left by the first reactant. In some embodiments, the second reactant removes ligands from the metal-containing species deposited in step 10. In the illustrated embodiment, the second reactant is an aluminum hydrocarbon compound that reacts with the layer deposited by the first reactant to form a carbide compound. The aluminum hydrocarbon compound deposits carbon in the metal layer formed in the metal phase. In some embodiments, the aluminum hydrocarbon compound is pulsed with a carrier gas (e.g., H2), preferably an inert carrier gas (e.g., He, Ar).

[0105] After a time period sufficient to deposit carbon in the growing film, provision of the aluminum hydrocarbon compound is terminated and reaction byproducts (preferably also volatile, if any, are removed 40 from the reaction space, preferably by a purge gas. The removal can be as described for step 20. Together, steps 30 and 40 represent a second phase of the illustrated ALD process, which can also be referred to as the carbon-contributing phase 60.

[0106] Steps 10-40 may be repeated 70 to form a metal carbide layer of a desired thickness. The repeat step 70 may be excluded if a metal carbide film with a thickness of about one monolayer or less is desired.

[0107] For the ALD-type processes describe herein, the substrate is preferably maintained at a temperature from about 150°C to about 550°C, more preferably from about 350°C to about 400°C. The chamber is preferably maintained at a pressure from about 200 mTorr to about 10 Torr, more preferably from about 1 Torr to about 8 Torr.

[0108] In some embodiments, the first reactant is a tantalum halide, such as TaCl5, the second reactant is a tantalum hydrocarbon compound such as TMA, DMAH, or TEA, and the film being formed is a tantalum carbide. The tantalum carbide film preferably comprises aluminum. In some embodiments the tantalum carbide film comprises from about 6 to about 16% aluminum.

[0109] In some embodiments, the film can be annealed after deposition. Annealing the film after deposition can modify the properties of the thin film. For example, annealing can modify the hydrogen and chlorine content of the film. Generally, during annealing the substrate temperature is about 500°C to about 1200°C. In some embodiments, the substrate temperature during the annealing step is about 600°C to about 1000°C. Preferably, the annealing step is carried out in an inert atmosphere. Preferred inert atmospheres for annealing comprise nitrogen, helium, and argon. Preferably the pressure is around atmospheric pressure during the annealing step. In some embodiments, the pressure can be above or below atmospheric pressure. In some embodiments, the annealing atmosphere comprises a low oxygen partial pressure.

Flash Memory

[0110] In some embodiments flash memory structures are provided in which a metal carbide film is deposited by ALD as described herein to form at least a part of the control gate. The main elements of an exemplary flash memory structure are illustrated in FIG. 2. A dielectric layer (tunnel oxide) 110 is deposited over a substrate 100. The dielectric layer 110 typically SiO2, although in some embodiments it may be a high-k material. High k materials are generally forms of metallic oxides with k values greater than about 7, such as aluminum.
oxide (Al₂O₃), zirconium oxide (ZrO₂), hafnium oxide (HfO₂), tantalum oxide (Ta₂O₅), barium strontium titanate (BST), strontium bismuth tantalate (SBT), lanthanum oxides, and combinations thereof, such as HfSiOₓ and HfZrOₓ. Although typically an oxide, the dielectric layer 110 may be another type of material.

[0111] A floating gate 120 is deposited directly over the dielectric layer. The floating gate 120 may comprise, for example, polysilicon. In some embodiments the floating gate 120 is replaced with a charge trap layer. In some embodiments the charge trap layer is silicon nitride, although other materials are possible.

[0112] A barrier oxide (or blocking dielectric) 130 is formed over the floating gate or charge trap layer 120. In some embodiments the barrier oxide 130 comprises Al₂O₃, although other materials such as AlₓOᵧ, Al₂ZrO₅, and Hf₁₋ₓAlₓO can be used. The barrier oxide 130 may also be, for example, an ONO (oxide nitride oxide) structure comprising a bottom dielectric material adjacent to the floating gate or charge trap layer 120, a top dielectric material adjacent to the overlying control gate 140 and an intervening nitride layer located between the top and bottom dielectric material.

[0113] A control gate 140 is formed over the barrier oxide. Forming control gate 140 preferably comprises depositing a metal carbide gate electrode layer 150 by ALD using one or more aluminum hydrocarbon compounds, such that the metal carbide layer controls the work function of the control gate 140. In preferred embodiments the metal carbide is TaC. The metal carbide layer 150 is preferably deposited to a thickness of about 100 to 200 Å. The deposition conditions, for example temperature, pressure, pulse and purge times, plasma conditions (if used) and reactant choice are adjusted to achieve a desired amount of aluminum in the metal carbide layer 150 and thus produce the desired work function. In addition, the aluminum content is preferably such that the film is able to self-passivate during subsequent patterning and/or deposition steps.

[0114] In some embodiments the metal carbide layer serves as the entire control gate. In other embodiments, a second upper gate electrode layer 160 is deposited over and adjacent to the first lower gate electrode layer 150. The second gate electrode layer 160 comprises a conductive material, such as polysilicon, titanium nitride and/or a metal, such as tungsten. The upper gate electrode layer 160 may be thicker than the lower gate electrode layer 150. In some embodiments the upper gate electrode layer 160 has a thickness of about 1000 Å. Typically, the upper gate electrode layer 160 does not contribute to the work function of the control gate 140. However, in some embodiments the thickness of the lower gate electrode layer 150 and the upper gate electrode layer 160 are selected so that each contributes to the work function such that a desired work function is achieved.

[0115] Suitable materials for the dielectric layer 110, the floating gate or charge trap layer 120, the barrier oxide 130 and upper gate electrode layer 160 (if present) are known in the art and may be selected by the skilled artisan based on the particular circumstances. The dielectric layer 110 can be deposited by any deposition method known in the art, such as ALD or PEALD. The upper gate electrode layer 160 is preferably deposited by a chemical vapor deposition (CVD) type or physical vapor deposition (PVD) type process. In some embodiments the barrier oxide 130 and the control gate 140 are deposited on the same platform without any air break.

[0116] The structure is then patterned, etched and passivated, for example with silicon oxide. During the passivation process, the edges of the metal carbide layer are exposed and the aluminum in the metal carbide reacts with oxygen to self-passivate the remaining metal film.

Gateway Electrodes

[0117] In some embodiments transistor structures are provided in which a metal carbide film is deposited by ALD as described herein and forms at least a part of the gate electrode. A schematic illustration of a gate stack in a CMOS transistor is provided in FIG. 3. In particular, a semiconductor substrate 200 is shown with a transistor gate stack 210 formed thereon. In the illustrated embodiment, the substrate 200 comprises an upper portion of a single-crystal silicon wafer, though the skilled artisan will appreciate that the substrate can also comprise other semiconductor materials. The gate stack 210 includes a gate electrode layer 220 comprising metal carbide. Sidewall spacers 230 and an insulating layer 240 protect and isolate the electrode 220 in a conventional manner. Also illustrated is a more highly conductive strapping layer 250, typically including metal, over the silicon-containing gate electrode layer 220. The strap 250 facilitates rapid signal propagation among transistor gates across the wafer, connecting the gates to logic circuits. Note that integrated circuit transistors can have a variety of forms that do not all resemble that of FIG. 3. The gate electrode layer 220 of the preferred embodiments, however, will have application to gate electrodes in a variety of transistor types (e.g. heterojunction BiCMOS transistors).

[0118] At least a portion of the gate electrode 220 is formed by depositing a metal carbide layer by ALD using one or more aluminum hydrocarbon compounds. In some embodiments the metal carbide layer controls the work function of the gate electrode 220. In preferred embodiments the metal carbide comprises TaC. The metal carbide layer 150 is preferably deposited to a thickness of about 20 to 200 Å. The deposition conditions, for example temperature, pressure, pulse and purge times, plasma conditions (if used) and reactant choice are adjusted to achieve a desired amount of aluminum in the metal carbide layer 150 and thus produce the desired work function. In addition, the aluminum content is preferably such that the film is able to self-passivate during subsequent patterning and/or deposition steps.

[0119] In some embodiments the metal carbide layer serves as the entire gate electrode 220. In other embodiments, a second upper gate electrode layer 160 is deposited over and adjacent to the metal carbide layer. The second upper gate electrode layer comprises a conductive material, such as polysilicon, titanium nitride and/or a metal, such as tungsten. The upper gate electrode layer 160 may be thicker than the metal carbide gate electrode layer 220. In some embodiments the upper gate electrode layer 160 has a thickness of about 1000 Å. Typically, the upper gate electrode layer 160 does not contribute to the work function of the gate electrode 220. However, in some embodiments the thickness of the lower gate electrode layer 150 and the upper gate electrode layer 160 are selected so that each contributes to the work function such that a desired work function is achieved.

Examples

[0120] Tantalum carbide films were deposited on silicon dioxide (SiO₂), Al₂O₃, SiN, HfO₂ and Ta₂O₅ substrates by
ALD-type processes in both EmerALD and Pulsar reactors. The sequence of steps in the processes included alternately and sequentially pulsing a metal compound (TaCl₃), an aluminum hydrocarbon (TMA or TEA) and a purge gas (Ar) into a reaction space containing the substrate. Deposition was conducted under a variety of reaction conditions.

0121] The sequence of gas pulses was as follows:

0122] (1) TaCl₃ pulse;

0123] (2) Ar purge;

0124] (3) TMA or TEA pulse; and

0125] (4) Ar purge.

0126] Steps (1)-(4) were repeated to form tantalum carbide films.

Tantalum Carbide Deposition using TMA

0127] In one experiment, tantalum carbide films were deposited on silicon dioxide (SiO₂) from TMA and TaCl₃ at a reaction temperature of about 375° C. The sequence of gas pulses and purges (milliseconds, “ms”) were as follows:

0128] (1) TaCl₃ pulse (1000 ms);

0129] (2) Ar purge (3000 ms);

0130] (3) TMA pulse (1000 ms); and

0131] (4) Ar purge (3000 ms).

0132] Steps (1)-(4) were repeated to form a uniform tantalum carbide film with a deposition rate of approximately 3.5 Å/cycle.

Tantalum Carbide Deposition using TEA

0133] In another experiment, tantalum carbide films were deposited on silicon dioxide (SiO₂) from TEA and TaCl₃ at a reaction temperature of about 375° C. The sequence of gas pulses and purges (milliseconds, “ms”) were as follows:

0134] (1) TaCl₃ pulse (1000 ms);

0135] (2) Ar purge (3000 ms);

0136] (3) TEA pulse (2000 ms); and

0137] (4) Ar purge (4000 ms).

0138] Steps (1)-(4) were repeated to form a uniform tantalum carbide film with a deposition rate of approximately 4 Å/cycle.

TaC Film Properties

0139] The resistivity of a 200 Å tantalum carbide film formed according to the procedure outlined in the examples above was about 1200 Ω*cm when TMA was used as the aluminum hydrocarbon and about 700 Ω*cm when TEA was used as the aluminum hydrocarbon reactant.

0140] When deposited on an Al₂O₃ substrate, a TaC film deposited using TMA as the aluminum hydrocarbon compound delaminated from the substrate in scratch and tape tests. However, a TaC film deposited using TEA under otherwise identical conditions showed good adhesion using the same measures.

0141] In at least some of the aforesaid embodiments, any element used in an embodiment can interchangeably be used in another embodiment unless such a replacement is not feasible.

0142] It will be appreciated by those skilled in the art that various other omissions, additions and modifications may be made to the methods and structures described above without departing from the scope of the invention. All such modifications and changes are intended to fall within the scope of the invention, as defined by the appended claims.

We claim:

1. An atomic layer deposition process for forming a metal carbide thin film on a substrate in a reaction space, comprising:

   alternately and sequentially contacting the substrate with vapor phase pulses of a first metal precursor and a first aluminum hydrocarbon compound, such that a metal carbide film comprising from about 6 to about 16% aluminum is formed.

   2. The method of claim 1, wherein alternately and sequentially contacting the substrate with vapor phase pulses of a metal precursor and an aluminum hydrocarbon compound comprises a thermal ALD cycle comprising:

      providing the first metal precursor to the reaction space; removing excess metal compound and reaction byproducts;

      providing the first aluminum hydrocarbon compound to the reaction space; and

      removing excess aluminum hydrocarbon compound and reaction byproducts from the reaction space.

3. The method of claim 2, further comprising a plasma ALD cycle comprising providing a plasma-excited species to the reaction space.

4. The method of claim 3, wherein the plasma excited species comprises hydrogen radicals.

5. The method of claim 3, wherein the thermal ALD cycle and plasma ALD cycle are performed in a ratio of about 5:1 to about 1:5.

6. The method of claim 1, wherein the metal precursor is a metal halide.

7. The method of claim 6, wherein the metal halide is a tantalum halide.

8. The method of claim 7, wherein the metal halide is TaCl₅.

9. The method of claim 1, wherein the aluminum hydrocarbon compound is selected from the group consisting of alkanes, alkenes and alkynes.

10. The method of claim 9, wherein the aluminum hydrocarbon compound is selected from the group consisting of trimethyl aluminum (TMA), triethyl aluminum (TEA), and dimethylaluminumhydride (DMAH).

11. The method of claim 2, further comprising a second thermal ALD cycle comprising providing a second aluminum hydrocarbon compound to the reaction space, wherein the second aluminum hydrocarbon compound is different from the first aluminum hydrocarbon.

12. The method of claim 10, wherein the thermal ALD cycle and second ALD cycle are performed in a ratio of about 5:1 to about 1:5.

13. The method of claim 1, wherein the metal carbide is tantalum carbide.

14. The method of claim 13, wherein the metal carbide is deposited to a thickness of about 1 to about 1000 Å.

15. The method of claim 14, wherein the metal carbide is deposited to a thickness of about 100 to about 200 Å.

16. The method of claim 1, wherein the atomic layer deposition process is carried out at a temperature of about 150° to about 550° C.

17. The method of claim 16, wherein the atomic layer deposition process is carried out at a temperature of about 350° to about 400° C.

18. The method of claim 1, wherein the atomic layer deposition process is carried out at a pressure of about 2 to 5 Torr.

19. The method of claim 1, wherein the metal carbide thin film serves as a control gate in a flash memory.

20. The method of claim 19, wherein the work function of the control gate is determined by the metal carbide.
21. The method of claim 1, wherein the metal carbide thin film serves as a gate metal for a gate electrode in a CMOS transistor.

22. The method of claim 21, wherein the metal carbide thin film sets the work function of the gate electrode.

23. The method of claim 1, further comprising annealing the thin film at a substrate temperature greater than 500°C.

24. A control gate in a flash memory structure comprising a tantalum carbide layer, wherein the tantalum carbide layer comprises aluminum and wherein the work function of the control gate is determined by the work function of the tantalum carbide layer.

25. The flash memory structure of claim 24, wherein the tantalum carbide layer comprises from about 6 to about 16% aluminum.

26. The flash memory structure of claim 24, wherein the tantalum carbide layer is from about 25 to about 200Å thick.

27. A method for forming a flash memory on a substrate comprising:
   forming a dielectric layer on the substrate;
   forming a charge trap layer directly over and adjacent to the dielectric layer;
   forming a barrier oxide directly over and adjacent to the charge trap layer;
   forming a metal carbide control gate over the barrier oxide;
   etching the dielectric layer, charge trap layer, barrier oxide and control gate to form a flash structure; and
   passivating the flash structure by depositing SiO₂, wherein the metal carbide control gate comprises aluminum and during the deposition of SiO₂ the aluminum in the metal carbide reacts with oxygen to self-passivate the control gate.

28. The method of claim 27, wherein the metal carbide comprises from about 6 to about 16% aluminum.

29. The method of claim 27, wherein the metal carbide control gate is from about 25 to about 200Å thick.

30. The method of claim 27, wherein the metal carbide control gate is formed by an atomic layer deposition process comprising alternately and sequentially contacting the substrate with a vapor phase pulse of a metal halide and an aluminum hydrocarbon compound.

31. The method of claim 27, wherein the aluminum hydrocarbon compound is selected from the group consisting of trimethyl aluminum (TMA), triethyl aluminum (TEA), and dimethylaluminumhydride (DMAH).

32. The method of claim 27, wherein the metal halide is a tantalum halide.

33. The method of claim 27, wherein the tantalum halide is TaCl₅.

34. A method of forming a metal carbide thin film with a desired level of oxidation resistance comprising:
   depositing a metal carbide thin film by alternately and sequentially contacting a substrate with vapor phase pulses of a metal precursor and an aluminum hydrocarbon compound, wherein one or more reaction conditions are selected to produce a desired concentration of aluminum in the metal carbide thin film, and wherein the concentration of aluminum in the metal carbide is from about 1 to about 30%.

35. The method of claim 34, wherein the metal carbide thin film comprises from about 6 to about 16% aluminum.

36. The method of claim 34, wherein the one or more reaction conditions are selected from the nature of the aluminum hydrocarbon compound, the reaction temperature, the reaction pressure, the pulse time of the metal precursor, the pulse time of the aluminum hydrocarbon compound, use of a reactant comprising plasma, the pulsing sequence and post deposition annealing.

37. The method of claim 34, wherein the metal precursor is a metal halide.

38. The method of claim 34, wherein the aluminum hydrocarbon compound is selected from the group consisting of alkanes, alkenes and alkynes.

39. The method of claim 38, wherein the aluminum hydrocarbon compound is selected from the group consisting of trimethyl aluminum (TMA), triethyl aluminum (TEA), and dimethylaluminumhydride (DMAH).

40. The method of claim 39, wherein multiple aluminum hydrocarbon compounds are used.

41. The method of claim 39, wherein the ratio between the multiple aluminum hydrocarbon compounds is selected to produce a desired concentration of aluminum in the metal carbide thin film.

42. The method of claim 34, wherein the metal carbide is tantalum carbide.

43. The method of claim 34, wherein the metal carbide is deposited to a thickness of about 1 to about 1000Å.

44. The method of claim 34, wherein the metal carbide thin film serves as a control gate in a flash memory.

45. The method of claim 34, wherein the work function of the control gate is determined by the metal carbide.

46. The method of claim 34, wherein the atomic layer deposition process is carried out at a pressure of about 0.5 to 10 Torr.

47. The method of claim 34, wherein the atomic layer deposition process is carried out at a temperature of about 150°C to about 550°C.

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