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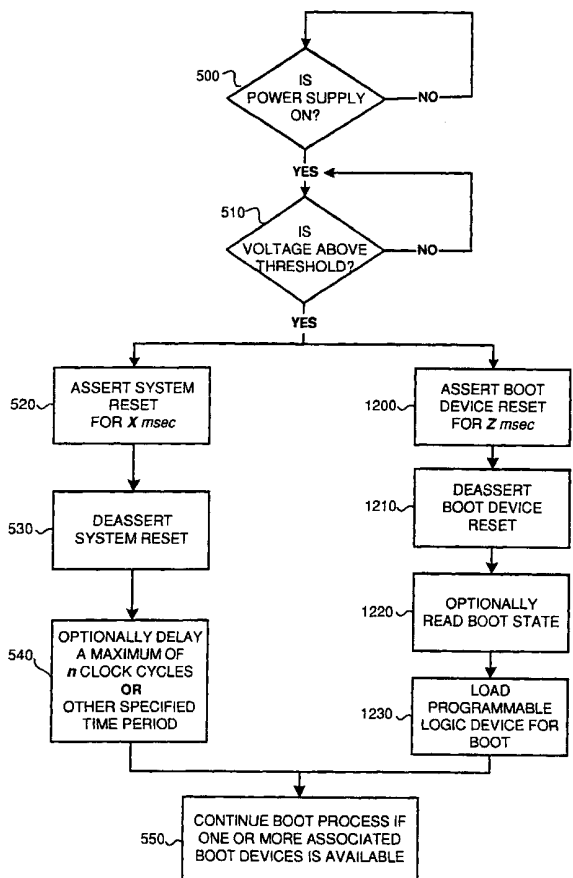
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[Continued on next page]

(54) Title: SYSTEMS AND METHODS FOR COMPUTER INITIALIZATION



(57) Abstract: Systems and methods for initializing devices such as computers, computer-based appliances, and processors and, in particular, systems and methods for initializing devices comprising one or more volatile logic devices after a power turn-on or a commanded reset. In one aspect, a method for initializing a computer system comprises sensing a command signal to boot the computer system (step 500, 510), generating a first control signal to initialize a boot process (steps 520, 530, 540), generating a second control signal to initialize a programmable logic device prior to completion of the initialization of the boot process (steps 1200, 1210, 1220, 1230), and booting the computer system using the initialized programmable logic device (step 550).



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B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
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Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ, IBM-TDB, INSPEC

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 5 263 168 A (JELEMENSKY JOSEPH ET AL) 16 November 1993 (1993-11-16) abstract; claims 1,6	1-14, 19-23
A	US 5 642 506 A (LEE VAN HOA) 24 June 1997 (1997-06-24) abstract	1-14, 19-23

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Patent family members are listed in annex.

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Information on patent family members

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Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 5263168	A	16-11-1993	NONE
US 5642506	A	24-06-1997	US 5867702 A
			02-02-1999