

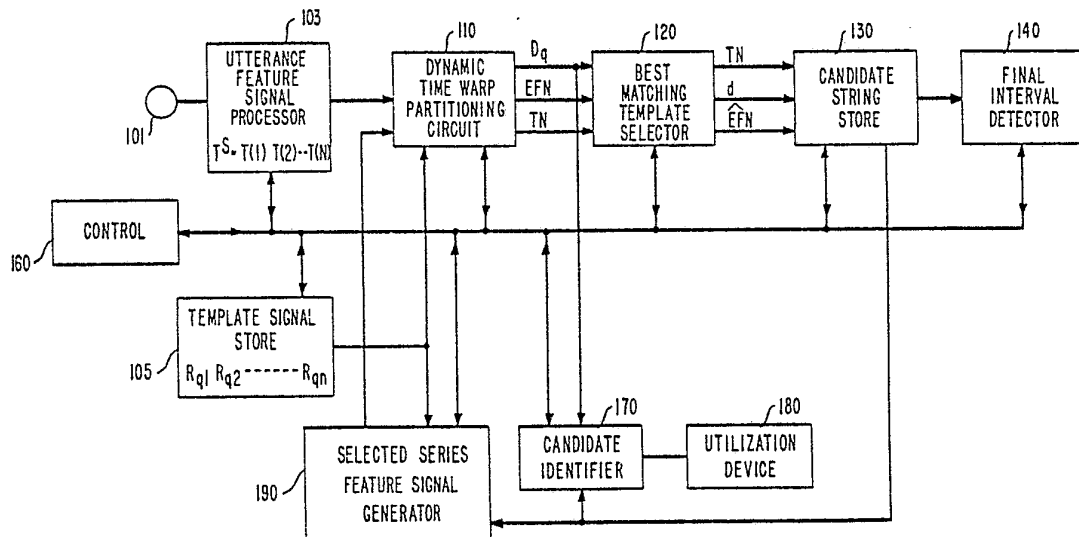
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(54) Title: CONTINUOUS SPEECH RECOGNITION SYSTEM



(57) Abstract

A continuous speech analyzer (Fig. 1) is adapted to recognize an utterance (101) as a series string of reference words (130) for which acoustic feature signals are stored (105). Responsive to the utterance (103) and reference word acoustic features (105), at least one reference word series is generated as a candidate for the utterance. Successive word positions for the utterance are identified. In each word position, partial candidate series are generated by a dynamic time WARP partitioning circuit (110) determining a distance signal reference corresponding to a prescribed similarity of utterance segment intervals and reference template involving a partial candidate series of the preceding word position. The candidate utterance segments (130) have beginning points within a predetermined range of the utterance position endpoint for the preceding word position candidate series to account for coarticulation and differences between acoustic features of the utterance and those for reference words (105) spoken in isolation. A minimum distance signal (170) selected from a plurality of partial candidates identifies the candidate string closest to the utterance.

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CONTINUOUS SPEECH RECOGNITION SYSTEM

Background of the Invention

Our invention relates to pattern recognition and
5 more particularly to arrangements for automatically
recognizing a continuous speech pattern as a series of
words.

In communication, data processing, and control
systems, it is often desirable to use speech as a direct
10 input for inquiries, commands, data or other information.
Speech recognition devices obviate the need for expensive
manually operated terminal equipment and permit individuals
to interact with automated equipment while simultaneously
engaging in other activities. The variability of speech
15 patterns from speaker to speaker and even for a particular
speaker, however, has limited the accuracy of speech
recognition. As a result, speech recognition arrangements
have been successful only in specially designed
environments.

20 Most speech recognition systems are adapted to
receive input speech signals and to transform the speech
signals into sets of prescribed acoustic features. The
input speech acoustic features are compared to stored sets
of previously obtained reference features for identified
25 words. The speech signal is identified when the input
speech features match the stored features of a particular
reference word in accordance with predetermined criteria.
The accuracy of such recognition systems is highly
dependent on the selected features and on the prescribed
30 recognition criteria. Best results are obtained when the
reference features and the input speech features are
derived from the same individual and the speech pattern to
be recognized is spoken with distinct pauses between
individual words.

35 Recognition of continuous speech patterns may be
accomplished by comparing the sequence of input speech



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features with every possible combination of reference word feature signal patterns derived from continuous speech. Such arrangements however require time consuming testing on all possible reference word pattern combinations and an
5 exhaustive search through the large number of reference word combinations. As is well known, the number of possible sequences increases exponentially with the number of words in the series. Consequently, it is generally impractical to perform the exhaustive search even for a
10 limited number of words in a pattern. Semantic and syntactic rules may be devised to limit the number of possible sequences in a search so that certain classes of information can be readily analyzed.

U. S. Patent 4,156,868 issued to S. E. Levinson May 29, 1979
15 and assigned to the same assignee discloses a recognition arrangement based on syntactic analysis. But recognition of random sequences of unrelated words such as a series of numbers is not improved by resorting to such contextual constraints.

20 U. S. Patents 4,049,913 and 4,059,725 disclose continuous speech recognition systems in which the similarity between the reference word feature patterns and all possible portions of the input speech pattern are calculated. Partial recognition results are derived from
25 the similarity measures and both the partial similarity measures and the partial recognition results are stored in a table. All possible partial pattern series which form continuous patterns are selected from the table. The continuous pattern for which the similarity is maximum is
30 then chosen. The recognized results from the table are extracted to provide the reference word series corresponding to the input speech pattern. These systems have been effective in continuous speech recognition. The signal processing to obtain reference patterns and partial
35 pattern similarity measures, however, is exceedingly complex and uneconomical for many applications.

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Alternative arrangements have been proposed in which an input speech pattern is segmented and each segment is recognized as one of a set of reference words. These alternatives require much less signal processing but do not
5 take into account the high degree of coarticulation in continuous speech which makes accurate segmentation difficult. The coarticulation or merging together of adjacent words in a continuous speech pattern makes recognition unreliable and also makes selection of
10 reference word training patterns difficult. It is an object of the invention to provide improved continuous speech recognition utilizing economical signal processing arrangements and simplified reference word training patterns.

15 Brief Summary of the Invention

The invention is directed to a continuous speech recognizer adapted to identify an unknown utterance as a series of reference words. Signals representative of the acoustic features of a set of reference words are stored
20 and a sequence of signals representative of the acoustic features of the utterance are produced. At least one reference word series is generated as a candidate for the utterance responsive to the reference word and utterance feature signals and the utterance is identified as one of
25 the candidate series. The candidate series generation includes producing a signal which identifies successive word position intervals for the utterance and forming partial candidate reference word series in each word position interval. In each word position interval, an
30 utterance segment is determined for each reference word which segment begins within a predetermined range of the utterance portion endpoint of the partial candidate series of the preceding word position and which corresponds to the reference word feature signals. A signal representative of
35 the similarity between each reference word and the corresponding determined utterance interval is formed. Each reference word having a prescribed similarity to its

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corresponding utterance segment is combined with the partial candidate series of the preceding word position to form a partial candidate series for said word position interval.

5 Brief Description of the Drawings

FIG. 1 depicts a general block diagram of a continuous speech recognizer illustrative of the invention;

FIG. 2 shows waveforms illustrating the utterance segmenting arrangement of the invention;

10 FIGS. 3 and 4 depict a detailed block diagram of the continuous speech recognizer of FIG. 1;

FIG. 5 depicts a memory circuit which may be used as the candidate string store of FIG. 3;

15 FIG. 6 depicts an arrangement which may be used as the repetition counter circuit of FIG. 3;

FIG. 7 depicts an arrangement which may be used as the template address circuit of FIG. 3;

FIG. 8 depicts an arrangement which may be used as the final interval detector of FIG. 3;

20 FIG. 9 depicts an arrangement which may be used as the frame address circuit of FIG. 3;

FIG. 10 depicts an arrangement which may be used as the candidate status circuit of FIG. 3;

25 FIG. 11 depicts an arrangement which may be used as the candidate string control circuit of FIG. 3; and

FIG. 12 shows waveforms illustrating the operation of the circuit of FIGS. 3 and 4.

General Description

30 FIG. 1 shows a general block diagram of a speech recognizer illustrative of the invention. Template store 105 in FIG. 1 stores a set of template signals representative of acoustic features of the reference words to which the utterance is compared. The acoustic feature signals are derived from isolated utterances of the reference words by means of a linear prediction analysis well known in the art. Although linear prediction parameters are used as feature signals in the arrangement

35

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of FIG. 1, it is to be understood that other features such as spectral or formant parameters may also be used. The utterance of each reference word q is divided into i frames and a p^{th} order feature vector $R_q(i)$ signal is produced for each frame. The feature vector signal corresponds to the autocorrelated linear prediction coefficients of the frame speech signal. The reference word utterance is then represented by the signal

$$R_q = R_q(1) R_q(2) \dots R_q(M_q) \quad (1)$$

where $R_q(i)$ is the feature vector of the i^{th} frame of the q^{th} reference word, and M_q is the total length in frames of the reference word utterance. The template signals R_q for the reference words $q_1, q_2 \dots q_V$ are stored in template store 105 and are addressed by template word number signal TN.

Utterance feature signal processor 103 is conditioned to receive a speech signal from microphone 101 and to convert the speech signal into a sequence of acoustic feature signals of the same type stored in template store 105. A linear prediction coding analysis is performed on each frame j of the speech signal to form feature vector signal $T^S(j)$. The utterance feature signal is then

$$T^S = T^S(1) T^S(2) \dots T^S(j) \dots T^S(N) \quad (2)$$

where N is the number of frames of T^S .

Assume for purposes of illustration that the speech signal is a digit string and that the reference words $q_0, q_1, q_2, \dots, q_9$ consist of the set of digits 0 through 9. The arrangement of FIG. 1 is adapted to select



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the concatenation of reference digits that is most similar to the speech signal. The K word concatenated reference digit string is

$$R^S = R_{q(1)} + R_{q(2)} + \dots + R_{q(k)} + \dots + R_{q(K)} \quad (3)$$

$$5 \quad R^S = R_{q(1)}(1) R_{q(1)}(2) \dots R_{q(K)}(M_q(K)) \quad (4a)$$

$$R^K = R^S(1) R^S(2) \dots R^S(P) \quad (4b)$$

where k denotes the word position in the series, K corresponds to the last word position and $+$ signifies concatenation, and P is the total number of reference frames for R^K is

$$10 \quad P = \sum_{k=1}^K M_q(k) \quad (5)$$

15

As disclosed in the article "Minimum Predictive Residual Principle Applied to Speech Recognition" by F. Itakura appearing in the IEEE Transactions on Acoustics, Speech, and Signal Processing, Vol. ASSP-23, pp 67-72, February 1975, one well known measure of the similarity between acoustic patterns is based on the frame distance d between reference word feature vector frames i and the unknown utterance feature vector frames j in accordance with Equation 6.

$$25 \quad d(i, j) = \hat{d}(R^S(i), T^S(j)) = \log(R^S(i) \cdot T^S(j)) \quad (6)$$

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$T^S(j)$ and $R^S(i)$ are $(p+1)^{th}$ order feature vectors and $R^S(i) \cdot T^S(j)$ is a ratio of prediction residuals as defined in the "Minimum Prediction Residual Applied to Speech Recognition", IEEE Transactions on Acoustics, Speech, and Signal Processing, Vol. ASSP-23, pp 67-72, February 1975 article. The concatenated series of reference words R^S that best matches the utterance feature vectors T^S is the series that has the minimum distance

$$D(R^S, T^S) = D(R_{q(1)} + R_{q(2)} + \dots + R_{q(K)}, T^S) \quad (7)$$

over all possible K and $q(K)$. If T is partitioned into K intervals, equation 7 may be expressed as

$$D(R^S, T^S) = \sum_{k=1}^K \sum_{i=1}^{q(k)} \hat{d}(R_{q(k)}(i), T^S(w(i))) \quad (8)$$

20

$w(i)$ is a distance minimizing warping function between reference frame $R_{q(k)}(i)$ and utterance frame $T^S(w(i))$ as obtained from the dynamic time warp disclosed in the article "Considerations in Dynamic Time Warping Algorithms for Discrete Word Recognition" by L. R. Rabiner, S. E. Levinson and A. E. Rosenberg, IEEE Transactions ASSP, Vol. ASSP-26, No. 6, pp 575-582, December 1978. The number of possible digit strings for even moderate values of K is large, e.g., 1000 strings for $K=3$ and each digit string requires at least one full dynamic time warp. The signal processing required to minimize $D(R^S, T^S)$ is very large and uneconomical for many applications.

30



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We have found that the signal processing required for accurate continuous speech recognition can be substantially reduced. This is accomplished by successively partitioning the utterance into reference
5 digit corresponding word position segments which segments are overlapped to account for merging of adjacent digits due to coarticulation. In the first partitioning ($k=1$), a set of segments are determined. Each segment corresponds to a reference digit. A signal representative of the
10 distance between the utterance segment feature vectors and the corresponding reference digit feature vectors is also generated. Reference digits whose features have a prescribed degree of similarity to the corresponding utterance segment features are selected and stored along
15 with the endpoints of their utterance segments.

In each successive partitioning, the utterance segment determined for each reference word begins in a region around the previously select digit segment endpoint and partial candidate digit sequences are selected which
20 have a prescribed similarity to the utterance from its beginning to the current selected reference digit segment endpoint. When all selected partial candidate reference word series endpoints are within a predetermined range of the utterance endpoint, the utterance is identified as the
25 selected reference digit sequence having the minimum feature vector distance to the utterance.

FIG. 2 illustrates the overlapping segmenting arrangement. An unknown utterance of N frames is represented in line 203 and a concatenated reference digit
30 string is represented in line 201. Bracketed region 210 is a reference digit interval on line 201 for digit $q(1)$ from its beginning point a_0 to its endpoint at frame a_1 . The utterance interval corresponding to reference digit $q(1)$ is determined by a dynamic time warp of feature signals to be
35 bracketed region 211 from b_0 to b_1 . Similarly, utterance intervals on line 201 are determined for the other digit positions $q(2)$, $q(3)$, $q(4)$ and $q(5)$. If digit $q(1)$ is

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selected as having a prescribed similarity to the utterance portion between b_0 and b_1 , frame b_1 is used as a beginning point for selection of the next digit of a sequence $q(2)$. Bracketed region 212 is a reference digit interval a_1-a_2 for digit $q(2)$ on concatenated reference string line 201. This region is transformed by dynamic time warping into region b'_1-b_2 on line 203.

Regions b_0-b_1 and b'_1-b_2 overlap to account for coarticulation between adjacent digits $q(1)$ and $q(2)$ and other effects of using isolated reference word templates. For example, if the sequence three eight is spoken, there is generally a merger between the ending of the three and the beginning of the eight. The overlapping of regions b_0-b_1 and b'_1-b_2 permits a realistic measure of the similarity between the utterance interval feature signals and the isolated reference word templates to be made. In this manner, digit coarticulation as well as differences between isolated versions of a spoken word and the word when used in a connected format are avoided. Similarly, the successively partitioned overlapping regions $b'_1-b_2, b'_2-b_3, b'_3-b_4$ and b'_4-b_5 of line 203 corresponding to concatenated isolated reference word regions $a_1-a_2, a_2-a_3, a_3-a_4$ and a_4-a_5 .

The first word position partitioning of the input utterance is started after the generated utterance feature signals are stored in processor 103. Partitioning circuit 110 receives the template feature signals for the zero digit $q(1) = 0$ from the template store 105 and utterance feature signals $T(1), T(2), T(3)...$ from the utterance store. A set of dynamic time warps are performed to determine the utterance interval from $T(1)$ which best matches template R_0 , and to generate a distance signal corresponding to the similarity of the utterance interval feature signals and the reference template signals $R_{q(1)}$ where $q(1)$ are the reference digits 0-9. The distance signal



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$$D(R_{q(k)}, T^S(n)) = \frac{1}{M_{q(k)}} \sum_{i=1}^{M_{q(k)}} \hat{d}(R_{q(k)}(i), T^S(w(i))), \quad (9)$$

5

is first produced for the $q(1) = 0$ digit. The utterance feature signals are supplied over the segment which starts at the utterance beginning point $n=n_1$ and ends at an endframe EFN corresponding to $w(M_0)$ in equation 9. The dynamic time warp is repeated for several precessed beginning points from $n=n_1$, i.e., $n=n_1-\delta, n_1-2\delta, \dots, n_1-N\delta$ in accordance with the overlap arrangement of the invention. The distance and segment endframe signals for the N time warps are placed in selector 120. The minimum distance signal $D_0(1)$ the corresponding ending frame for $q_0(1)$ are stored as the best matching results for digit $q_0(1)$. In similar manner, distance and ending frame signals are generated for the first word position digits $q_1(1), q_2(1) \dots q_9(1)$.

Selector 120 is then rendered operative to determine and store the minimum of the best matching distance signals $D_0(1), D_1(1), \dots, D_9(1)$. If the selected minimum $D_q(1)$ (the selected minimum of the best matched distance) is below a predetermined threshold, it is stored as a candidate digit for word position $k=1$. The best matching distance signals within a predetermined range of $D_q(1)$ are also stored as candidate digits for the utterance first word position. If no best matching distance signal is below the predetermined threshold, the candidate is discarded. The candidate identification TN, the candidate distance signal $d_c(1)$, and the candidate utterance endframe signal EFN_c(1) for each partial candidate string are stored in candidate string store 130. The candidate endframe signals EFN_c(1) are then compared to the utterance termination frame in final interval detector 140.

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Unless all $EFN_c(1)$ are within a predetermined range of the utterance termination frame U_T , a second word position segmentation is initiated. In the second and each succeeding word position, the first stored partial

5 candidate endframe $EFN_c(1)$ is a reference word corresponding utterance segment beginning point. The distance signals for each template, e.g.,

$$D_{0(k)1}^{n_1}, D_{0(k)}^{n_1-0}, \dots, D_{0(k)}^{n_1-n_0}$$

and the corresponding interval endframe signals are generated. As

10 in the first word position, the best matching distance signal of the set of overlapping dynamic time warps is stored. After the dynamic time warps are completed for the $q(k)=9$ reference digit, the candidate digits are selected for the k^{th} digit position of the candidate string. The

15 dynamic time warp processing is then repeated for the remaining partial candidate strings with the endframe of each candidate string as the beginning frame for the segmentation. At the end of each word position partitioning, there are generally only one to four

20 candidate strings stored in string store 130. The dynamic time warp processing is limited to only the likely partial candidate strings taking into account coarticulation and isolated reference word templates. Consequently, the signal processing needed for selection of digit strings is

25 substantially reduced.

When the endframes of all candidate strings $EFN_c(k)$ are in the region of the utterance endframe U_T , the word position processing is terminated. The candidate string identifications for the utterance in store 130 are

30 then utilized to form a sequence of template feature signals $R^S = R_{q(1)} + R_{q(2)} + \dots + R_{q(K)}$ in string feature signal generator 190. A distance signal jointly responsive to the candidate string and the utterance feature signal is generated in partitioning circuit 110 in accordance with

35 equation 8. The string accumulated distance signal from string store 130 is added to the concatenated reference string distance signal in candidate identifier 170. The



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resulting sum is stored in string store 130 and the minimum summed distance signal for all candidate strings is used to identify the string which most closely corresponds to the utterance. The utterance identification signal from
5 identifier 170 is then applied to utilization device 180 which may be a suitable data processor.

Detailed Description

A detailed block diagram of a speech recognizer illustrative of the invention is depicted in FIGS. 3 and 4.
10 Referring to FIG. 4, control signal ST is supplied to general controller 401 from utilization device 315 in FIG. 3 when a speech signal to be recognized is produced at microphone 301. Signal ST is shown in waveform 1201 in FIG. 12. Responsive to momentary signal ST, control
15 signal SU (waveform 1203) is produced in controller 401. Signal SU is applied to utterance feature generator 303 between times t_1 and t_2 . Utterance feature generator 303 may comprise a digital processor arrangement such as the micro NOVA MP/100 system processor, the MP/100 8K
20 programmable read only memory, the MP/100 4K/8K dynamic random access memory and a model 4222 digital I/O interface unit. The operation of generator 303 may be controlled by permanently stored instruction signals in accordance with the FORTRAN language program of Appendix A of "Formant
25 Trajectory Estimation from a Linear-Least-Squares Inverse Filter Formulation" by John D. Markel, Speech Communications Research Laboratory, Inc., Monograph No.7 October 1971. The generator is operative to produce a sequence of acoustic feature vector signals $T(1)$,
30 $T(2), \dots, T(n)$. A multidimensional feature vector signal $T(j)$ is generated for each speech sample frame j . Each sample frame j is of 10 millisecond duration.

The generated feature signals are transferred to utterance feature signal store 310 under control of frame
35 address signal FR. When the end of the utterance is detected in generator 303 at time t_2 , signals FI and FE of waveforms 1205 and 1207 are produced. Signal FE



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corresponds to the endframe region of the utterance $U_T - \Delta_T$ and is supplied to final interval detector 335. Assume that the endframe FE is $U_t - \Delta_t = 100$. Signal FI is applied to controller 401 to indicate the termination of the feature
5 signal generation.

Responsive to signal FI, controller 401 produces signal IMEM (waveform 1209) at time t_3 in FIG. 12. Signal IMEM clears candidate string store 311 so that new information may be entered therein. Signal IMEM is also
10 supplied to controller 401 to generate signal ISD at time t_4 in FIG. 12. The ISD signal (waveform 1211) initially clears candidate string control circuit 345 and produces an ITC signal (waveform 1213) at time t_5 which is operative to clear template address circuit 325.

The FI and FE signals are supplied to final
15 interval detector 335 shown in detail in FIG. 8. Referring to FIG. 8, the FE utterance end signal is placed in latch 801 responsive to the FI utterance feature generation ending signal. The output of latch 801 is applied to one
20 input of comparator 820. The other input of comparator 820 receives a starting frame signal SFN (waveform 1215) from candidate string store 311. As aforementioned candidate string store 311 is reset to zero at this time. Thus an SFN=0 signal is compared to the frame ending region of the
25 utterance. Since SFN=0 is substantially smaller than the utterance ending frame 100, the $\overline{\text{SAE}}$ signal from comparator 820 (waveform 1217) is enabled at time t_6 in FIG. 12. Gate 413 is then enabled responsive to the ITC and $\overline{\text{SAE}}$ signals whereby signal SC1 (waveform 1219) is
30 produced by controller 401 at time t_7 . Signal ITC also sets flip-flop 380 to its set state and signal A (waveform 1200) is enabled.

Signal SC1 is supplied to DTW partitioning controller 440 which enables signal IRC responsive thereto.
35 Signal IRC (waveform 1221) resets repetition counter circuit 330 to its initial state at time t_8 .



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The dynamic time warp positioning and candidate selection operations of generator 307 and selector 309 are started. The timing of these operations is generally indicated in FIG. 12 between times t_8 and t_{20} . The IRC signal at time t_8 causes controller 440 to produce an SDG signal. The SDG signal is supplied to the dynamic time warp distance generator 307 in FIG. 3 to start the initial segmentation. DTW generator 307 may comprise an arrangement such as described in the Microproducts Hardware Systems Reference published by Data General Corporation, Westboro, Massachusetts and copyrighted by Data General Corporation, 1979 or other microprocessor systems well known in the art. Generator 307 may include the micro Nova MP100 system processing unit, the MP100 4K/8K dynamic random access memory, the MP/100 8K programmable read only memory, and one or more model 4222 digital I/O interface units. The operation sequence of generator 307 is determined by the permanently stored instructions in the read only memory thereof. These instructions are listed in FORTRAN language form in Appendix A. Similarly, string selector 309 may also comprise the Micro Nova MP100 system, MP/100 4K/8K memory, MP100 8K programmable memory and one or more model 4222 interface units. The permanent instructions controlling the string selector operation are listed in FORTRAN language in Appendix B.

The output of template address circuit is then $TN=T_0$. Flip-flop 380 provides a high A control signal. Jointly responsive to the T_0 and A signals, zero digit template from template store 305 is transferred into DTW distance generator 307. Generator 307 provides a sequence of template frame addressing codes FN corresponding to the successive reference feature vector signals to frame address circuit 320 which is shown in greater detail in FIG. 9. Frame address circuit 320 includes δ counter 901, subtractor 905, adder 907 and selector 909. δ counter 901 was initially reset to zero by signal IRC. The output coded signal of counter 901 is applied to the negative



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input of subtractor 905 in which the δ code is subtracted from the starting frame signal SFN. The resultant signal SFN- δ is applied to one input of adder 907 wherein the frame number codes of the template feature signals are
5 added to the output of subtractor 905. The EFN signal from adder 907 is supplied to utterance feature signal store 310 via selector 909 as an utterance frame address signal for the feature vectors T(j).

At the beginning of the first partitioning δ
10 counter 901 is set to zero. The starting frame number SFN of the interval is zero corresponding to the beginning of the utterance. Consequently, the output of adder 907 is identically the frame numbers from distance generator 307. In this manner, the initial utterance feature vector
15 signals $T^S(0), T^S(1) \dots$ are sequentially supplied to distance generator 307 as addressed by the reference template feature signal frames FN. For the first time warp processing, EFN begins at zero. The next time warp begins at frame $-\delta$ and the N^{th} time warp begins at $-N\delta$. A prefix
20 of zero feature vectors are inserted to utterance store for the first word position time warp processing.

In the first partitioning operation for
template $R_0(1)$ the distance signal $D_0^{n_1}(1)$ is produced in accordance with equation 9. When the end of the utterance
25 interval corresponding to reference template feature vector $R_0(1)$ is reached, generator 307 produces a DGD signal which signal is supplied to controller 440. DTW partitioning controller 440 then supplies an advance repetition counter signal ARC which increments repetition counter circuit 330
30 shown in FIG. 6.

As indicated in FIG. 6, repetition counter 601 is initially reset to its zero state by signal IRC at time t_8 on FIG. 12. Responsive to the ARC signal from controller 440, the repetition count is advanced to its
35 first state. The ARC signal is also supplied to frame address circuit 320 to advance the δ counter 901. In this



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manner, the starting frame number is precessed by δ frames responsive to each ARC signal whereby overlapping interval dynamic time warping is accomplished. Signal \overline{ARD} from comparator 603 is enabled until repetition counter 601 reaches the last repetition position $N1$.

The ARC signal is supplied to AND-gate 449 which causes controller 440 to produce another SDG signal. The second dynamic time warp partitioning for the first reference template is then started. In the second dynamic time warp, the first EFN frame number from adder 907 is $SFN-\delta$. Upon completion of the dynamic time warp in accordance with equation 9, partitioning generator 307 supplies a DGD signal to controller 440 which in turn generates an ARC pulse to increment repetition counter 601 and δ counter 901. In this manner the dynamic time warp partitioning is repeated for N different starting points. At each repetition, the first EFN frame is cut back by δ frames so that the succession of initial frames is $SFN, SFN-\delta, SFN-2\delta, \dots, SFN-N\delta$.

The output distance codes from the sequence of first reference template time warps and the ending frames of these time warps are transferred to and stored in string selector 309. At the beginning of the N^{δ} repetition, counter 601 is incremented to its N^{th} state and an \overline{ARD} signal is obtained from comparator 603. When the last repetition of the $q(1)=0$ time warp is completed, the DGD signal from generator 307 is applied through gate 451 to controller 440. In this manner, an SS1 selection signal is produced. The SS1 signal is applied to string selector 309 and is effective to initiate the selection of the best matching distance signal of those received from

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generator 307 for the $q(1)=0$ digit. In the arrangement of selector 309, the minimum distance code for the $q(1)=0$ digit is determined and stored.

Signal S1D is produced by selector 309 after the storage of the minimum distance signal for the $q(1)=0$ digit. Responsive to the S1D pulse, controller 440 produces an ATC pulse to advance template counter 702 in FIG. 7 to its next state. The output of template counter addressing signal TN is then changed so that the $q(1)=1$ digit template feature set in template store 305 is transferred to DTW distance generator 307. In the absence of an ATD (all templates done) signal from comparator 704, controller 440 generates an IRC (initialize repetition counter) pulse responsive to the signals applied to gate 442. Repetition counter 601 is then reset to its zero state and an SDG signal is produced by controller 440.

As described with respect to the $q(1)=0$ digit, the SDG, DGD and ARC control signal sequence produces the set of distance signals $D_{q(1)}^{n_1}, D_{q(1)}^{n_2}, \dots, D_{q(1)}^{n_6}$, for the digit $q(1)=1$ of the first word position. The distance signals and the segment ending frame signals obtained in the repeated time warping operations for $q(1)=1$ are stored in string selector 309. Upon completion of the $q(1)$ digit processing in generator 307, the minimum distance signal for the $q_1(1)$ digit is determined and stored responsive to the SS1 signal from controller 440.

After all repetitions for the final first position $q(1)=9$ digit are completed and the $q(1)=9$ minimum distance signal is stored in selector circuit 309, controller 440 produces an SS2 signal responsive to enabled ATC and ATD signals at AND-gate 453. At this time, template counter 702 is in its last template (Q^{th}) state and the ATD (all templates done) signal is enabled. The SS2 pulse from controller 440 is operative to initiate the selection of first word position candidates. In this selection, the minimum of the stored best matching distance



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signals for $q(1)=0,1,2,\dots,9$ is determined. Its distance value $D''_{q(1)}$ is then compared to the predetermined threshold distance in selector 309. If $D''_{q(1)}$ is less than the threshold, the distance value is retained as a first
5 word position candidate distance. The first word position identification code T1 and the ending frame code for the selected candidate $ENF_C(1)$ is also stored. The remaining minimum difference signals which are within a preassigned distance of the first selected candidate are also retained
10 along with their template identification and ending frame codes as first word position candidates. In general, only one or two candidates are found in each word position partitioning. Signal SS2 also resets flip-flop 592 to activate the INS signal therefore.

15 Assume for purposes of illustration that two candidate digits, 5 and 9, are selected for the first word position of the utterance. The minimum of the best matching time warping distance $D''_{q(1)}$ is 0.44 for $q(1)=5$ and the ending frame $EFN_5(1)$ is 35. The best matching time
20 warping distance within the preassigned distance of $q(1)=5$, is 0.48 for $q(1)=9$ and the corresponding ending frame $ENF_9(1)$ is 24. At this time, the identification, ending frame, and distance signals for the first position candidates 5 and 9 are inserted into candidate string
25 store 311 shown in greater detail in FIG. 5.

As shown in FIG. 5, the candidate string store includes a pair of memories 530 and 540. These memories were previously cleared by signal IMEM at time t_4 on FIG 12 so that all memory positions were reset to zero. At the
30 end of the first utterance DTW segmentation, and selection the identification, distance, and ending frame information for each generated candidate is inserted into memory 530 as addressed by the output of DST (destination) counter 1103 in candidate string store control circuit 345.

35 Destination counter 1103 as well as SRC (source) counter 1101 were previously reset to their one states by signal ISD at time t_4 in FIG. 12. Consequently, both



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source and destination counters 1101 and 1103 provide one outputs. Flip-flop 520 was placed in its set state by signal IMEM whereby the SA signal therefrom is enabled and signal $\bar{S}A$ is not enabling. Responsive to signal SA from
5 flip-flop 520, the output of destination counter 1103 is supplied to memory 530 via selector 532. The output of selector 532 addresses the first candidate string position of memory 530. Signal $\bar{S}A$ controls selector 542 so that the source signal SRC now addresses the first candidate string
10 position in memory 540.

In the candidate string storage operation for the first word position, the zero valued signals in the first candidate position of memory 540 is supplied to buffer 550. In general, the output from buffer 550 includes the
15 identification codes for each digit position T_1, T_2, \dots, T_n , the ending frame number for the last stored candidate string $SFN(k)$, the accumulated distance for the candidate string d_{str} , and the number of digits in the candidate string NC. The signals from buffer 550 are supplied to the
20 inputs of adder 518, selectors 581 through 589, selector 512, and adder 510. The signals from buffer 550 are updated in these selectors and adders and the updated signals therefrom are inserted into memory 530 via lines 590 and 534.

25 After each word position, the state of flip-flop 520 is reversed by signal MEX so that signal, $\bar{S}A$ and signal SA are alternately enabling. The signals in one of memories 530 and 540 are then read out of buffer 550 and the updated signals are written into the other memory. In
30 this manner, the results of each partitioning are supplied to candidate string store 311 and the partial candidate string selection signals are updated.

Upon completion of the selection of the first word position candidates 5 and 9, signal S2D is emitted
35 from string selector 309. During the string selection, the number of selected digits for the word position is counted and stored as signal M. Signal M is applied to



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comparator 1005 in candidate status circuit 340 wherein it is compared to a zero code Z. In the event there were no candidate digits in the first word position, $M=0$ and the ZC output of comparator 1005 is enabled. Responsive to the S2D signal from string selector 309 and signal ZC, gate 415 is activated. Signal FIRST is enabled during the first word position and general controller 401 generates an RPT signal responsive to the operation of AND gate 417. The RPT signal is supplied to utilization device 315 to indicate that the recognition operation should be restarted or aborted.

Since there are two candidates in the first word position, $M=2$ and the $\bar{Z}C$ signal comparator 1005 are enabling. An SC2 signal (waveform 1235) is produced in general controller 401 at time t_{20} responsive to the operation of AND-gate 419. The SC2 signal is supplied to candidate string store controller 480 via OR-gate 483 so that signals MWR and IDST are produced.

Signal MWR (waveform 1237) causes the signals on line 539 to be inserted into memory 530 at time t_{21} . Signal IDST (waveform 1239) then increments destination counter 1103 at time t_{22} . In this manner, the first candidate string position of memory 530 is addressed through selector 532. Since memory 540 was initially cleared, the addressing of this memory produces all zero signals in buffer 550. The NC signal from buffer 550 is supplied to one input of adder 518. A one signal is applied at the other input of adder 518 responsive to the INS(insert) signal from flip-flop 592 applied to selector 516. The NC' output of adder 518 is applied to the first word position of the NC row of memory 530 via lines 590 and 534. The NC' word position signal from adder 518 is also supplied to position decoder 501 which enables line S1 to selector 581. The candidate identification signal \hat{T} corresponding to the digit 5 from string selector 309 is passed through selector 581. The remaining identification selectors pass the signals

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T₂, T₃, ..., T_n from buffer 550. In this manner, the T₁ row of the first candidate string of memory 530 receives the identification signal for digit 5. The remaining first word position identification rows receive the zero valued
5 T₂, T₃, T_n outputs from buffer 550.

Selector 512 is enabled by signal INS, and the $\hat{E}FN$ signal from string selector 309 is supplied to the first candidate string position of the EFN row of memory 530. The d_{str} accumulated distance output of
10 buffer 550 is applied to one input of adder 510. The \hat{d} signal from string selector 309 is passed through distance selectors 503 and 505 responsive to signal INS. The resulting sum from adder 510 corresponding to the accumulated distance signal for the 5 digit is supplied to
15 the d_{str} row of first partial candidate word position of the memory 530.

Responsive to the MWR signal from controller 480 at time t_{21} , gate 571 is enabled to produce a WA signal. The WA signal is supplied to memory 530 and causes the
20 updated candidate string signals to be inserted into memory 530. The updated string signals stored in memory 530 for the candidate digit 5 are listed in the first candidate string row of table 1. After the signals are inserted into the first candidate string position of
25 memory 530 responsive to signal WA, the IDST signal from OR-gate 487 occurring at t_{22} increments candidate counter 1001 in FIG. 10 and DST counter 1103 to their two states. The candidate counter output is compared to signal M in comparator 1003. Signal M corresponds to the
30 total number of first word position candidates. The ClD signal from comparator 1003 is enabled after controller 480 produces a second signal MWR at time t_{23} to write the signals from cable 534 into the second candidate string position of memory 530.

35 Counter 1103 is in its second state so that the second word position of memory 530 is addressed via selector 532. At this time, the identification, distance,



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and ending frame number signals corresponding to the selected digit 9 are stored in the second candidate string position of memory 530. Buffer 550 still provides zero signals on all lines therefrom. The \hat{T} signal from string selector 309 corresponds to digit 9. The d_{str} signal for the 9 digit is 0.48 and the ending frame \hat{EFN} signal is 24. As described with respect to the storage operations for the preceding selected digit (5), the NC code from buffer 550 is incremented by one in adder 518 and the updated NC'=1 signal is supplied to the NC row of the second candidate string position of memory 530. The \hat{T} identification signal from string selector 309 passes through selector 581 responsive to the enabled S1 signal from decoder 501. In this manner, the T1 row of the second candidate string position of memory 530 is supplied with a code corresponding to the selected digit 9.

The d_{str} signal output of selector 309 passes through distance selectors 503 and 505 to adder 510. The resultant accumulated distance signal d_{str} for the second candidate string of the first word position is thereby applied into memory 530. In like manner, the ending frame signal corresponding to the second candidate string of the first word position is also supplied to memory 530 via selector 512. Signal WA from gate 571 inserts the signals from cable 534 into memory 530 responsive to the MWR pulse at t_{23} .

Table 1 lists the contents of memory 530 after the second candidate string signals are inserted therein.

Table 1

Candidate String Pos.	String Identification					SFN	d_{str}	NC
	T ₁	T ₂	T ₃	T ₄	T ₅			
1	5	0	0	0	0	35	0.44	1
2	9	0	0	0	0	24	0.48	1



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As indicated in Table 1 there are two candidate digits for the first word position. These candidate digits are selected in accordance with the string distance signals d_{str} . The string distance signal d_{str} for the digit 5 is the minimum of the distance signals found during the first partitioning. The digit 9 is selected because its accumulated distance signal is only slightly larger than that for digit 5. The ending frame for digit 5 is 35 and for digit 9 is 24.

10 After the two first position partial candidate strings are stored in memory 530, the next IDST signal from OR-gate 487 at time t_{24} passes through AND gate 485 in the presence of previously enabled signal C1D. String store controller 480 is thereby signaled to terminate the memory
15 storing operations for the first word position and controller 480 produces a C2D signal. The C2D signal is supplied to general controller 401 via OR-gate 426 so that an ISN pulse (waveform 1241) is enabled at time t_{26} . Prior to time t_{26} , the output of source counter 1101 is a one
20 code. Latch 1105 was initially set to its one state by a preceding ISD signal at time t_4 . Consequently, the DAS output of comparator 1107 is enabling.

Jointly responsive to the ISN and DAS signals, gate 428 causes controller 401 to produce signals LDST and
25 MEX at time t_{27} . Signal MEX (waveform 1243) reverses the state of flip-flop 520. During the second word position segmentation, the signals from memory 530 are transferred to buffer 550 and the modified candidate string signals is inserted into memory 540 via cable 544. The LDST signal
30 from controller 401 enables latch 1105 whereby the 2 output of DST counter 1103 is inserted therein for the next word position. The count in DST counter 1103 corresponds to the number of partial candidate reference word series of the first word position in memory 530. Latch 1105 now contains
35 the number of source candidate strings in memory 530. These strings are read from memory 530 in the second word position and the endpoints thereof are used to determine



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the predetermined segment beginning range for the second word position.

Prior to the second word position segmentation, the ending frames for the strings in memory 530 must be tested to detect whether the just finished word position is the final word position for the utterance. The final position detection is accomplished in detector 335 shown in greater detail in FIG. 8. Flip-flop 815 is placed in its set state at the beginning of each partitioning by signal ISD. As each $\hat{E}FN$ signal is transferred from string selector 309 to candidate string store 311, it is also applied to one input of comparator 805. Comparator 805 receives signal FE corresponding to the utterance end range from latch 801 and provides an output for each $\hat{E}FN$ signal which is less than the $U_t - \Delta$ signal from latch 801. If any transferred $\hat{E}FN$ signal from selector 309 is less than the end range signal for the utterance, flip-flop 815 is reset and signal \overline{EDP} therefrom is enabled while signal EDP (waveform 1245) is disabled. A final position detection signal EDP is produced when all $\hat{E}FN$ signals in a word position are equal to or greater than the utterance termination range signal in latch 805. At the time signal MEX is active, the output of flip-flop 815 is inspected in gates 403 and 430. Assume for purposes of illustration that the first word position segmentation does not result in the detection of the final position. Gate 403 is activated by signals MEX and \overline{EDP} so that controller 401 produces a second ISD signal at time t_{27} which initiates the second word position processing.

In the second and subsequent word positions an utterance segment is added for each candidate string in store 311 responsive to the dynamic time warp partitioning for the utterance feature signals in store 310. The initial frame for the second partitioning is the ending frame $\hat{E}FN$ for the string in store 311 as signal SFN. The beginning point for each string is supplied to frame address circuit 320 from candidate string store 311. DTW



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partitioning generator 307 determines the distance signals for the overlapping segments of each template under control of frame address circuit 320 as described with respect to the first segmentation. The best matched string is selected
5 in string selector 309 for each template and the minimum distance template of the best matched distance signals is selected for transfer to partial candidate string store 311. The string store then receives the set of
10 template identification, ending frame number, and distance signals which meet the prescribed distance threshold criteria. These signals augment the string signals in store 311 and are combined with the stored partial candidate string information of the preceding word position in the memory. While the sequence of candidate strings are
15 processed in store 311, the newly added candidate segment endpoints are compared to the utterance termination range in final interval detector 335.

At the beginning of the second word position partitioning (t_{27} in FIG. 12), signal ISD resets SRC
20 counter 1101 and DST counter 1103 to their one states. Controller 401 then produces an ITC signal at time t_{28} which is effective to reset template counter 702 to its zero state. Signal ITC also sets flip-flop 380 whereby the feature vector signals for the zero digit template in
25 store 305 are transferred to generator 307. Comparator 820 of final interval detector 335 is now operative to compare the SFN(2) starting frame number of the first partial candidate string (5) stored in memory 530 to the utterance termination range. If the starting frame number is within
30 the predetermined utterance termination range, signal SAE is produced.

Signal SAE occurs when a candidate string no longer has feature signals to be analyzed. Since no additional digits can be added to the string, the
35 recognizer circuit is operative to transfer the candidate string from store 530 to store 540 without alteration. The transfer is accomplished in the following manner.

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Signal ITC from general controller 401 sets flip-flop 592 in FIG. 5 so that the CPY output therefrom is enabled. Responsive to the enabled $\bar{S}A$ and disabled SA signals from flip-flop 520, selector 542 addresses the first candidate
5 string position of memory 540. Selector 532 addresses the first memory string position of memory 530. The output of memory 530 appears in buffer 550. The CPY signal applied to selectors 503 and 505 cause a zero code signal to be transferred from the input of selector 503 to the input of
10 adder 510. Thus, the distance output of the first candidate string in memory 530 is applied to the input of memory 540 via adder 510. A zero signal is supplied to selector 516 responsive to the CPY output of flip-flop 592 whereby the NC signal from memory 530 is transferred
15 without change into memory 540. Position decoder 501 is inhibited by signal CPY so that the T1, T2, Tn outputs, from memory 530 are placed in the corresponding positions of memory 540.

Jointly responsive to the ITC and SAE signals applied to gate 412, general controller 401 produces an MWR
20 and an IDST1 signal. The MWR signal is supplied to gates 571 and 573 so that the signals applied to memory 540 via cable 544 are inserted in the first candidate storing position therein. The IDST1 signal causes an IDST signal
25 to appear at the output of OR-gate 487 whereby the DST counter 1103 is incremented. In this manner, the second string position of memory 540 is now addressed via selector 542. Signal IDST1 also passes through gate 426. Controller 401 produces an ISN signal responsive to the
30 operation of gate 426. Since source counter 1101 is in its first state, the $\bar{D}AS$ output of comparator 1107 is enabled and another ITC signal is produced via AND-gate 407, OR-gate 410 and general controller 401. Source counter 1101 is incremented by the ISN pulse so that the second
35 candidate string in store 530 is addressed after the first string is copied from memory 530 to memory 540.

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The dynamic time warping for the first candidate string in the second word position partitioning begins at frame SFN=35 which frame is substantially less than the utterance termination frame (100) stored in latch 801.

5 Consequently, signal \overline{SAE} rather than SAE is enabled at time t_{29} . Responsive to the \overline{SAE} and ITC signals, general controller 401 transmits an SC1 pulse (waveform 1219) to segmenting controller 440 via OR-gate 444 at t_{30} . Counter 601 in repetition counter circuit 330 and δ

10 counter 901 in frame address circuit 320 are reset to their zero states by the IRC output of controller 440 at time t_{31} . Thus, the first frame position of utterance store 310 to be addressed is frame position n_2 ($EFN(2)=35$). The IRC signal is supplied to OR-gate 448 to start the second

15 word position DTW segmentation and selection. These operations are generally indicated in FIG. 12 between times t_{31} and t_{50} . Signal IRC causes controller 440 to apply an SDG signal to DTW partitioning generator 307. As a result of the dynamic time warping operations of generator 307,

20 the distance signals $D_{q(2)}^{n_2}$, $D_{q(2)}^{n_2-\delta}$, ..., $D_{q(2)}^{n_2-N\delta}$ for the $q(2)=0$ digit template and the corresponding interval endpoint frame numbers $\hat{EFN}(2)$ are stored in string selector 309. At the end of each warping, repetition counter 601 and δ counter 901 are incremented by the

25 ARC signal produced in controller 440 responsive to the DGD signal from generator 307. During the final repetition dynamic time warping for the zero digit, the ARD signal from comparator 603 is enabled. The ARC and ARD signals at the end of the N^{th} time warping enable gate 451 so that an

30 SS1 select signal is produced by controller 440. Responsive to the SS1 control signal, the best matched distance signal for the zero digit $D'_{q(2)}$ is selected in string selector 309. Upon completion of the selection, the S1D signal from selector 309 causes controller 440 to

35 generate an ATC advance template counter signal which increments template counter 702 in template address circuit 325. The operations of generator 307 and string

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selector 309 with respect to the distance signals
 $D_{q(2)}^{n_2}, D_{q(2)}^{n_2-0}, \dots, D_{q(2)}^{n_2-N_0}$ for $q(2)=1$ in the second
 word position are then performed under control of
 controller 440. After the selection of the best matched
 5 distance signals for the digits 0 through 9 in the second
 word position for the first partial candidate series of the
 first word position, signal ATD from comparator 704 and
 signal ATC from the output of controller 440 enable AND-
 gate 453. Controller 440 then produces an SS2 signal. The
 10 SS2 signal causes string selector 309 to select the minimum
 $D_{q(2)}''$ of the best matching template digit
 signals D_0', D_1', \dots, D_q' for the second word position where
 the minimum $D_{q(2)}''$ is below the predetermined threshold
 set in the selector. As previously described with respect
 15 to the first word position partitioning, other distance
 signals are selected as additional candidates where their
 distance signals are within a predetermined range of the
 minimum distance candidate.

Assume for purposes of illustration that the
 20 minimum of the best matched candidate distance signals is
 found for the zero digit and no other digit candidates are
 obtained in the second word position partitioning of the
 first candidate string (5). The S2D signal produced in
 string selector 309 after the selection of the zero digit
 25 is supplied to an input of gate 417. The other input of
 this gate is obtained from comparator 1005 in candidate
 status circuit 340. Since an $M=1$ signal is applied to the
 candidate status circuit from string selector 309,
 comparator 1005 produces a ZC signal. Gate 419 in FIG. 4
 30 is enabled and an SC2 signal is sent from general
 controller 401 to string store controller 480 via OR-
 gate 483.

During the next time interval, the first
 candidate string of the first partitioning in memory 530 is
 35 augmented by the distance, identification and ending frame
 signals from string selector 309. The resulting candidate
 string is then inserted into the first string position of

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memory 540. At the beginning of the transfer, the output of SRC counter 1101 addresses the first candidate string position of memory 530 via selector 532 under control of signal SA from flip-flop 520. The output of DST
5 counter 1103 addresses the first candidate string position of memory 540 via selector 542. The first candidate string signals from memory 530 are available at the output of buffer 550. Flip-flop 592 is reset so that signal INS is obtained therefrom. Consequently, adder 518 is operative
10 to augment the $NC=1$ signal from buffer 550 by 1.

The $NC'=2$ signal is supplied to memory 540 via cable 544. The NC' signal also causes the S2 output of position decoder 501 to be enabled. Thus, the template identification signal from string selector 309 is passed
15 through selector 585 to the input of memory 540. In like manner, selector 512 is enabled to transfer the $\hat{E}FN$ ending frame signal from string selector 309 to the input of memory 540, and the distance signal \hat{d} from the string selector 309 is added to the accumulated distance signal
20 from buffer 550. The resultant sum from adder 510 is supplied to the input of memory 540 via cable 544.

Signal SC2 at time t_{50} causes controller 480 to generate an MWR signal at time t_{51} as well as a delayed IDST signal at time t_{52} . The MWR signal is applied to
25 AND-gate 573 from which a WB memory insert pulse is supplied to memory 540. The candidate string signals at the input of memory 540 are thereby inserted into the first candidate string position thereof. At this time the C1D signal from comparator 1003 is enabled responsive to
30 the one signal from candidate counter 1001 and the $M=1$ signal corresponding to the single candidate found in string selector 309. The IDST signal from OR-gate 487 passes through AND-gate 485 and causes controller 480 to produce a C2D signal. The C2D signal is in turn applied to
35 controller 401 which produces an ISX pulse at time t_{53} .

At this time, the SRC counter is still in its first state and latch 1105 contains a two code



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corresponding to the number of candidate strings placed in memory 530 during the first word position. Consequently, a $\overline{\text{DAS}}$ signal is obtained from comparator 1107. Jointly responsive to the ISN and $\overline{\text{DAS}}$ signals at gate 407, general controller 401 produces a second ITC signal at time t_{54} . The ITC signal resets template counter 702 to its zero state. The ISN signal also increments SRC counter 1101 to its second state so that the second candidate string in memory 530 is addressed. Comparator 820 in final interval detector 335 now compares the SFN signal from the second candidate string in memory 530 to the termination signal in latch 801. The frame number in memory 530 corresponding to the 9 digit is 24. Since this frame number is less than the termination frame in latch 801, the $\overline{\text{SAE}}$ signal is enabled at time t_{55} and general controller 401 produces an SC1 pulse at time t_{56} responsive to the operation of AND-gate 413.

The SC1 signal activates controller 440 as generally indicated in FIG. 12 between times t_{60} and t_{70} . Responsive to control signals from controller 440, partitioning generator 307 successively produces the distance signals corresponding to the second candidate string. The minimum of each digit distance signal is stored in selector 309. As previously described, string selector 309 is then operative to select the second word position candidates for the second partial candidate string of the first word position.

Assume for purposes of illustration that the zero digit template is found to provide the minimum of the best matching distance signals for all templates to be added to the second candidate string and that the ending frame for the utterance interval found during the overlapping interval warping is 72. The resulting template identification ending frame number and distance signals are then combined with the second candidate string signals from memory 530. The resulting candidate string is then placed in the second candidate string position of memory 540 as

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addressed by DST counter 1103. The storage process is done under control of string store controller 480 as previously described with respect to the storage of the first candidate string 50.

5 SRC counter 1101 is in its second state during the storage operation of the second candidate string. Since the output of counter 1101 matches the output of latch 1105, the DAS signal from comparator 1107 is enabled. Candidate counter 1001 is in its one state and an
10 M=1 signal is obtained from string selector 309. Thus, signal C1D from comparator 1003 is enabled. The IDST signal for the second candidate string transfer is then passed through gate 485 whereby a C2D signal is produced in controller 480. The C2D signal causes general
15 controller 401 to generate an ISN signal via gate 417 at time t_{71} . Gate 428 is then enabled by the ISN and DAS signals so that controller 401 produces an MEX (memory exchange) and LDST signals at time t_{72} . The LDST signal is applied to latch 1105 whereby the two code in
20 DST counter 1103 is transferred into the latch. The MEX signal reverses the state of flip-flop 520. Memory 540 is thereby conditioned to supply signals to buffer 550 while memory 530 is conditioned to receive signals from cable 534.

25 During the transfer of the selected candidate signals from string selector 309 to candidate string store 311 in the second word position, final interval detector 335 is operative to determine whether the second position ending frame signals are within the prescribed
30 range of the utterance termination. As described with the previous word position flip-flop 815 is initially set by the ISD signal at time t_{27} . Comparator 805 is operative to compare the ending frame number signals from string selector 309 to the utterance termination frame signal. In
35 the event that all ending frame signals applied to comparator 805 are equal to or greater than the utterance termination frame, flip-flop 815 remains set. In the

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second partitioning, however, the EFN signals are less than the utterance termination so that signal \bar{EDP} is enabled. The MEX and \bar{EDP} signals are utilized in AND-gate 403 to initiate the third word position processing.

5 Table 2 shows the contents of memory 540 at the end of the second partitioning.

Table 2

Candidate String Pos.	String Identification					SFN	d_{str}	NC
	T ₁	T ₂	T ₃	T ₄	T ₅			
10 1	5	0	0	0	0	72	0.80	2
2	9	0	0	0	0	72	0.74	2

As indicated in Table 2 there are two candidate strings selected on the basis of the overlapping utterance interval partitioning. Candidate string 90 has an ending frame of
 15 72 and an accumulated distance over the two selected digits of 0.74. Candidate string 50 also has an ending frame of 72 and has an accumulated distance over the 50 string of 0.80. No other candidate strings meet the prescribed distance threshold criteria stored in string selector 309.
 20 Since the EFN(2) signals 72 are smaller than the utterance termination point, a third word position is required to complete the candidate strings for the input utterance.

The third word position segmentation is initiated by the ISD signal produced by general controller 401 at
 25 time t_{73} responsive to the enabling of AND-gate 403 by signals MEX and \bar{EDP} . At the beginning of the third word position both SRC counter 1101 and DST counter 1103 are reset to their first states. Thus, the signals from the first candidate string in memory 540 are supplied to
 30 buffer 550 and the first candidate string position of memory 530 is addressed.

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When signal ITC is produced at time t_{74} responsive to the operation of gate 410, template counter 702 of template address circuit 325 is reset and the ending frame of the first candidate string in memory 540 is supplied to final interval detector 335 as signal SFN. As illustrated in FIG. 8, the SFN signal for the candidate string is compared to the termination frame of the utterance. Since the SFN=72 code is less than the code stored in latch 801, signal SAE is enabled by comparator 820 at time t_{75} . In the event the SAE signal is obtained from comparator 820, the MWR and IDST1 signals are generated and the first candidate string is transferred without alteration into the first candidate string position of memory 530.

At time t_{76} , controller 401 generates a SC1 signal responsive to the operation of AND-gate 413. The third word position then proceeds as generally indicated between t_{77} and t_{90} in FIG. 12. Dynamic time warping generator 307 and string selector 309 are rendered operative by signals from partitioning controller 440 to produce the distance codes, template identification signals, and ending frame signals for each of the templates obtained from store 305. As described with respect to the first and second partitioning operations, generator 307 receives the feature vector signals $R_{q(3)}$ for each digit $q(3)=0,1,\dots,9$ from template store 305. Utterance intervals and distance signals are determined for the template feature vector signals and the resulting distance signals $D_{0(3)}, D_{0(3)}, \dots, D_{0(3)}$ for the zero digit are placed in selector 309 responsive to signal SS1 from controller 440. The best matching distance signal $D'_{0(3)}$ is stored in string selector 309 and the time warping distance generation in generator 307 is reinitiated for the next digit by signal SDG. After all the best matching distance signals $D'_{0(3)}, D'_{1(3)}, \dots, D'_{9(3)}$ are placed in selector 309, signal SS2 causes the minimum of the best matching distance signals $D''_{q(3)}$ and those within a predetermined threshold of said minimum to be selected.

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For purposes of illustration, it is assumed that the minimum of the best matching distance signals is obtained for the digit 7 and that the best matching distance signal for the digit 6 is within the preassigned distance of the distance signal for digit 7.

The first candidate string 50 stored in memory 540 during the second word position results in two candidate strings 507 and 506 in the third word position. After the digits 6 and 7 are selected in string selector 309 for the first candidate string 50, signal S2D passes through gate 417 and causes controller 401 to produce an SC2 signal. The SC2 signal is applied to string store controller 480 which initiates the modification of the first candidate string in memory 540. At this time, SRC counter 1101 and DST counter 1103 are in their first states so that the first candidate positions in memories 540 and 530 are addressed. Flip-flop 520 is set whereby the first string position of memory 540 is supplied to buffer 550 and memory 530 is adapted to receive signals from cable 534. Candidate counter 1001 is in its first state and the $\bar{C}\bar{1}\bar{D}$ signal from comparator 1003 is enabled.

As previously described with respect to the preceding word positions, adder 510 is operative to sum the distance signal d_{str} and the distance signal corresponding to digit 7 from string selector 309. The resulting sum is applied to the input of memory 530. Similarly, selector 512 receives the ending frame EFN(3) for the digit 7 from string selector 309 and supplies this ending frame signal to memory 530. Adder 511 is effective to augment the NC word position signal from buffer 550 so that the identification code \hat{T} for the digit 7 is supplied to cable 534. When the MWR pulse is obtained from controller 480, gate 571 is enabled and the WA signal therefrom inserts the signals on cable 534 into the first candidate string position of memory 530.

Subsequent to the insertion into memory 530, signal IDST is obtained from OR-gate 487. The IDST signal

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increments candidate counter 1001 and DST counter 1103 to their second states. In this way, the second candidate string position of memory 530 is addressed. Since SRC counter 1101 remains in its first state, the signals
5 from the 50 candidate string in memory 540 are augmented by the outputs of string selector 309 and the resultant string signals for the 507 string are made available at the input of memory 530. The IDST signal also passes through AND-gate 481 and OR-gate 483 so that a second MWR signal is
10 produced by controller 480. The second MWR signal inserts the signals for candidate string 506 into memory 530. Candidate counter 1001 is now in its second state whereby signal C1D from comparator 1003 is enabled. The next IDST signal from gate 487 then passes through AND-gate 435
15 to generate a C2D signal. The C2D signal indicates the termination of the transfers of the candidate string 507 and 506 into memory 530.

Signal C2D is applied to general controller 401 via OR-gate 419. The ISN signal produced by controller 401
20 at this time enables AND-gate 407 and increments SRC counter 1101 to its second state. With counter 1101 in its second state, the second candidate string position of memory 540 (string 90) is addressed. DST counter 1103 has been incremented to its third state so that the third
25 candidate position of memory 530 is addressed.

Generator 401 produces an ITC signal responsive to enabled gate 407. This ITC signal resets template counter 702 in template address circuit 325 and alerts AND-gate 413. The SFN signal corresponding to the second candidate string in
30 memory 540 is supplied to comparator 820 where it is compared to the utterance termination frame stored in latch 801. Since SFN frame 72 from memory 540 is less than the utterance termination frame, an SAE signal is produced by comparator 820. Generator 401 then provides an
35 SCl signal to enable DTW controller 440.

Controller 440 is operative to activate generator 307 and string selector 309 so that third word

BUREAU
OMPI

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position digits may be selected for the string 90 in memory 540. Assume for purposes of illustration that digits 7 and 6 are also selected in string selector 309 for the 90 candidate string in memory 540. At the end of the selection of the 7 and 6 digits for the 90 string in-selector 309, signal S2D is produced. Signal S2D causes controller 401 to activate signal SC2. Signal SC2 then is applied to string store controller 480 and the memory transfer operations for the second candidate string in memory 540 are initiated. During the memory transfer, the signals for string 90 in the second word position of memory 540 are augmented by the output of selector 309. The signals for the newly generated candidate strings 907 and 906 then sequentially inserted into the third and fourth positions of memory 530 respectively. Table 3 lists the contents of the first four candidate string positions in memory 530 after the termination of the memory transfer for the third digit position.

Table 3

Candidate String Pos.	String Identification					SFN	d_{str}	NC
	T ₁	T ₂	T ₃	T ₄	T ₅			
1	5	0	7	0	0	107	1.08	3
2	5	0	6	0	0	109	1.31	3
3	9	0	7	0	0	107	1.02	3
4	9	0	6	0	0	109	1.25	3

As indicated in Table 3, there are four candidate strings each with three digits. The best matching string is 9-0-7 which has an accumulated distance signal over the three digits of $d_{str}=1.02$. The string 5-0-7 has a slightly larger accumulated distance d_{str} and the strings 9-0-6 and 5-0-6 have much larger accumulated distance signals. The ending frames EFN(3) for the candidate strings are 107 and

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109. During the transfer of the selected signals from string selector 309 to candidate string store 311, the ending frame signals $EFN_1(3)$, $EFN_2(3)$, $EFN_3(3)$, and $EFN_4(3)$ are supplied to comparator 805 wherein they are

5 successively compared to the stored utterance termination frame of $U_T=100$ in latch 801. Since each ending frame listed in Table 3 is greater than the utterance termination frame, flip-flop 815 remains set throughout the third partitioning. The EDP signal from flip-flop 815

10 (waveform 1245) signals the occurrence of the final utterance word position.

As previously described with respect to the first and second word position operations, signal MEX occurs on the termination of the last memory transfer of a

15 partitioning (time t_{g0} for the third word position). Gate 430 is enabled by signals MEX and EDP and controller 401 sends an SC3 signal to reconstruction controller 460 at time t_{g1} . Controller 460 is operative to cause the circuit of FIG. 1 to produce the reference digit

20 feature signals $R^S = R_q(1) + R_q(2) + R_q(3)$ corresponding to each candidate string in store 530. An accumulated distance signal between the utterance feature signals and the R^k for each string is produced. The distance signal d_{str} in memory 530 is added to the string distance

25 signal for reference string R^S and the result is stored in memory 540. Candidate selector 313 then selects the string with the minimum distance signal of $(D_{str}+D_s)$ as the utterance identification. Alternatively, the d_{str} signals for the candidate strings may be processed and the

30 utterance identified as the minimum d_{str} string or the D_s signals can be processed to determine the candidate string that most closely corresponds to the utterance.

In accordance with the circuit of FIGS. 3 and 4, signal SC3 from controller 401 causes controller 460 to

35 produce an ISD signal. As previously described, signal ISD resets SRC counter 1101 and DST counter 1103 to their first states. The ISD signal is also supplied to controller 460



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via OR-gate 464 so that an ICC signal is obtained from the controller. Signal ICC resets word position counter 387 to its one state, places flip-flop 380 in its reset state and generates signals ACC and WTCS in controller 460.

5 Responsive to the output of counter 387 and the B signal from flip-flop 380, template selector 389 is operative to transfer the T1 identification signal from the first string position in memory 530 to the address input of template store 305. In this way, the reference template feature
10 vector for the digit 5 is made available at the output of template store 305.

Template candidate store 370 is also addressed by the output of word position counter 387 and the reference template feature vector from template store 305 is
15 transferred to candidate store 370 under control of write signal WTCS from controller 460. Counter 387 is advanced to its second state responsive to the ACC pulse from controller 460. The second word position signal at the
20 output of buffer 550 then addresses template store 305 and candidate store 370 whereby the reference template feature vector signals for the digit 0 are transferred from store 305 to candidate store 370.

During the transfer of the third word position reference template feature vector, the DAC signal appears
25 at the output of comparator 365. Jointly responsive to the ACC and DAC signals at AND-gate 469, controller 460 supplies an SDG signal to dynamic time warping distance generator 307. Flip-flop 395 is set by signal SDG and the
30 C signal therefrom is enabled. Generator 307 is rendered operative by the C signal applied thereto from flip-flop 395 to produce a distance signal relating the utterance feature vector sequence from store 310 to the reference feature vector sequence R^k from template candidate store 370. The distance signal from
35 generator 307, DS, is supplied to distance selector 505. Signal C transfers the DS signal to the input of adder 510 wherein the sum of the first candidate string distance

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signal d_{str} and the distance signal from generator 307 is formed.

Flip-flop 592 is in its set state during this operation so that the CPY output is obtained therefrom. Consequently, the signals at the output of buffer 550, except for the d_{str} signal, are applied unmodified to the input of memory 540. Upon termination of the distance signal formation in generator 307, signal DGD from generator 307 causes controller 460 to produce an MWR write pulse. The write pulse inserts the signals on cable 544 into the first candidate string position of memory 540. The distance signal in the first candidate string position of memory 540 is then $d_{str}+DS$.

The RDST signal also produced by the DGD output of generator 307 increments SRC counter 1101 and DST counter 1102 to their second states whereby the second string positions of memories 530 and 540 are addressed. The ICC signal also reinitiates word position counter 387 and resets flip-flop 380 so that the dynamic time warping for the second candidate string position of memory 530 is performed responsive to the ACC and SDG signals from controller 460. The distance signal DS resulting from the second candidate string dynamic time warping in generator 307 is added to the d_{str} signal from memory 539 and the sum is inserted in the second string position of memory 540 responsive to the MWR signal from controller 460.

The RDST signal produced after the second candidate string is inserted into memory 540 initiates the reconstruction and dynamic time warping for the third candidate string in memory 530. After the last memory string has been processed under control of controller 460, gate 471 is enabled. Responsive to the operation of gate 471, controller 460 produces a C3D ending pulse which ending pulse is applied to controller 401. The SS3 signal generated by controller 401 at this time is applied to candidate selector 313 which is rendered operative to

select the minimum of the distance signals stored in memory 540. Table 4 lists the contents of memory 540 when the C3D signal is produced by controller 460.

Table 4

Candidate	String Identification					SFN	d _{str} +DSNC	
	String Pos.	T ₁	T ₂	T ₃	T ₄		T ₅	
1	5	0	7	0	0	107	2.30	3
2	5	0	6	0	0	109	2.9	3
3	9	0	7	0	0	107	2.1	3
10 4	9	0	6	0	0	109	2.7	3

As shown in table 4, the minimum distance signal corresponds to the candidate string 507. Candidate selector 313 is responsive to signal DONE from controller 401 to generate a code corresponding to the selected sequence and to transfer the code to utilization device 315. Candidate selector 313 may comprise the Micro Nova MP10C system, MP/1004K/8K memory, MP 100 8K programable memory and one or more 4222 interface units as described with respect to generators 303, 307 and string selector 309. The permanently stored instructions controlling the candidate selector operation are listed in FORTRAN language in Appendix C. The circuit of FIG. 1 is then reset preparatory to another utterance identification.

Although the invention has been described with reference to a particular illustrative embodiment, it is to be understood that various modifications may be made by one skilled in the art without departing from the spirit and the scope of the invention.

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APPENDIX A

```

C DTW DISTANCE GENERATOR
C NN=NUMBER OF FRAMES OF TEST (CAN BE ARBITRARILY LARGE)
C R=REFERENCE ARRAY OF AUTOCORRELATION OF LPC COEFFICIENTS
5 C NM=NUMBER OF FRAMES OF REFERENCE (FIXED TO 40)
C DRN=NORMALIZED MINIMUM DISTANCE
  DIMENSION D(NMAX) ,DCU(NMAX)
  PARAMETER NMAX=2+(IDLT+1) , TMIN=1.0 ,TMAX=0.5 ,IDLT=8 ,ME=9 ,NN=50
  DATA XLRG/1000./
10 IMIN(IMN)=MAX0(1,IMN-IDLT)
  999 CONTINUE
  IMAX(IMN)=MIN0(NN,IMN+IDLT)
    WAIT (SDG)
    INPUT (C,NC)
15 NM=40
    IF(C)MM=40+NC
  C INITIALIZE IR=1 COLUMN FIRST
    ITMN=3
    IT1=IMIN(ITMN)
20 IT2=IMAX(ITMN)
    XDIST=10000.
    DO 60 IT=IT1,IT2
      CALL DIST(1,IT,DCU(IT-IT1+1))
      IF(DCU(IT-IT1+1).LT.XDIST)XDIST=DCU(IT-IT1+1)
25 60 CONTINUE
    NFC=1
    DO 65 IT=IT2+1,NMAX
      DCU(IT-IT1+1)=XLRG
65 CONTINUE
30 XMNR=10000.
    DO 200 IR=2,MM
      XMND=XLRG
      IT1P=IT1
      IT2P=IT2
35 IT1=MAX0(IMIN(ITMN) , IT1P)
      IT2=MAX0(IMAX(ITMN) , IT2P)

```



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```
IF (IT2.GT.(IT2P+2)) IT2=IT2P+2
D1=XLRG
D2=XLRG
IF (IT1-1.GE.IT1P) D1=ABS (DCU (IT1-IT1P))
5 IF (IT1-2.GE.IT1-IT1P) D2=ABS (DCU (IT1P-1))
D0 30 IT=IT1,IT2
CALL DIST (IR,IT,D (IT-IT1+1))
30 CONTINUE
56 CONTINUE
10 D0 100 IT=IT1,IT2
D0=ABS (DCU (IT-IT1P+1))
DI=D2
LPTH=2
IF (D1-D2) 10,11,11
15 10 LPTH=1
DI=D1
11 IF (D0-DI) 12,12,20
12 CONTINUE
IF (IT.GT.IT2P) GO TO 21
20 IF (DCU (IT-IT1P+1) .LT.0) GO TO 20
LPTH=0
DI=D0
20 CONTINUE
21 CONTINUE
25 DI=DI+D (IT-IT1+1)
IF (XMND-DI) 50,50,51
51 XMND=DI
ITMN=IT
50 CONTINUE
30 IF (LPTH.EQ.0) DI=-DI
D2=D1
D1=D0
DCU (IT-IT1+1) =DI
100 CONTINUE
35 IF (XMND.GE.XLRG) GO TO 300
ITSAV=ITMN
IF (IT2.LT.NN) GO TO 110
```

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```

DIN=XMND*FLOAT(MM)/FLOAT(IR)
IF(XMNR.LT.DIN)GO TO 110
XMNR=DIN
IRR=IR
5   ITT=ITMN
110  XTST=AMIN1(XMND,XMNR)
      THR1=(TMIN+((TMAX-TMIN)/(FLOAT(MM)-1))*FLOAT(IR)*MM
      IF(XTST.GT.THR1)GO TO 300
      DO 93 IT=IT2-IT1+2,NMAX
10  93  DCU(IT)=XLRG
      200 CONTINUE
      NFC=ITSAV
      IF(XMND.GT.XMNR)NFC=ITT
      DR=AMIN1(XMND,XMNR)
15  NFR=MM
      IF(XMND.GT.XMNR)NFR=IRR
      DRN=DR/FLOAT(NFR)
      OUTPUT(DS=DRN,DGD)
      GO TO 999
20  300 OUTPUT LDS=0000,D60
      GO TO 999
      END
C    DIST--DISTANCE ROUTINE USING AUTOCORRELATION OF A'S
      SUBROUTINE DIST(IR,IT,DST)
25  COMMON CR(9,40),CT(9)
      PARAMETER M=8,ME=M+1
C
C    CT=TEST ARRAY
C    M=8, ME=9
30  C    CR=NORMALIZED CORRELATION FUNCTION OF A'S OF REFERENCE
      C          FRAME CR(I)=2*COR(I)/COR(0)WHERE COR IS TRUE
      C          .CORRELATION OF A'S
C    RC=CR(1,IR)
C    TC=LOG RESIDUAL ERROR OF TEST FRAME
35  C    TC=CT(1,IT)
C
C    DST=RC+LOG(ACOR DOT R)-TC

```

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```
C      FORM R DOT T AND STORE IN PR1
C      OUTPUT(FN=IT);READ IN FROM UTTERANCE FEATURE SIGNAL STO
      INPUT(CT,CR)
      RC=CR(1,IR)
5      PR1=0.
      DO 5 I=2,M+1
5      PR1=PR1+CR(I,IR)*CT(I)
      TC=CT(1)
      PR1=PR1+1.
10     IF (PR1.LT.1.E-5) PR1=1.E-5
      DST=ALOG (PR1)+RC-TC
      RETURN
      END
```

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APPENDIX B

```

C      STRING SELECTOR
      PARAMETER K1=4,K2=10
      DIMENSION EFN(K1),DS(K1),TN(K1)
5     DIMENSION EFNH(K2),DH(K2),TH(K2)
      COMMON NT1,NT2
      PARAMETER T1=.6,T2=.08
C      WAIT FOR SOMETHING TO DO
100    CONTINUE
10     WAIT(IN.T.OR. D60 .OR. SS1 .OR. SS2)
      IF(D60) GO TO 200
      IF(SS1) GO TO 300
      IF(SS2) GO TO 400
C      INITIALIZATION
15     NT1=0
      NT2=0
      GO TO 100
C      TIME WARP DONE (DGD) COLLECT DATA
200    CONTINUE
20     NT1=NT1-1
      INPUT (EFNT,DST,TNT)
      EFN(NT1)=EFNT
      DS(NT1)=DST
      TN(NT2)=TNT
25     GO TO 100
C      ALL TIME WARPS DONE FOR THIS TEMPLATE
C      PICK BEST MATCH (SS1).
300    CONTINUE
      C1=DS(1)
30     N1=1
      DO 310 IP=2,K1
      IF (DS(TP).GE.C1) GO TO 310
      C1=DS(IP)
      N1=IP
35 310 CONTINUE
C      N1 NOW POINTS TO SMALLER DISTANCE

```


 A circular stamp with the text "BUREAU" on the top arc and "OMPI" on the bottom arc.

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```
C      TRANSFER TO SECOND ARRAY
      NT2=NT2+1
      EFNH(NT2)=EFN(N1)
      DH(NT2)=DS(N1)
5     TH(NT2)=TN(N1)
C      RESET POINTER FOR FIRST ARRAY
      NT1=0
      OUTPUT(S10)
      GO TO 100
10    C      ALL TIME WARPS DONE FOR ALL TEMPLATES
      C      PICK BEST CANDIDATES AND REPORT.(SS2)
400   CONTINUE
      C      FIRST SORT LIST OF DISTANCES
      DO 410 K=1,K2-1
15    DO 410 KP=K+1,K2
      IF (DK(KP).GT.DH(K)) GO TO 410
      EFNT=EFNH(K)
      DST=DH(K)
      TNT=TH(K)
20    EFNH(K)=EFNH(KP)
      DH(K)=DH(KP)
      TH(K)=TH(KP)
      EFNH(KP)=EFNT
      DH(KP)=DST
25    TH(KP)=TNT
410   CONTINUE
      C      CHECK DISTANCES AGAINST THRESHOLDS
      M=0
      IF (DH(1).GT.T1) GO TO 450
30    M=1
      DO 420 K=2,K2
      IF (DH(K)-DH(1).GT.T2) GO TO 450
      M=K
420   CONTINUE
35    C      DONE OUTPUT # OF CANDIDATES
      OUTPUT (M,S2D)
      C      RESET POINTER FOR SECOND ARRAY
```

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NT2=0
GO TO 100
END



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APPENDIX C

```
C   BEST CANDIDATE STRING SELECTOR
    PARAMETER NSTRMAX=10 ;NO.CHAR IN STRING(MAX)
    DIMENSION T(NSTRMAX),TBEST(NSTRMAX)
5   100  CONTINUE
      WAIT (SS3)
      INPUT (NCBEST,DSTRBEST,TBEST) ; GET FIRST CAND.
      INPUT (NSTR) ; GET OF CAND. STRINGS
      IF(NSTR.EQ.1) GO TO 300 ; ONLY ONE-DONE
10  DO 200 J=1, NSTR ; LOOP FOR ALL CAND.
      INPUT (NC,DSTR,T) ; GET NEXT CAND.
      IF(DSTR.GE.DSTRBEST) GO TO 200 ; SMALLER?
      DSTRBEST = DSTR
      NCBEST = NC
15  DO 150 I = 1,NC
      TBEST(I) = T(I)
    150  CONTINUE
    200  CONTINUE
    C   DONE --BEST HAS CANDIDATE STRING WITH
20  C   SMALLEST DISTANCE
    300  CONTINUE
      OUTPUT (NCBEST, DSTRBEST, TBEST)
      OUTPUT (S3D)
      GO TO 100
25  END
```



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Claims

1. Apparatus for recognizing continuous speech comprising:

means (105) for storing signals representative of
5 the acoustic features of a set of reference words;

means (103) responsive to an unknown utterance for producing a sequence of signals representative of the utterance;

means (110, 120, 130, 160) responsive to the
10 reference word and utterance feature signals for generating at least one reference word series as a candidate for said utterance;

and means (170) responsive to said candidate series and said utterance for identifying said utterance as
15 one of said candidate series;

CHARACTERIZED IN THAT

said candidate series generating means (110, 120, 130, 160) comprises;

means (401, 340) for generating a signal for
20 identifying successive word position intervals;

means (307, 309, 311) operative in each successive word position interval for forming partial candidate reference word series including means (307, 309, 311, 320, 345) responsive to each partial candidate.

25 reference word series of the preceding word position for determining for each reference word an utterance segment beginning within a predetermined range of the utterance segment endpoint of said preceding word position candidate series and corresponding to the reference word feature
30 signals;

means (307, 320) responsive to each reference word feature signals and the corresponding determined utterance segment feature signals for forming a signal representative of the similarity between said reference
35 word and said utterance segment;

means (309, 325) responsive to said similarity signals for selecting reference words having a prescribed



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similarity to their corresponding utterance segments, and means (510, 512, 518, 581, 585, 589) for combining said selected reference words with the partial candidate reference word series of the preceding word position to form partial candidate reference word series for said word position interval.

2. Apparatus for recognizing continuous speech according to claim 1,

CHARACTERIZED IN THAT

10 said utterance identifying means comprises means responsive to the endpoints of all partial candidate reference word series of a word position interval being within a predetermined range of the utterance endpoint for generating a selection signal;

15 and means responsive to the selection signal and said word position similarity signals for generating a signal for each candidate series representative of the correspondence of the candidate series to the utterance; and

20 means responsive to the candidate series correspondence signals for selecting the candidate series most closely corresponding to the utterance.

3. Apparatus for recognizing continuous speech according to claim 1,

25 CHARACTERIZED IN THAT

said utterance identifying means comprises means responsive to the endpoints of all partial candidate reference word series of a word position interval being within a predetermined range of the utterance endpoint for generating a selection signal;

means responsive to said selection signal for generating a sequence of feature signals corresponding to each candidate reference word series;

35 means jointly responsive to each candidate reference word series feature signal sequence and the utterance feature signal sequence for producing a signal representative of the correspondence of the utterance to

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said candidate reference word series;

and means responsive to said correspondence signals for identifying the candidate reference word series having the closest correspondence to said utterance.

5 4. Apparatus for recognizing continuous speech according to claim 3,

CHARACTERIZED IN THAT

the word position reference word combining means further comprises means for generating a signal
10 representative of the similarity between each partial candidate reference word series of the word position and the utterance portion corresponding thereto;

and said candidate reference word series identifying means comprises means for combining the
15 similarity and correspondence signals for each candidate reference word series;

and means responsive to said combined similarity and correspondence signals for determining the candidate reference word series most closely corresponding to the
20 utterance.

5. Apparatus for recognizing continuous speech according to claim 4,

CHARACTERIZED IN THAT

said utterance segment determining means
25 comprises means operative for each partial candidate reference word series of the preceding word position responsive to the series utterance portion endpoint of the preceding word-position interval for generating a set of utterance segment beginning points for each reference word
30 of the current word position interval;

said word position interval similarity signal forming means comprises

means jointly responsive to said set of utterance segment beginning points, the utterance feature signal
35 sequence and the feature signals of said reference word for selecting the utterance segment for said reference word and for generating a signal representative of the distance

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between the feature signals of the reference word and the feature signals of the selected utterance segment for said reference word.

6. Apparatus for recognizing continuous speech
5 according to claim 5,
CHARACTERIZED IN THAT
said word position reference word selecting means
comprises
means responsive to said reference word distance
10 signals of the word position for identifying the reference
word with the minimum distance signal below a first
preassigned threshold and the reference words with distance
signals within a preassigned distance of said minimum
distance signal reference word.

15 7. Apparatus for recognizing continuous speech
according to any of the preceding claims,
CHARACTERIZED IN THAT
said signals representative of the acoustic
features of a set of reference words are representative of
20 the acoustic features of reference words spoken in
isolation.

8. A method for recognizing continuous speech
comprising
storing signals representative of the acoustic
25 features of a set of reference words;
producing a sequence of signals representative of
an unknown utterance;
generating at least one reference word series as
a candidate for said utterance responsive to the reference
30 word and utterance feature signals;
and identifying the utterance as one of said
candidate series;
CHARACTERIZED IN THAT
said candidate series generating step comprises;
35 generating a signal identifying successive word
position intervals for said utterance;

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forming partial candidate reference word series
in each word position interval including

5 determining for each partial candidate series of
the preceding word position interval an utterance segment
for each reference word beginning within a predetermined
range of the utterance segment endpoint of the partial
candidate series of the preceding word position and
corresponding to the reference word feature signals;

10 forming a signal representative of the similarity
between each reference word and the corresponding
determined utterance segment responsive to the reference
word feature signals and said determined utterance segment
feature signals;

15 selecting reference words having a prescribed
similarity to their corresponding utterance segments
responsive to said similarity signals;

and combining the selected reference words with
the partial candidate reference word series of the
preceding word position to form a partial candidate series
20 for said word position interval.

9. A method for recognizing continuous speech
according to claim 8,

CHARACTERIZED IN THAT

25 said utterance identifying step comprises
generating a selection signal responsive to the endpoints
of all partial candidate series of a word position interval
being within a predetermined range of the utterance
endpoint;

30 generating a signal for each candidate series
representative of the correspondence of the candidate
series to the utterance responsive to the selection signal;
and selecting the candidate series most closely
corresponding to the utterance responsive to said candidate
series correspondence signals.

35 10. A method for recognizing continuous speech
according to claim 8,

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CHARACTERIZED IN THAT

said utterance identifying step comprises
generating a selection signal responsive to the
endpoints of all partial candidate series of a word
5 position interval being within a predetermined range of the
utterance endpoint;

generating a sequence of feature signals
corresponding to each candidate series responsive to said
selection signal;

10 producing a signal representative of the
correspondence of the utterance to each candidate reference
word series responsive to the candidate reference word
series feature signal sequence and said utterance feature
signal sequence;

15 and identifying the candidate reference word
series having the closest correspondence to the utterance
responsive to said correspondence signals.

11. A method for recognizing continuous speech
according to claim 10,

20 CHARACTERIZED IN THAT

said word position reference word combining step
comprises

generating a signal representative of the
similarity between each partial candidate reference word
25 series and the utterance portion corresponding thereto
responsive to said word position interval similarity
signals;

and said candidate reference word series
identifying step comprises combining the similarity and
30 correspondence signals for each candidate reference word
series;

and determining the candidate word reference
series most closely corresponding to the utterance
responsive to the combined similarity and correspondence
35 signals for the candidate reference word series.

12. A method for recognizing continuous speech
according to claim 11,

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CHARACTERIZED IN THAT

said utterance segment determining step comprises
for each partial candidate reference word series
of the preceding word position interval generating a set of
5 utterance segment beginning points for each reference word
of the current word position interval;

said word position interval similarity forming
step comprises selecting the utterance segment for said
reference word jointly responsive to the set of utterance
10 beginning points, the utterance feature signals and said
reference word feature signals; and generating a signal
representative of the distance between the feature signals
of the reference word and the feature signals of the
selected utterance segment for said reference word.

15 13. A method for recognizing continuous speech
according to claim 12,

CHARACTERIZED IN THAT

the word position reference word selecting step
comprises identifying the reference word with the minimum
20 distance signal below a first preassigned threshold and the
reference words with distance signals within a preassigned
distance of the minimum distance signal reference word
responsive to the reference word distance signals for each
partial candidate series.

25 14. A method for recognizing continuous speech
according to any of claims 8 through 13,

CHARACTERIZED IN THAT

said signals representative of the acoustic
features of a set of reference words are representative of
30 the acoustic features of a set of reference words spoken in
isolation.

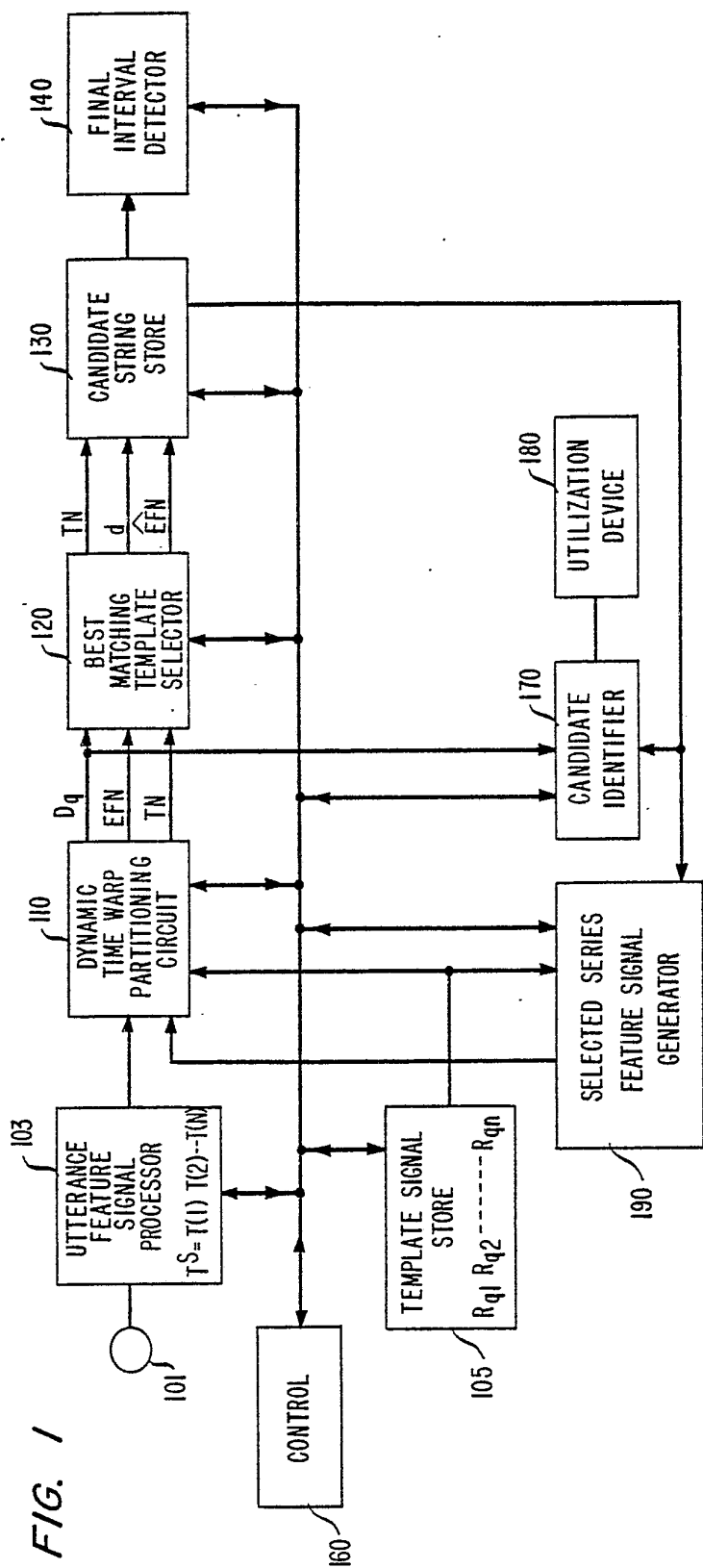


FIG. 1

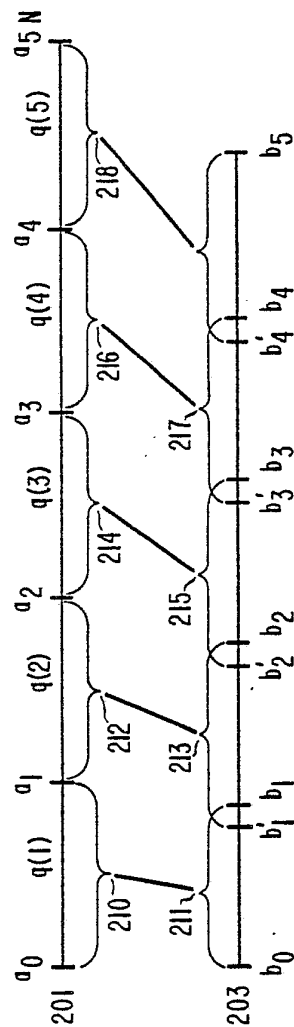
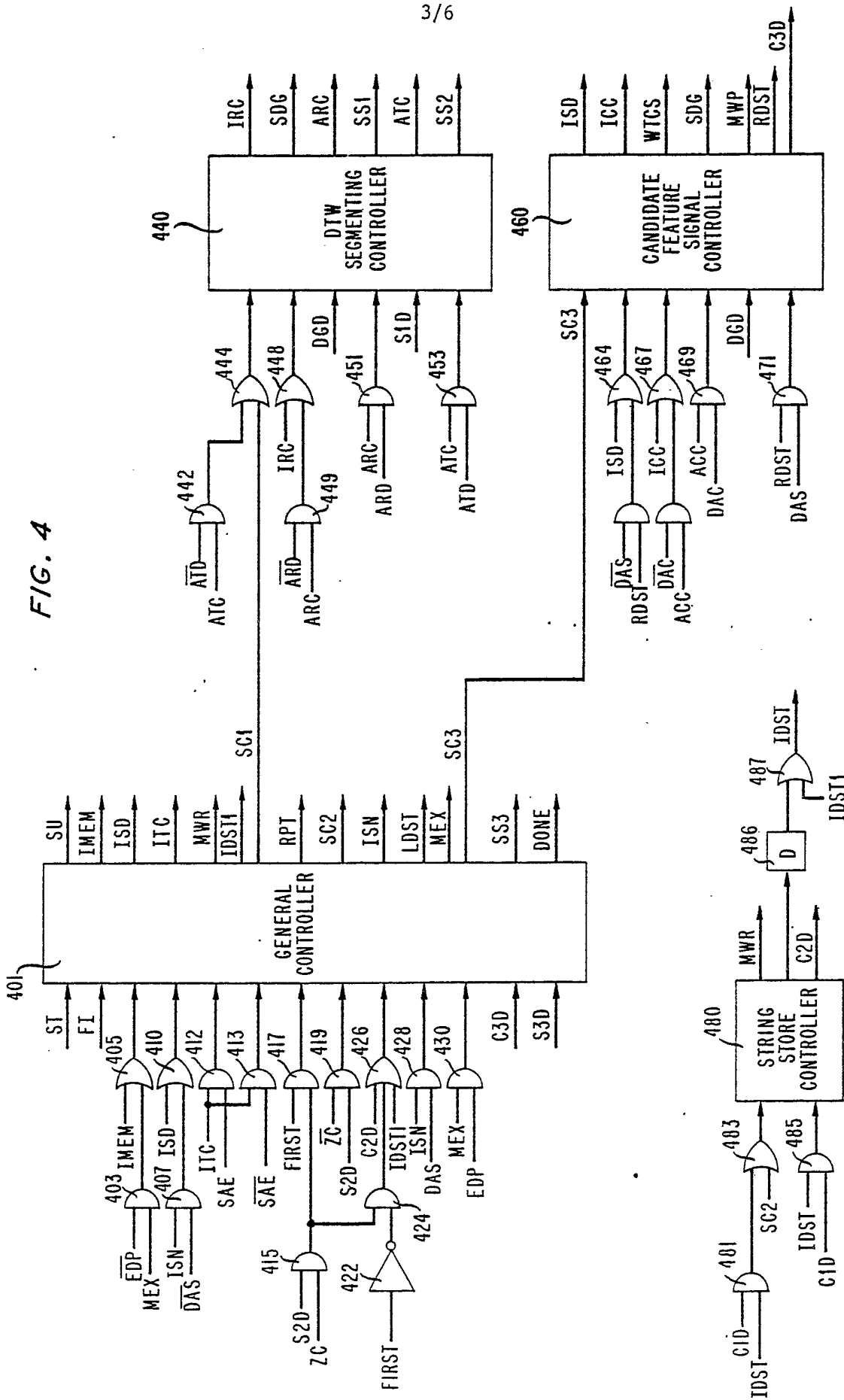
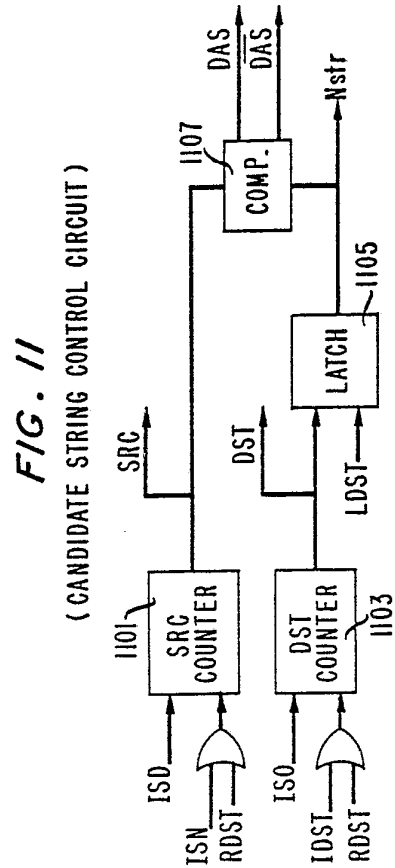
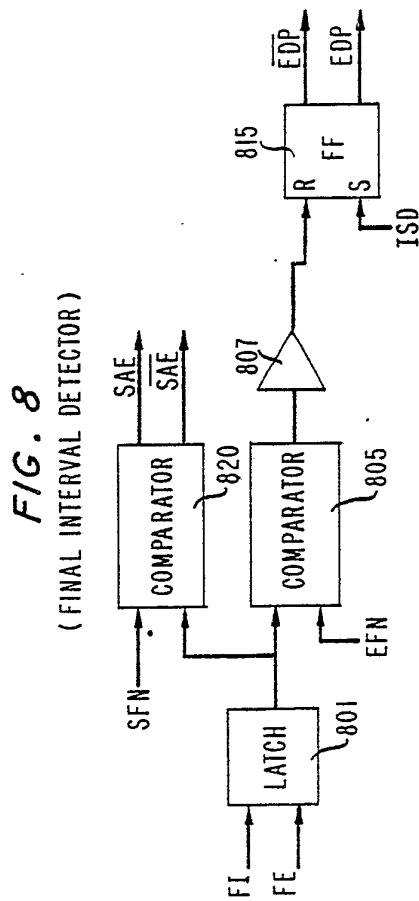
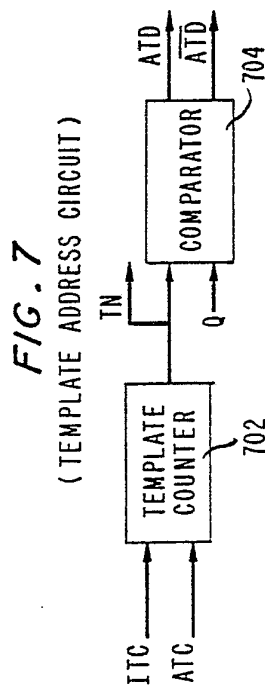
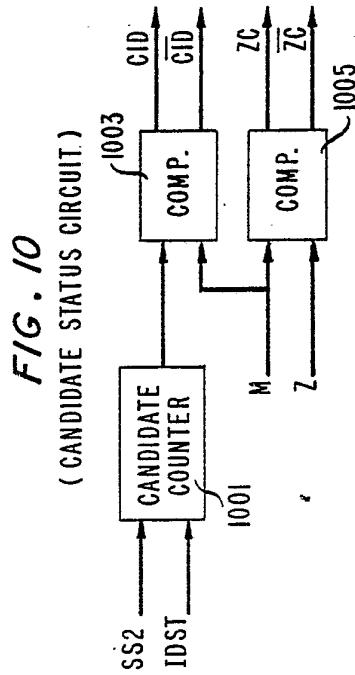
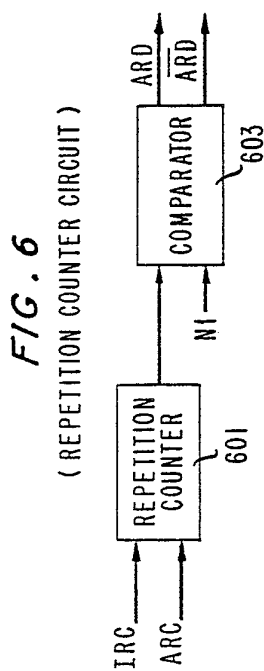
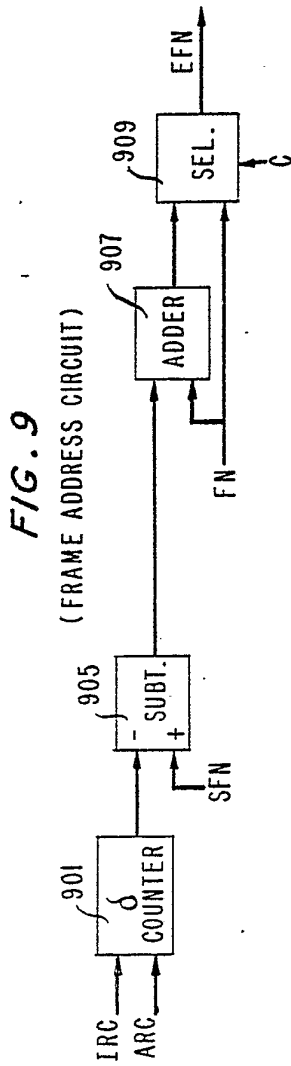
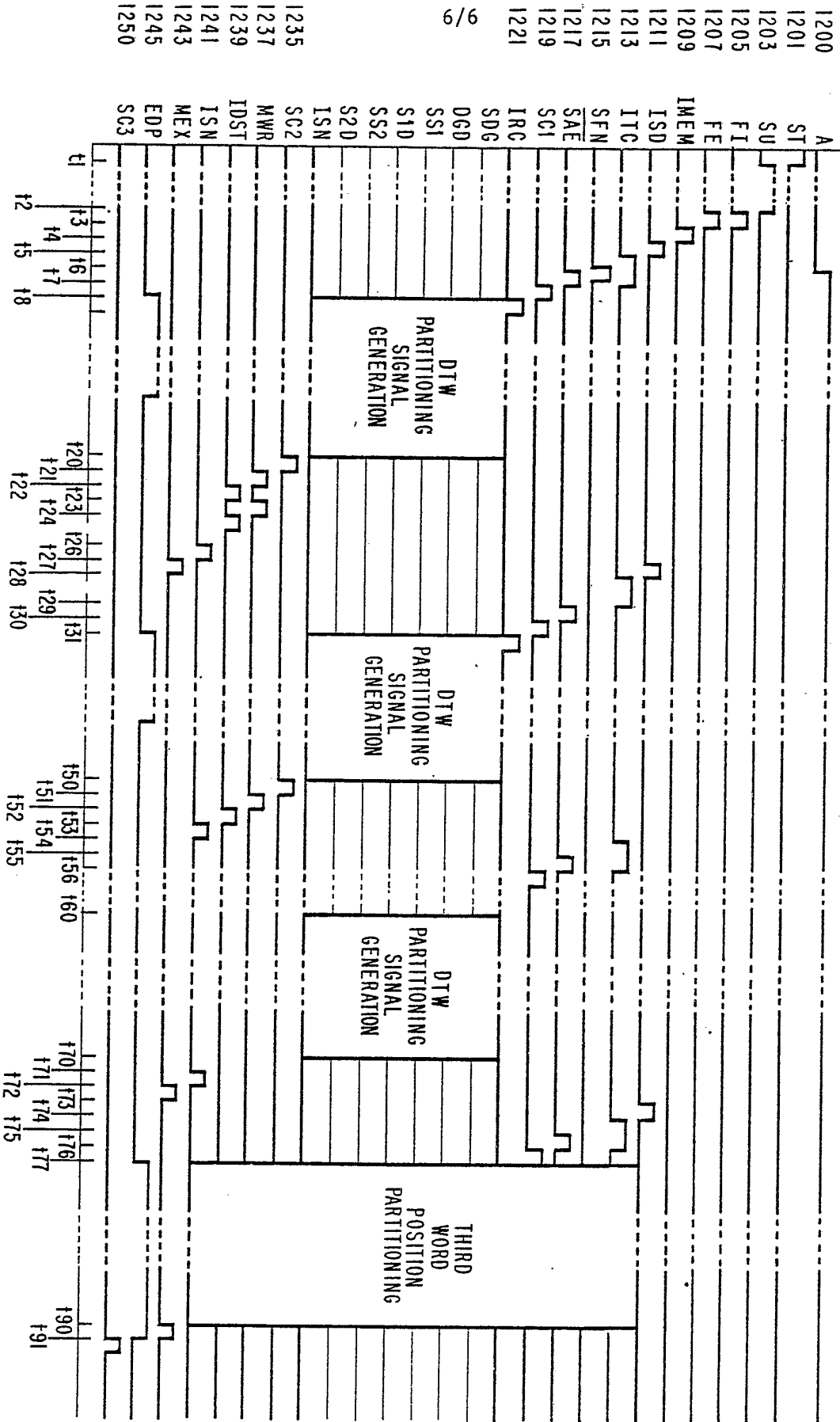


FIG. 2

FIG. 4







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FIG. 12

INTERNATIONAL SEARCH REPORT

International Application No PCT/US 81/00425

I. CLASSIFICATION OF SUBJECT MATTER (if several classification symbols apply, indicate all) ³				
According to International Patent Classification (IPC) or to both National Classification and IPC				
US. CL: 179-1.SD, 1.SB				
INT. CL ³ : G10L 1/00				
II. FIELDS SEARCHED				
Minimum Documentation Searched ⁴				
Classification System	Classification Symbols			
US	179-1.SD, 1.SB; 364-728; 340-146.3WD, 146.3A2, 146,3SG			
Documentation Searched other than Minimum Documentation to the Extent that such Documents are Included in the Fields Searched ⁵				
III. DOCUMENTS CONSIDERED TO BE RELEVANT ¹⁴				
Category ⁶	Citation of Document, ¹⁵ with indication, where appropriate, of the relevant passages ¹⁷	Relevant to Claim No. ¹⁸		
A	US, A, 4,107,460 Published 15 Aug. 1978 GRUNZA et al	1-14		
A	US, A, 3,816,722 Published 11 June 1974 SAKOE et al	1-14		
A	US, A, 3,700,815 Published 24 Oct. 1972 DODDINGTON et al	1-14		
A	US, A, 4,156,868 Published 29 May 1979 LEVINSON	1-14		
A	US, A, 3,770,892 Published 6 Nov. 1973 CLAPPER	1-14		
<p>⁶ Special categories of cited documents: ¹⁵</p> <table style="width: 100%; border: none;"> <tr> <td style="width: 50%; border: none;"> <p>"A" document defining the general state of the art</p> <p>"E" earlier document but published on or after the international filing date</p> <p>"L" document cited for special reason other than those referred to in the other categories</p> <p>"O" document referring to an oral disclosure, use, exhibition or other means</p> </td> <td style="width: 50%; border: none;"> <p>"P" document published prior to the international filing date but on or after the priority date claimed</p> <p>"T" later document published on or after the international filing date or priority date and not in conflict with the application, but cited to understand the principle or theory underlying the invention</p> <p>"X" document of particular relevance</p> </td> </tr> </table>			<p>"A" document defining the general state of the art</p> <p>"E" earlier document but published on or after the international filing date</p> <p>"L" document cited for special reason other than those referred to in the other categories</p> <p>"O" document referring to an oral disclosure, use, exhibition or other means</p>	<p>"P" document published prior to the international filing date but on or after the priority date claimed</p> <p>"T" later document published on or after the international filing date or priority date and not in conflict with the application, but cited to understand the principle or theory underlying the invention</p> <p>"X" document of particular relevance</p>
<p>"A" document defining the general state of the art</p> <p>"E" earlier document but published on or after the international filing date</p> <p>"L" document cited for special reason other than those referred to in the other categories</p> <p>"O" document referring to an oral disclosure, use, exhibition or other means</p>	<p>"P" document published prior to the international filing date but on or after the priority date claimed</p> <p>"T" later document published on or after the international filing date or priority date and not in conflict with the application, but cited to understand the principle or theory underlying the invention</p> <p>"X" document of particular relevance</p>			
IV. CERTIFICATION				
Date of the Actual Completion of the International Search ¹	Date of Mailing of this International Search Report ²			
10 June 1981	22 JUN 1981			
International Searching Authority ¹	Signature of Authorized Officer ²⁰			
ISA/US	E. S. Kemery			