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(12) **United States Design Patent**
Hsiau

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(45) **Date of Patent:** **** Aug. 1, 2006**

(54) **STEPPED SIGNAL TRANSCEIVER**

D367,481 S * 2/1996 Bacnik D14/230
D491,926 S * 6/2004 Tai et al. D14/230
D501,201 S * 1/2005 Chiang et al. D14/230

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(73) Assignee: **D-Link Corporation** (TW)

* cited by examiner

(**) Term: **14 Years**

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(21) Appl. No.: **29/218,993**

(22) Filed: **Dec. 13, 2004**

(57) **CLAIM**

The ornamental design for a stepped signal transceiver, as shown.

(51) **LOC (8) Cl.** **14-03**

(52) **U.S. Cl.** **D14/230**

(58) **Field of Classification Search** D14/137,
D14/138, 230–238, 299, 358; D12/42, 43;
343/700 R–705, 871–908, 795, 840, 711–713,
343/819, 846; 455/90.2, 90.3, 91, 128, 269,
455/344, 347, 562.1

See application file for complete search history.

DESCRIPTION

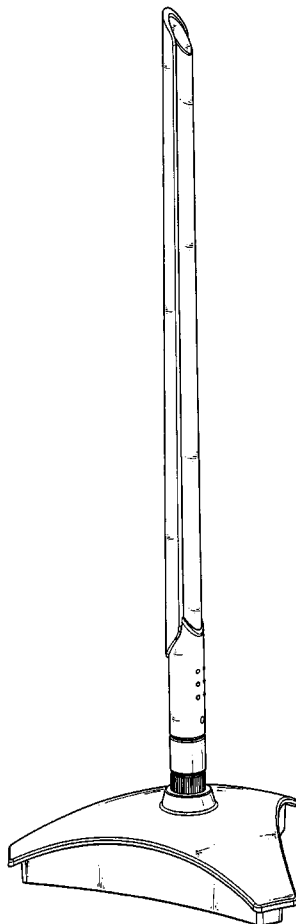
FIG. 1 is a perspective view of a stepped signal transceiver showing my new design;
FIG. 2 is a front elevational view thereof;
FIG. 3 is a rear elevational view thereof;
FIG. 4 is a left side elevational view thereof;
FIG. 5 is a right side elevational view thereof;
FIG. 6 is a top plan view thereof; and,
FIG. 7 is a bottom plan view thereof.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D157,090 S * 1/1950 Schmidt D14/234
D320,393 S * 10/1991 Cooper D14/230

1 Claim, 6 Drawing Sheets



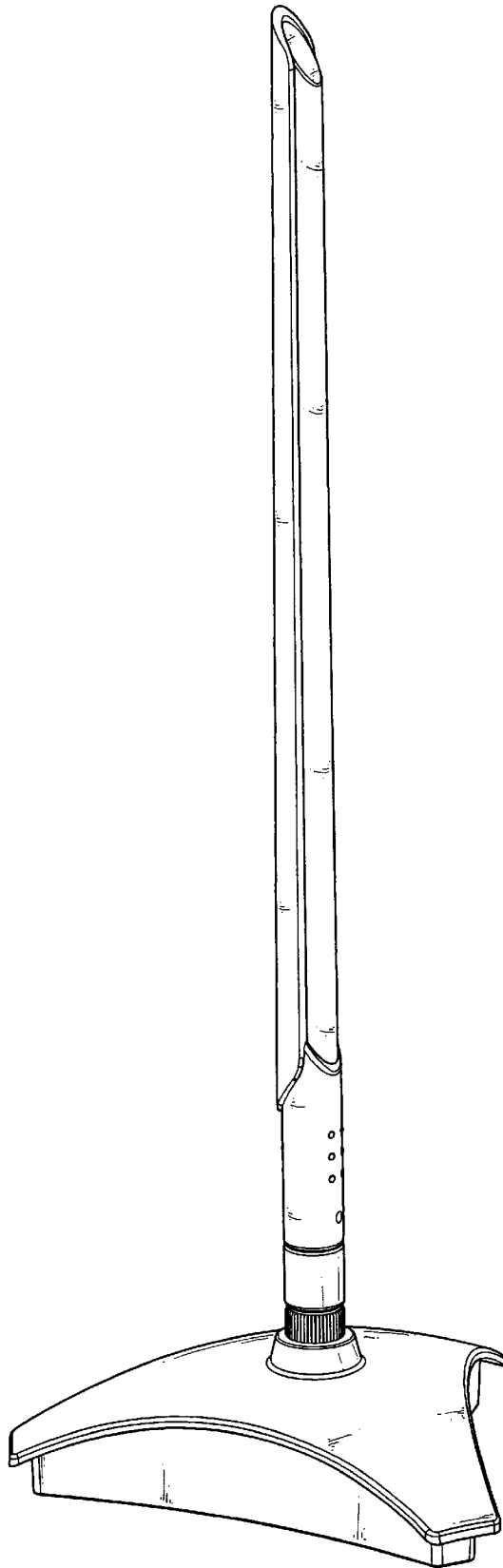


FIG. 1

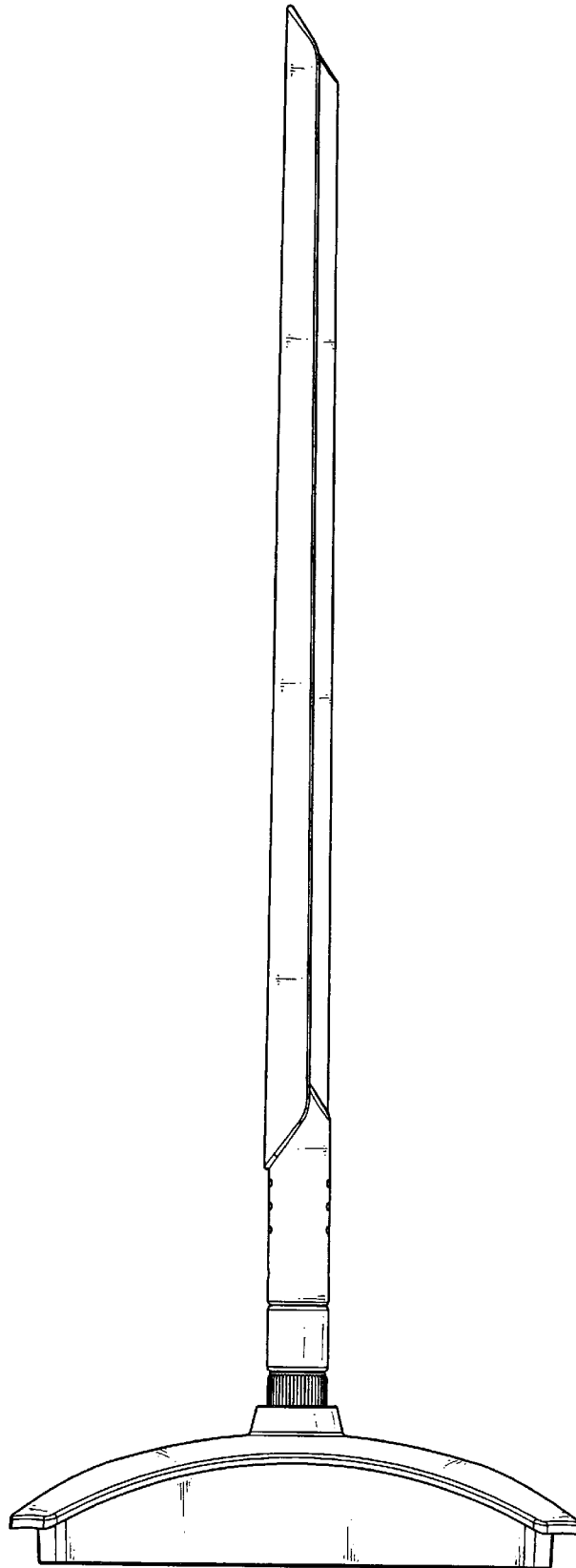


FIG. 2

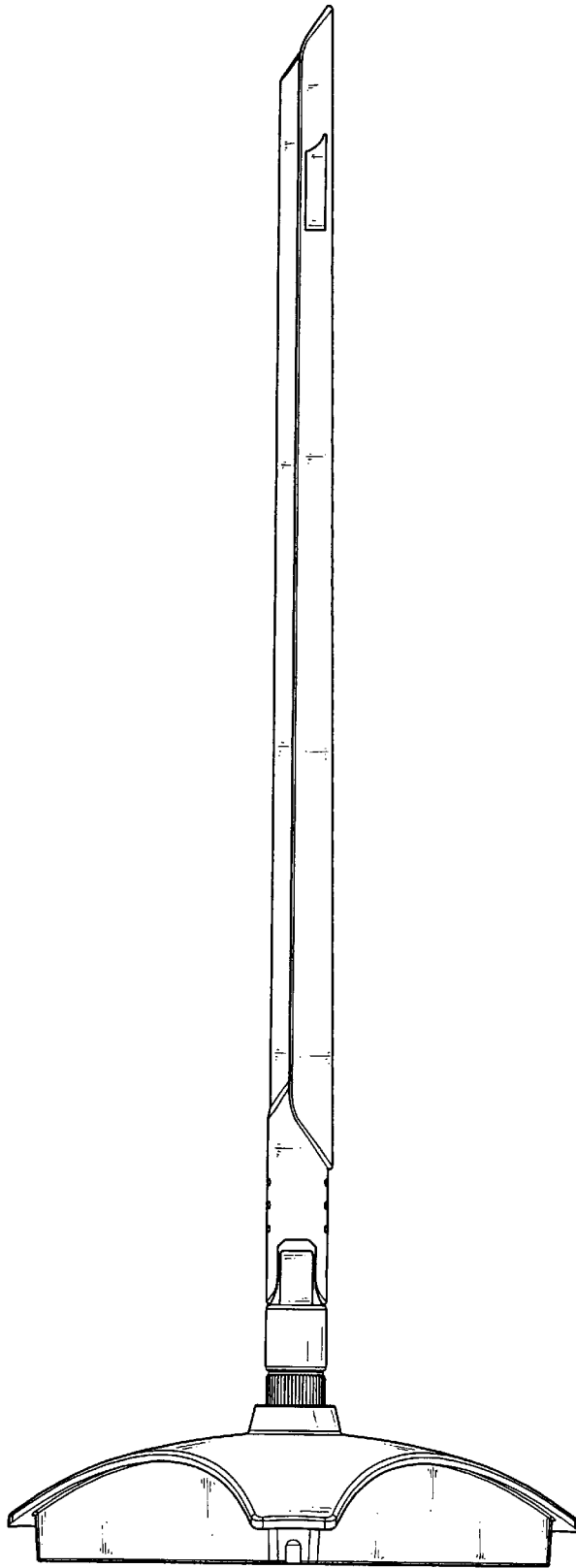


FIG. 3

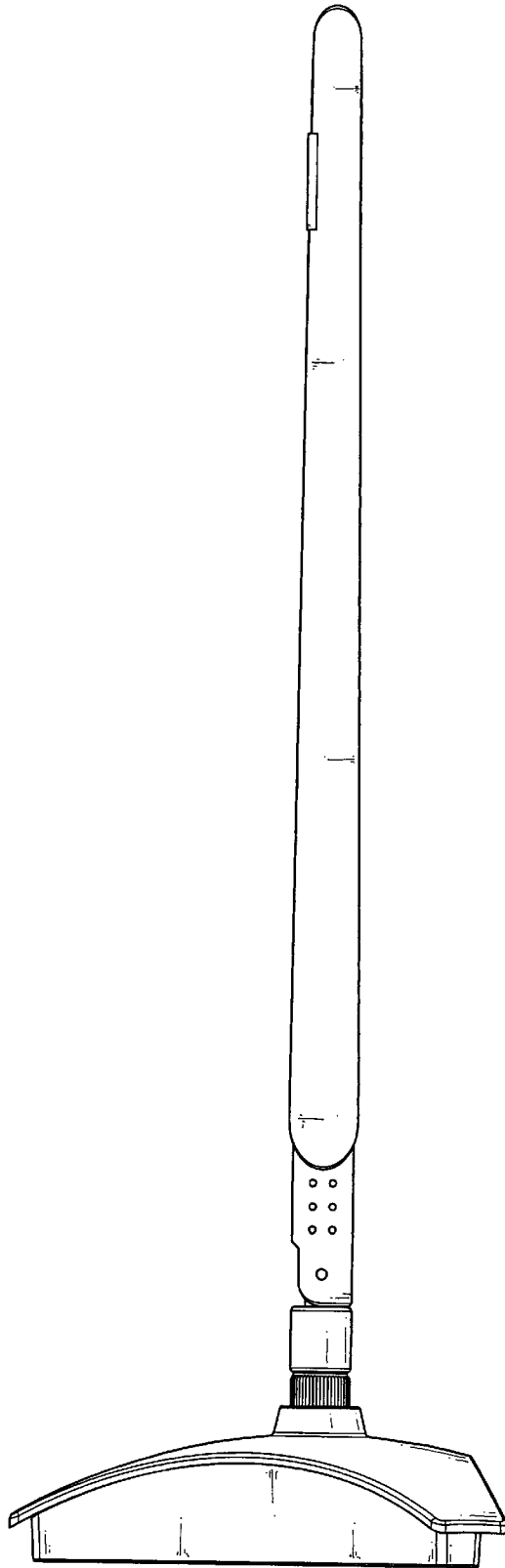


FIG. 4

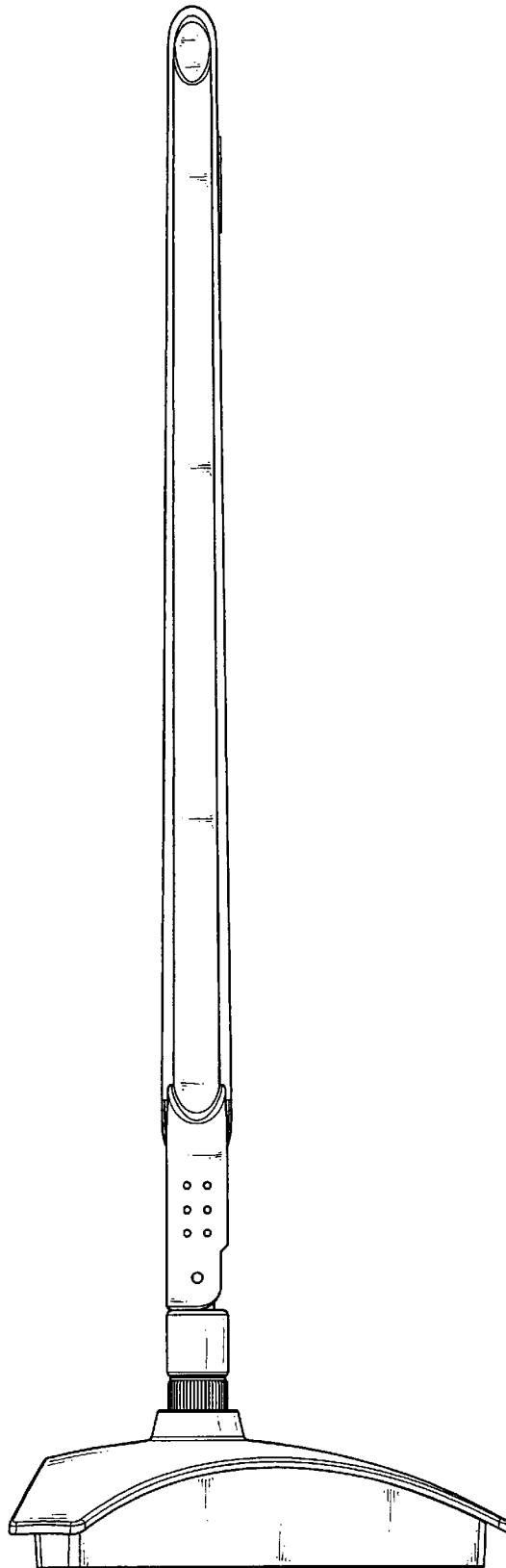


FIG. 5

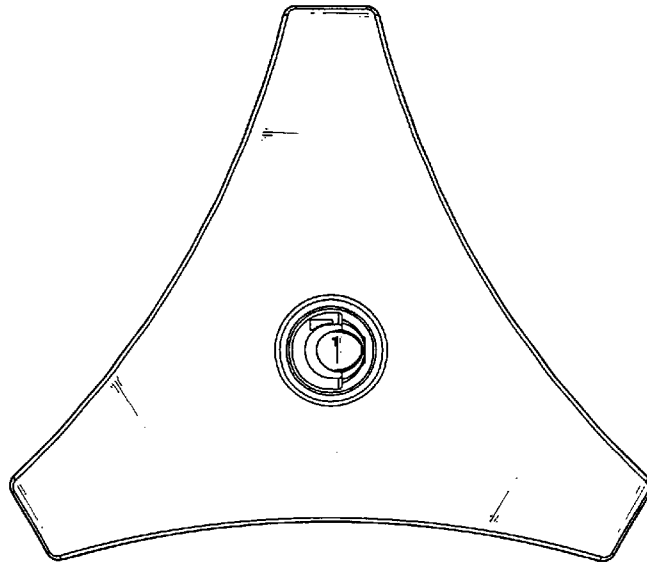


FIG. 6

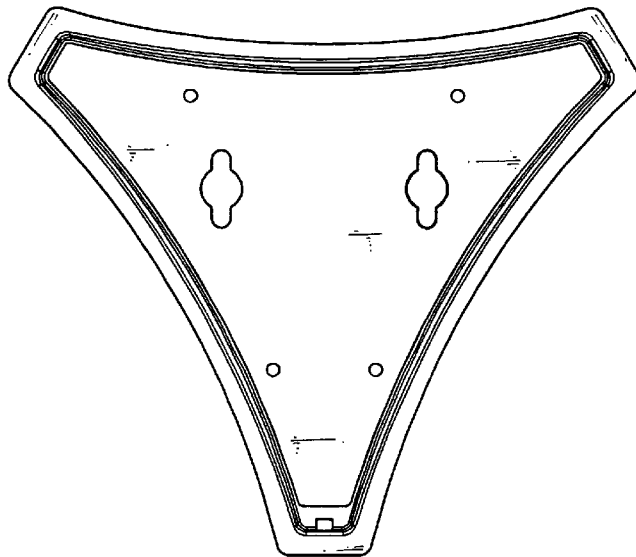


FIG. 7