Polymer (preferably polyimide) sheets (10) with laser ablated through-holes plated with metal (20) are used for making electrical connections to specified microcircuits, especially unbumped microships and Tape Automated Bonding (TAB) articles. Bonding with different melting-point materials at opposite ends of the plated holes is disclosed. Preferred polyimides are those derived from polymerisation of 4,4'-biphenyldianhydride and (4,4'-diaminobiphenyl, or 4,4'-diaminobiphenylether, or phenylenediamine, preferably p-phenylenediamine).
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USES OF UNIAXially ELECTRICALLY CONDUCTIVE ARTICLES

This invention relates to new practical applications of uniaxially electrically conductive articles, and to special forms of such article for use therein.

It is known from U.S. Patent No. 3303085 to produce porous mica sheets in which relatively straight-line through-holes of 5 to 20,000 Angstroms diameter, produced by heavy particle radiation followed by etching of the mica, are simply filled with superconductive material or iron particles to provide oriented single domain ferromagnetic sheet, or with material suitable for imaging in television cameras. The surfaces of the mica sheet may be polished or abraded to assure electrical contact with the conductive filler.

Porous polymer sheets in which selected areas of the interconnecting through-holes are masked and the unmasked through-holes are metal plated to provide electrically conductive spots surrounded by insulating areas, are known from Japanese Published Patent Application 80161306.

Published EP-A-0213774 describes a superior form of uniaxially electrically conductive article comprising porous electrically insulating sheet material at least a selected portion of which has at least 25 substantially non-interconnected through-holes per square millimetre of its surface, at least a significant proportion of which through-holes individually contain electrically conductive material which provides an electrically conductive path between, and projects beyond at least one of, the main surfaces of the sheet material, each such conductive path being electrically separate from substantially all of the other such conductive paths.
The preferred forms of uniaxially electrically conductive article for all aspects of the present invention are those supplied by Raychem Limited, for example as described in the aforementioned EP-A-0213774 or as described in copending British Patent Applications 8802567, 8802566, 8802568. For convenience, the description of the present invention hereinafter will refer to uniaxially conductive sheets, it being understood, however, that the invention is not limited to the use of those specific preferred forms of uniaxially conductive article.

Formation of the conductive article by laser drilling of through-holes in polymer sheets, especially by U.V. laser ablation (ablative photodecomposition), has advantages in producing a relatively low degree of taper in the holes compared with other perforating techniques such as etching with liquids or reactive gases. The lower taper permits closer pitch between through-holes. This is clearly advantageous, given that microcircuits are becoming progressively smaller and more densely patterned. Through-holes with taper (measured between the substantially straight inner surface of the hole and the axis of the hole) less than 10°, preferably less than 8°, more preferably less than 6°, and especially less than 4° can advantageously be achieved by laser drilling of suitable polymer sheets, and may usefully be incorporated in all aspects of the invention described herein.

**BUMP-FREE BONDING**

One application according to this invention provides a method of making substantially permanent electrical connections between connection sites on a first substrate and corresponding connection sites on a second substrate, comprising:

(a) aligning the connection sites of first substrate face to face with the corresponding connection sites on the second substrate,
(b) placing between the facing substrates a uniaxially electrically conductive article comprising electrically insulating sheet material with at least one through-hole aligned between each pair of facing connection sites, the through-holes being internally plated with electrically conductive material which provides an electrically conductive path between, and projects beyond, the main surfaces of the sheet material and including bonding material capable of bonding to the said connection site, and

(c) bonding the end of the electrically conductive material in the through-holes to the respective corresponding pairs of facing connection sites, one or both of the connection sites in each pair prior to the bonding being substantially free of fusible bonding material.

Preferably, the electrically conductive material comprises a tube of the plated metal in each through-hole. The bonding material, at least at one end of each through-hole, may be solder (e.g. held in a tube of plated metal), or may be gold, preferably plated to a thickness of at least 1.5, more preferably at least 2 micrometres. Gold, especially at thicknesses of 2 micrometres or more (e.g. 2.5, 3, or 3.5 micrometres), can advantageously be bonded to chip connection sites by thermocompression bonding without the inconvenience of first having to apply solder "bumps" to the connection sites.

The uniaxial sheet may be constructed with only one through-hole containing the electrically conductive material between pairs of facing connection sites, e.g. on a chip and a circuit board, in which case the size of the through-holes will preferably be chosen to suit the size of the connection sites. Alternatively, there may be two or more small through-holes independently containing the electrically conductive material between each pair of connection sites. In
either case, the sheet may have substantially no other conductive through-holes, thus providing a single "one-to-one" pattern of conductive paths (one path or one group of paths for each pair of connection sites) as described in the aforementioned copending application; or the sheet may have two or more such "one-to-one" patterns interspersed with each other for selective use as also described in copending international application........ (RK360COM); or the sheet may have a multitude of conductive through-holes as described in the aforementioned EP-A-0213774 for random alignment between the facing pairs of connection sites; always provided that any conductive through-holes not acting between the facing pairs of connection sites do not interfere unacceptably with the operation of the resulting assembly of a first substrate (preferably a microcircuit chip) substantially permanently electrically connected to a second substrate by this method.

Uniaxially conductive sheets can thus be used to bond chips (and other active devices, for example liquid crystal displays) permanently to circuit boards, chip carriers, or even to other chips.

Known technology of this kind includes IBM's "flip chip", in which method solder balls are formed on the chip's bond pads, the chip is flipped upside down and aligned with the tracks on a circuit board, and the solder is refloowed to make a joint. This method has the advantage of having a very small "footprint" (no larger than the chip itself), but a major drawback of "flip chip" is the tendency of the solder bonds to shear on thermal cycling. This is because the chip (made of silicon) often has a different thermal expansion coefficient from the substrate it is attached to (a glass-epoxy board, for example). Thus the two components expand and contract by different amounts, and failure occurs. Flip chip is also unpopular because a further pro-
cessing step is required to place solder bumps on IC bonding pads.

In the accompanying drawings, Figure FC1 illustrates a solder-bumped chip in a known "flip chip" assembly.

Figure FC2 shows an assembly of a "flip chip" prior to bonding to a uniaxially conductive article according to the present invention.

Figure FC3 illustrates the Fig. FC2 uniaxially conductive article bonded by thermocompression to the "flip chip", before bonding to a hybrid circuit board or multichip module.

Figure FC4 shows the Fig.FC3 assembly after bonding by solder reflow to the hybrid board or module.

Figure FC5 illustrates an assembly ready for thermocompression bonding of the projecting plated tubular metal uniaxial conductors to an unbumped integrated circuit chip according to the present invention.

Figure FC6 shows the Fig. FC5 assembly being bonded by thermocompression.

These drawings, provided by way of example to illustrate aspects of the invention, will be self-explanatory to those familiar with microcircuit technology.

The uniaxial sheets act as stand-offs, tending to reduce the shear stress at the bond interfaces, and calculations suggest that significant increases in working lifetime over solder bumps may be achievable. We have achieved thermocompression joining to normal Al - 1% Si pad material deposited on Si, with no special chip preparation by means of a uniaxial sheet having 2 micrometres of gold plated on tubular copper linings in the through-holes.
THICKNESS OF FOLD

The invention accordingly includes a uniaxially electrically conductive article comprising electrically insulating sheet material with through-holes individually containing electrically conductive material which provides an electrically conductive path between the main surfaces of the sheet material, the surface of the electrically conductive material which is exposed to contact comprising at least 2 micrometres thickness of gold.

Preferably, the gold is plated on at least part of a tubular formation of another metal plated in the through-holes, which through-holes are preferably not more than 200 micrometres in diameter. Gold plating techniques are described, for example, in "Gold Plating Technology" by F.H. Reid & W. Goldie, Electrochemical Publications Limited (1974). To obtain the desired thickness of at least 2 micrometres gold, it is preferred to use electroless methods in which gold is deposited onto copper or nickel electrolessly deposited layers without depositing on the polymer sheet, but other methods, for example thick-film deposition from pastes introduced into the through-holes, may be used if acceptable results are produced. The polymer sheet material will be selected to withstand the chosen metal-deposition techniques.

DIFFERENT BONDS AT OPPOSITE ENDS

Another aspect of the invention provides an anisotropically (preferably substantially uniaxially) electrically conductive article comprising electrically insulating sheet material and electrically conductive material providing electrically separate conductive paths within (preferably close-fitting within holes in) the sheet material between the opposed main surface regions thereof, wherein Case (a) the conductive material at one end of the said paths (preferably projecting beyond one of the sheet surface
regions) is bondable by thermocompression, and the conductive material (which may be the same or different) at the other end of the said paths is bondable by solder, and/or Case (b) the conductive material at one end of the said paths is bondable at a temperature lower than the minimum bonding temperature of the conductive material at the other end of the said paths.

References to "solder" include low-melting-point metals and alloys which can be used in ways similar to commercially available solders.

As implied by the term "anisotropically conductive", the articles in question may conduct electricity in more than one plane, for example by way of conductive paths running parallel to the sheet surfaces either within or on the sheet in addition to those extending from one surface region to the other within the sheet. However, it is often most useful for the article to be substantially uniaxially conductive with the conductive paths extending within the sheet material substantially only from one surface region to the other, preferably substantially straight and normal to the sheet surfaces, although non-linear paths and/or other angles to the sheet surfaces are possible.

It is usually convenient for the sheet material to have through-holes lined with the conductive material, and the conductive material in each through-hole is preferably electrically separate from that in the other through-holes. The through-holes may be internally plated with a tubular formation of metal to provide at least part of the conductive paths, for example by the plating methods described in the aforementioned European, British and International patent applications.

Preferably the thermocompression-bondable material of case (a) above comprises gold metal and the solder-bondable material may comprise gold or may comprise at least one
other metal. In the latter case, it is preferred that the conductive paths comprise at least one metal which is not thermocompression-bondable carrying at least two micrometres thickness of the gold metal at the thermocompression-bondable end.

In all cases, the conductive material may carry solder (applied by known methods) for bonding the solder-bondable ends of the path, but it may be preferable in many practical applications to effect the solder bonding by reflow of solder carried by the substrate to which the article is to be solder-bonded, for example a solder-bumped microchip or inter-connect device.

In some forms of the articles according to the invention the solder-bondable conductive material projects beyond the other sheet surface region; while in other forms the solder-bondable conductive material ends in a recess below the level of the sheet surface in the sense that it ends sufficiently far below the surface to avoid making electrical contact with an electrically conductive member when a surface of that member which is wider than the recess is brought into contact with the sheet surface so as to lie across the recess. In the latter case bonding may be effected by flow of solder into the recesses to contact the conductive material therein, for example from solder-bumped chips of interconnect devices.

An article according to the invention may be designed for making electrical connections to a predetermined pattern of connection sites on a first substrate when arranged in face-to-face alignment with a corresponding pattern of connection sites on a second substrate, the article having a pattern of the said conductive paths corresponding to the said patterns of connection sites to make electrical connections between facing pairs of the connection sites when the sheet material in use is arranged between the substrates.
with the conductive material at the respective ends of each path in contact with its corresponding pair of connection sites. Alternatively, an article could be designed at least a portion of which has at least 25 of the conductive paths per square millimetre of the sheet surface. Such an article, especially at higher path densities per square millimetre, could be used to provide microelectronic connections in the manner described in the aforementioned European application, i.e. without the pattern of conductive paths having to correspond accurately with the connection sites on other substrates. Preferably in all cases some (more preferably substantially all) of the conductive paths are not more than 200 micrometres in diameter.

This aspect of the invention includes an assembly wherein an article of the kind (a) hereinbefore described has the thermocompression-bondable ends of at least some of the conductive paths thermocompression-bonded to connection sites on an integrated circuit semiconductor device. Such an assembly may be useful in manufacturing a further assembly or device in which the said article has the solder-bondable ends of at least some of the conductive paths solder bonded to microelectronic interconnect circuitry. Also included is an alternative assembly wherein the said article has the thermocompression-bondable ends of at least some of the conductive paths thermocompression-bonded to micro electronic interconnect circuitry, which could be useful in manufacturing an alternative further assembly or device wherein the said article has the solder-bondable ends of at least some of the conductive paths solder bonded to connection sites on an integrated circuit semiconductor device. The integrated circuit devices referred to could for example be silicon or gallium arsenide microchips for "flip chip" mounting in an interconnect circuitry package. The interconnect circuitry could for example be a fan-out circuit board, multi-chip module, or another microchip, for example a "mirror" chip as described in the aforementioned
British Application 8802565. It is an advantage of the articles according to this aspect of the present invention that thermocompression-bonds are relatively permanent, while the solder-bonded ends of the conductive paths can be relatively easily disconnected to permit replacement of defective components without destroying the whole assembly.

The anisotropically (preferably uniaxially) conductive articles according to this aspect of the invention may be fabricated using any of the structures and/or methods and/or materials described in any of the aforementioned European, British and International applications, with the thermocompression-bondable material (and the solder if present) being applied during or after the other fabrication steps. The following description will refer to gold, this being the preferred thermocompression-bondable material, although other materials can be used, for example copper, aluminium, or indium, with suitable bonding sites.

Thus permanent bonding of a chip or other active device (e.g. optical detector array) to a chip carrier, printed circuit board, hybrid board, second chip or other interface circuitry/device can be advantageously effected by the aforementioned aspects of the present invention, with the potential advantages over known wire bonding, TAB, and flip chip techniques of low vertical profile, reduced shear stress on thermal cycling, small footprint, and rapid bonding of many connection sites.

Embodiments of this aspect of the invention will now be described by way of example with reference to the accompanying drawings, wherein:

Figure 7GS shows schematically a uniaxially conductive sheet according to the invention and an integrated circuit microchip to be bonded thereto;

Figure 8GS shows the sheet and chip of Figure 1 after bonding;
Figure 9GS shows schematically the inverted assembly of sheet and chip bonded to a fanout circuit board; and

Figure 10GS shows schematic enlargements of two forms of plated through-holes capable of providing the conductive paths through the sheet of Figures 7GS to 9GS.

Referring to Figure 7GS, the electrically insulating polyimide sheet 10 of an article according to the present invention carries electrically conductive plated nickel-on-copper tubes 12 (greatly enlarged for clarity) which provide the conductive paths projecting from both surfaces of the sheet. The upper ends (as illustrated) of the copper tubes are the plated solder-bondable nickel, while the lower ends are overplated with thermocompression-bondable gold to a thickness of 3 micrometres. The gold-plated ends of the tubes 12 are aligned for bonding to aluminium bonding sites (not shown) on an integrated circuit microchip 14.

In Figure 8GS, the gold-plated ends of the tubes 12 have been bonded to the pads of the chip 14 by known thermocompression bonding techniques.

In Figure 9GS, the assembly of the chip 14 with the uniaxially conductive article bonded thereto has been inverted ("flip chip") and the solder-bondable ends of the metal tubes have been soldered by known techniques to bonding sites 16 on a fan-out circuit board 18.

Figures 10AGS and 10BGS respectively show in enlarged schematic cross-section one of the through-holes in the sheet 10 which has been plated with copper and then with nickel to form a tapered tube 20 of metal. Surface layers have been removed from the sheet after the plating to leave the tapered metal tubes 20 projecting from both sheet surfaces, as described in the aforementioned European, British and International applications.
In the preferred methods of forming the through-holes by laser ablation, as described in the aforementioned European, British and International applications, the holes, and therefore the plated metal tubes, are usually somewhat tapered, as shown. Thermocompression-bondable gold layer 22 illustrated in Figure 10AGS has been plated on the wider end of the tapered tube 20, while Figure 10BGS illustrates the alternative plating of the gold 22 on the narrower end of the tapered tube 20. The gold plating 22 need not extend as far into the tube 20 as illustrated. The tube 20 could carry solder at its other (not gold-plated) end, which solder could partially fill the tube 20. Preferably the temperatures used for thermocompression bonding the gold-plated end would not prematurely melt the chosen solder.

When it is desired to place thermocompression-bondable material (or solder) on only one end of the conductive paths, this is easily achieved for example, by masking all the other side of the sheet with a suitable resist, electrolessly plating the material, e.g. gold (or melt coating the solder) on the exposed ends of suitable metal e.g. copper-nickel passing through the sheet, then removing the resist to expose the unplated (or unsoldered) ends of the paths. The unplated (unsoldered) ends may then be similarly provided with an appropriate bonding material, preferably a solder having a lower melt temperature than the material applied to the first ends, which may be thermocompression-bondable in the aforesaid case (a), or another solder in case (b).

This aspect of the invention includes a method of making electrical connections wherein an anisotropically electrically conductive article according to the aforesaid case (a) is thermocompression bonded to electrical conductors at one end of the said paths, and is solder bonded to electrical conductors at the other end of the said paths, or wherein an anisotropically electrically conductive article
according to the aforesaid case (b) is solder bonded to electrical conductors at one end of the said paths by solder having a first minimum bonding temperature, and is solder bonded to electrical conductors at the other end of the said paths at a temperature below the said first minimum bonding temperature by solder having a second minimum bonding temperature which is lower than the first minimum bonding temperature.

Preferably, the higher-temperature bonding material will be able to survive the exposure to lower temperatures involved in bonding the lower temperature ends.

Two different tin/lead solders could for example be used to provide the different solder bonding temperatures. Thermocompression bonding is preferably conducted at significantly higher temperatures than solder-bonding.

Example lGS

A sample of the uniaxially conducting material was prepared with a 2 micron gold plating layer on the protruding tubes both above and below the polyimide layer. An integrated circuit with aluminium bond pads was placed face upwards on a Farco Tape Automated (Thermocompression) Bonder (model FL20), and the uniaxially conducting material was then aligned over the top of the chip so that the tubes were above the bond pads. Once aligned, the thermode was brought into contact with the uniaxially conducting material, the thermode temperature raised to 350 celsius during the application of pressure for approximately 2 seconds in order to form a thermocompression bond. At this stage the integrated circuit was bonded to the sheet of uniaxially conducting material. The sub-assembly was then 'flipped' so as to bring the side of the uniaxially conducting material not yet used for bonding into contact and alignment with a circuit board. The tracks on the circuit board had previously been solder tipped with an 80% gold - 20% tin solder which forms
a good solder bond to gold metallisations. The stage on which the circuit board was held was heated so as to raise the temperature of the solder above its melting point i.e. greater than 280 celsius. Once cooled, a strong solder bond had been achieved between the uniaxially conductive material and the circuit board.

The result of these two bonding stages is an integrated circuit bonded to a circuit board via a sheet of uniaxially conducting material.

**SHEET MATERIALS**

A preferred polymer material for this and the other aspects of the invention is a polyimide capable of retaining at least 50%, preferably at least 75%, more preferably at least 85%, of its original elongation after immersion in water of pH10 at 100°C for 4 days according to ASTM D882. It will be readily understood that a sufficiently fully cyclised polyimide having less than 15%, preferably less than 10%, more preferably less than 5%, and if possible substantially no open imide rings or uncyclised amic acid groupings may be better able to survive hot alkaline metal plating baths, which attack incompletely cyclised polyimides such as Kapton (TM). Preferred materials include polyimides derived from polymerisation of 4,4'-biphenyl dianhydride and (4,4'-diaminobiphenyl, or 4,4'-diaminobiphenyl ether, or phenylenediamine).

The currently more preferred commercially available polyimides are those available under the Trade Mark "UPILEX" from Ube/ICI. One of these, "UPILEX R", is believed to be a relatively completed cyclised polymer having a repeat unit derived from biphenyl dianhydride and dianinodiphenylether, viz.

\[
-N\left[\begin{array}{cccc}
O & C & C & \cdots & C \\
C & C & C & \cdots & C \\
C & C & C & \cdots & C \\
O & C & C & \cdots & C \\
\end{array}\right]_n-N
\]
Most preferred, however, is "UPILEX S", which is believed to have a repeat unit derived from the same anhydride and phenylene diamine, viz.

\[
\text{\begin{bmatrix}
\text{\(O\)} & \text{\(\ddots\)} & \text{\(O\)} \\
\text{\(\ddots\)} & \text{\(C\)} & \text{\(\ddots\)} \\
\text{\(O\)} & \text{\(\ddots\)} & \text{\(N\)}
\end{bmatrix}}
\]

The polyimide of the 4,4'-diaminobiphenyl has thermal expansion/contraction characteristics which may render it particularly suitable for use with microcircuit chips and circuit boards.

Appropriate forms of uniaxially conductive sheet for the various embodiments of the present invention may be made, for example, by the methods and materials described in the aforementioned EP-A-0213774 or copending applications, the respective disclosures of which are incorporated herein by reference.

**DIMENSIONS AND CONDUCTIVITY**

The diameter and distribution of the through-holes will preferably be such that the through-holes occupy from 3 to 35 per cent, more preferably 10 to 20 per cent, of the sheet surface area. Through-holes of less than 500, preferably less than 200, e.g. 5 to 150, micrometres diameter are preferred. A sheet with at least 25 through-holes per square millimetre having plated electrically conductive material (metal) within (preferably substantially only within) the through-holes is especially useful, the through-holes thus providing high density electrically conductive paths between the sheet surfaces, each such path being electrically separate from substantially all the other paths.

While a "significant proportion" of the through-holes in the selected areas, for example at least 10%, preferably at least 20%, more preferably at least 30% or better still at least 40%, may contain the conductive material, it is
preferred that a majority (more than 50%) do so. Proportions of at least 70%, or better still at least 85% are preferred, and in many cases, substantially all of the through-holes will contain the conductive material. The through-holes, whether containing the conductive material or not, may be confined to selected areas of the sheet.

The through-holes in "one-to-one" or in "multitudinous" uniaxial sheets may for example be 1 to 200 micrometres in diameter, and it is also preferable in some cases that the conductive material project beyond both main surfaces of the sheet material, the projections from either surface preferably being, for example, 0.2 to 100 micrometres in height, preferably 0.5 to 30 micrometres.

The insulating sheet material will preferably be a flexible polymeric material, and the number of through-holes per square millimetre of the sheet material (whether flexible polymeric or not) may be as high as 10000, but will preferably be in the range from 25 to 2000, more preferably 50 to 1000. The through-holes will preferably have a tortuosity factor (mean path length/sheet thickness) less than 3, preferably less than 1.2; and will preferably have an aspect ratio (length/diameter) of at least 2.5.

Although the "electrically conductive" paths between the sheet surfaces may give the sheet an average electrical conductivity in the thickness direction within the semiconductive range, it is preferable to achieve generally metallic levels of conductivity e.g. at least 1000 Siemens per centimetre, preferably at least 5000 Siemens, especially at least 10000 Siemens per centimetre. The preferred conductive materials are metals, preferably plated, especially electrolessly plated, on the interior surface of the through-holes. Any suitably applicable metals may be used, for example Ni, Cu, Ag, Au, Co, Pd, Pd-Ni, Sn, Pb, Pb-Sn, In.
In some forms of the articles, at least a selected portion of the sheet has a plurality (at least 4, preferably at least 8, more preferably 25 to 1000) of substantially non-interconnected through-holes per square millimetre of its surface, at least a significant proportion of which through-holes individually contain a tubular formation of electrically conductive material projecting beyond at least one of the sheet surfaces.

TUBULAR FORM AND PROJECTION

This and other forms of the invention could be used to provide relatively large "posts" of solder or other fusible metals eg. indium supported by the tubular formation, for use in making connections to microcircuits. Gold filling material could also be used to make such "permanent" electrical connections, instead of, or in addition to, plated gold.

Preferably, this tubular formation comprises a first portion of electrically conductive material on the interior through-hole surface, and a second portion of electrically conductive material, on at least one of the end surfaces of the first portion, at least the parts of the second portion on one or both of the end surfaces of the first portion projecting beyond the sheet surface(s). Preferably, the first portion of electrically conductive material on the through-hole surface is tubular and the second portion of electrically conductive material is on the interior surface as well as the end surface(s) of the first portion. The first portion is preferably metal and is preferably plated, especially electrolessly plated, on the interior through-hole surface, and the second portion is preferably metal plated, especially electrolessly plated, on the first portion. The first and second portions respectively may comprise different electrically conductive materials, and the second portion may fill the tubular first portion or may itself be
tubular, in which case it may be filled with further electrically conductive or insulating material. Where the metal-lined through-holes are filled with another metal, this will preferably be a solder, a low-melting-point metal, a fusible alloy, or a plated metal. Either the tubular metal lining, or the metal fillings, or both, may project beyond the sheet surface(s), and the other criteria mentioned above may apply as appropriate.

If desired, electrically insulating material may be removed from one or both surfaces of the sheet material to expose portions of the electrically conductive material originally within the through-holes, thus producing or increasing the desire projections of the conductive material beyond the sheet surface(s). This may be done by any convenient means, for example by dissolving away a surface layer of the sheet material, as described in the aforementioned EP-A-0213774, for which purpose a sheet having surface layers of materials more readily soluble in a selected solvent than the underlying layers may be used, especially the sheets described in copending International application...... (RK362COM).

STRIPE FEED

The invention also provides a strip of electrically insulating material carrying two or more arrays of the through-holes containing electrically conductive material as hereinbefore described, the strip preferably being adapted for feeding to suitable processing equipment (e.g. for chip testing, permanent bonding, or heat-sink applications as hereinbefore described) by mechanical, preferably automatic, feed means.

Examples of various permanent bonding aspects of the invention will now be described with reference to the accompanying drawings, wherein:
Figure 11PB illustrates schematically permanent attachment of a microchip to associated circuitry by bonding to a uniaxially conductive sheet;

Figure 12PB shows schematically a magnified portion of a uniaxially conductive sheet suitable for permanent bonding as shown in Fig.11PB;

Figure 13PB shows schematically a permanent bond formed by one of the plated through-holes as shown in Fig.12PB.

Referring to Figures 11PB to 13PB thermocompression-bonding was used to fabricate a three layer structure comprising (a) a silicon chip, with bonding sites a' metallised with aluminium -1% silicon, (b) a sheet of uniaxially conducting material, the through-holes b' of which have been plated with 5 um copper, 1 um nickel, 2 um gold, (c) a silicon substrate, metallised with aluminium -1% silicon conductive tracks c'. Two types of uniaxially conducting sheets were evaluated, the first consisting of an area with 35 micrometre diameter tubes in a multi-tube array (not shown), the second consisting of an area containing a small number of specially-positioned 35 micrometre diameter tubes as shown corresponding to the positions of the bonding sites a' and conductive tracks c'. Bonding conditions for each case were similar, and typically used a thermode temperature of 350-500°C and bonding forces from 80-200N, with a duration of 2-4s. Thermocompression-bonds as illustrated in Figure 13PB were obtained using this method.

The through-holes before bonding are shown in Figure 12PB comprising an electrolessly plated layer of copper, overlaid with nickel and then with gold as aforesaid, the polyamide surface layers originally present on the sheet during the copper plating process having been removed prior to the nickel and gold plating to produce the sheet b shown in Fig. 12PB with the metal tubes protruding from it.
In Figure. 13PB the structure of Figure. 12PB has been thermocompression-bonded to the chip a and substrate c as aforesaid.

**TAB APPLICATIONS**

The uniaxial conductor can advantageously be used as a permanent bonding medium in conjunction with TAB (tape automated bonding). TAB is known as a method of achieving electrical connection between a bare chip and a substrate such as a hybrid board, printed circuit board, chip carrier or other package.

Known TAB devices have a structure akin to that shown schematically in the accompanying Sketch A14. A polymer tape A1 having edge sprocket holes A2 for feeding the tape in automated bonding equipment carries a series of arrays (one shown) of conductive tracks A3 arranged in a pattern suitable for bonding their inner ends A4 to the bonding sites of a microchip (not shown). The tape A1 is cut away under the ends A4 of the tracks so that the ends project as unsupported "fingers" over the cut-away space A5. Bonding is effected by bending the fingers into the space A5 to make contact with the chip positioned against the opposite side of the tape from that carrying the tracks. This is followed by bonding by known techniques which often require the chip bonding sites to be "bumped" with bonding material, e.g. solder.

The present invention can avoid such disadvantageous "bumping" processes.

The invention provides a method of making electrical connections between an array of connection sites on an integrated circuit microelectronic device and electrical conductors carried on a film or tape support material with an array of unsupported end portions of the conductors projecting beyond an edge of the support material wherein:
(a) the said end portions of the conductors are respectively aligned with the said connection sites to which connections are to be made,

(b) an anisotropically (preferably uniaxially) electrically conductive article comprising electrically insulating sheet material with through-holes containing electrically conductive material which provides an electrically conductive path from one of the main surface regions of the sheet material to the other is positioned with the respective ends of at least one such path aligned between each connection site and the conductor end portion aligned therewith, and

(c) the ends of the path(s) so aligned are bonded respectively to the connection sites and to the conductor end portions.

The uniaxially conductive article for use with TAB articles according to the present invention maybe in any appropriate form among those described in the aforementioned copending Applications or in European Published Application EP-A-0213774, the disclosures of all of which are incorporated herein by reference. The uniaxially conductive articles may be made using any of the methods and/or materials disclosed in those copending or published applications. The invention includes the use of a TAB article or process with any novel sub-set or sub-combination of the aforesaid forms, methods of materials.

These and other aspects of the invention are further illustrated by the accompanying schematic drawings, which are largely self-explanatory, in which:

Figure 15 TAB shows two plated metal tubes projecting from a uniaxially conducting article made by the aforesaid methods aligned with the ends of conductive tracks by a TAB device.
Figure 16 TAB illustrates thermocompression (TC) bonding of the outer track ends of a TAB device using multtube uniaxially conductive articles as described in the aforementioned EP-A-0213774; and

Figure 17 TAB shows a TAB production line bonding TAB tracks to a series of unbumped integrated circuit chips.

Figure 18 illustrates connections made by fusible metal (e.g. solder, gold, indium) columns presented in a uniaxially conductive article from which the polymeric carrier sheet (dotted lines in inset) has been removed, e.g. by a suitable solvent after bonding of the columns. Such columns could be used in devices embodying various aspects of the present invention, and may be especially useful in high-density arrays of interconnections, for example a closely-packed array of "III" interconnections, having one such column for each pair of opposed bonding sites. The columns are made, for example, by filling the initial plated metal tubes with molten metal, and allowing the molten metal to solidify. The thin tubular plating but may remain as a surface layer theron. Solid columns of other metals may be similarly made, and the filling metal may be the same as or different from the plating metal.
CLAIMS:

1. A method of making substantially permanent electrical connections between connection sites on a first substrate and corresponding connection sites on a second substrate, comprising

(a) aligning the connection sites of the first substrate face to face with the corresponding connection sites on the second substrate,

(b) placing between the facing substrates a uniaxially electrically conductive article comprising electrically insulating sheet material with at least one through-hole aligned between each pair of facing connection sites, the through-holes being internally plated with electrically conductive material which provides an electrically conductive path between, and projects beyond, the main surfaces of the sheet material and including bonding material capable of bonding to the said connection sites, and

(c) bonding the ends of the electrically conductive material in the through-holes to the respective corresponding pairs of facing connection sites, one or both of the connection sites in each pair prior to the bonding being substantially free of fusible bonding material.

2. A method according to Claim 1 wherein the electrically conductive material comprises a tube of the plated metal in each through-hole.

3. A method according to Claim 1 or 2, wherein the bonding material is solder at least at one end of each through-hole.

4. A method according to Claim 1 or 2, wherein the bonding material is gold at least at one end of each through-hole.
5. A method according to Claim 4 wherein the gold has a thickness of at least 1.5, preferably at least 2, micrometres.

6. A method according to any of Claims 1 to 5 wherein the electrically conductive material is bonded to the connection sites by thermocompression bonding.

7. A method according to any preceding claim, wherein the uniaxially electrically conductive article has only one through-hole containing the electrically conductive material between each pair of facing connection sites.

8. A method according to any of Claims 1 to 6, wherein the uniaxially electrically conductive article has more than one through-hole containing the electrically conductive material between each pair of facing connection sites.

9. A method according to any preceding claim, wherein the uniaxially electrically conductive article has through-holes containing the electrically conductive material in areas of the sheet material which are not aligned between the facing pairs of connection sites.

10. An assembly comprising a first substrate electrically connected to a second substrate by a method according to any of Claims 1 to 9.

11. An assembly according to Claim 10, wherein at least one of the said substrates is a microcircuit chip.

12. A method of making electrical connections between an array of connection sites on an integrated circuit microelectronic device and electrical conductors carried on a film or tape support material with an array of unsupported end portions of the conductors projecting beyond an edge of the support material, wherein:

(a) the said end portions of the conductors are respectively aligned with the said connection sites to which
connections are to be made,

(b) an anisotropically electrically conductive article comprising electrically insulating sheet material with through-holes containing electrically conductive material which provides an electrically conductive path from one of the main surface regions of the sheet material to the other is positioned with the respective ends of at least one such path aligned between each connection site and the conductor end portion aligned therewith, and

(c) the ends of the path(s) so aligned are bonded respectively to the connection sites and to the conductor end portions.

13. An anisotropically electrically conductive article comprising electrically insulating sheet material and electrically conductive material providing electrically separate conductive paths (preferably close-fitting within holes in the sheet material) between the opposed main surface regions thereof, wherein the conductive material at one end of the said paths (preferably projecting beyond one of the sheet surface regions) is bondable by thermocompression, and the conductive material at the other end of the said paths is bondable by solder.

14. An article according to Claim 13 which is substantially uniaxially conductive.

15. An article according to Claim 13 or 14, wherein the sheet material has through-holes therein containing the electrically conductive material.

16. An article according to Claim 15, wherein the conductive material in each through-hole is electrically separate from that in the other through-holes.

17. An article according to Claim 14 or 15, wherein the through-holes are internally plated with a tubular formation
of metal.

18. An article according to any of Claims 13 to 17, wherein the thermocompression-bondable material comprises gold metal and the solder-bondable material comprises at least one other metal.

19. An article according to any of Claims 13 to 18 comprising gold metal at both ends of the said paths.

20. An article according to Claim 18 or 19, wherein the conductive paths comprise at least one metal which is not thermocompression-bondable carrying at least two micrometres thickness of gold metal at one or both ends.

21. An article according to any of Claims 13 to 20, wherein the conductive material carries solder for bonding the solder-bondable ends of the paths.

22. An article according to any of Claims 13 to 21, wherein the solder bondable conductive material projects beyond the adjacent sheet surface region.

23. An article according to any of Claims 13 to 21, wherein the solder-bondable conductive material ends in a recess below the level of the sheet surface in the sense that it ends sufficiently far below the surface to avoid making electrical contact with an electrically conductive member when a surface of that member which is wider than the recess is brought into contact with the sheet surface containing the recess so as to overlie the recess.

24. An article according to any of Claims 13 to 23, for making electrical connections to a predetermined pattern of connection sites on a first substrate when arranged in face-to-face alignment with a corresponding pattern of connection sites on a second substrate, the article having a pattern of the said conductive paths corresponding to the said patterns of connection sites to make electrical connections between
facing pairs of the connection sites when the sheet material in use is arranged between the substrates with the conductive material at the respective ends of each path in contact with its corresponding pair of connection sites.

25. An article according to any of Claims 13 to 23, at least a portion of which has at least 25 of the conductive paths per square millimetre of the sheet surface.

26. An article according to any of Claims 13 to 25, having the thermocompression-bondable ends of at least some of the conductive paths thermocompression-bonded to connection sites on an integrated circuit semiconductor device.

27. An article according to Claim 26, having the solder-bondable ends of at least some of the conductive paths solder bonded to microelectronic interconnect circuitry.

28. An article according any of the Claims 13 to 25, having the thermocompression-bondable ends of at least some of the conductive paths thermocompression-bonded to microelectronic interconnect circuitry.

29. An article according to Claim 28, having the solder-bondable ends of at least some of the conductive paths solder bonded to connection sites on an integrated circuit semiconductor device.

30. An anisotropically (preferably substantially uniaxially) electrically conductive article comprising electrically insulating sheet material and electrically conductive material providing electrically separate conductive paths close-fitting within holes in the sheet material between the opposed main surface regions thereof, wherein the conductive material at one end of the said paths is bondable at a temperature lower than the minimum bonding temperature of the conductive material at the other end of the said paths.
31. An article according to Claim 19 wherein the bondable material comprises a first solder at one end of the said paths and a second solder at the other end of the said paths.

32. A method of making electrical connections wherein an anisotropically electrically conductive article according to any of Claims 13 to 31:-

(a) is thermocompression bonded to electrical conductors at one end of the said paths, and is solder bonded to electrical conductors at the other end of the said paths, or

(b) is solder bonded to electrical conductors at one end of the said paths by solder having a first minimum bonding temperature, and is solder bonded to electrical conductors at the other end of the said paths at a temperature below the said first minimum bonding temperature by solder having a second minimum bonding temperature which is lower than the first minimum bonding temperature.

33. A uniaxially electrically conductive article comprising electrically insulating sheet material with through-holes individually containing electrically conductive material which provides an electrically conductive path between the main surfaces of the sheet material, the surface of the electrically conductive material which is exposed to contact comprising at least 2 micrometres thickness of gold.

34. An article according to Claim 33, wherein the gold is plated on at least part of a tubular formation of another metal plated in the through-holes.

35. An article comprising a strip of electrically insulating sheet material carrying two or more arrays of the through-holes containing electrically conductive material as specified in any preceding claim.
36. An article according to Claim 35 adapted for feeding to processing equipment by mechanical, preferably automatic, feed means.

37. A method, assembly, or article according to any preceding claim, wherein the said insulating sheet material is a polyimide capable of retaining at least 50%, preferably at least 75%, more preferably at least 85%, of its original elongation after immersion in water of pH10 at 100°C for 4 days according to ASTM D882.

38. A method, assembly, or article according to any preceding claim, wherein the said insulating sheet material is a polyimide derived from polymerisation of 4,4'-biphenyl dianhydride and (4,4'-diaminobiphenyl, or 4,4' diaminobiphenyl ether, or phenylenediamine, preferably p-phenylenediamine).

39. A method, assembly, or article according to any preceding claim, wherein at least some, preferably substantially all, of the conductive paths are not more than 200 micrometres in diameter, preferably not more than 100 micrometres in diameter.

40. A method, assembly, or article according to any preceding claim, wherein the said holes are made by laser drilling, preferably by U.V. laser ablation.

41. A method, assembly, or article according to any preceding claim, wherein the said holes have a taper relative to the hole axis of less than 10°, preferably less than 8°.

42. A method, assembly, or article according to claim 41, wherein the taper is less than 6°, preferably less than 4°.
**Fig. FC1.**

- HYBRID BOARD
- SOLDER BUMPED IC

**Fig. FC2.**

- UNIAx FILM
- STANDARD UNBUMPED IC

HYBRID BOARD
Fig. FC3.

STANDARD UNBUMPED IC

UNIAXIAL FILM

SOLDER BUMPED HYBRID BOARD

Fig. FC4.

STANDARD UNBUMPED IC

UNIAXIAL FILM

FINAL REFLOWED ASSEMBLY
3/10

**Fig. FC5.**

![Diagram](image1)

- **UNIAXIAL FILM**
- **STANDARD UNBUMPED IC**

**Fig. FC6.**

![Diagram](image2)

- **HEATED THERMODE BONDER**
- **STANDARD UNBUMPED IC**
- **UNIAXIAL FILM**
Fig. 11PB.

1. CHIP UNDERSIDE.

2. UNIAXIAL CONDUCTIVE SHEET.

3. CIRCUIT BOARD.
Fig. A14.

Fig. 15TAB.

COPPER TAB
TRACK

UNIAXIAL CONDUCTING FILM
Fig. 16 TAB.

Tracks on TAB film

IC

UNITAX TC BOND REPLACES SOLDER

BOARD
Fig. 18.

INDIUM COLUMNS
## INTERNATIONAL SEARCH REPORT

### I. CLASSIFICATION OF SUBJECT MATTER

**International Application No.** PCT/GB 89/00111

According to International Patent Classification (IPC) or to both National Classification and IPC

**IPC:** H 01 L 23/50; H 01 L 21/48

### II. FIELDS SEARCHED

**Classification System | Classification Symbols**

- **IPC:** H 01 L

**Documentation Search other than Minimum Documentation**

to the extent that such Documents are Included in the Fields Searched

### III. DOCUMENTS CONSIDERED TO BE RELEVANT

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<th>Category</th>
<th>Citation of Document, with indication, where appropriate, of the relevant passages</th>
<th>Relevant to Claim No.</th>
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<td>EP, A, 0245179 (DIGITAL EQUIPMENT) 11 November 1987  see figure 2; claim 1; column 4, line 52 - column 5, line 6</td>
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* Special categories of cited documents: 19
  - "A" document defining the general state of the art which is not considered to be of particular relevance
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  - "X" document of particular relevance; the claimed invention cannot be considered without the document
  - "Y" document of particular relevance; the claimed invention cannot be considered without the invention
  - "Z" document of particular relevance; the claimed invention cannot be considered without the document
  - "P" document published prior to the international filing date but later than the priority date

### IV. CERTIFICATION

- **Date of the Actual Completion of the International Search:** 9th May 1989
- **Date of Mailing of this International Search Report:** 01 Jun 1989

**International Searching Authority:** EUROPEAN PATENT OFFICE

**Signature of Authority Officer:**

Form PCT/ISA/210 (second sheet) (January 1985)
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ANNEX TO THE INTERNATIONAL SEARCH REPORT
ON INTERNATIONAL PATENT APPLICATION NO. GB 8900111
SA 26824

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