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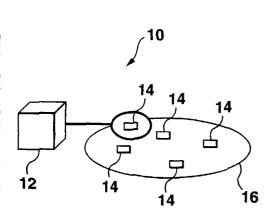
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(54) Title: WIRELESS RADIO FREQUENCY TESTING METHODE OF INTEGRATED CIRCUITS AND WAFERS



(57) Abstract: The present invention is for an apparatus and method for the wireless testing of Integrated Circuits and wafers. The apparatus comprises a test unit external from the wafer and at least one test circuit which is fabricated on the wafer which contains the Integrated Circuit. The test unit transmits an RF signal to power the test circuit. The test circuit, comprising a variable ring oscillator, performs a series of parametric tests at the normal operating frequency of the Integrated Circuit and transmits the test results to the test unit for analysis.

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'ional Application No

PCI/CA 01/00688 A. CLASSIFICATION OF SUBJECT MATTER IPC 7 H01L21/66 G01F GO1R31/303 G01R31/3185 According to International Patent Classification (IPC) or to both national classification and IPC **B. FIELDS SEARCHED** Minimum documentation searched (classification system followed by classification symbols) IPC 7 HO1L GO1R Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Electronic data base consulted during the international search (name of data base and, where practical, search terms used) EPO-Internal, WPI Data, INSPEC C. DOCUMENTS CONSIDERED TO BE RELEVANT Relevant to claim No. Category 9 Citation of document, with indication, where appropriate, of the relevant passages Υ WO 99 17353 A (BLON THOMAS ; SIEMENS AG 1,2,11, 28,30, (DE)) 8 April 1999 (1999-04-08) 34,35, 44,54 the whole document 3-10.Α 12-27, 29, 31-33, 36 - 43, 45-53. 55-69 1,2,11, Υ US 5 446 395 A (GOTO JUNICHI) 29 August 1995 (1995-08-29) 28,30, 34,35, 44,54 column 4, line 15-30; figure 3 Χ Patent family members are listed in annex. X Further documents are listed in the continuation of box C. Special categories of cited documents: "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the "A" document defining the general state of the art which is not considered to be of particular relevance *E* earlier document but published on or after the international "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to filing date involve an inventive step when the document is taken alone document which may throw doubts on priority claim(s) or

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Name and mailing address of the ISA European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Tx. 31 651 epo nl, Fax: (+31-70) 340-3016

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