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-1-

APPARATUS AND METHODS OF GENERATING A TEST PATTERN OF DATA,
ANALYSING A TEST PATTERN OF DATA, AND TESTING
A DATA STORAGE DISK MEDIUM AND/OR A READ/WRITE HEAD

5 The present invention relates to apparatus and methods of generating a test pattern of data, analysing a test pattern of data, and testing a data storage disk medium and/or a read/write head.

10 In embodiments, the present invention relates generally to head media test apparatus such as are commonly known as "spinstands" or "dynamic electrical test machines" in the art. Spinstands were first developed in the art as a tool for use during research and development to allow the
15 performance of the various components of disk drives, for example the heads, disks and channels, to be evaluated and optimised. It is now common to also use spinstands in the field of disk drive manufacturing to test each manufactured read/write head or disk before it is assembled into a disk
20 drive unit.

A typical spinstand comprises a motor-driven spindle on which a disk to be tested can be mounted and spun, and a head load mechanism for holding and positioning the
25 read/write head to be tested. The spinstand also comprises a spinstand controller under the control of which the head is "flown" over the surface of the disk when spun, so that test data can be written to and read from the disk with the head. When conducting a test with a spinstand, the head is
30 first positioned over a track on the disk and test data is written to the track. The test data is subsequently read back by the head, measured, and analysed and the results then displayed to the user. Various parameters under which

-2-

the data is written and/or read back can be controlled and varied by the spinstand, allowing the performance and characteristics of the head or disk to be investigated under various conditions. Usually, a computer or similar
5 processing apparatus is provided to carry out the various tests performed on the spinstand, and to analyse and display the measurements made with the spinstand. Additionally, dedicated parametric measurement electronics, a spectrum analyser or an oscilloscope may be provided for
10 analysing and displaying the measurements made with the spinstand. In this way a series of tests may be conducted, including for example so-called bit error rate (BER) bathtubs, track squeeze, track centre, read/write offset, overwrite, etc.

15

One possible source of error in testing is due to fluctuations in the speed of rotation of the disk. A highly accurate motor is usually used to rotate the spindle and thereby the disk attached to the spindle.
20 Nevertheless, the motor speed can only be controlled to within a certain tolerance, giving rise to undesired fluctuations in the rotational speed of the disk. Such fluctuations cause the test data pattern to be distorted both when being written to the disk as well as when being
25 read back from the disk. These distortions can cause significant problems in performing tests where high precision is required.

According to a first aspect of the present invention,
30 there is provided a method of generating a test pattern of data to be written to a data storage disk medium for testing, the method comprising: rotating the disk; detecting fluctuations in the speed of rotation of the

-3-

disk; producing a reference clock signal in accordance with
said fluctuations so as to be synchronised with the
rotation of the disk; and, generating a test pattern of
data using said reference clock signal as a timing
5 reference.

By producing a reference clock signal that is
synchronised with the rotation of the disk and using this
to generate a test pattern of data, it is possible to
10 generate a test pattern that accounts or allows for
fluctuations in the rotation of the disk such that when the
test pattern is written to the disk, it is spatially
correct with respect to the surface of the disk. This
means that the data can be read back and the signal
15 obtained will be a truer representation of the original
test pattern of data.

For example, where a test frequency is written to a
track on the disk, in prior art arrangements the test
20 frequency actually present on the disk will vary to some
degree along the track, meaning that the recovered
frequency will also be spread, leading to loss of accuracy
in measuring the signal. In contrast, this aspect of the
present invention allows the frequency to be modulated to
25 accommodate the rotational error of the disk, thereby
making the written frequency pattern more spatially correct
on the disk so that the signal read back from the disk will
be less spread. This means that detecting the frequency in
the read back signal will be simpler as the frequency power
30 will be more sharply concentrated about the correct
frequency and less spread.

-4-

In a preferred embodiment, the step of detecting the fluctuations comprises: detecting a plurality of marks that rotate synchronously with the disk; and, producing a clock signal corresponding to the movement of the detected marks, said clock signal being used in the production of the reference clock signal. This arrangement provides a preferred method of measuring the fluctuations in the rotation of the disk to allow the reference clock signal to be produced. The marks may be optical marks that are read using laser interferometry. The optical marks may be attached to the disk itself or to the spindle or to any other part that rotates with the disk. Alternatively, the marks may be magnetic marks on the disk that are detected magnetically. It is generally preferred to have many marks, to allow a higher resolution in determining the fluctuations in the rotation of the disk.

In a preferred embodiment, the method comprises providing the clock signal as an input to a phase locked loop, the phase locked loop being arranged to produce said reference clock signal. This provides a simple way of producing the reference clock signal. Using a phase locked loop allows the production of the reference signal to be tailored to suit the application according to the well known principles governing the operation of phase locked loops.

In an embodiment, the method comprises producing an oscillator clock signal from an oscillator; the disk being rotated with a motor that is locked to the oscillator clock signal; and, the reference clock signal being generated using the oscillator clock signal, the oscillator clock signal being modulated in accordance with the fluctuations

-5-

in the speed of rotation of the disk in the generation of the reference clock signal. In effect, in this embodiment one oscillator is used to produce the reference clock and to clock the motor. If separate clocks were used for each, then thermal drift could cause the clocks to drift with respect to each other, which would have the effect of introducing phase error in the pattern written to the disk. Having one clock for both has the advantage of substantially eliminating this error.

10

The method may comprise rotating the disk; detecting fluctuations in the speed of rotation of the disk; producing a reference clock signal in accordance with said fluctuations so as to be synchronised with the rotation of the disk; reading a test pattern of data from the disk to provide a test data signal; and, analysing said test data signal using the reference clock signal as a timing reference. This arrangement provides a preferred phase locked loop architecture. The divide-by-n counter means that the reference clock signal can be at a different frequency from the clock signal.

According to a second aspect of the present invention, there is provided a method of analysing a test pattern of data read from a data storage disk medium, the method comprising: rotating the disk; detecting fluctuations in the speed of rotation of the disk; producing a reference clock signal in accordance with said fluctuations so as to be synchronised with the rotation of the disk; reading a test pattern of data from the disk to provide a test data signal; and, analysing said test data signal using the reference clock signal as a timing reference.

By producing a reference clock signal that is synchronised with the rotation of the disk and using this to analyse the test data signal, it is possible to account or allow for fluctuations in the rotation of the disk when
5 reading back the data. This makes analysing the data more simple and improves the accuracy of the results.

For example, where a test frequency is written to a track on the disk, in prior art arrangements the recovered
10 test data signal read from the disk will be distorted due to the fluctuations in rotation of the disk, meaning that the recovered frequency will be frequency spread, leading to difficulties in measuring the signal. In contrast, this aspect allows the recovered data test signal to be
15 demodulated with the rotational error of the disk, rejecting fluctuations in the speed of rotation of the disk, so that the analysis of the signal read back from the disk will be less affected by frequency spreading. This means that detecting the frequency in the read back signal
20 will be simpler as the frequency power will be more sharply concentrated about the correct frequency and less spread.

The step of analysing may comprise spectrum analysing said test data signal. As discussed below, the present
25 arrangement has particular advantages when used in narrow band measurement tests.

The step of spectrum analysing may comprise converting said test data signal to an intermediate frequency by
30 mixing said test data signal with a sinusoidal signal generated using said reference clock as a timing reference. This arrangement allows a more accurate super-heterodyning operation to be carried out on the data signal as part of a

-7-

spectrum analysing test and allows the spectrum analyser to become normalised to disk speed.

The step of spectrum analysing may comprise sampling
5 and digitising the test data signal using an analogue-to-digital converter that is clocked by the reference clock. This allows the spectrum analyser to become normalised to disk speed. The present arrangement can also be used in a spectrum analyser that performs some or all of the spectrum
10 analysis digitally.

According to a third aspect of the present invention, there is provided a method of testing at least one of a data storage disk medium and a read/write head, the method
15 comprising: rotating the disk; detecting fluctuations in the speed of rotation of the disk; producing a reference clock signal in accordance with said fluctuations so as to be synchronised with the rotation of the disk; generating a test pattern of data using said reference clock signal as a
20 timing reference; writing the test pattern of data to the disk with the head; reading the test pattern of data from the disk with the head to provide a test data signal; and, analysing said test data signal using the reference clock signal as a timing reference.

25

This provides a more accurate method of testing by utilising both methods described above. The reference clock signal is used both when generating the pattern of data to be written to the disk to compensate for
30 fluctuations in the disk rotation and also when analysing the data signal read back from the disk to compensate for fluctuations in the disk rotation.

-8-

In an embodiment, the method comprises: 1) the disk being rotated by a motor that is locked to an oscillator clock signal; and, 2) the reference clock signal being provided from an oscillator clock signal, the oscillator
5 clock signal being modulated in accordance with the fluctuations in the speed of rotation of the disk in the production of the reference clock signal, wherein the same oscillator clock signal is used for steps 1) and 2). In this arrangement, a single oscillator clock is used as
10 reference for the entire testing apparatus rather than having separate oscillator clocks in the apparatus. This avoids the effects of thermal drift which typically arise over time between two separate clocks, and therefore helps avoid a source of error entering the system, both when
15 writing data to the disk and when reading back and analysing data from the disk.

According to a fourth aspect of the present invention, there is provided apparatus for generating a test pattern
20 of data to be written to a data storage disk medium for testing, the apparatus comprising: a spindle for rotating a said disk; a detector arranged to detect fluctuations in the speed of rotation of the disk; a processor arranged to produce a reference clock signal in accordance with said
25 fluctuations so as to be synchronised with the rotation of the disk; and, a pattern generator arranged to generate a test pattern of data using said reference clock signal as a timing reference.

30 According to a fifth aspect of the present invention, there is provided apparatus for analysing a test pattern of data read from a data storage disk medium, the apparatus comprising: a spindle for rotating a said disk; a detector

-9-

arranged to detect fluctuations in the speed of rotation of the disk; a processor arranged to produce a reference clock signal in accordance with said fluctuations so as to be synchronised with the rotation of the disk; a controller
5 arranged to read a test pattern of data from the disk to provide a test data signal; and, an analyser arranged to analyse the test data signal using the reference clock signal as a timing reference.

10 According to a sixth aspect of the present invention, there is provided apparatus for testing at least one of a data storage disk medium and a read/write head, the apparatus comprising: a spindle for rotating a said disk; a detector arranged to detect fluctuations in the speed of
15 rotation of the disk; a processor arranged to produce a reference clock signal in accordance with said fluctuations so as to be synchronised with the rotation of the disk; a pattern generator arranged to generate a test pattern of data using said reference clock signal as a timing
20 reference; a controller arranged to write the test pattern of data to the disk with a said head, and to subsequently read a test data from the disk with the head to provide a test data signal; and, an analyser arranged to analyse the test data signal using the reference clock signal as a
25 timing reference.

Embodiments of the present invention will now be described by way of example with reference to the accompanying drawings, in which:

30

Figure 1 shows schematically an example of a spinstand according to an embodiment of the present invention; and,

Figure 2 shows schematically an example of a spectrum analyser to be used in conjunction with the spinstand of Figure 1.

5 Referring to Figure 1, a spinstand 1 comprises an air-bearing spindle 2 driven by a motor 3 to which a disk 4 is mounted. The spinstand 1 also has a head load mechanism (not shown) for holding a head gimbal assembly 5 and positioning the read/write head 6 of the head gimbal
10 assembly 5 over the surface of the disk 4 such that data can be written to and read from a track on the surface of the disk 4. As described thus far, the spinstand 1 is of a known type.

15 The spinstand 1 has a laser encoder system 7 associated with its spindle 2 to allow the position of the spindle 2 to be accurately determined. The principles of a laser encoder system 7 are generally well known in the art and therefore will not be described in detail herein. In
20 particular, suitable laser encoder systems 7 have been developed for use with servo track writing machines, where they are used to allow successively written servo tracks to be kept in the correct phase with each other with the necessary high degree of accuracy. Nevertheless in brief,
25 the spindle has an encoder scale 7a attached thereto at its end opposite the disk 4. The scale 7a comprises a diffraction grating, i.e. a series of transitions from black to white, arranged in a ring positioned to be concentric with the axis of the spindle 2. There are
30 preferably at least thousands of such transitions around the ring, and possibly millions of transitions. The transitions preferably occur with an equiangular spacing around the ring.

-11-

A grating interferometer 7b is mounted to the spinstand 1 for reading the scale 7a on the spindle 2. The interferometer 7b has a laser (not specifically shown) which is directed onto the scale 7a to allow the transitions in the scale 7a to be detected by a detector (not specifically shown) in the interferometer 7b as they move past the laser. In this way the laser encoder system 7 produces a raw encoder signal 51 measuring the detected scale 7a.

The raw encoder signal 51 is passed to optical timing clock electronics 8 which are arranged to process the raw encoder signal 51 to produce a processed encoder signal in the form of a clock signal 52 whose edges correspond to the detected transitions of the scale 7a.

It should be noted that other arrangements are possible for measuring fluctuations in the rotation of the disk 4. For example, the scale 7a can be attached to the disk 4 or any other part that rotates with the disk 4 instead of being attached to the spindle 2. It is not necessary to use optical marks. Magnetic marks can be used. The magnetic marks can be formed on the surface of the disk 4.

The clock signal 52 is fed to a phase locked loop (PLL) 10, which produces as its output a reference clock signal 53. As will be appreciated by one skilled in the relevant art, there are many ways of implementing a PLL. Only one way will be described in detail in the following.

-12-

The PLL 10 generally extracts phase information from the clock signal 52 and modulates an oscillator clock signal 54 from a crystal oscillator 11 with the phase information to produce the reference clock signal 53. This is done by feeding back the reference clock signal 53 in a negative feedback loop. In this example, the reference clock signal 53 is first divided by a divide-by-n counter 12 to provide a divided reference signal 55. This division lowers the frequency to somewhere near the frequency of the clock signal 52. The clock signal 52 and the divided reference signal 55 are fed to a phase detector 13, which determines the phase difference between the two. The phase difference is expressed as a DC level at the detector output 56. This may be optionally filtered by a filter 14 to improve the performance of the PLL 10, as is known in the art. This filtered signal 57 is provided to a vector modulator 15 as a control signal. A vector modulator 15 has the filtered signal 57 as a first input. The crystal oscillator 11 produces the oscillator clock signal 54 which is also fed to the vector modulator 15. The vector modulator 15 is arranged to modulate the phase of the incoming oscillator signal 54 so that the phase difference between the divided reference clock signal 55 and the clock signal 52 derived from the motor 3 is reduced to or reduced towards zero.

Thus a reference clock signal 53 is generated that is modulated to take account of the fluctuations in the rotation of the disk 4 so as to be synchronised with the rotation of the disk 4.

Now, a number of standard tests made by a spinstand require using narrow band power measurements. One such

-13-

test is the so-called "overwrite" test. This test involves writing a pattern of test data having a first frequency to a track on the disk 4 with the read/write head 6, and then subsequently overwriting this first pattern with a second
5 pattern of test data having a different frequency. The track is then read back with the head 6, and the signal obtained is analysed to measure the residual signal power of the overwritten pattern at the first frequency. This is accomplished by a narrow band measurement system focussed
10 on the first frequency. It is generally desirable that the narrow band measurement system is operable over a wide frequency range with good resolution so as to be flexible enough to work with different frequencies of the data patterns written to the disk in testing. In practice, a
15 tuned spectrum analyser or apparatus similar thereto is commonly used to perform the signal analysis.

Because the residual signal power of the first frequency pattern is generally relatively low, it is
20 necessary to use a measurement system with a high dynamic range in detecting and measuring the signal. In practice, this means that the spectrum analyser needs to have a low resolution bandwidth (RBW) for this purpose. As an example, next generation spinstands are specifying a RBW of
25 down to 10 kHz. However, due to fluctuations in the speed of the motor both when writing the test data and when reading back the test data, the frequency content of the data signal ultimately read by the head can be distorted and spread. For example, for a typical motor speed error
30 of $\pm 0.001\%$, in the worst case there would be an error of $\pm 0.002\%$ in the frequency of the measurement signal (i.e., $\pm 0.001\%$ error when the pattern is written, and $\pm 0.001\%$ error when the pattern is read back). When this error is

-14-

applied to a typical written frequency signal of 500MHz, the read back frequency will vary by more than ± 10 kHz in the worse case. Thus, it is becoming increasingly problematic to implement a RBW that is narrow enough to
5 pick up the low level signal whilst coping with possible spreading of the frequency. Furthermore, this is a growing problem as the trend in the art is for the signal-to-noise ratio of modern heads 6 to become ever lower, meaning that it is necessary to further lower the width of the narrow
10 band filter in the spectrum analyser. Also, next generation spinstand equipment is expected to operate with a written frequency of up to 2.5 GHz. Thus standard techniques are becoming increasingly inadequate.

15 Similar or equivalent problems caused by fluctuation in the spindle speed arise in other types of spinstand test.

In one embodiment, the reference clock signal 53 is
20 fed to a pattern generator 16 and used as a timing reference for generating a test pattern of data 58. The test pattern of data 58 is then written to the disk 4 by the head 6. The spatial spreading of the test pattern of data 58 written to the disk 4 due to fluctuations in the
25 rotation of the disk 4 is therefore reduced. Thus, when for example conducting an overwrite test as described above, this technique of generating a test pattern of data 58 may be used in writing the first frequency pattern of test data and/or the second frequency pattern of test data
30 to the disk 4 in order to mitigate the problems of distortion and spreading of the written frequency.

-15-

The fluctuations in the rotation of the disk 4 will also affect the fidelity of the test data signal 59 (also known as the head signal 59) that is read back by the head 6. To mitigate the effects of this, in one embodiment, the analysis is performed on the test data signal 59 using the reference clock signal 53 as a timing reference so that the test data signal 59 is demodulated with the reference clock signal 53. Thus, again when conducting an overwrite test, this technique of analysing the test data signal 59 can be used to perform spectrum analysis on test data signal 59 to detect the residual signal power of the overwritten first frequency pattern of test data so as to further mitigate the problems of distortion and spreading of the written frequency when it is read back from the disk 4.

15

Figure 2 shows an example of apparatus 20 for performing spectral analysis on the test data signal 59. The spectrum analyser 20 has an input stage that works on the "super-heterodyning" principle. In this, the test data signal 59 measured by the head 6 is mixed with a selected frequency sine wave 60 in order to frequency shift the frequency of interest to an intermediate frequency (IF). In the present example, a tunable synthesiser 21 is used to generate the desired frequency 60. The test data signal 59 and the generated frequency 60 are fed to a mixer 22, where the signals are mixed in order to shift the frequency of interest to the IF. Alternatively, the tunable synthesiser can be linked to a sweep generator so as to perform a spectrum analysis across a frequency range of interest.

30

The shifted test data signal 61 is filtered by an IF filter 23 to remove unwanted heterodyning tones. The filtered signal 62 is then sampled and digitised by an

-16-

analogue-to-digital converter (ADC) 24. The clock 63 for the ADC 24 is derived from the reference clock signal 53. The reference clock signal 53 is fed to a ADC clock synthesiser 25 which generates a clock signal 63 for the ADC 24 based upon the reference clock signal 53 as a timing reference. Thus the ADC 24 is clocked by the reference clock signal 53 and so the sampled signal 64 produced by the ADC 24 rejects frequency spreading due to fluctuations in the rotation of the disk 4.

10

The sampled signal 64 is then passed to a digital signal processing (DSP) system 30. A first-in-first-out buffer 31 (FIFO) is used as the input stage to the DSP system 30 and receives the sampled signal 64. The FIFO 31 preferably has protection for crossing asynchronous clock domains so that the DSP system 30 performing the processing can be clocked at a much higher rate than the sampling frequency of the ADC 24 to improve the latency of the analyser 20. The DSP system 30 also comprises a digital down-converter 32 which receives the sampled signal 64 from the FIFO 31 and then down-converts the sampled signal 64 to a baseband signal 65. This baseband signal 65 is passed through a low pass filter 33, which acts as a RBW filter. Finally, the filtered signal 66 is passed to a power detector 34, which measures the power in the signal 66, so that this can be recorded or displayed to the user.

Since the ADC sampling clock 63 is derived from the reference clock signal 53 modulated with the speed of rotation of the disk 4, the sampled signal 64 produced by the ADC 24 rejects frequency spreading due to fluctuations in the rotation of the disk 4. This means that the spectrum analyser 20 becomes normalised to disk speed.

-17-

Nonetheless, there are other ways in which the spectrum analyser 20 can be made normalised to disk speed. For example, instead of deriving the ADC sampling clock 63 from the reference clock signal 53, the super heterodyning frequency 60 can be derived from the reference clock signal 53. This also has the effect that the sampled signal 64 produced by the ADC 24 rejects frequency spreading due to fluctuations in the rotation of the disk 4, and so
10 normalises the spectrum analyser 20 to disk speed.

In addition, referring to Figure 1, the motor controller 40, which is used to control the rotation of the disk 4, is clocked by, or otherwise locked to, the same
15 oscillator 11 that clocks the PLL 10. In this way, a single oscillator clock 54 is used as a reference for the entire testing apparatus 1,20 rather than having separate oscillator clocks in the apparatus. This avoids the effects of thermal drift between two separate clocks and
20 therefore helps avoid a source of error entering the system, both when writing data to the disk 4 and when reading back and analysing data from the disk 4.

These are only some examples of how the reference
25 clock signal 53 can be used as a timing reference for analysing the test data signal 59. Other forms are possible depending on the application.

Embodiments of the present invention have been
30 described with particular reference to the examples illustrated. However, it will be appreciated that variations and modifications may be made to the examples described within the scope of the present invention.

-18-

CLAIMS

1. A method of generating a test pattern of data to be written to a data storage disk medium for testing, the
5 method comprising:
rotating the disk;
detecting fluctuations in the speed of rotation of the disk;
producing a reference clock signal in accordance with
10 said fluctuations so as to be synchronised with the rotation of the disk; and,
generating a test pattern of data using said reference clock signal as a timing reference.
- 15 2. A method according to claim 1, wherein the step of detecting the fluctuations comprises:
detecting a plurality of marks that rotate synchronously with the disk; and,
producing a clock signal corresponding to the movement
20 of the detected marks, said clock signal being used in the production of the reference clock signal.
3. A method according to claim 2, comprising providing the clock signal as an input to a phase locked loop, the
25 phase locked loop being arranged to produce said reference clock signal.
4. A method according to any of claims 1 to 3, comprising:
30 producing an oscillator clock signal from an oscillator;
the disk being rotated with a motor that is locked to the oscillator clock signal; and,

-19-

the reference clock signal being generated using the oscillator clock signal, the oscillator clock signal being modulated in accordance with the fluctuations in the speed of rotation of the disk in the generation of the reference
5 clock signal.

5. A method of analysing a test pattern of data read from a data storage disk medium, the method comprising:
rotating the disk;
10 detecting fluctuations in the speed of rotation of the disk;
producing a reference clock signal in accordance with said fluctuations so as to be synchronised with the rotation of the disk;
15 reading a test pattern of data from the disk to provide a test data signal; and,
analysing said test data signal using the reference clock signal as a timing reference.

20 6. A method according to claim 5, wherein the step of detecting the fluctuations comprises:
detecting a plurality of marks that rotate synchronously with the disk; and,
producing a clock signal corresponding to the movement
25 of the detected marks, said clock signal being used in the production of the reference clock signal.

7. A method according to claim 6, comprising providing the clock signal as an input to a phase locked loop, the
30 phase locked loop being arranged to produce said reference clock signal.

8. A method according to claim 6 or claim 7, comprising:

-20-

producing an oscillator clock signal from an oscillator;

the disk being rotated by a motor that is locked to the oscillator clock signal; and,

5 the reference clock signal being generated using the oscillator clock signal, the oscillator clock signal being modulated in accordance with the fluctuations in the speed of rotation of the disk in the generation of the reference clock signal.

10

9. A method according to any of claims 5 to 8, wherein the step of analysing comprises spectrum analysing said test data signal.

15 10. A method according to claim 9, wherein the step of spectrum analysing comprises converting said test data signal to an intermediate frequency by mixing said test data signal with a sinusoidal signal generated using said reference clock as a timing reference.

20

11. A method according to claim 9, wherein the step of spectrum analysing comprises sampling and digitising the test data signal using an analogue-to-digital converter that is clocked by the reference clock.

25

12. A method of testing at least one of a data storage disk medium and a read/write head, the method comprising:

rotating the disk;

detecting fluctuations in the speed of rotation of the

30 disk;

producing a reference clock signal in accordance with said fluctuations so as to be synchronised with the rotation of the disk;

-21-

generating a test pattern of data using said reference clock signal as a timing reference;

writing the test pattern of data to the disk with the head;

5 reading the test pattern of data from the disk with the head to provide a test data signal; and,

analysing said test data signal using the reference clock signal as a timing reference.

10 13. A method according to claim 12, wherein the step of detecting the fluctuations comprises:

detecting a plurality of marks that rotate synchronously with the disk; and,

15 producing a clock signal corresponding to the movement of the detected marks, said clock signal being used in the production of the reference clock signal.

14. A method according to claim 13, comprising providing the clock signal as an input to a phase locked loop, the
20 phase locked loop being arranged to produce said reference clock signal.

15. A method according to any of claims 12 to 14, wherein the step of analysing comprises spectrum analysing said
25 test data signal.

16. A method according to any of claims 12 to 15, comprising:

1) the disk being rotated by a motor that is locked to
30 an oscillator clock signal; and,

2) the reference clock signal being generated using an oscillator clock signal, the oscillator clock signal being modulated in accordance with the fluctuations in the speed

-22-

of rotation of the disk in the generation of the reference clock signal,

wherein the same oscillator clock signal is used for steps 1) and 2).

5

17. Apparatus for generating a test pattern of data to be written to a data storage disk medium for testing, the apparatus comprising:

a spindle for rotating a said disk;

10 a detector arranged to detect fluctuations in the speed of rotation of the disk;

a processor arranged to produce a reference clock signal in accordance with said fluctuations so as to be synchronised with the rotation of the disk; and,

15 a pattern generator arranged to generate a test pattern of data using said reference clock signal as a timing reference.

18. Apparatus according to claim 17, comprising:

20 a plurality of marks arranged to rotate synchronously with the disk, the reader being arranged to read said marks;

a decoder arranged to produce a clock signal corresponding to the movement of the detected marks, said
25 clock signal being used in the production of the reference clock signal.

19. Apparatus according to claim 18, comprising a phase locked loop arranged to receive the clock signal as an

30 input, the phase locked loop being arranged to produce said reference clock signal.

-23-

20. Apparatus according to claim 18 or claim 19,
comprising:

an oscillator arranged to produce an oscillator clock
signal;

5 a motor that is arranged to rotate the disk and is
locked to the oscillator clock signal,

wherein the processor is arranged to generate the
reference clock signal using the oscillator clock signal by
the oscillator clock signal being modulated in accordance
10 with the fluctuations in the speed of rotation of the disk.

21. Apparatus for analysing a test pattern of data read
from a data storage disk medium, the apparatus comprising:

a spindle for rotating a said disk;

15 a detector arranged to detect fluctuations in the
speed of rotation of the disk;

a processor arranged to produce a reference clock
signal in accordance with said fluctuations so as to be
synchronised with the rotation of the disk;

20 a controller arranged to read a test pattern of data
from the disk to provide a test data signal; and,

an analyser arranged to analyse the test data signal
using the reference clock signal as a timing reference.

25 22. Apparatus according to claim 21, comprising:

a plurality of marks arranged to rotate synchronously
with the disk, the reader being arranged to read said
marks;

a decoder arranged to produce a clock signal
30 corresponding to the movement of the detected marks, said
clock signal being used in the production of the reference
clock signal.

-24-

23. Apparatus according to claim 22, comprising a phase locked loop arranged to receive the clock signal as an input, the phase locked loop being arranged to produce said reference clock signal.

5

24. Apparatus according to any of claims 21 to 23, comprising:

an oscillator arranged to produce an oscillator clock signal;

10 a motor that is arranged to rotate the disk and is locked to the oscillator clock signal,

wherein the processor is arranged to generate the reference clock signal using the oscillator clock signal by the oscillator clock signal being modulated in accordance
15 with the fluctuations in the speed of rotation of the disk.

25. Apparatus according to any of claims 21 to 24, wherein the analyser is arranged to spectrum analyse said test data signal.

20

26. Apparatus according to claim 25, wherein the analyser comprises a mixer arranged to convert said test data signal to an intermediate frequency by mixing said test data signal with a sinusoidal signal generated using said
25 reference clock as a timing reference.

27. Apparatus according to claim 25, wherein the analyser comprises an analogue-to-digital converter that is clocked by the reference clock and arranged to sample and digitise
30 the test data signal.

-25-

28. Apparatus for testing at least one of a data storage disk medium and a read/write head, the apparatus comprising:

- a spindle for rotating a said disk;
- 5 a detector arranged to detect fluctuations in the speed of rotation of the disk;
- a processor arranged to produce a reference clock signal in accordance with said fluctuations so as to be synchronised with the rotation of the disk;
- 10 a pattern generator arranged to generate a test pattern of data using said reference clock signal as a timing reference;
- a controller arranged to write the test pattern of data to the disk with a said head, and to subsequently read
- 15 a test data from the disk with the head to provide a test data signal; and,
- an analyser arranged to analyse the test data signal using the reference clock signal as a timing reference.

20 29. Apparatus according to claim 28, comprising:

- a plurality of marks arranged to rotate synchronously with the disk, the reader being arranged to read said marks;
- a decoder arranged to produce a clock signal
- 25 corresponding to the movement of the detected marks, said clock signal being used in the production of the reference clock signal.

30. Apparatus according to claim 29, comprising a phase
30 locked loop arranged to receive the clock signal as an input, the phase locked loop being arranged to produce said reference clock signal.

-26-

31. Apparatus according to any of claims 28 to 30, wherein the analyser is arranged to spectrum analyse said test data signal.

5 32. Apparatus according to any of claims 28 to 31, comprising:

an oscillator arranged to produce an oscillator clock signal;

a motor that is arranged to rotate the disk and is
10 locked to the oscillator clock signal,

wherein the processor is arranged to generate the reference clock signal using the oscillator clock signal by the oscillator clock signal being modulated in accordance with the fluctuations in the speed of rotation of the disk,
15 the oscillator clock signal used by the motor and by the processor being the same.

1/2

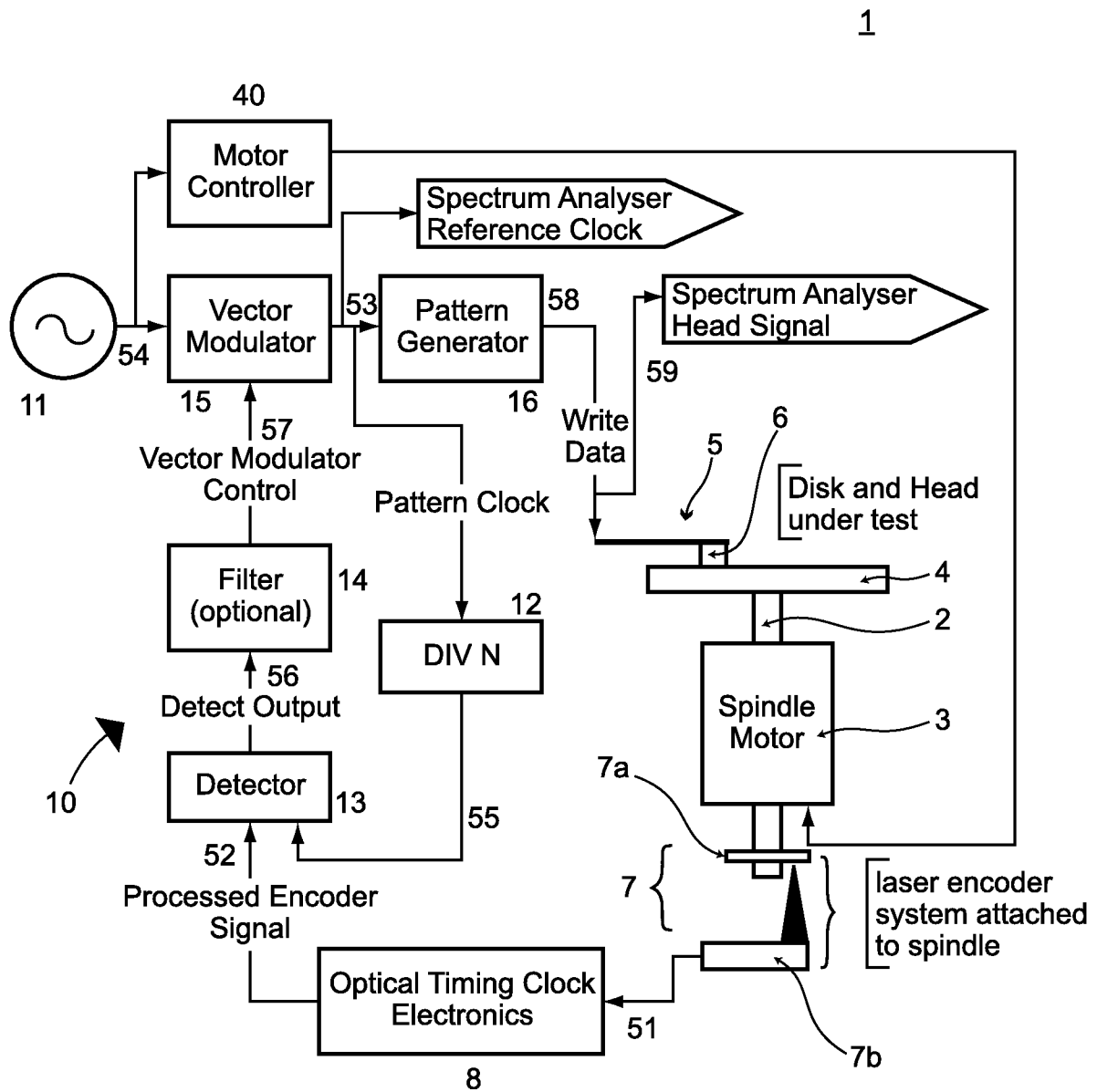


Fig. 1

2/2

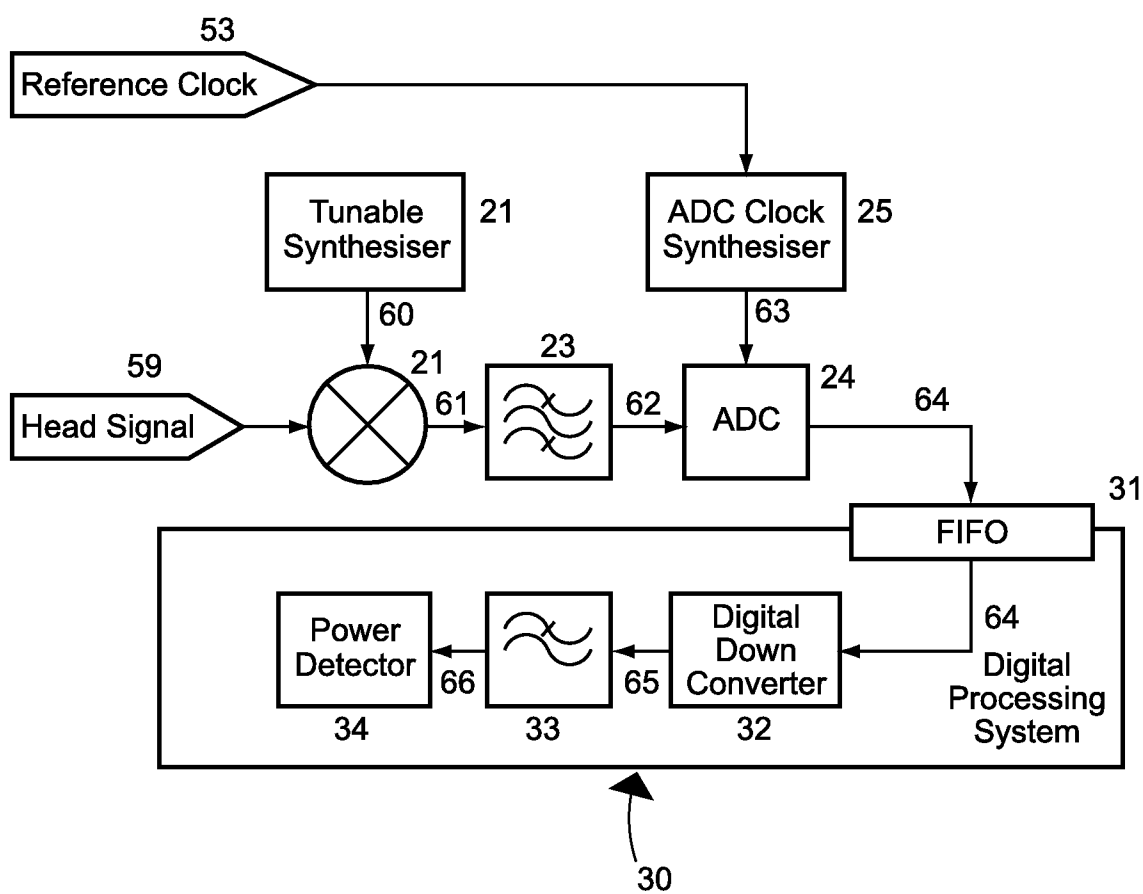
20

Fig. 2

INTERNATIONAL SEARCH REPORT

International application No
PCT/EP2008/055988

A. CLASSIFICATION OF SUBJECT MATTER
INV. G11B5/596 G11B19/28

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
G11B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 7 126 776 B1 (WARREN JR ROBERT W [US] ET AL) 24 October 2006 (2006-10-24) column 1, line 58 - line 67 column 3, line 38 - column 4, line 49	1-32
X	EP 1 182 656 A (ST MICROELECTRONICS INC [US]) 27 February 2002 (2002-02-27)	1-3, 5-7, 12-14, 17-19, 21-23, 28-30
Y	paragraph [0010] - paragraph [0012]; figure 4	4, 8-11, 15, 16, 20, 24-27, 31, 32
	----- -/-	

☒ Further documents are listed in the continuation of Box C.

☒ See patent family annex.

* Special categories of cited documents:

- *A* document defining the general state of the art which is not considered to be of particular relevance
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Date of the actual completion of the international search

9 September 2008

Date of mailing of the international search report

22/09/2008

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INTERNATIONAL SEARCH REPORT

International application No

PCT/EP2008/055988

C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5 477 103 A (ROMANO PAUL M. [US] ET AL) 19 December 1995 (1995-12-19)	1-3, 5-7, 12-14, 17-19, 21-23, 28-30
Y	column 3, lines 17-40	4, 8-11, 15, 16, 20, 24-27, 31, 32
A	----- "SPINDLE RETATION VELOCITY MONITOR BY SECTOR INTERVAL ANALYSIS" IBM TECHNICAL DISCLOSURE BULLETIN, IBM CORP. NEW YORK, US, vol. 36, no. 5, 1 May 1993 (1993-05-01), page 69, XP000408914 ISSN: 0018-8689 the whole document -----	1-32

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No

PCT/EP2008/055988

Patent document cited in search report		Publication date	Patent family member(s)	Publication date
US 7126776	B1	24-10-2006	NONE	
EP 1182656	A	27-02-2002	JP 2002133771 A US 6583944 B1	10-05-2002 24-06-2003
US 5477103	A	19-12-1995	NONE	