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[US/US]; 17454 NW Gilbert Lane, Portland, OR 97229 (US).

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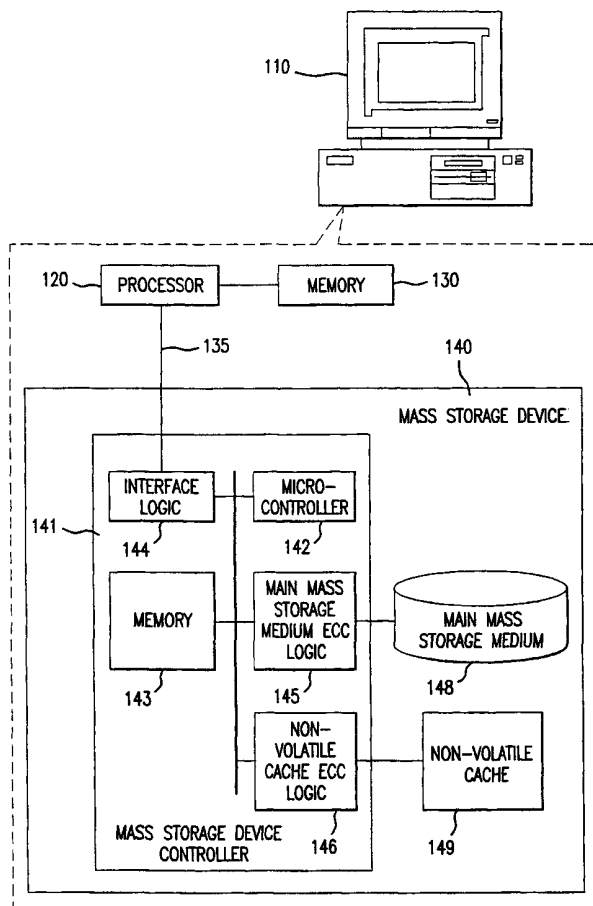
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(75) Inventor/Applicant (for US only): COULSON, Richard

[Continued on next page]

(54) Title: NON-VOLATILE CACHE INTEGRATED WITH MASS STORAGE DEVICE

(57) Abstract: Apparatus and methods relating to a non-volatile mass storage device including a non-volatile cache.



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INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 G06F12/08

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, PAJ

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	PATENT ABSTRACTS OF JAPAN vol. 018, no. 621 (P-1832), 25 November 1994 (1994-11-25) & JP 06 236241 A (SHARP CORP), 23 August 1994 (1994-08-23) abstract ---	1-6, 8-15, 18, 19
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A	US 5 615 353 A (LAUTZENHEISER MARVIN) 25 March 1997 (1997-03-25) column 9, line 14 - line 57; figure 3 -----	1,7, 15-17

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Patent family members are listed in annex.

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Information on patent family members

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