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(54) **A HIGH VOLTAGE DC BREAKER APPARATUS**

GLEICHSTROM-LEISTUNGSSCHALTER FÜR DIE HOCHSPANNUNG

DISJONCTEUR HAUTE TENSION À COURANT CONTINU

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Description

TECHNICAL FIELD

[0001] The present invention relates to a DC breaker apparatus configured to interrupt a fault current occurring in a high voltage DC conductor.

BACKGROUND

[0002] A high voltage DC breaker apparatus may be arranged in a high DC voltage carrying system, for example in Voltage Source Converter based multi-terminal HVDC (High Voltage Direct Current) networks where fast-rising DC fault currents must be interrupted quickly at high voltages. Such faults may be line-to-line faults or line-to-ground faults, i.e. short-circuits of the DC conductor to earth. By high voltage is here meant a voltage ≥ 10 kV (and often a voltage of several hundreds of kV) with respect to ground.

[0003] In one type of known such breaker apparatuses said arrangement configured to obtain zero-crossing of a fault current through the interrupter comprises a passive LC-circuit generating an oscillation of fault current through the interrupter with the aim to obtaining a zero-crossing of the fault current. A disadvantage of using such an arrangement is the uncertainty as to when zero-crossing of the current will actually occur, which results in a risk of failing the arc extinguishing window when interruption of the current is possible, so that interruption of the fault current may not be reliably ensured.

[0004] Another type of known high voltage DC breaker apparatuses uses a said arrangement having a pre-charged capacitor connected in parallel with the mechanical interrupter for obtaining current-zero-crossing of the fault current. However, this solution is comparatively costly, since a large capacitor as well as a separate high voltage charging device for charging the capacitor are needed.

[0005] US 5 517 378 discloses a high voltage DC breaker which has two mechanical breakers adapted to be traversed by the line current and to be opened for breaking the current. A capacitor is connected in parallel with the series connection of the breakers. A semiconductor member is connected in parallel with a first one of the breakers. Upon opening of the breakers, a control member controls the semiconductor member such that a zero crossing of the current through the second breaker is obtained, whereby the line current is commutated over to the capacitor.

SUMMARY

[0006] The object of the present invention is to provide a DC breaker apparatus which can provide a reliable breaking of a high DC current at a higher speed.

[0007] This object is obtained by providing a DC breaker apparatus configured to interrupt a fault current occur-

ring in a high voltage DC conductor, and comprising the features of claim 1.

[0008] Compared to the DC breaker of US 5 517 378, the inventive DC breaker apparatus can be faster, since no commutation of the current into the semiconductor device used for switching is necessary.

[0009] By using the fault current to charge the capacitance, no high voltage charging device is needed, but a well defined zero-crossing of the fault current will still be obtained by an appropriate control of said switching of the semiconductor device. Furthermore, only a small capacitance will be needed for obtaining said zero-crossing of the fault current.

[0010] Thanks to the fact that said semiconductor device only needs to be rated for a voltage in the order of the protective voltage level of said arrester, which may be only a small fraction of the system voltage, i.e. the voltage of a said high voltage DC conductor with respect to ground, investment costs will be low. The on-state losses in said semiconductor device will also be low under normal operation of the high voltage DC system in question.

[0011] Another advantage of an apparatus according to the invention is that a gradually growing current oscillation through the interrupter prevents current zero overshoot and results in a feasible switching duty for the mechanical interrupter, i.e. a low enough current and voltage time derivative. The protective voltage level of said arrester and the values of said inductance and capacitance of the LC-circuit may be selected for obtaining secure interruption by the mechanical interrupter within an acceptable period of time after occurrence of a said fault. Accordingly, the generation of current zero-crossings for obtaining interruption of the fault current is dealt with by the semiconductor device, whereas interrupting of the fault current is taken care of by the mechanical interrupter. This combines the benefits of the semiconductor device of fast switching and well-defined blocking voltage with the advantages of a mechanical interrupter relating to high dielectric withstand capability and low losses.

[0012] According to one embodiment, the switching frequency is fixed and set in accordance with the eigenfrequency of the LC circuit. According to the invention the apparatus further comprises at least one measuring device configured to measure at least one parameter relating to the operation of said arrangement upon occurrence of a said fault and send the result of this measurement to said control unit configured to adapt said switching of this measurement result. Such a feedback for the switching of the semiconductor device results in a possibility to control how and when zero-crossing of the fault current will appear for obtaining reliable interruption of the fault current. Suitable parameters to measure may be the current through said semiconductor device and/or the voltage across said capacitance, which constitutes further embodiments of the invention.

[0013] The arrangement configured to obtain zero crossing can advantageously comprise a rectifying ele-

ment connected in anti-parallel with a said at least one semiconductor device. In one implementation, the rectifying element is a diode. In order to obtain a bi-directional DC current breaker, the arrangement could include a series connection of two semiconductor devices of opposite conducting directions, where each of the semiconductor devices are connected in parallel with a diode of opposite conducting direction to that of the semiconductor device. In this configuration, the two diodes (which are anti-parallel) are connected in series. In an alternative implementation of the arrangement to obtain zero crossing, a rectifying element in the form of an additional semiconductor device is provided, so that two semiconductor devices are connected in anti-parallel, thus facilitating for bi-directional breaking possibilities.

[0014] According to an embodiment of the invention the control unit is configured to carry out said switching of the semiconductor device at a frequency being 90%-110% of the eigenfrequency of said LC-circuit. A switching frequency close to the eigenfrequency of the LC-circuit can result in a desired oscillation of the fault current through the interrupter and reaching of zero-crossing of the fault current when the current derivative is zero or close to zero, so that it will be favourable to then interrupt the current.

[0015] According to an embodiment of the invention said control unit is configured to delay initiation of separation of the mechanical contacts of the interrupter by a determined period of time with respect to the start of the switching of said semiconductor device for possibly refraining from opening the interrupter should said control unit receive information about a disappearance of said fault within this period of time. It has turned out that it is well possible to obtain interruption even if such a delay is used, which may then result in avoidance of unnecessary interruption in the case of a quick disappearance of the fault condition.

[0016] According to an embodiment of the invention the apparatus comprises a further arrester connected in parallel with said LC-circuit. This further arrester will limit the rising recovery voltage across the mechanical interrupter after interruption of the fault current.

[0017] According to an embodiment of the invention the protective voltage level of said arrester connected in parallel with the LC-circuit is at least 120% or 130%-200% of the DC voltage intended for a said DC conductor with respect to ground. A protective voltage level of about 150% of the system voltage is normally suitable for an arrester in parallel with such a mechanical interrupter.

[0018] According to an embodiment of the invention the apparatus comprises a current derivative limiting reactor configured to be connected in series with said parallel connection of the LC-circuit and the series connection of the semiconductor device and the mechanical interrupter in said DC conductor. Such a reactor will limit the rise of the fault current.

[0019] According to an embodiment of the invention said control unit is configured to carry out said switching

of the semiconductor device with a frequency of 100 Hz - 10 kHz or 500 Hz - 5 kHz. Such switching frequencies are suitable for the operation of a switching device in an apparatus of this type, in which the inductance may typically have a value of 0.1 mH - 10 mH and the capacitance a value of 10 nF - 100 pF or 0.5 pF - 5 pF.

[0020] According to an embodiment of the invention the apparatus is configured to interrupt a fault current occurring in a high voltage DC conductor intended to be at a voltage level of ≥ 10 kV with respect to ground, for example at a voltage level within one of the following ranges: 10 kV - 1000 kV, 100 kV - 1000 kV or 300 kV - 1000 kV with respect to ground, or higher.

[0021] According to an embodiment of the invention the protective level of said arrester connected in parallel with said semiconductor device is less than 50% or less than 10% of the voltage level intended for said high voltage DC conductor with respect to ground. It may be mentioned that said protective voltage level may for instance be about 10 kV at the same time as the voltage level intended for said high voltage DC conductor with respect to ground may be 400 kV.

[0022] According to an embodiment of the invention the apparatus is configured to be connected to a said high voltage DC conductor in an AC/DC converter station, and according to another embodiment the apparatus is configured to be arranged in a DC grid for protecting equipment connected therein.

[0023] The invention also relates to a plant for transmitting electric power through High Voltage Direct Current, which is characterized in that it is provided with a DC breaker apparatus according to the invention. Such a plant may benefit from the positive features mentioned above of such an apparatus.

[0024] The invention also relates to a method for controlling a high voltage DC breaker apparatus according to the invention so as to interrupt a fault current detected in said high voltage DC conductor as well as a computer program product and a computer readable medium associated with such a method.

[0025] Further advantages as well as advantageous features of the invention will appear from the following description.

BRIEF DESCRIPTION OF THE DRAWING

[0026] With reference to the appended drawing, below follows a specific description of an embodiment of the invention cited as examples.

[0027] In the drawing:

Fig. 1 is a schematic view of a high voltage DC breaker apparatus according to an embodiment of the invention,

Fig. 2a-c schematically illustrate different embodiments of an arrangement for obtaining current-zero-crossing

- Fig.3 is a graph of the fault current I through the mechanical interrupter of the apparatus according to Fig. 1 versus time t upon occurrence of a fault in the high voltage DC conductor shown in Fig. 1,
- Fig. 4 is a graph of the voltage U across the semiconductor device of the apparatus shown in Fig. 1 versus time t upon occurrence of a said fault, and
- Fig. 5 is a flowchart schematically illustrating an embodiment of a method performed by a control unit for controlling the operation of a DC breaker apparatus comprising an arrangement for obtaining current-zero-crossing.

DETAILED DESCRIPTION OF AN EMBODIMENT OF THE INVENTION

[0028] Fig. 1 shows schematically a high voltage DC breaker apparatus 100 according to an embodiment of the present invention. This DC breaker apparatus 100 is configured to interrupt a fault current occurring in a high voltage DC conductor 1 and comprises a mechanical interrupter 2 which may stand for one or several mechanical interrupters, e.g. an SF6- or vacuum-interrupter or a combination thereof, and connected in series therewith a semiconductor device 3 of turn-off type (i.e. a device which can be controlled to switch off, as well as to switch on). In the embodiment of Fig. 1, the semiconductor device 3 is an IGBT which is configured to conduct in the direction towards the interrupter. A rectifying diode 4 is connected in anti-parallel with the semiconductor device 3, so that the conducting direction of the rectifying diode is opposite to the conducting direction of the semiconductor device. An arrester 5 is connected in parallel with the semiconductor device for defining a maximum voltage across this semiconductor device by having a suitable protective voltage level. In one implementation, the protective voltage level is 10 kV. However, other values can be used, and in particular, the range of suitable levels will vary with the voltage rating of the breaker apparatus.

[0029] Semiconductor device 3 of Fig. 1 is an IGBT (Insulated Gate Bipolar Transistor) device. Other types of semiconductor devices of turn-off type could alternatively be used in semiconductor device 3, such as GTO:s (Gate Turn-Off thyristors) and IGCT:s (Insulated Gate Commutated Thyristors). Semiconductor device 3 could be implemented as a single semiconductor element, or as a series and/or parallel connection of more than one semiconductor element.

[0030] The blocking voltage capability of the semiconductor device 3 shall be higher than this protective voltage level. Accordingly, the arrester 5 ensures that the voltage across the semiconductor device will never reach the level of the voltage blocking capability of the semi-

conductor device when this is turned off.

[0031] Furthermore, an LC-circuit 6 in the form of a series connection of an inductance 7 and a capacitance 8 is connected in parallel with the series connection of the semiconductor device 3 and the mechanical interrupter 2. A further arrester 9 is connected in parallel with the LC-circuit 6, and this arrester has a protective voltage level exceeding the level of the DC voltage intended for said DC-conductor 1 with respect to ground and is for example 150% of that voltage level.

[0032] The DC breaker apparatus 100 also comprises an arrangement 10 configured to obtain zero-crossing of a fault current through the interrupter 2 upon occurrence of a fault in the DC conductor 1 or in equipment connected to the DC conductor, and this arrangement will partly be formed by components of the apparatus already described. In the following, this arrangement will be referred to as an arrangement 10 for obtaining current-zero-crossing. A current-zero-crossing is an instance in time where the current through the mechanical interrupter 2 is reversed. Arrangement 10 of Fig. 1 is shown to include the parallel connection of the semiconductor device 3, the rectifying diode 4 and the arrester 5, as well as the a pulse generator 13 connected to the control terminal of the semiconductor device 3, and arranged to generate a pulse signal for turning on and turning off the semiconductor device 3. The control terminal of an IGBT is typically the gate terminal, while the control terminal of other types of semiconductor devices could e.g. be the gate or the base terminal. The arrangement 10 for obtaining current-zero-crossing further includes the LC circuit 6.

[0033] Arrangement 10 of Fig. 1 is further shown to include a current measurement device arranged to measure the current through the semiconductor device 3 and a voltage measurement device 15 arranged to measure the voltage across the capacitor 8.

[0034] The output of current measurement device 14 and the voltage measurement device 15 of Fig. 1 are connected to the pulse generator 13, possibly via a control unit 12, so that the frequency of the pulses generated by pulse generator 13 and provided to the control terminal of the semiconductor device 3 can, in one embodiment, be adjusted in dependence on the current through the semiconductor device 3 and the voltage across the capacitor 8. Such adjustment of the switching frequency will be further described in relation to Fig. 5.

[0035] The current measurement device 14 could for example be an optical current transducer (OCT) or a DC current feedback compensation transducer (DCCT), or any other suitable current transducer, sensor or other current measurement means. The voltage measurement devices 15 could for example be direct voltage divider, or any other suitable voltage measurement means.

[0036] DC breaker apparatus 100 of Fig. 1 further comprises means arranged and configured to detect occurrence of a fault current in said DC conductor 1, said means shown in Fig. 1 as fault-current detector 11. Fault-current detector 11 could for example include an optical

current transducer (OCT) or a DC current feedback compensation transducer (DCCT), or any other suitable current transducer, sensor or means for detecting a fault current. Fault-current detector 11 could for example further include hardware or software for comparing a measurement (or a set of measurements) from the current transducer with a current and/or current derivative threshold, in order to detect a fault which causes, or is at risk to cause, the current through the DC breaker apparatus 100 to increase beyond an acceptable level. Fault-current detector 11 could furthermore be arranged to generate a signal in response to detection of a fault current. The output at which such signal will be generated can advantageously be connected to an input of a control unit 12.

[0037] The DC breaker apparatus 100 of Fig. 1 comprises a control unit 12 configured to control the arrangement 10 for obtaining current-zero-crossing as well as for controlling the mechanical interrupter 2. The control unit 12 is configured to control switching of the semiconductor device 3, in case a fault has been detected, by sending control signals to the pulse generator 13. The switching induced by the pulse generator 13 is then carried out at a frequency adapted to the values of the inductance 7 and capacitance 8 for charging said capacitance by the fault current as will be described more in detail further below. In Fig. 1, it is illustrated that the DC breaker apparatus 100 may also have measuring means, said measuring means shown in Fig. 1 as current measurement device 14 for measuring the current through the semiconductor device 3 and voltage measuring device 15 for measuring the voltage across the capacitance 8, as discussed above. Voltage and current measurement devices 14, 15 of Fig. 1 are configured to send measurement results to the control unit 12 (pulse generator 13), which may then consider these results when controlling the switching of the semiconductor device 3. In Fig. 1, pulse generator 13 has, for illustrative purposes, been illustrated as a separate unit to control unit 12. However, pulse generator 13 is often seen as part of the control unit 12.

[0038] The DC breaker apparatus 100 of Fig. 1 also includes a current derivative limiting reactor 16 connected in series with the parallel connection of the LC-circuit 6 and the series connection of the semiconductor device 3 and the mechanical interrupter 2 in said DC conductor 1. In Fig. 1, the current derivative limiting reactor 16 is connected upstream this parallel connection as seen in the conducting direction of the semiconductor device. However, the reactor 16 could alternatively be connected downstream this parallel connection. A current limiting device in the form of at least one module of a parallel connection of a semiconductor device of turn-off type, such as an IGBT, and an arrester may be connected in series with the reactor 16 and assist this in limiting a fault current or even replace the reactor.

[0039] Possible values of components of the DC breaker apparatus 100 of Fig. 1 may be as follows: the reactor

16 has an inductance of 100 mH and the inductance 7 an inductance of 0.6 mH. The capacitance 8 is 1 μ F. The DC conductor 1 is at a voltage of 320 kV with respect to ground and the protective level of the arrester 5 is 10 kV.

These values are given as examples only, and other values of the components could be used for a 320 kV DC breaker apparatus. Furthermore, the suitable value ranges depend on the voltage rating of the DC breaker apparatus 100, and when designing a DC breaker apparatus of a different voltage rating, the values of the components could advantageously be adjusted accordingly.

[0040] It is pointed out that the DC breaker apparatus 100 as shown in Fig. 1 is configured to interrupt a fault current upon occurrence of a fault 17 on the right side of the interrupter as seen in the drawing, but it is of course within the scope of the invention to modify the DC breaker apparatus 100 so as to instead be able to take care of a fault occurring on the left side of the interrupter and also so as to be able to take care of faults occurring on both sides of the DC breaker apparatus 100, which for example may be obtained by connecting a further parallel connection of an arrester and a semiconductor device of turn-off type in series with the parallel connection shown in Fig. 1 and with that semi-conductor device having opposite conducting direction. Examples of such alternative embodiments of the arrangement 10 will be further described in relation to Figs. 2a-c. In order to simplify the drawings, the LC circuit 6 of arrangement 10 has been left out in Figs. 2a-c. Another option would be to have a breaker configuration in an adjacent switch gear preventing current in more than one direction.

[0041] Fig. 2a shows an embodiment of the arrangement 10 for obtaining current-zero-crossing, wherein the arrangement 10 comprises a parallel connection of a semiconductor device 3 of turn-off type, a rectifying diode 4 and an arrester 5. The arrangement 10 of Fig. 2a differs from the arrangement 10 of Fig. 1 in that the semiconductor device 3 and the rectifying diode 4 of Fig. 2a are arranged to conduct in the opposite direction to that of the corresponding components of Fig. 1. The arrangement 10 of Figs. 1 and 2a are capable of inducing an oscillation giving rise to a current-zero-crossing for DC currents flowing in the conducting direction of the semiconductor device 3. Hence, an arrangement 10 of Fig. 1 or Fig. 2a can be useful in applications where a DC current will only flow in one direction, e.g. in a mono-directional DC breaker apparatus 100, or at least, where the capability of breaking a DC current will only be required in relation to DC currents of a single direction. The arrangements 10 of Fig. 1 and Fig. 2a could be connected either so that the semiconductor device 3 will be conducting in the direction towards the mechanical interrupter 2, or so that the semiconductor device 3 will be conducting in the direction away from the mechanical interrupter 2 along the series connection of the semiconductor device 3 and the mechanical interrupter 2.

[0042] Figs. 2b and 2c each show an embodiment of the arrangement 10 wherein the arrangement 10 is ca-

pable of inducing an oscillation giving rise to a current-zero-crossing for DC currents flowing in both directions of the DC conductor 1. Hence, the embodiments of arrangement 10 shown in Figs. 2b and 2c could advantageously be used in a bidirectional DC breaker apparatus 100.

[0043] The arrangement 10 of Fig. 2b comprises a parallel connection of a first semiconductor device 3a of turn-off type and a second semiconductor device 3b of turn-off type, where the first and second semiconductor devices 3a and 3b are capable of conducting current in opposite directions, the semiconductor devices 3a and 3b thus being connected in anti-parallel. The parallel connection of the two semiconductor devices is connected in parallel with an arrester 5. When an excessive current has been detected by fault-current detector 11 in a first current direction, the semiconductor device which is capable of conducting in this first direction will be switched on and off in order to introduce an oscillation (the semiconductor device of the opposite current direction could in the meantime be switched on and off at the same frequency, or could be turned on or off as desired). A first current measurement device 14a is arranged to measure the current in the first semiconductor device 3a, while a second current measurement device 14b is arranged to measure the current in the second semiconductor device 3b. Current measurement devices 14a and 14b are connected to an input of pulse generator 13, in order to allow for adjustment of the switching in dependence on the current through the superconductor devices.

[0044] The arrangement 10 of Fig. 2c comprises two semiconductor devices 3a and 3b which are connected in series, and which are arranged to conduct current in opposite directions. Each semiconductor device 3a, 3b has a rectifying diode 4a, 4b, respectively, connected in anti-parallel. Furthermore, an arrester 5 is connected in parallel with the series connection of semiconductor devices 3a, 3b. When an excessive current has been detected by fault-current detector 11 in a first current direction, the semiconductor device which is capable of conducting in this first direction will be switched on and off in order to introduce an oscillation (the semiconductor device of the opposite current direction could in the meantime be switched on and off at the same frequency, or could be turned on or off as desired). As in the arrangement 10 of Fig. 2b, a first current measurement device 14a is arranged to measure the current in the first semiconductor device 3a, while a second current measurement device 14b is arranged to measure the current in the second semiconductor device 3b. Current measurement devices 14a and 14b are connected to an input of pulse generator 13, in order to allow for adjustment of the switching in dependence on the current through the superconductor devices.

[0045] In the following, the term forward-conducting semiconductor device 3 will be used to refer to a semiconductor device which is capable of conducting current in the direction of a DC current to be broken and which

therefore can be used for obtaining an oscillating current through the mechanical interrupter 2, in order to simplify the description in relation to a DC breaking apparatus including semiconductor devices 3a, 3b of opposite conducting directions. Which semiconductor device that acts as the forward-conducting semiconductor device of such DC breaking apparatus depends on the direction of the DC current flowing through the conductor 1. The semiconductor device which is currently not the forward-conducting semiconductor device could, as mentioned above, be switched at the same frequency as the forward-conducting semiconductor device, in order to simplify the control of the switching. Reference numeral 3 will be used when generally referring to a semiconductor device, thus including semiconductor devices 3, 3a and 3b; reference numeral 14 will be used when generally referring to a current measurement device, thus including current measurement devices 14, 14a or 14b; etc.

[0046] An example of the operation of the DC breaker apparatus 100 in the case of occurrence of a fault will now be explained while making reference also to Figs. 3 and 4. Fig. 3 illustrates the fault current I through the mechanical interrupter 2 as a function of time t in a fault scenario, while Fig. 4 illustrates the voltage across the forward-conducting semiconductor device 3 as a function of time t in the same scenario.

[0047] During normal operation, the forward-conducting semiconductor device 3 and the mechanical interrupter 2 will be closed. A current, for example a current of magnitude in the order of 2000 A, will flow in the DC conductor 1 through the forward-conducting semiconductor device 3 and the mechanical interrupter 2. The energy transfer loss will be comparatively low in the semiconductor device 3, since the semiconductor device 3 has only to be rated for a value somewhat higher than the protective voltage level of the arrester 5. Hence, semiconductor device 3 could typically be implemented without having to use a large number of semiconductor elements.

[0048] When a fault 17 occurs on the DC line, which may be a line-to-line fault or a line-to-ground fault, this is detected by the fault-current detector 11 and information thereabout is sent to the control unit 12. This occurs at the time t_1 shown in Figs. 3 and 4. After the fault has occurred, the fault current through the interrupter rises at a rate given by the ratio between the system voltage of the DC conductor and the reactance of the fault current derivative limiting reactor 16. The rise of the current takes place until the time t_2 , at which time the control unit 12 controls the mechanical interrupter 2 to trip. Upon tripping of the mechanical interrupter 2, the mechanical contacts thereof start to separate, so that an arc is formed therebetween. Time t_2 can for example occur when a protection time t_p has lapsed since the fault was detected, so that $t_2 = t_1 + t_p$. The duration of T_p could for example be 2 ms. In another embodiment, T_p is essentially zero, so that the tripping of the mechanical interrupter is performed as soon as a fault current which requires tripping

has been detected. The control unit 12 further triggers the switching of the forward-conducting semiconductor device 3 at a frequency close to the eigenfrequency of the LC-circuit 6, so that the capacitance 8 will be charged by the fault current through the interrupter 2. The fault current will then oscillate with an increasing amplitude as shown in Fig. 3, this oscillation caused by the switching of the forward-conducting semiconductor device 3, and the voltage across the capacitance 8 will increase by each firing pulse sent to the forward-conducting semiconductor device 3 until current zero 0 is reached by the fault current through the interrupter 2. In the scenario illustrated by Figs. 3 and 4, the switching of the forward-conducting semiconductor device 3 is initiated at the same time as the tripping of the mechanical interrupter 2.

[0049] The switching of the forward-conducting semiconductor device 3 can advantageously be performed such that the zero-crossing is reached at a point in time when the current changes direction so that the current derivative is zero. A current derivative which is favourable for current-interruption is also obtained when the amplitude of the oscillating part of the current is close to, but slightly higher than, the DC fault current, that is, when the minor current loop of the total current is small (the minor current loop being the part of the total fault current which flows in the direction opposite to the DC part of the fault current). An example of a criterion that could be used for defining when the current derivative is favourable is that the minor current loop is smaller than approximately 5-10% of the total amplitude of the oscillating component of the current. Another way of expressing such a criterion is that the magnitude of the oscillating part of the current through the interrupter 2 should be larger than the magnitude of DC part of this current, but only by a small amount, such as by around 10-20%. By providing for current zero-crossing at a current magnitude (absolute value) maximum, proper interruption of the current through the interrupter is facilitated. Once the current through the mechanical interrupter 2 has been interrupted, the interrupter 2 will withstand the rising recovery voltage which will be limited by the further arrester 9 connected in parallel with the LC-circuit 6.

[0050] The presence of the measuring devices 14 and 15 for providing feedback, makes it possible to adapt the switching of the forward-conducting semiconductor device 3 to reach zero-crossing of the fault current through the interrupter in the most favourable way (at a zero or low current derivative as described above) and at the instant desired. This happens in the embodiment shown in Figs. 3 and 4 about 5.6 ms after tripping of the interrupter. In Figs. 1 and 2a-2c, the current measurement device 14 has been shown to be arranged to measure the current through the forward-conducting semiconductor device 3, thus providing a measure of the current through the interrupter. Alternatively, the current measurement device 14 could be arranged to obtain a measure of the current through the interrupter by measuring the common current of the forward-conducting semiconductor

device 3 and the anti-parallel diode 4/anti-parallel semiconductor device 3. In another implementation, a current measurement device 14 could be arranged in the LC circuit 6, by means of which the oscillating component of the current through the mechanical interrupter 2 may be obtained. The DC component of the current through the mechanical interrupter 2 could then be obtained from the fault-current detector 11.

[0051] Although it is shown in Figs. 3 and 4 that the switching of the forward-conducting semiconductor device 3 is started at the same time as the mechanical interrupter 2 is tripped, the tripping of the mechanical interrupter 2 may be delayed by a determined period of time with respect to the start of the switching of the forward-conducting semiconductor device 3. A determined period between the triggering of the switching of the forward-conducting semiconductor device 3 and the tripping of the mechanical interrupter 2 facilitates for refraining from opening the mechanical interrupter 2 should the control unit 12 receive information about a disappearance of the fault within the determined period. Often-times, the time it takes to open the mechanical interrupter 2 is the limiting factor when a fast breaking operation is desired, in which case the opening of the interrupter 2 could advantageously be initiated at the same time as the initiation of the switching. Separation of the contacts of the interrupter 2 does not have to be reached at the same time as the first zero-crossing of the current occurs, but could be reached either before or after the occurrence of the first zero-crossing, as long as the contacts will be separated at the occurrence of a zero-crossing of the current.

[0052] Parameters influencing the interrupter procedure include the protective voltage level of the arrester 5, a higher such level means that the capacitance will be charged more rapidly. Furthermore, higher values of the inductance and the capacitance of the LC circuit 6 will result in a lower eigenfrequency of the LC-circuit and a lower suitable frequency of the switching of the semiconductor device 3. The values of L and C should be chosen for obtaining an interruption situation to be managed by the mechanical interrupter 2, such as with respect to recovery voltage across the interrupter 2. It also has to be considered which frequency the semiconductor device 3 may operate at, and the semiconductor device 3 also has to be able to break the current therethrough when being turned off in said switching.

[0053] Fig. 5 is a flowchart illustrating an example of a method performed in control unit 12 for controlling the operation of the semiconductor switch(es) 3 and the mechanical interrupter 2 in case of a detected fault current, in an embodiment wherein the switching frequency can be adjusted in dependence on measurements of at least one parameter relating to the operation of the arrangement 10. At step 500, a fault indication is received by the control unit 12, typically from fault-current detector 11. Switching of the forward-conducting semiconductor 3 is then initiated in step 505, where the switching is per-

formed at a predetermined frequency f_0 which is typically set at or in the vicinity of the eigenfrequency of the LC circuit 6, as described above. The opening of interrupter 2 is also initiated in step 505, at the same time as the initiating of the switching, or, if desired, at a different point in time. In step 510, control unit 12 receives measurements results, from the current measurement device 14, of the current I_i through the interrupter 2. At step 515, it is checked whether the magnitude of the oscillating part, I_{osc} , of I_i is smaller than the magnitude of the DC part, I_{DC} , of I_i . If so, this indicates that the magnitude of the oscillating part I_{osc} will have to increase for any current zero-crossings to occur. Step 530 is then entered, wherein it is checked whether the switching frequency f is smaller than f_0 . If so, step 535 is entered, wherein the switching frequency f is increased. Step 510 is then re-entered for continued monitoring of the current I_i through the interrupter 2. If, it was found in step 520 that $f = f_0$, then step 510 is re-entered directly following step 520.

[0054] If, on the other hand, it is found in step 515 that the magnitude of I_{DC} does not exceed the magnitude of I_{osc} , step 530 is entered. In step 530, the ratio of the magnitude of I_{osc} to the magnitude of I_{DC} is compared to a predetermined ratio maximum M , which could for example be set in the range of 110-120%. If it is found that the ratio exceeds the ratio maximum M , then step 535 is entered, wherein f is reduced. The reduction can for example be determined in dependence on the ratio of I_{osc} to I_{DC} . Step 510 is then re-entered. If it is found in step 530 that the ratio does not exceed M , then step 510 is entered directly.

[0055] By the method of Fig. 5 can be ensured that a current-zero crossing in the interrupter 2 will occur at a point where the current derivative is small in accordance with the above. The method shown in Fig. 5 is an example only, and alternative implementations could be contemplated. For example, measurements of the voltage across the capacitor 8 could be considered in determining if and how the switching frequency should be adapted, in addition to, or in lieu of, measurements of the current I_i ; switching frequencies higher than the initial frequency f_0 could be allowed, etc.

[0056] Control unit 12 can be implemented as a suitable combination of hardware and software. For example, control unit 12 could include one or more processors and a memory which stores software for performing the method shown in Fig. 5, the processor being arranged to carry out the method of Fig. 5 when the software is executed on the processor. The software could e.g. be provided the form of a computer program product which is provided at least partially through a network, such as the Internet, or on a computer readable medium.

[0057] The invention is of course not in any way restricted to the embodiments described above, but many possibilities to modifications thereof will be apparent to a person with ordinary skill in the art without departing from the scope of the invention as defined in the appended claims.

[0058] It would be possible to cascade several interrupters so as to obtain higher voltages, i.e. to connect a plurality of modules comprising the elements 2-9 and 13-15 shown in Fig. 1 in series.

Claims

1. A DC breaker apparatus (100) capable of interrupting a fault current occurring in a high voltage DC conductor (1), said apparatus comprising

- at least one mechanical interrupter (2) configured to be connected in series with said DC conductor, and
- an arrangement (10) configured to obtain zero-crossing of the current through said interrupter upon occurrence of a fault current in said conductor,
- a control unit (12) configured to control said arrangement and mechanical interrupter, wherein said arrangement comprises
- at least one semiconductor device (3; 3a, 3b) of turn-off type connected in series with said mechanical interrupter (2),
- an arrester (5) connected in parallel with said semiconductor device for defining a maximum voltage across said device, and
- an LC-circuit (6) in the form of a series connection of an inductance (7) and a capacitance (8) connected in parallel with the series connection of said at least one semiconductor device (3a, 3a, 3b) and the mechanical interrupter (2),

and wherein

said control unit (12) is configured to, upon detection of a fault current:

control switching of at least one of said at least one semiconductor devices (3; 3a, 3b) at a frequency adapted to the values of said inductance (7) and capacitance (8), in order to charge said capacitance by the fault current while making the current through the mechanical interrupter (2) oscillate with an increasing amplitude, and

control the tripping of the mechanical interrupter so that the mechanical contacts thereof will be separated when a current zero-crossing is reached, so that interruption of the fault current through the mechanical interrupter is obtained, the DC breaker apparatus being **characterized in that** it further comprises at least one measuring device (14, 15) configured to measure at least one parameter relating to the operation of said arrangement (10) upon occurrence of a said fault and send the result of a measurement to said control unit, and wherein the control unit is configured to adapt said switching in response to

such measurement result.

2. The apparatus of claim 1, wherein said arrangement comprises a rectifying diode (4) connected in anti-parallel with a said semiconductor device.
3. The apparatus of claim 2, wherein said arrangement comprises a series connection of two semiconductor devices (3a, 3b) of opposite conducting directions and a series connection of two rectifying diodes (4a, 4b) of opposite conducting directions, wherein the semiconductor devices and rectifying diodes are connected such that each semiconductor device is connected in anti-parallel with a rectifying diode.
4. The apparatus of claim 1, wherein said arrangement comprises two semiconductor devices (3a, 3b) of opposite current direction connected in anti-parallel, said anti-parallel connection of semiconductor devices being connected in parallel with said arrester (5).
5. An apparatus according to any one of the preceding claims, wherein the control unit is configured to carry out said switching of the semiconductor device (3) at a frequency being 90%-110% of the eigenfrequency of said LC-circuit (6).
6. An apparatus according to claim 1, wherein said measuring device (14) is configured to measure the current through a said semiconductor device (3).
7. An apparatus according to claim 1 or 6, wherein said measuring device (15) is configured to measure the voltage across said capacitance (8).
8. An apparatus according to any one of the preceding claims, wherein said control unit (12) is configured to delay initiation of separation of the mechanical contacts of the interrupter (2) by a determined period of time with respect to the start of the switching of said semiconductor device (3) for possibly refraining from opening the interrupter should said control unit receive information about a disappearance of said fault within this period of time.
9. An apparatus according to any of the preceding claims, further comprising a further arrester (9) connected in parallel with said LC-circuit (6).
10. An apparatus according to any one of the preceding claims, wherein the control unit (12) is configured to carry out said switching of the semiconductor device with a frequency of 100 Hz - 10 kHz or 500 Hz - 5 kHz.
11. An apparatus according to any of the preceding claims, wherein said inductance (7) has a value of 0.1 mH - 10 mH.
12. An apparatus according to any of the preceding claims, wherein said capacitance (8) has a value of 10 nF - 100 μ F or 0.5 μ F - 5 μ F.
13. An apparatus according to any of the preceding claims, wherein the protective level of said arrester (5) connected in parallel with said semiconductor device (3) is less than 50% or less than 10% of the voltage level intended for said high voltage DC conductor (1) with respect to ground.
14. An apparatus according to any one of the preceding claims, further comprising a fault-current detector (11) configured to detect occurrence of a fault current in a conductor in which the apparatus is connected.
15. An AC/DC converter station comprising an apparatus according to any of the preceding claims, wherein the DC breaker apparatus is connected to a said high voltage DC conductor (1) of the AC/DC converter station.
16. A DC grid comprising an apparatus according to any of the preceding claims, wherein the apparatus is configured to protect equipment of the DC grid.
17. A method of controlling a DC breaker apparatus for interrupting a fault current occurring in a high voltage DC conductor, the DC breaker apparatus including a mechanical interrupter (2) and an arrangement (10) configured to obtain zero-crossing of the current through said interrupter upon occurrence of a fault current in said DC conductor, the arrangement including at least one semiconductor device (3; 3a, 3b) of turn-off type and an LC circuit (6) in the form of a series connection (6) of an inductance (7) and a capacitance (8), wherein the method comprises:
 - initiating (505) the switching of a said semiconductor device of turn-off type at a first frequency adapted to the values of said inductance and capacitance;
 - initiating (505) the opening of the mechanical interrupter;
 - receiving (505) measurements of at least one parameter relating to the magnitude of the current through the mechanical interrupter;
 - checking (515, 530) whether the ratio of an oscillating part of said current to a DC part of said current is larger than one but smaller than a predetermined value; and if not,
 - adjusting (535, 525) the frequency of the switching of the semiconductor device.

Patentansprüche

1. Gleichstromabschaltvorrichtung (100), die in der Lage ist, einen Fehlerstrom, der in einem Hochspannungsgleichstromleiter (1) auftritt, zu unterbrechen, wobei die Vorrichtung aufweist:

- mindestens einen mechanischen Unterbrecher (2), der eingerichtet ist, in Reihe mit dem Gleichstromleiter geschaltet zu werden, und
 - eine Anordnung (10), die eingerichtet ist, einen Nulldurchgang des Stromes durch den Unterbrecher bei Auftreten eines Fehlerstroms in dem Leiter zu erhalten,
 - eine Steuereinheit (12), die eingerichtet ist, die Anordnung und den mechanischen Unterbrecher zu steuern,
- wobei die Anordnung aufweist
- mindestens ein Halbleiterbauelement (3; 3a, 3b) vom Abschalttyp, das in Reihe mit dem mechanischen Unterbrecher (2) geschaltet ist,
 - einen Ableiter (5), der parallel zu dem Halbleiterbauelement geschaltet ist, um eine Maximalspannung über das Bauelement hinweg festzulegen, und
 - einen LC-Schaltkreis (6) in der Form einer Reihenschaltung einer Induktivität (7) und einer Kapazität (8), die parallel zu der Reihenschaltung von dem mindestens einen Halbleiterbauelement (3; 3a, 3b) und dem mechanischen Unterbrecher (2) geschaltet ist,

und wobei

die Steuereinheit (12) bei Nachweis eines Fehlerstroms eingerichtet ist zum:

Steuern des Schaltens mindestens eines von dem mindestens einen Halbleiterbauelement (3; 3a, 3b) bei einer Frequenz, die an die Werte der Induktivität (7) und der Kapazität (8) angepasst ist, um die Kapazität durch den Fehlerstrom aufzuladen, während erreicht wird, dass der Strom durch den mechanischen Unterbrecher (2) mit einer anwachsenden Amplitude oszilliert, und

Steuern des Auslösens des mechanischen Unterbrechers, sodass dessen mechanische Kontakte getrennt werden, wenn ein Strom-Nulldurchgang erreicht wird, sodass eine Unterbrechung des Fehlerstroms durch den mechanischen Unterbrecher erhalten wird, wobei die Gleichstromabschaltvorrichtung **dadurch gekennzeichnet ist, dass** sie ferner aufweist:

mindestens ein Messgerät (14, 15), das eingerichtet ist, mindestens einen Parameter zu messen, der sich auf den Betrieb der Anordnung (10) beim Auftreten des Fehler-

stroms bezieht, und das Ergebnis der Messung an die Steuereinheit zu senden, und wobei die Steuereinheit eingerichtet ist, das Schalten als Reaktion auf ein derartiges Messergebnis anzupassen.

2. Vorrichtung nach Anspruch 1, wobei die Anordnung eine Gleichrichterdiode (4) aufweist, die antiparallel zu einem besagten Halbleiterbauelement geschaltet ist.
3. Vorrichtung nach Anspruch 2, wobei die Anordnung eine Reihenschaltung von zwei Halbleiterbauelementen (3a, 3b) mit entgegengesetzten Durchlassrichtungen und eine Reihenschaltung von zwei Gleichrichterdioden (4a, 4b) mit entgegengesetzten Durchlassrichtungen aufweist, wobei die Halbleiterbauelemente und Gleichrichterdioden derart geschaltet sind, dass jedes Halbleiterbauelement antiparallel zu einer Gleichrichterdiode geschaltet ist.
4. Vorrichtung nach Anspruch 1, wobei die Anordnung zwei Halbleiterbauelemente (3a, 3b) mit entgegengesetzter Stromrichtung aufweist, die antiparallel geschaltet sind, wobei die antiparallele Schaltung der Halbleiterbauelemente parallel zu dem Ableiter (5) geschaltet ist.
5. Vorrichtung nach einem der vorhergehenden Ansprüche, wobei die Steuereinheit eingerichtet ist, das Schalten des Halbleiterbauelements (3) bei einer Frequenz auszuführen, die 90% - 110% der Eigenfrequenz des LC-Schaltkreises (6) ist.
6. Vorrichtung nach Anspruch 1, wobei das Messgerät (14) eingerichtet ist, den Strom durch ein besagtes Halbleiterbauelement (3) zu messen.
7. Vorrichtung nach Anspruch 1 oder 6, wobei das Messgerät (15) eingerichtet ist, die Spannung über die Kapazität (8) hinweg zu messen.
8. Vorrichtung nach einem der vorhergehenden Ansprüche, wobei die Steuereinheit (12) eingerichtet ist, das Einleiten der Trennung der mechanischen Kontakte des Unterbrechers (2) um eine bestimmte Zeitspanne bezogen auf den Beginn des Schaltens des Halbleiterbauelements (3) zu verzögern, um möglicherweise das Öffnen des Unterbrechers zu unterlassen, sollte die Steuereinheit eine Information über das Verschwinden des Fehlers innerhalb dieser Zeitspanne empfangen.
9. Vorrichtung nach einem der vorhergehenden Ansprüche, ferner mit einem weiteren Ableiter (9), der parallel zu dem LC-Schaltkreis (6) geschaltet ist.

10. Vorrichtung nach einem der vorhergehenden Ansprüche, wobei die Steuereinheit (12) eingerichtet ist, das Schalten des Halbleiterbauelements mit einer Frequenz von 100 Hz - 10 kHz oder 500 Hz - 5 kHz auszuführen. 5
11. Vorrichtung nach einem der vorhergehenden Ansprüche, wobei die Induktivität (7) einen Wert von 0,1 mH - 10 mH aufweist. 10
12. Vorrichtung nach einem der vorhergehenden Ansprüche, wobei die Kapazität (8) einen Wert von 10 nF - 100 μ F oder 0,5 μ F - 5 μ F aufweist. 15
13. Vorrichtung nach einem der vorhergehenden Ansprüche, wobei der Schutzpegel des Ableiters (5), der parallel zu dem Halbleiterbauelement (3) geschaltet ist, kleiner als 50% oder kleiner als 10% des Spannungspegels ist, der für den Hochspannungsgleichstromleiter (1) hinsichtlich Erde vorgesehen ist. 20
14. Vorrichtung nach einem der vorhergehenden Ansprüche, ferner mit einem Fehlerstromdetektor (11), der eingerichtet ist, das Auftreten eines Fehlerstroms in einem Leiter zu erkennen, in dem die Vorrichtung geschaltet ist. 25
15. Wechselstrom-Gleichstrom-Wandlerstation mit einer Vorrichtung nach einem der vorhergehenden Ansprüche, wobei die Gleichstromabschaltvorrichtung an einen besagten Hochspannungsgleichstromleiter (1) der Wechselstrom-Gleichstrom-Wandlerstation angeschlossen ist. 30 35
16. Gleichstromnetz mit einer Vorrichtung nach einem der vorhergehenden Ansprüche, wobei die Vorrichtung eingerichtet ist, die Anlagen des Gleichstromnetzes zu schützen. 40
17. Verfahren zum Steuern einer Gleichstromabschaltvorrichtung für das Unterbrechen eines Fehlerstroms, der in einem Hochspannungsgleichstromleiter auftritt, wobei die Gleichstromabschaltvorrichtung einen mechanischen Unterbrecher (2) und eine Anordnung (10) aufweist, die eingerichtet ist, einen Nulldurchgang des Stromes durch den Unterbrecher bei Auftreten eines Fehlerstroms in dem Gleichstromleiter zu erhalten, wobei die Anordnung mindestens ein Halbleiterbauelement (3; 3a, 3b) vom Abschalttyp, und einen LC-Schaltkreis (6) in der Form einer Reihenschaltung (6) einer Induktivität (7) und einer Kapazität (8) aufweist, wobei das Verfahren umfasst: 45 50 55
- Einleiten (505) des Schaltens des Halbleiterbauelements vom Abschalttyp bei einer ersten

Frequenz, die an die Werte der Induktivität und Kapazität angepasst ist,
 Einleiten (505) des Öffnens des mechanischen Unterbrechers,
 Empfangen (505) von Messungen von mindestens einem Parameter, der auf die Größe des Stromes durch den mechanischen Unterbrecher bezogen ist,
 Prüfen (515, 530), ob das Verhältnis eines oszillierenden Teils des Stromes zu einem Gleichstromteil des Stromes größer als eins aber kleiner als ein vorgegebener Wert ist, und wenn nicht, dann
 Abstimmen (535, 525) der Frequenz des Schaltens des Halbleiterbauelements.

Revendications

1. Appareil rupteur CC (100) pouvant interrompre un courant de panne survenant dans un conducteur CC haute tension (1), ledit appareil comprenant :
- au moins un interrupteur mécanique (2) conçu pour être connecté en série avec ledit conducteur CC ; et
 - un système (10) conçu pour obtenir un passage à zéro du courant dans ledit interrupteur lors de la survenance d'un courant de panne dans ledit conducteur ;
 - une unité de commande (12) conçue pour commander ledit système et ledit interrupteur mécanique, lequel système comprend :
 - au moins un dispositif semi-conducteur (3 ; 3a, 3b) de type arrêt connecté en série avec ledit interrupteur mécanique (2) ;
 - un parasurtenseur (5) connecté en parallèle audit dispositif semi-conducteur afin de définir une tension maximale dans ledit dispositif ; et
 - un circuit LC (6) sous forme d'une connexion série d'une inductance (7) et d'une capacitance (8) connectées en parallèle à la connexion série dudit au moins un dispositif semi-conducteur (3 ; 3a, 3b) et de l'interrupteur mécanique (2), et dans lequel
 - ladite unité de commande (12) est conçue, lors de la détection d'un courant de panne, pour :
 - commander la commutation d'au moins un desdits au moins un dispositifs semi-conducteurs (3 ; 3a, 3b) à une fréquence adaptée aux valeurs de ladite inductance (7) et de ladite capacitance (8) afin de charger ladite capacitance avec le courant de panne tout en faisant osciller le courant dans l'interrupteur mécanique (2) selon une amplitude croissante ; et
 - commander le déclenchement de l'interrupteur mécanique de sorte que ses contacts mécani-

- ques soient séparés lorsqu'un passage à zéro de courant est atteint, de sorte que l'interruption du courant de panne dans l'interrupteur mécanique puisse être obtenue, lequel appareil rupteur CC est **caractérisé en ce qu'il** comprend en outre :
- au moins un dispositif de mesure (14, 15) conçu pour mesurer au moins un paramètre concernant le fonctionnement dudit système (10) lors de la survenance d'une dite panne et envoyer le résultat d'une mesure à ladite unité de commande, et laquelle unité de commande est conçue pour adapter ladite commutation en réponse à un tel résultat de mesure.
2. Appareil selon la revendication 1, dans lequel ledit système comprend une diode redresseuse (4) connectée en anti-parallèle à un dit dispositif semi-conducteur.
 3. Appareil selon la revendication 2, dans lequel ledit système comprend une connexion série de deux dispositifs semi-conducteurs (3a, 3b) ayant des directions de conduction opposées et une connexion série de deux diodes redresseuses (4a, 4b) ayant des directions de conduction opposées, les dispositifs semi-conducteurs et les diodes redresseuses étant connectées de sorte que chaque dispositif semi-conducteur soit connecté en anti-parallèle à une diode redresseuse.
 4. Appareil selon la revendication 1, dans lequel ledit système comprend deux dispositifs semi-conducteurs (3a, 3b) ayant des directions de courant opposées connectés en anti-parallèle, ladite connexion anti-parallèle des dispositifs semi-conducteurs étant connectée en parallèle audit parasurtenseur (5).
 5. Appareil selon l'une quelconque des revendications précédentes, dans lequel l'unité de commande est conçue pour effectuer ladite commutation du dispositif semi-conducteur (3) à une fréquence représentant de 90% à 110% de la fréquence propre dudit circuit LC (6).
 6. Appareil selon la revendication 1, dans lequel ledit dispositif de mesure (14) est conçu pour mesurer le courant dans un dit dispositif semi-conducteur (3).
 7. Appareil selon les revendications 1 ou 6, dans lequel ledit dispositif de mesure (15) est conçu pour mesurer la tension dans ladite capacitance (8).
 8. Appareil selon l'une quelconque des revendications précédentes, dans lequel ladite unité de commande (12) est conçue pour retarder le lancement de la séparation des contacts mécaniques de l'interrupteur (2) d'une période de temps déterminée par rapport au début de la commutation dudit dispositif semi-conducteur (3) afin de prévenir potentiellement l'ouverture de l'interrupteur dans le cas où ladite unité de commande reçoit une information sur la disparition de ladite panne pendant cette période de temps.
 9. Appareil selon l'une quelconque des revendications précédentes, comprenant en outre un autre parasurtenseur (9) connecté en parallèle audit circuit LC (6).
 10. Appareil selon l'une quelconque des revendications précédentes, dans lequel l'unité de commande (12) est conçue pour effectuer ladite commutation du dispositif semi-conducteur à une fréquence de 100 Hz à 10 kHz ou de 500 Hz à 5 kHz.
 11. Appareil selon l'une quelconque des revendications précédentes, dans lequel ladite inductance (7) a une valeur de 0,1 mH à 10 mH.
 12. Appareil selon l'une quelconque des revendications précédentes, dans lequel ladite capacitance (8) a une valeur de 10 nF à 100 μ F ou de 0,5 μ f à 5 μ F.
 13. Appareil selon l'une quelconque des revendications précédentes, dans lequel le niveau de protection dudit parasurtenseur (5) connecté en parallèle audit dispositif semi-conducteur (3) représente moins de 50% ou moins de 10% du niveau de tension prévu pour ledit conducteur CC haute tension (1) par rapport à la terre.
 14. Appareil selon l'une quelconque des revendications précédentes, comprenant en outre un détecteur de courant de panne (11) conçu pour détecter la survenance d'un courant de panne dans un conducteur dans lequel l'appareil est connecté.
 15. Station de convertisseur CA/CC comprenant un appareil selon l'une quelconque des revendications précédentes, dans lequel l'appareil rupteur CC est connecté à un dit conducteur CC haute tension (1) de la station de convertisseur CA/CC.
 16. Réseau CC comprenant un appareil selon l'une quelconque des revendications précédentes, dans lequel l'appareil est conçu pour protéger un équipement du réseau CC.
 17. Procédé de commande d'un appareil rupteur CC pour interrompre un courant de panne survenant dans un conducteur CC haute tension, l'appareil rupteur CC comprenant un interrupteur mécanique (2) et un système (10) conçu pour obtenir un passage à zéro du courant dans ledit interrupteur lors de la survenance d'un courant de panne dans ledit conducteur CC, lequel système comprend au moins un

dispositif semi-conducteur (3 ; 3a, 3b) de type arrêt et un circuit LC (6) sous forme d'une connexion série (6) d'une inductance (7) et d'une capacitance (8), le procédé consistant à :

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- initier (505) la commutation d'un dit dispositif semi-conducteur de type arrêt à une première fréquence adaptée aux valeurs de ladite inductance et de ladite capacitance ;

- initier (505) l'ouverture de l'interrupteur mécanique ; 10

- recevoir (505) des mesures d'au moins un paramètre concernant la magnitude du courant dans l'interrupteur mécanique ;

- vérifier (515, 530) si le rapport entre une partie oscillante dudit courant et une partie CC dudit courant est supérieur à un mais inférieur à une valeur prédéterminée ; et si tel n'est pas le cas, 15

- ajuster (535, 525) la fréquence de la commutation du dispositif semi-conducteur. 20

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Fig. 1

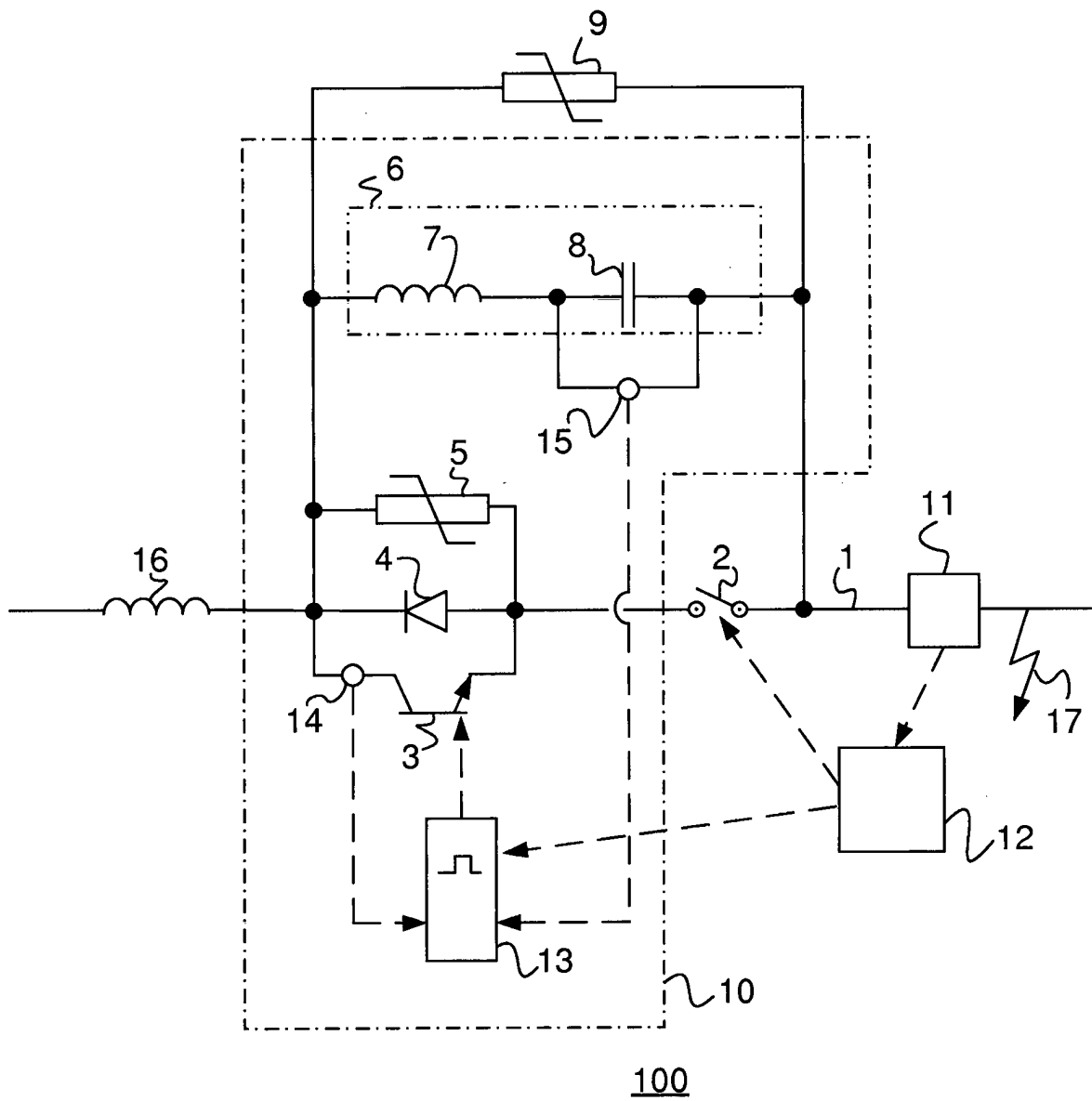


Fig. 2a

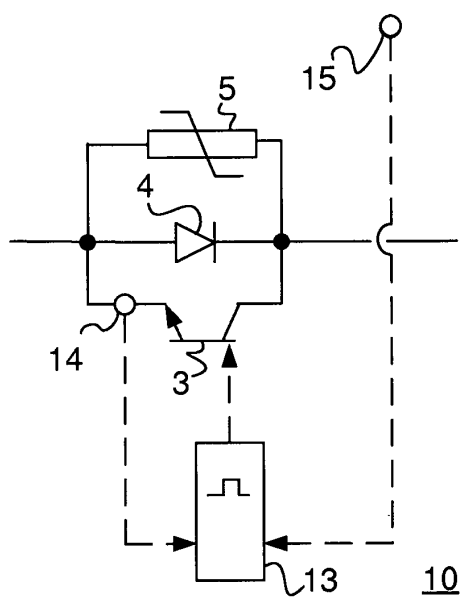


Fig. 2b

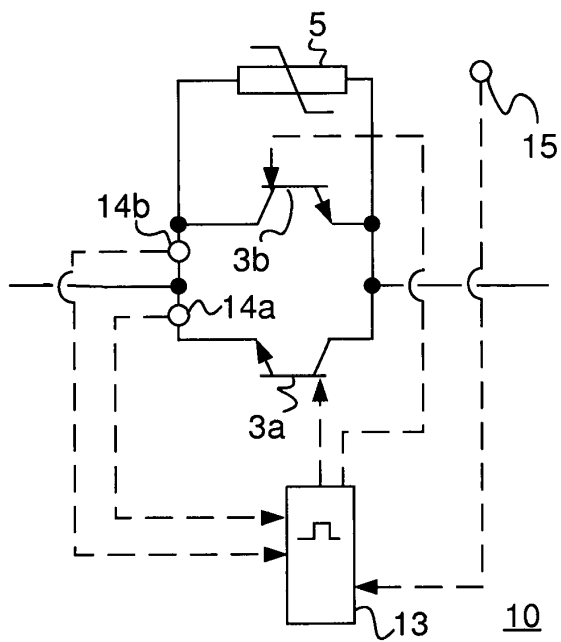
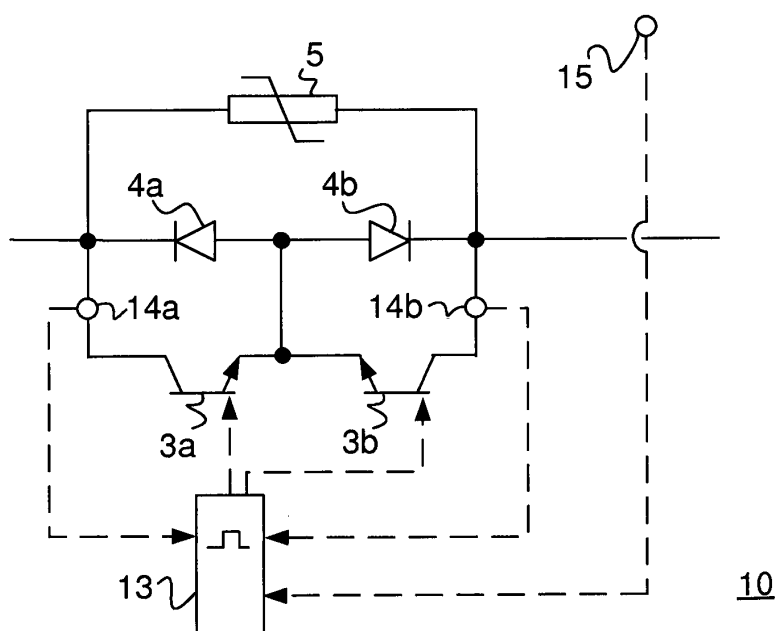


Fig. 2c



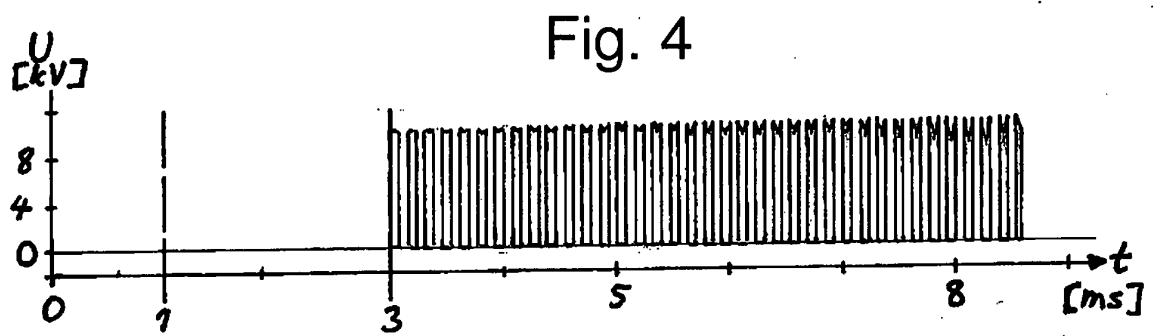
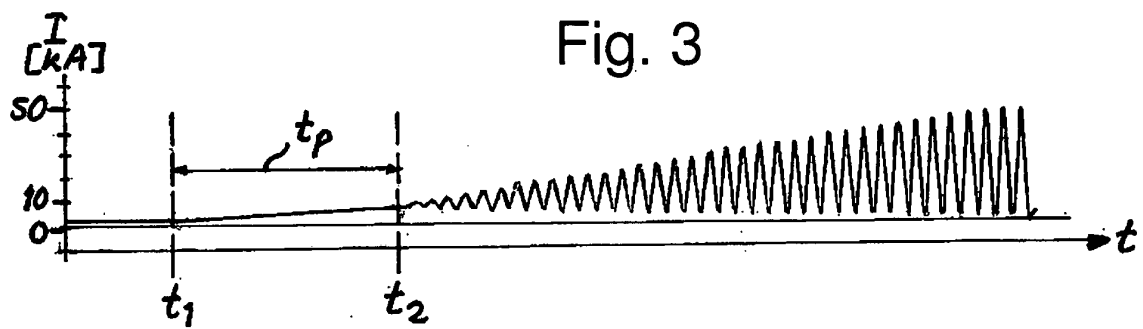
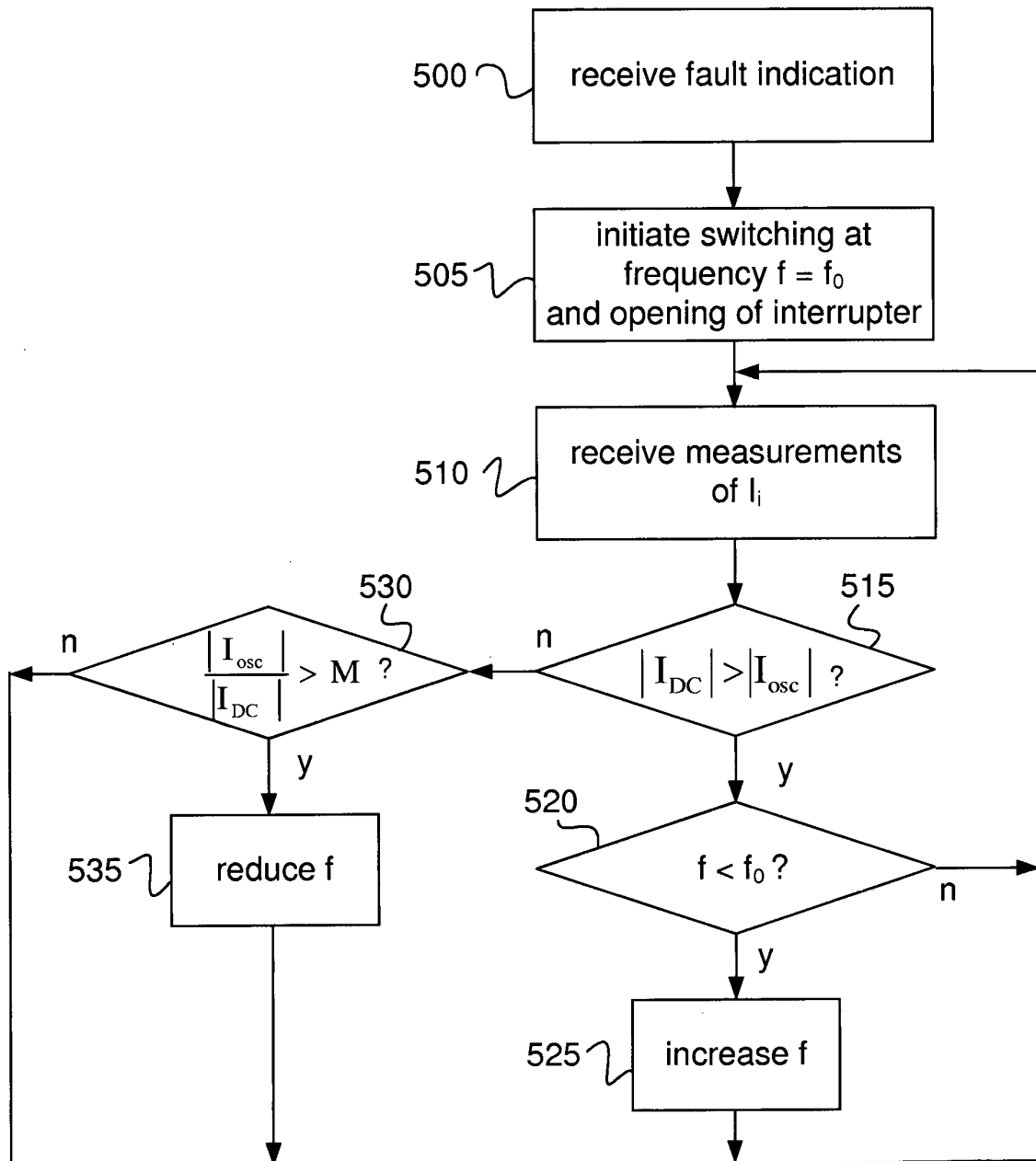


Fig. 5



REFERENCES CITED IN THE DESCRIPTION

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