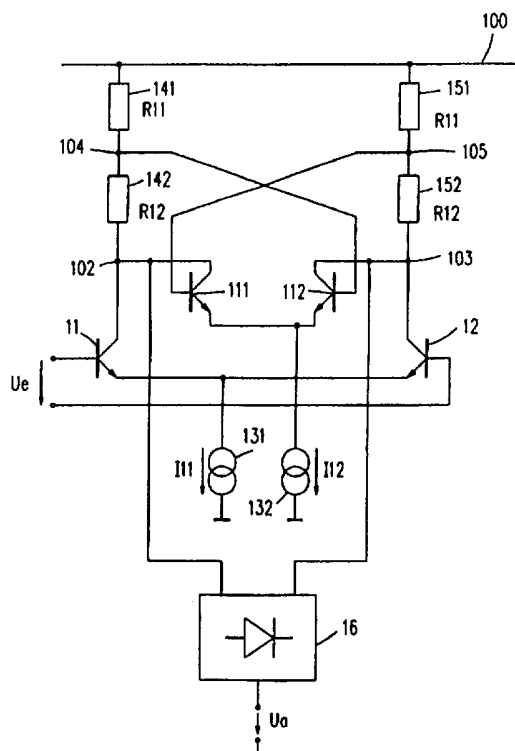




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(21) International Application Number: PCT/IB97/00200 (22) International Filing Date: 5 March 1997 (05.03.97) (30) Priority Data: 196 08 861.5 7 March 1996 (07.03.96) DE (71) Applicant: PHILIPS ELECTRONICS N.V. [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL). (71) Applicant (for DE only): PHILIPS PATENTVERWALTUNG GMBH [DE/DE]; Röntgenstrasse 24, D-22335 Hamburg (DE). (71) Applicant (for SE only): PHILIPS NORDEN AB [SE/SE]; Kottbygatan 7, Kista, S-164 85 Stockholm (SE). (72) Inventor: DICK, Burkhard; Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL). (74) Agent: PETERS, Carl, H.; Internationaal Octrooibureau B.V., P.O. Box 220, NL-5600 AE Eindhoven (NL).		(81) Designated States: CN, JP, KR, SG, European patent (AT, BE, CH, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE). Published <i>With international search report.</i> <i>Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.</i> (88) Date of publication of the international search report: 30 October 1997 (30.10.97)
(54) Title: CIRCUIT ARRANGEMENT COMPRISING A LOGARITHMIC TRANSFER FUNCTION (57) Abstract A description is given of a circuit arrangement comprising a logarithmic transfer function between an input signal and an output signal in a predefined level range of the input signal, comprising a pair of amplifier elements forming a first differential amplifier, the main current paths of which amplifier elements are connected, on the one hand, to each other and to a first current source and on the other hand, to working impedances subdivided by respective taps, and the control terminals of which amplifier elements are supplied with the input signal; a pair of amplifier elements forming a second differential amplifier, the main current paths of which amplifier elements are connected, on the one hand, to each other and to a second current source and on the other hand, to the connections of the main current paths of the first differential amplifier and the working impedances, and whose control terminals are cross-connected to the taps of the working impedances; and a rectifier stage whose input is connected to the connections of the main current paths of the differential amplifiers and the working impedances and whose output signal is tapped from its output. This circuit arrangement has a very low power consumption and low circuit complexity.		



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INTERNATIONAL SEARCH REPORT

International application No.

PCT/IB 97/00200

A. CLASSIFICATION OF SUBJECT MATTER

IPC6: H03G 7/06

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC6: H03G, H03F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

SE,DK,FI,NO classes as above

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

WPIL, EDOC, JAPIO

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 5471166 A (KATSUJI KIMURA), 28 November 1995 (28.11.95), abstract --	1-8
A	EP 0505751 A2 (NEC CORPORATION), 30 Sept 1992 (30.09.92), abstract --	1-8
A	EP 0599399 A1 (PHILIPS ELECTRONICS N.V.), 1 June 1994 (01.06.94), abstract -- -----	1-8

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Date of the actual completion of the international search

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INTERNATIONAL SEARCH REPORT

Information on patent family members

06/08/97

International application No.

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