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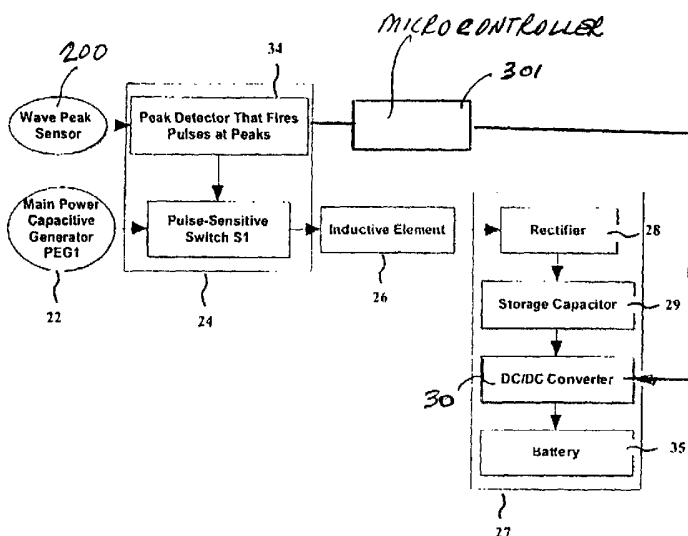
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- (71) Applicant: **OCEAN POWER TECHNOLOGIES, INC.**
[US/US]; 1590 Reed Road, Pennington, NJ 08534 (US).
- (72) Inventors: **BURNS, Joseph, R. (deceased)**; 406 Oak Street, Pennington, NJ 08534 (US). **TAYLOR, George, W.**; 277 Sayre Drive, Princetn, NJ 08540 (US). **WELSH, Thomas, R.**; 14 Vandeventer Avenue, Apt. 5, Princeton, NJ 08542 (US).
- (74) Agent: **SCHANZER, Henry, T.**; 29 Brookfall Road, Edison, NJ 08817 (US).
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(54) Title: SENSORS FOR POWER CONVERSION SYSTEMS



(57) Abstract: Systems and circuits embodying the invention include means for sensing at least one of the peaks, amplitude and frequency of an oscillatory input force impinging on a power generating device (22) for controlling the turn-on of a switch used to transfer energy collected by the power generating device to an associated load. The power generator may be a transducer such as a piezoelectric device or a wave energy converter (WEC). Sensors and sensing means embodying the invention are used to control the point in time at which power extracting circuits are switched in circuit with the power generating device. The power extracting circuit may include components which can resonate with the power generating circuit at a higher frequency than, and independent of, the frequency at which the power generating device is being operated. Thus, an electric power generator device operated and controlled by a slowly changing source of energy (e.g., ocean waves, wind, eddies of water) may develop energy at one frequency and may be operated to transfer the energy, at a selected point in time, at another frequency.



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INVENTION: SENSORS FOR POWER CONVERSION SYSTEMS

BACKGROUND OF THE INVENTION

This invention relates to sensors for controlling the transfer of energy from a power generator to a load so as to increase the efficiency of the transfer.

In many power conversion applications, transducers, functioning as power generators, are used which are responsive to recurring (oscillating) natural sources of energy (e.g., ocean waves, wind, eddies of water) for converting energy captured from these sources of energy into electrical energy.

In many applications, such as those involving extracting energy from ocean waves, it is necessary and/or desirable to sense the occurrence of at least one of the peak, amplitude and frequency of the ocean waves to optimize the transfer of energy from the ocean waves.

By way of example, a capacitive electric power generator may include a piezoelectric device functioning as a capacitive piezoelectric generator (PEG) which when subjected to mechanical stresses and strains produces an electrical signal. The electrical signals of one, or more, of these piezoelectric devices may be processed to produce electrical power which can be used to operate electrical/electronic devices and/or which can be part of an electrical power grid. Systems making use of piezoelectric devices to produce electrical power are shown, for example, in U.S. Pat. Nos. 5,552,656 and 5,703,474 which issued Sep. 3, 1996 and Dec. 30, 1997, respectively, and which are assigned to the

assignee of the present application, and whose teachings are incorporated herein by reference.

Piezoelectric devices used as electric power generators are characterized by an inherent inefficiency in the transformation ("coupling") of the mechanical strains and stress into electrical charge. As a result, often only a small portion (e.g., approximately 10%) of the mechanical stress/strain applied to a piezoelectric device is available as electrical power when a constant load is applied to the piezoelectric device. It is therefore desirable to increase the efficiency with which the energy generated by a piezoelectric device is transferred to a load to compensate for, and overcome, the low "coupling" factor of the piezoelectric devices.

A known method for increasing the efficiency of the transfer from the piezoelectric generator to a load includes forming a resonant circuit. This is shown, for example, in Fig. 1, which is a highly simplified block diagram representation of a prior-art piezoelectric electric power generator circuit. The stresses and/or strains applied to the piezoelectric device are provided by sources of energy (e.g., ocean waves, wind, eddies of water) which may vary slowly (e.g., a few cycles per second). Consequently, the piezoelectric devices may be operated at very low frequencies and the frequency of the electrical signals produced by these piezoelectric devices is also in the range of a few cycles per second. These low operating frequencies present significant problems to the efficient transfer of energy from the piezoelectric device to a load.

For example, it is difficult to form inductors and transformers of reasonable size and at a reasonable cost which can operate at those frequencies.

Referring to Fig. 1, by way of example, note that the circuit includes a piezoelectric device 22 coupled by an inductor 16 to a load 27. The resonant frequency (f_0) of the circuit may be expressed as $f_0 = 1/2\pi(LC_p)^{.5}$; where C_p is the capacitance of the piezoelectric device 22; and L is the inductance of inductor 16, with the value of L being selected to resonate with the capacitance of the piezoelectric device. [Note: for ease of explanation and discussion, the contribution of other capacitances in the circuit have been ignored in the specification and claims which follow]. The capacitance of C_p may be assumed to be in the range of .01 to 10 microfarads (10^{-6} farads). Assume now that the frequency of the electric signal, produced by the piezoelectric device in response to the mechanical driving force, is in the range of 2Hz. Then, in order to have a circuit that resonates at 2 Hz, an inductor 16 having a value in the range of 12,000 Henrys would be needed. An inductor of this value would be the size of a small room. In addition, direct electrical resonance is not practical because of the expected variability of the frequency due to the random nature of ocean waves.

As disclosed and claimed in co-pending applications titled "Apparatus And Method For Optimizing The Power Transfer Produced By A Wave Energy Converter (WEC)" filed 8/6/01 and bearing serial number 09/922877 and "Switched Resonant Power Conversion Electronics" bearing serial number _____ and both assigned to the assignee of the present application, and

the teachings of which are incorporated herein by reference, Applicants recognized that it is advantageous to selectively switch a load in circuit with the power generating device which is designed to resonate at a higher frequency than the frequency of the input force. However, it is important to determine the point at which the switching should take place.

SUMMARY OF THE INVENTION

Systems and circuits embodying the invention include means for sensing at least one of the peaks, amplitude and frequency of an oscillatory input force for controlling the transfer of energy collected by a transducer (power generator) to an associated load. In systems embodying the invention the power generator may be a transducer such as a piezoelectric device or a wave energy converter (WEC) or any like device responsive to an oscillatory input force for generating electrical energy.

In one embodiment of the invention, a power generating device captures energy at a low frequency rate of the input force. Sensors embodying the invention enable the collected energy to be extracted at a much higher frequency. Extracting the energy at a higher frequency enables the use of components, such as inductors, having reasonable values and sizes compared to the prior art systems. In accordance with the invention, sensors and sensing means embodying the invention are used to control the point in time at which power extracting circuits are switched in circuit with a power generating device. The power extracting circuit may include components which can resonate with

the power generating circuit at a higher frequency than, and independent of, the frequency at which the power generating device is being operated. Thus, the electric power generator device operated and controlled by a slowly changing source of energy (e.g., ocean waves, wind, eddies of water) may develop energy at one frequency and may be operated to transfer the energy, at a selected point in time, at another frequency.

In certain embodiments where the power generator is capacitive, sensors are used to switch an inductive power extracting circuit on the positive and negative peaks of the input force. In accordance with the invention where a capacitive power generator produces an oscillatory electrical signal at a low first frequency (f_1), there is switched into the system an inductive power extracting circuit designed to resonate with the capacitive power generator at a resonant frequency (f_0), which is substantially greater than f_1 , on the positive and negative peaks of the input force such that power will be extracted in an electrical pulse which begins at switch closure and ends when the current reaches zero in the inductor. The time of switch closure, T_c , is equal to approximately $1/2f_0$, where f_0 is the resonant frequency of the source and load circuit.

Sensors embodying the invention include circuits and systems for reliably and accurately detecting at least one of the peak(s), amplitude and frequency of an input force and circuitry for controlling the turn-on and turn-off of a switch selectively coupling a power extracting circuit to a power generating device.

BRIEF DESCRIPTION OF THE DRAWING

In the accompanying figures like reference characters denote like components; and

FIGURE 1 is a simplified circuit diagram of a prior art system using piezoelectric devices to generate electric power;

FIGURE 2 is a block diagram of a system for generating power in accordance with the invention;

FIGURE 3A is a simplified circuit schematic diagram of one embodiment of the invention;

FIGURES 3B and 3C are simplified schematic diagrams showing different embodiments of the invention;

FIGURE 3D is a simplified schematic diagram of a circuit embodying the invention using MOSFET switches;

FIGURE 4 is a waveform diagram illustrating various waveforms associated with the circuit of Fig. 3A;

FIGURE 5 is a waveform diagram of signals produced in circuits embodying the invention;

FIGURE 6 is a circuit diagram showing selected values for certain components;

FIGURE 7 is a waveform of a steady state voltage produced in circuits embodying the invention;

FIGURE 8 is a waveform diagram of steady state load current pulses produced in circuits embodying the invention;

FIGURE 9 is a simplified circuit diagram of another circuit embodying the invention;

FIGURE 10 is a simplified circuit diagram showing a switching circuit in accordance with the invention;

FIGURE 11 is a simplified rectifying circuit for use in circuits embodying the invention;

FIGURES 12A, 12B, 12C, 12D, 12E, and 12F are diagrams illustrating various signal conditions associated with peak detection;

FIGURES 12G and 12H are diagrams illustrating a peak detection scheme embodying the invention; and

FIGURES 13, 14, 15 and 16 are diagrams illustrating other peak detection schemes embodying the invention.

DETAILED DESCRIPTION OF THE INVENTION

Figure 2 includes a piezoelectric device 22, also referred to herein as PEG1, which may be placed in an environment where it is subject to mechanical stress. For purpose of illustration, assume that PEG1 is placed in an environment (e.g., an ocean, a river, a windmill) such that PEG1 is stressed and/or strained and produces an electrical signal whose amplitude and frequency varies as a function of the amplitude and frequency of the driving mechanical force (e.g., an ocean wave). PEG1 functions to collect energy imparted to it from the environment in which it is placed. PEG1 then supplies its collected energy to the system to which it is connected. In this example, it is assumed

that PEG1 produces an oscillating voltage due to an oscillating mechanical force applied to it.

The piezoelectric device 22 is selectively coupled via a "smart" switch 24 to one end of an inductive circuit 26 whose other end is connected to load 27. Switch 24 is referred to as a "smart" switch because its turn-on (closure) and the length of time it is turned-on is carefully controlled. Smart switch 24 may include a switch S1 which may be an electronic switch or a relay contact or any like device providing a very low impedance connection for one signal condition (switch closure) and a very high impedance for another signal condition (switch open). The turn on and turn-off of smart switch 24 is controlled by a network 34 as further detailed below.

The inductive circuit 26 may include an inductor having an inductance, L . The value of the inductor is selected such that, when switch S1 is closed, the inductor 26 resonates with the capacitance C_p of piezoelectric device 22. In Fig. 2, the output side of inductor 26 is connected to a load 27 which includes a rectification circuit 28 whose output is connected to a storage capacitor 29 which is coupled to a DC/DC converter 30 which charges a battery 35 and regulates the charging of the battery.

Referring to Fig. 3A, which is a simplified equivalent circuit of the embodiment shown in Fig. 2, note that the piezoelectric device (PEG) may be represented by a voltage source, E_p , connected in series with a capacitor C_p between terminals 31 and 33 and a resistor R_p connected between terminals 31 and 33 so as to shunt the voltage source E_p in series with C_p . Resistor R_p

represents an equivalent dielectric loss resistance of the piezoelectric material (PVDF) combined with an effective loss resulting from the phase difference between the strain and the voltage within a piezoelectric element under an oscillating mechanical force. Typically, the ohmic value of R_p is in the range of several megohms. In one embodiment capacitor C_p was determined to have a value of 0.45 microfarads. The PEG also includes an electrode resistance ($R_{electrode}$) shown connected between nodes 30 and 31. Voltage source E_p produces a voltage whose amplitude and frequency is a function of the amplitude and frequency of the mechanical driving force stressing it. The amplitude of E_p may vary over a wide range (e.g., 0 to 500 volts) and its frequency may typically vary over a range extending from 0hz to several cycles per second.

One aspect of Applicants' invention is directed to extracting the power collected in the piezoelectric generator by selectively connecting an inductive element 26 in circuit with the electric power generator causing the transfer of energy from C_p into the inductive element and load at a resonant frequency which is primarily determined by the capacitance of C_p and the inductance (L) of the inductive element (L_1 or 26). The value L of the inductive element 26 is selected such that the resonant frequency (f_0) due to C_p and the inductive element 26 is significantly greater than the maximum "input" frequency (f_1) of mechanical input force or that of the electrical signal produced by PEG1 in response to being stressed and/or strained.

In one embodiment, assuming a driving frequency of 2Hz and a C_p of .45 microfarad, the resonant frequency was selected to be about 70Hz, for which an

inductor 26 having a value of approximately 11.6 Henrys was used to selectively connect PEG1 to the load 27. For the particular embodiment, the value of the series winding resistance (R_w) of inductor L1 was 65 ohms and the electrode resistance ($R_{\text{electrode}}$) of PEG1 was 100 ohms, whereby the "loop" series resistance (R_s) which represents the sum of R_w and $R_{\text{electrode}}$ was 165 ohms.

In Fig. 3A the load 27 is represented by a resistor. However, it should be understood that, as shown in Fig. 2, the load 27 may be a complex load (including various resistive networks and/or inductive and/or capacitive elements). The load 27 as shown in Fig. 2 may also include a bridge rectifier, 28, to convert the positive and negative power pulses generated by the circuit into a unidirectional power source, a temporary storage capacitor 29, a battery 35 to store the charge, and a DC/DC converter 30 to transfer the charge from the temporary storage capacitor 29 to battery 35 at the required voltage for charging the battery and storing the accumulated charge.

An important aspect of the invention is that: (a) the circuit formed during the closure of switch S1 has a resonant frequency (f_0) primarily determined by the source capacitance C_p and the inductance of inductor 26 including any other series inductance along the loop [However, for ease of discussion, inductances other than that of inductor 26 have been neglected.]; (b) f_0 is made to be significantly larger than f_1 , where f_1 is the frequency of the input or driving signal (E_p) generated by PEG1; (c) the switch S1 is closed at the peak (positive and negative) of the input signal (E_p); and (d) switch S1 is closed for a period of time (T_c) approximately equal to one half of a cycle of the resonant frequency f_0 ,

during which time a pulse of charge moves from the source PEG1 to the load. The switch S1 is closed for a period which is long enough to allow current to flow through the inductor until the current reaches zero. Switch S1 is then opened.

The opening and closing of switch S1 may be accomplished in several ways, as discussed below. One way is with a self-commutating ("self-quenching") switch which is turned-on when triggered and which turns itself off when the current reaches zero. Another way is to actively control the turn-on and turn-off of switch S1.

The operation of the circuit of Fig. 3A may be better explained with reference to the waveform diagrams shown in Fig. 4 and waveform A of Fig. 5 and assuming that: (a) the electrical signal generated by the PEG is a sinusoidal signal, (b) when E_p reaches a peak (negative or positive) at time t_1, t_3, t_5 , etc..., the switch S1 is triggered closed (i.e., turned-on). (c) When the current flowing through switch S1 reaches zero, or approximately zero, the switch is opened until the next peak/trough of the input signal. For ease of illustration, assume also that the driving frequency is 2Hz and the resonant frequency is 70Hz, whereby the period (T_c) is approximately 7 milliseconds. Referring to Figs. 4 and 5 the operation of the circuit for turning on S1 at the positive and negative peaks of the input signal E_p may be described as follows:

- 1- circuit operation from time t_0 to time t_1 : switch S1 is open and as E_p increases from 0 volts to a peak of E_1 , the voltage (V_x) at plate X of C_p also increases to a value of E_1 .
- 2- circuit operation from time t_1 to t_{11} : when switch S1 is closed at time

t_1 , V_x goes from a value of E_1 to a value of approximately $(-)E_1$. That is, following the switch closure the resonant circuit (capacitor C_p and inductor L_{26}) causes a reversal of the voltage at plate X of capacitor C_p during the first one-half cycle of the resonant circuit. Note that it is during this period that power is extracted from the PEG and transferred to the load circuit. The switch S_1 remains closed as long as current passes through it. When the current through the switch S_1 reaches zero, or approximately zero, switch S_1 opens (or is actively opened) at time t_{11} .

3- Circuit operation from time t_{11} to time t_3 : at time t_{11} , with V_x near $(-)E_1$, switch S_1 opens. Then, after E_p goes from E_1 to approximately $(-)E_1$, from time t_{11} to time t_3 the voltage on V_x follows that of E_p , and thus V_x goes from $(-)E_1$ at time t_{11} to $(-)3(E_1)$ at time t_3 .

4- Circuit operation from time t_3 to time t_{31} : At time t_3 with E_p at $(-)E_1$ and V_x at $(-)3(E_1)$, switch S_1 is closed at time t_3 , forming the series resonant circuit which includes C_p and L_{26} . The resonant circuit causes a reversal in the polarity of the voltage at plate X of C_p with the voltage V_x going from $(-)3E_1$ to nearly $+3E_1$. From time t_3 to time t_{31} , the charge is pushed through the load. At time t_{31} , after a pulse of charge has moved through the switch and the current in the switch returns to zero, switch S_1 opens or is opened. As before, it is during the time period of t_3 to t_{31} that power is extracted from the PEG and transferred to the load circuit.

- 5- Circuit operation from time t_{31} until time t_5 : as E_p goes from $-E_1$ at time t_{31} to $+E_1$ at time t_5 , V_x follows from a value of $+3E_1$ to a value of $+5E_1$.
- 6- The operation described above is repeated at each positive peak and trough (valley or negative peak) of the input signal. However, instead of building indefinitely, the voltage of V_x approaches a steady-state value, when electrical losses in the resonant system, including power lost in the load, prevent the voltage of V_x from reversing itself completely. Consequently, the amplitude of the signal at V_x reaches a quiescent value as shown in Figs. 4, 5, 7 and 8. When the system reaches the steady state, the power extracted in the form of resonant pulses matches the power supplied by the input power source, PEG1. When the resonant circuit is not overdamped and the voltage at V_x is allowed to build up, then the magnitude of the steady state voltage at V_x will be comparatively large compared to that of E_p .
- 7- In circuits embodying the invention, the power extraction occurs for a time T_c ; where T_c is equal to $1/2f_0$, and where f_0 is the resonant frequency of the series loop with the switch closed. For the simplified circuit showing PEG1 in series with a switch an inductive element and a load: f_0 is approximately equal to $1/2\pi(LC_p)^{0.5}$ and the switch is closed for a period of time which is approximately equal to $T_c = \pi(LC_p)^{0.5}$.
- 8- Referring to the waveform for V_{load} in Fig. 5, note that the current

pulses through the resistive element result in pulses for a time period T_c following each peak (e.g., at times t_1 , t_3 and t_5) of the input sinusoidal wave. For each pulse, the current rises from zero increases to its maximum value and then returns to zero a time T_c after each switch closure corresponding to a peak.

There is a trade-off between voltage build-up and power extraction, as there is in any resonant system. Consequently, there is an optimum load for transferring the maximum power using this method. An approximation of this optimum and maximum can be derived mathematically or computed empirically.

Theory of Operation

[Note that certain reasonable and realistic approximations have been made in the mathematical analysis below]

Circuit operation is best described by referring to the schematic shown in **Figure 6**. In **figure 6**, R_P represents an equivalent dielectric loss resistance of the PVDF and R_S a series loss term equal to the combined effect of coil winding resistance and the electrode resistance of the piezoelectric element. Thus, $R_S = R_W + R_{\text{electrode}}$. In one embodiment, the 424 volt rms voltage source and $0.45 \mu\text{F}$ capacitance correspond to 1 electroded piezoelectric film being strained at $\pm 1\%$. The parallel loss resistance R_P of $10 \text{ M}\Omega$ is based on a $\tan \delta$ of $\approx 2\%$. In the embodiment of Fig. 6 the inductor was selected to have a value of 11.6 H and had a winding resistance of 65Ω . The $R_{\text{electrode}}$ was assumed to equal 100 ohms , whereby R_S was equal to 165Ω . As discussed below a value of R_L

equal to 345Ω was selected, since for the circuit of Fig. 6 a value of $R_L = 345$ ohms is the optimum for maximum power transfer with the parameters shown.

For purpose of illustration, assume E_p to provide a sinusoidal input to the piezoelectric element with an input frequency $f_{IN} = 2$ Hz, and further assume that intelligent switch S1 closes at the positive and negative peaks of the input signal E_p . The switch opens or is opened when the current passing through the inductor is equal to zero or approximately equal to zero. In the case where the impedance of the source is much greater than the impedance of the load, a quasi-sinusoidal current pulse, with a duration of one half cycle of the resonant period of the L-Cp network, moves through the inductor to the load. The switch closure time may then be approximated as $T_c = \pi (LCp)^{.5}$ which in this embodiment would be 7.18 msec.

Given an initial value V_N across Cp, the voltage during the closure interval is

$$V_C(t) = V_N \exp(-\omega_0 t / (2Q_L)) \cos(\omega_0 t) \quad (1)$$

where $\omega_0 = 1/(LCp)^{.5}$ and $Q_L = \omega_0 L / (R_L + R_S) = (L/Cp)^{.5} / (R_L + R_S) \equiv R_0 / (R_L + R_S)$

During this same interval, the current through the inductor L and the load R_L is

$$i_L(t) \approx V_N / R_0 \sin(\omega_0 t) \quad (2)$$

At $t = T_c$, the switch S opens with $v_C(T_c) = -V_i \exp(-\pi / (2Q_L)) = -aV_i$ ($a \approx 1$) and

$i_L(T_c) = 0$.

with a defined as

$$a \equiv \exp(-\pi / (2Q_L)) \quad (3)$$

After the switch is opened, the switch remains open until the next peak (a negative peak) occurs. During this period, V_C will continue to charge (more negatively) due to the charge produced by the piezo element under the applied strain. The waveform here is

$$V_C(t) = V_P \cos(\omega_{IN} t) - (a V_N + V_P) \exp(-t/\tau) \quad (4)$$

where $\tau = R_P C_P$ represents the dielectric loss time constant of the piezo and V_P is the peak open circuit voltage.

At the next peak, S closes for the same period T_c and produces the same behavior except that $(V_i)_+$ is now a much higher value. Just prior to this next switch closure at $t = 1/(2f_{IN})$, $\omega_{IN} t = \pi$, V_C reaches a value V_{N+1} which is

$$V_{N+1} = - (V_P (1+b) + abV_N) \quad (5)$$

and $b \equiv \exp(-1/(2 f_{IN} \tau)) = \exp(-\pi/Q_C)$, $Q_C = 2 \pi f_{IN} \tau = 1/\tan \delta$.

This process repeats every half cycle (with a sign change) until steady state is reached when the energy added by the piezo device is offset by the energy transferred to the load R_L plus losses in the electrodes, inductor, and PVDF dielectric. Using Eqn. (5) recursively predicts the build up in voltage as a function of the number of half cycles of the input. It can be shown that the build up time constant is approximately Q_L cycles. The steady state voltage is the parameter of interest as it determines the steady state power transferred to the load. This is obtained by setting $V_{N+1} = -V_N$ in Eqn. (5) which yields

$$(V_C)_{SS} = V_P (1+b)/(1-ab) \quad (6)$$

This peak voltage will produce a half sine wave current pulse into the load having width T_C and peak amplitude

$$(I_L)_{MAX} = (V_C)_{SS}/R_0 \quad (7)$$

Finally, the average power output is obtained from Eqn. (7) as

$$P_{OUT} = ((I_L)_{MAX})^2 R_L T_C f_{IN} \quad (8)$$

The high-voltage build up across the reactive elements proportional to Q is typical of a resonant circuit. The significant aspect of this pulsed approach is that the resonant frequency and input frequency are independent so that realistic component values are possible, despite very low input frequencies. Shown in Figures 7 and 8 are the steady state voltage and load current waveforms produced by a computerized software circuit program (SPICE) simulation of the circuit of Figure 6.

3. Optimum Power Transfer

There is an optimum value for R_L which maximizes power output. This can be seen by manipulation of the equations 6-8. First, recognize that the parameters a and b as defined previously are both ≈ 1 for large Q_L and Q_C (>5). Thus, Eq. (6) can be approximated by

$$(V_C)_{SS} \approx 2 V_P / \pi (1/(2 Q_L) + 1/(Q_C)) = 4 V_P Q_L Q_C / \pi (2 Q_L + Q_C) \quad (9)$$

and the power output can be rewritten as

$$\begin{aligned} P_{OUT} &= (V_{CSS})^2 T_C f_{IN} R_L / R_0^2 \\ &\approx 16 C_P V_P^2 Q_L^2 Q_C^2 f_{IN} R_L / (\pi R_0 (2 Q_L + Q_C)^2) \end{aligned} \quad (10)$$

Recalling that the mechanical power input is

$$P_{IN} = C_P V_P^2 f_{IN} / (2 k^2) \quad (11)$$

$$(k^2 = \text{electro-mechanical coupling factor} = d_{21}^2 Y / \epsilon)$$

the mechanical to electrical power conversion efficiency P_{OUT} / P_{IN} is then

$$\eta = (32 k^2 / \pi R_0) * ((Q_L^2 Q_C^2 / (2 Q_L + Q_C)^2) * R_L) \quad (12)$$

Substituting for $Q_L = R_0 / (R_L + R_S)$, and maximizing with respect to R_L

$$(R_L)_{OPT} = R_S + (2 R_0 / Q_C) \quad (13(a))$$

$$(Q_L)_{OPT} = R_0 / 2 (R_S + R_0 / Q_C) \quad (13(b))$$

Incorporation of the above into Eqn. (12) gives the maximum value of conversion efficiency

$$\eta_{MAX} = 8 k^2 R_0 / (\pi (R_S + 2 R_0 / Q_C)) \quad (14)$$

Applicants' invention thus includes the recognition that there is an optimum value of resistance, $(R_L)_{OPT}$, and that this value of resistance is a function of: (a) $R_S = R_W + R_{\text{electrode}}$ and (b) a function of $2R_0/Q_C$ where R_0 is equal to $(L/Cp)^{.5}$ and Q_C is equal $Q_C = 2 \pi f_{IN} \tau$ and τ is equal to $RpCp$.

Applicants further recognized that since $(R_L)_{OPT}$ may vary as a function of a frequency of the input signal, the frequency of the input signal may be monitored and the value of $(R_L)_{OPT}$ may be varied as a function of frequency to keep $(R_L)_{OPT}$ optimized. As discussed below this may be accomplished by applying (via switches) more or less load to the system. That is, the average value of the load may be changed by controlling the on to off period during which the load is applied to the system. This is illustrated in Fig. 2 where the wave peak sensor 200 responsive to the amplitude and frequency of input mechanical stresses and strains supplies signals to peak detector 34 which supplies corresponding signals to microcontroller 301 which can then control DC/DC

converter 30. Similarly, Fig. 9 shows that a sensor 303 responsive to the amplitude and frequency of input mechanical stresses and strains (e.g., from ocean waves) supplies signals to a microcontroller 301 which is programmed to control one or more of the subsystem components, that is: (a) switch S1; (b) the inductance of L1; and (c) various components of the load (e.g., the load capacitance and the resistive load). Note that Fig. 9 also illustrates that the output may be monitored and signals may be supplied to an output load sensor 305 whose output is then fed to the microcontroller which again may be programmed to take whatever action is needed to optimize the response of the system.

Type of Load May Be Varied

While the above theory of operation applies to the purely resistive load case, applicants have found that the switched resonant power extraction circuit need not be restricted to a purely resistive load. In fact, a parallel resistive-capacitive load may be preferable in some cases where the desired output is a DC voltage rather than a pulse. This can be accomplished using a circuit of the type shown in Fig. 9. In Fig. 9 a full wave rectifier 270 is used to convert the AC signal produced in the series loop (PEG22, switch S1, inductor L1 and the load) to a DC output voltage which may be stored in a capacitor and/or a battery and/or to drive the load illustrated as RLOAD, which may be, in practice, a complex load.

Changing Location of Switch

In addition to varying the impedance characteristics of the load, it is also

possible to rearrange the order of the circuit elements which are connected in series. In Figs. 3A, and 6 the switch S1 is located between PEG (capacitor C_p) and the inductor L. However, the location of switch S1 can be moved to another location along the series path of the resonant circuit. For instance, the switch S1 can be placed between the inductor and the load as in Figure 3B. Alternatively, the switch S1 may be located between the ground return terminal and the load R_L , as in Figure 3C. Moving the switch from between the inductor L and the piezoelectric device PEG1 as shown in Fig. 3A to another position on the load side of the inductor as shown in Fig. 3B or in the bottom leg of the load as shown in Fig. 3C presents certain advantages in the turn on and turn off voltages needed to operate the switch. This is particularly so, where the switch is formed using a combination of transistors which can conduct bi-directionally (e.g., MOSFETs) as illustrated in Fig. 3D.

The switching network of Fig. 3D includes a smart switch, S1, connected between terminal 39 and ground return 33. The smart switch includes two parallel paths connected between terminals 39 and 33 to control conduction of current through the load for a positive going input voltage and for negative going input voltage. One path includes a diode D1A connected in series with the conduction path of an n-channel MOSFET, N1, whose source and substrate are connected to node 33. The other path includes a diode D2A connected in series with the conduction path of a p-channel MOSFET, P1, whose source and substrate are connected to node 33.

N1 can be turned on (when E_p is highly positive) by the application of a

relatively small value (e.g., 3-10 volts) of a positive going voltage to the gate of N1. N1 stops conducting when the current through the inductor goes to zero. Alternatively, N1 can be turned-off by clamping the gate of N1 to terminal 33 (or by the application of a small negative voltage to the gate of N1). Note that when E_p goes positive conduction through P1 is blocked by diode D2A.

P1 can be turned on (when E_p is highly negative) by the application of a relatively small value (e.g., 3-10 volts) of a negative going voltage to the gate of P1. P1 stops conducting when the current through the inductor goes to zero. Alternatively, P1 can be turned-off by clamping the gate of P1 to terminal 33 (or by the application of a small positive voltage to the gate of P1). Note that when E_p goes positive conduction through N1 is blocked by diode D1A.

The location of the smart switch between ground and the load 27 as shown in Fig. 3D illustrates the importance of locating the switch at certain points along the loop to enable the transistors to be turned on and off with low gate to source voltages (V_{GS}). It should also be noted that Diodes D1A and D2A ensure that only one of the two "parallel" conduction paths is conducting at any one time. In the absence of Diodes D1A and D2A the transistors (N1 and/or P1) would be driven into conduction (i.e., N1 when E_p goes negative, and P1 when E_p goes positive) during the wrong phase of the conduction cycles. Note that in Fig. 3D the switch S1 may be controlled by a controller 301 which is responsive to signals from a sensor 303 which senses the frequency and/or the peaks and/or the amplitude of the naturally recurring force applied to the transducer (PEG 22). Fig. 3D illustrates that sensor 303 can supply signals to controller 301 which can

then be used to control the turn on and turn off of switch S1 by supplying turn on and turn off signals to the gate electrodes of transistors N1 and P1. Thus, the controller 301 determines when the switch is to be turned on (e.g., positive signal to the gate of N1 for positive going E_p and negative going signal to the gate of P1 for negative going E_p) and when the switch is to be turned off by applying a turn off signal to the gates of N1 and/or P1.

Properties of the Switch

Finding a switch that can operate in a switched resonant circuit may be difficult for several reasons. First, it must have a very high impedance in the off state, and a very low impedance in the on state. Second, it may be required to block significantly large positive and negative voltages. Third, it may be required to handle significantly large pulsed currents. Fourth, it must have a turn-on and turn-off time that is substantially shorter than the expected time of switch closure. Fifth, it may be required to handle current in both directions. Sixth, it must be able to be closed precisely by the controller. When the term "switch" is used, it refers to a device which performs the function of a switch, and the device itself may be composed of several different components.

Applicants have examined several circuit designs which address the requirements of a switch. Any type of switch can work with varying degrees of success. In a mechanical design, the switch in a switched resonant circuit may be a hydraulic valve. Alternatively, the switch may be a relay contact driven by a relay coil with signals from a control or peak detector, or any other suitable control circuit. However, there may be a problem using relays in high speed

operation. For electrical circuit designs for use with the previously described piezo sources, a solid state electrical device presents certain advantages. Under most conditions, to achieve high impedance with short turn-on and turn-off time periods, MOSFET transistors work well (as already shown in Fig. 3D).

Another embodiment of a circuit using a smart switch including MOSFETs and diodes is shown in Figure 10. Note that in the circuit of Fig. 3D, switch S1 was turned on by a controller and turned off again by the same controller at time T_c after S1 was turned on and the current through the loop is zero, or close to zero. However, it is possible to arrange a circuit such that the controller turns on switch S1, but S1 shuts itself off when the current passing through it and inductor L1 reaches zero. This can be accomplished by placing diodes in series with switching transistors, as shown in Figure 10.

In Figure 10, PEG 22 is connected to one side of the inductor L1 and the other side of the inductor is connected to the load 27 via a smart switch. The smart switch of Fig. 10, like the smart switch of Fig. 3D, includes a first path for conducting current through the load when E_p is positive and another path for conducting current through the load when E_p is negative. The one path includes an N channel MOSFET T1A having its conduction path connected between terminal 37 and a node 38 and a diode D1B having its anode connected to node 38 and its cathode connected to terminal 39. The other path includes a P channel MOSFET T1B having its conduction path connected between terminal 39 and node 38 and a diode D1A having its anode connected to node 38 and its cathode connected to terminal 37. In Fig. 10 a CMOS logic inverter comprised

of MOSFETs T1B and T2B is used to bias the gates of MOSFETs T1A and T1B. When the voltage across C_p is large and positive, the current is blocked by the diode D1A and T1A, whose gate is in the off state biased at V_{X2} Volts; where V_{X2} may be in the range of -5 to -100 volts, or more, as described below. Though T1B is in the "on" state, it does not conduct electricity because there is no free path for the charges to move past the T1A gate. When E_p across C_p reaches a peak positive voltage, a signal from the peak detector toggles T2A and T2B, which in turn causes the gate voltages of T1A and T2B to go from V_{X2} Volts to $+V_{X1}$ Volts; where V_{X1} volts may be in the range of 5 volts to 100 volts, or more. With T1A turned-on charges move from C_p through the T1A through diode D1B through R_{load} , to ground, causing a voltage and current pulse seen by the components of the smart switch. However, once the voltage pulse reaches zero, charge cannot move back through T1A because the diode D1B prevents current from passing back through that path. Conduction then ceases.

When E_p across C_p reaches its most negative peak, T2A and T2B toggle again, causing V_{X2} Volts to be placed on the gates of T1A and T1B. This closes T1A and opens T1B. Thus, negative charges move through D1A and pass through T1B and R_{load} until the negative current and voltage through T1B equal zero. After the "negative" current goes to zero, current can not move in the opposite direction through D1A, and thus the smart switch S1 is effectively open. Thus, every time a toggle occurs, reversing the conducting states of T1A and T1B, they only conduct current in one direction for a period of time until the resonant circuit tries to draw current in the reverse direction. When it does, the

diodes block the current reversal, effectively opening the switch. Using this method, it is possible to open and close the smart switch controllably and reliably.

It should be noted that as long as switch S1 is closed at each peak, and stays closed until the current through S1 goes to, or close to, zero and the switch then opens, or is opened, any type of load may be used so long as a resonant circuit is formed whose resonant frequency is significantly higher than the driving source frequency.

It should be appreciated that in the circuit of Fig. 10, transistors T1A and T1B are operated in the source follower mode. Hence the voltage developed across the load may be limited by the gate voltage applied to T1A and T1B. Accordingly, in systems embodying the invention a rectifying network 110 of the type shown in Fig. 11 may be used to generate a relatively large positive voltage (e.g., VX1) and a relatively large negative voltage (e.g., VX2). The network 110 may be connected at any point along the loop at which some of the high positive and negative transient voltages are generated. Thus, for a positive going transient a current flows through resistor R110 and diode D110 to charge capacitor C110 to produce VX1. Similarly, for a negative going transient a current flows through resistor R111 and diode D111 to charge capacitor C111 to produce VX2. An important aspect of generating the voltages applied to T1A and T1B in this manner enables the positive turn-on and subsequent turn-off of the switching transistors.

Peak Detection Schemes

Referring to Fig. 12A there is shown a segment of a piezoelectric material 121 with electrodes 122 and 123. The piezoelectric material 121 is subjected to naturally recurring forces which cause the material to be stressed resulting in the generation of an oscillating voltage across the electrodes 122 and 123. The piezoelectric material 121 with electrodes 122 and 123 may be represented as a capacitor with AC signals being generated by the piezo device as shown in Fig. 12B. For ease of discussion it has been assumed that unloaded or open circuit voltage (V_{oc}) produced across electrodes 122 and 123, as shown in Fig. 12C, may be sinusoidal in nature oscillating between a positive peak value $+V_{op}$ and a negative peak value $-V_{op}$, as shown in Fig. 12D. As discussed above, and as shown in Fig. 12E in order to optimize the power transfer from the PEG 22, it is desirable that a switch S1 be closed at each peak ($+V_{op}$ and $-V_{op}$) of the PEG output. However, when the switch S1 is closed forming a highly desirable resonant circuit, the voltage at node X is subjected to sharp positive and negative going transients as shown in Fig. 12F. The sharp transients and large voltage levels compared to the maximum input amplitude (V_{op}) make it difficult to design a sensitive noise free detector. Noise and phase shifting are the causes of basic problems (discussed below) making it difficult to detect the peaks.

The problem of sensing the occurrence of the peaks is rendered more difficult by the generation of "false" peaks and bottoms as shown in Fig. 12F1. Consequently, ascertaining the peak values and when to close the switch S1 is a significant problem.

Another major difficulty with a switched resonant circuit is detecting the

peaks and troughs accurately to close (and open) the switch at the appropriate time. First, when using capacitive devices, such as PEG 22, there is often a lag between the driving mechanical source (e.g., the naturally recurring force applied to the PEG 22) and its electrical output, as in the case of piezoelectric materials. Consequently, when switching the capacitive devices in a resonating circuit, this phase shift can cause a voltage "snap back" to occur slightly after a switch is completed, causing a false peak or bottom.

Also, the voltage build-up on the capacitor demands that one sense the voltage for peak detection over a much wider range, thereby reducing the sensitivity of the detector.

The problem of detecting the peaks more accurately may be resolved in the following manner. A dummy capacitive source, such as another piezoelectric device (PEG2), is used in addition to the power source, piezoelectric device (PEG1), which is switched to the load. Whereas the output of PEG1 is distorted as shown for V_x in Figs. 12F, 12F1 and 5, the output of PEG2 will remain as shown for V_{oc} in Figs. 12D and 12F and E_p in Fig. 5. The output of PEG2 may be fed into a high impedance circuit whereby a signal (e.g., a sine wave or oscillatory signal) which is true to the original can be sensed and used to control the switching of the inductive load via S1 across PEG1. Thus, as shown in Fig. 12G, PEG1 may be termed a power collector and PEG2 may be termed a sensor and the output of PEG2 is applied to a peak detector 130 which is used to control the closing of switch S1 at each positive and negative peak of the input signal. Thus, while the output of the power collecting device PEG1 may be a highly

complex, high amplitude transient voltage, the output of the sensor device PEG2 will conform to the driving signal. Thus, if the driving signal on PEG1 and PEG2 is a relatively smooth sine wave, PEG2 will respond and produce a sinusoidal wave and cause PEG1 to be switched and function in the switched-resonant power collection mode as discussed above. Concurrently, PEG2 will be used by the peak detector to accurately and reliably sense the peaks and troughs of the smooth sine wave and to control the switching of PEG1.

PEG2 may be of the same material as PEG1 so as to have similar characteristics. PEG2 may be mounted on PEG1, as shown in Fig. 12H, so as to sense the same or similar stresses applied to PEG1. Typically, PEG2 would be made smaller than PEG1 and may be connected or attached (e.g., glued) to PEG1 in any number of ways. The output of PEG2 will mirror the output of PEG1, with the output of PEG2 being generally smaller in amplitude. Where PEG1 is very large, PEG2 may be comprised of a number of different piezoelectric devices to sense different portions of the corresponding PEG1.

As also shown in Fig. 12H, the output of PEG2 can be fed to a peak detector circuit 130a whose output is fed to a microcontroller 301a which can process the signal received from the peak detector and provide turn-on and turn-off signals to switch S1. Referring to Fig. 2 note that wave peak sensor 200 may be a PEG2 type sensor or any other suitable sensor. Similarly in Fig. 2 the peak detector that fires pulses at peaks 34 may be a peak detector such as 130a and microcontroller 301a shown in Fig. 12H.

Using a "sensing" PEG, it is much easier to detect the peak and to

generate signals to control the turn-on and turn-off of switch S1 at the proper time. Thus a peak detecting scheme in accordance with one aspect of the invention may include two capacitive elements; one to collect power and to be switched into a resonant power collecting circuit, and the other to monitor the open circuit voltage to enable an easier form of peak detection.

For ease of illustration, it has been assumed that PEG1 is being stressed in a sinusoidal manner. However, it should be understood that this is not a necessary condition as long as the periodic positive and negative peaks of an input signal are detected and control a switch closure coupling an inductive network across a piezoelectric device at the right time for the proper length of time.

Depending on the nature of the mechanical system being used in the switched resonance mode, different types of sensors may be used. For example, if an ocean wave power device is being used, a small wave sensor buoy 410 can detect the characteristic of an incoming wave and generate signals which are then used to control the turn-on and turn-off of switch S1, as shown in Fig. 13. In the system of Fig. 13 the sensor could anticipate the wave which would impinge on the transducer a moment later. Fig. 3 illustrate that the transducer may be a Buoy 414. The sensor 410 may be a wave rider physically located just before the transducer. So placed, the sensor is in a position to alert the power capturing and converting system so that appropriate changes in the system can be introduce and be taken care of before the wave impinges on (or hits) the transducer which can be any device (e.g., a piezoelectric device or a

buoy) capable of producing an electric signal in response to a mechanical input force.

Still another scheme may include electromagnetic sensors 412 as shown in Fig. 13 to sense wave height using radio waves. Many different types of sensors may be used so long as they can reliably detect peak signals and be used to turn on the switch S1 for the required time period T_c . Nevertheless, the use of a separate sensor operating in parallel to the main power device greatly simplifies the cost, size, and amount of equipment needed.

Once a signal is detected by a sensor, a number of circuits can be used to detect the peaks and troughs of the circuit. Peak detectors have been studied extensively in electronics, and many different kinds have been used in thousands of different applications. In the case of a piezoelectric that is driven by the a mechanical source such as ocean waves or river currents, the source may have a frequency and amplitude which varies slightly due to environmental changes and random turbulence. Under such conditions, it is necessary to develop a peak detection scheme that is flexible enough to adapt to changing conditions as shown in Fig. 14.

Digitizing the signal from the sensor, fitting it to a curve through software, and then predicting when the peak will occur is one method of doing this as shown in Fig. 15. However, such calculations may require a significant amount of computational sophistication, time, and power.

Another method of performing peak detection is shown in Fig. 16. If the input force is approximately sinusoidal, then a sinusoid sensor signal (a) can be

integrated or differentiated to get a cosine wave which can be applied to simple op-amp circuit. Then, the zero crossings of the integrated/differentiated signal may be detected. If the integration/differentiation is perfect, and the signal is a true sinusoid, then the zero crossings will occur at exactly the peak of the sensor signal. Other electronics (not shown) can be used to detect a zero crossing and generate a pulse, which can be used to trigger a switch closure.

It should be understood that the switched resonance approach of the invention is applicable to any system in which the driving force varies slowly. Also, it should be appreciated that the stress and strain applied to the piezoelectric device, or any device which produces an electric voltage when stressed, may be provided by ocean waves or by being placed in a river or stream with means in the river or stream causing alternating ripples to spiral downstream and cause the piezo devices to bend around them. The piezo devices than look like a flag flapping or an eel swimming in the water. Thus, ocean surface waves and any underwater waves (or the wind) may be used. These may be referred to as mechanical (forcing) waves or naturally recurring forces like ocean surface waves and oscillating underwater or water surface vortices.

WHAT IS CLAIMED IS:

1. A combination comprising:

a first transducer for collecting power from an oscillating recurrent natural source of energy impinging on said first transducer; said oscillating recurrent natural source of energy having a variable frequency, f_1 , and a variable amplitude;

a switch means for selectively coupling a load across said first transducer, said switch means, when closed, causing said load and said first transducer to form a circuit designed to resonate at a frequency which is higher than f_1 ; and said switch closure causing a relatively large transient signal to develop within said circuit;

a second transducer for sensing at least one of the amplitude and frequency of the oscillating recurrent source of energy impinging on said first transducer and for producing an output signal corresponding to and representative of at least one of the amplitude and frequency of the oscillating recurrent source of energy; and

means responsive to said output signal produced by the second transducer for controlling the turn-on (closure) and turn-off of said switch means.

2. The combination as claimed in claim 1 wherein said first and second transducers are piezoelectric devices.
3. The combination as claimed in claim 2, wherein said first and second transducers are attached to each other so as to cause the second

transducer to be subjected to, and to sense, the same forces being applied to the first transducer.

4. The combination as claimed in claim 1, wherein said means responsive to said output signal produced by the second transducer for controlling the turn-on (closure) and turn-off of said switch means includes a peak detector.
5. The combination as claimed in claim 1, wherein said means responsive to said output signal produced by the second transducer for controlling the turn-on (closure) and turn-off of said switch means includes a peak detector and a microprocessor coupled to said switch means.
6. The combination as claimed in claim 1, wherein said means responsive to said output signal produced by the second transducer for controlling the turn-on (closure) and turn-off of said switch means includes computing means for detecting the peaks and valleys of the oscillating recurrent natural source of energy impinging on said first and second transducers for generating signals to turn-on the switch means.
7. The combination as claimed in claim 1, wherein said means responsive to said output signal produced by the second transducer for controlling the turn-on (closure) and turn-off of said switch means includes at least one of differentiating and integrating circuit means for processing said output signal.

8. A combination comprising:

a power generator for collecting power from an oscillating input force impinging on said power generator, where said input force has a variable frequency, f_1 , and a varying amplitude;

a switch means for selectively coupling a load across said power generator; said load being formed such that, when said switch means is closed, the load and said power generator form a circuit designed to resonate;

a sensor for sensing at least one of the amplitude and frequency of the input force impinging on said power generator and for producing an output signal corresponding to and representative of at least one of the amplitude and frequency of the input force; and

means responsive to the output signal produced by the sensor for controlling the turn-on of the switch means.

9. The combination as claimed in claim 8, wherein said power generator is a first transducer and said sensor is a second transducer; and wherein said first and second transducers are formed of the same material.

10. The combination as claimed in claim 8, wherein, when said switch means is closed, the load and said power generator form a circuit designed to resonate at a frequency higher than f_1 .

11. The combination as claimed in claim 8, wherein said sensor is located to sense the input force before it impinges on the power generator.

12. The combination as claimed in claim 11, wherein the power generator includes a buoy for collecting power from the input force and converting it to electrical energy.
13. The combination as claimed in claim 12 wherein the input force is ocean wave energy and wherein the sensor includes a wave rider for sensing the peak and valleys of the waves before they impinge on the buoy.
14. The combination as claimed in claim 12 wherein the input force is ocean wave energy and wherein the sensor includes electromagnetic means for sensing the peak and valleys of the waves impinging on the buoy.
15. The combination as claimed in claim 10 wherein the sensor includes means for sensing at least one of the frequency and the amplitude of the input force impinging on the power generator.
16. The combination as claimed in claim 9, wherein the first and second transducers have different sizes, the first transducer being generally larger than the second transducer.

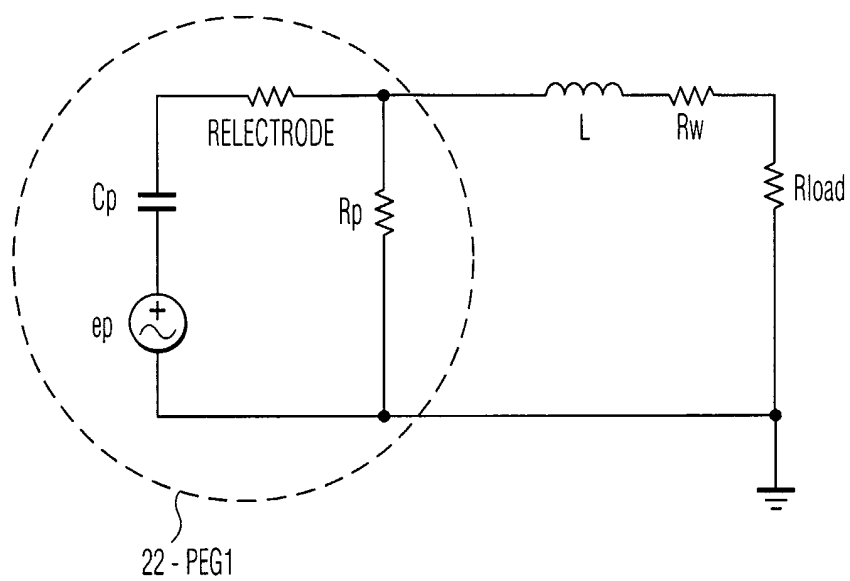


FIG. 1
PRIOR ART

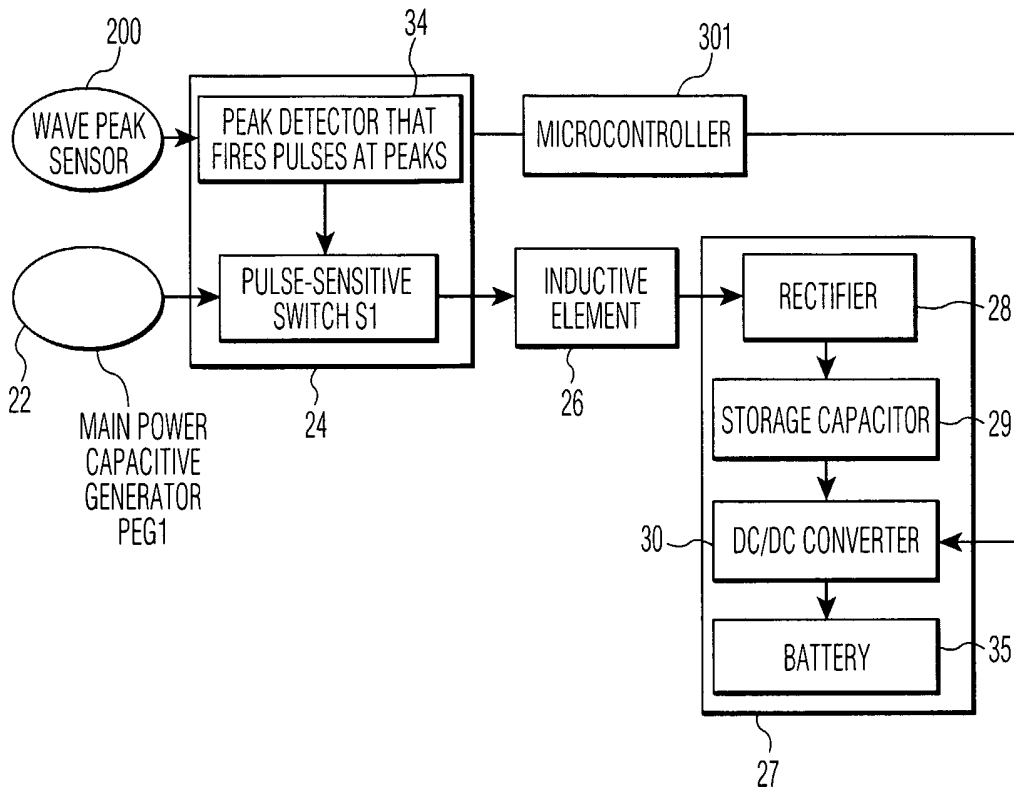


FIG. 2

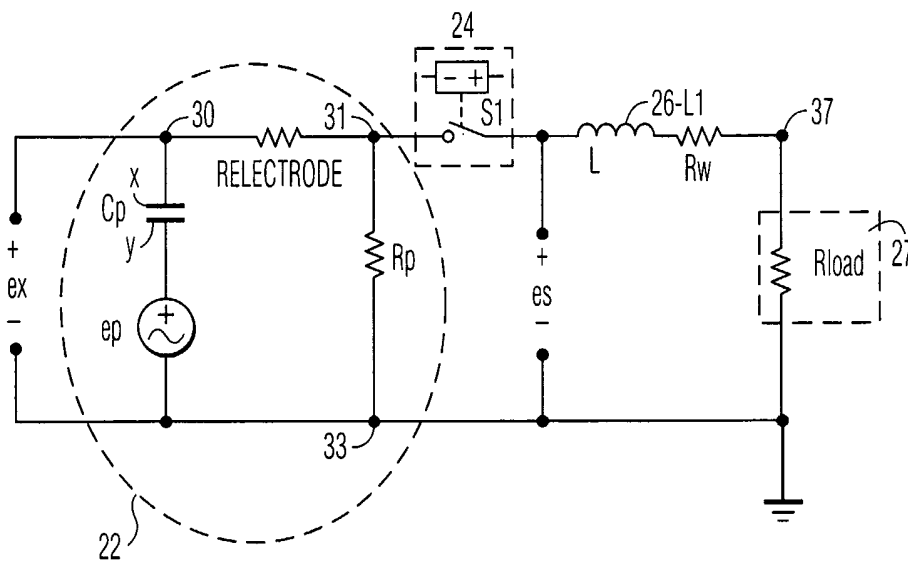


FIG. 3A

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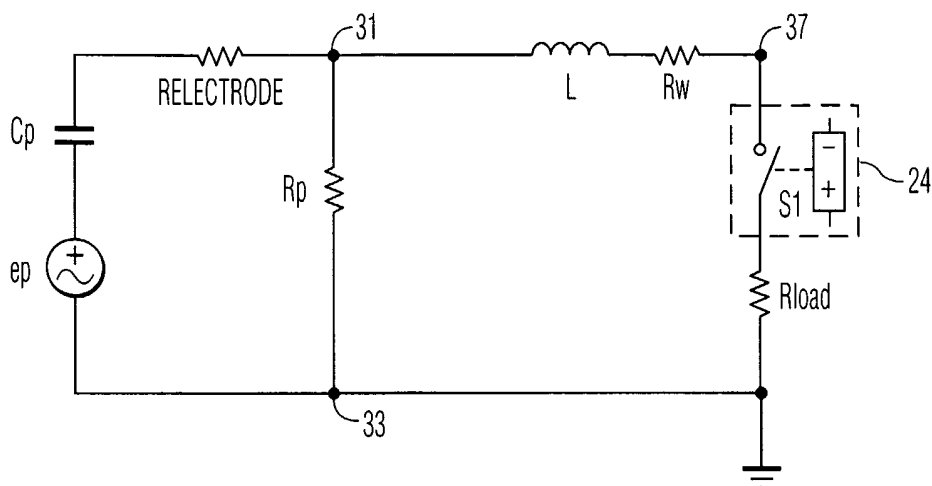


FIG. 3B

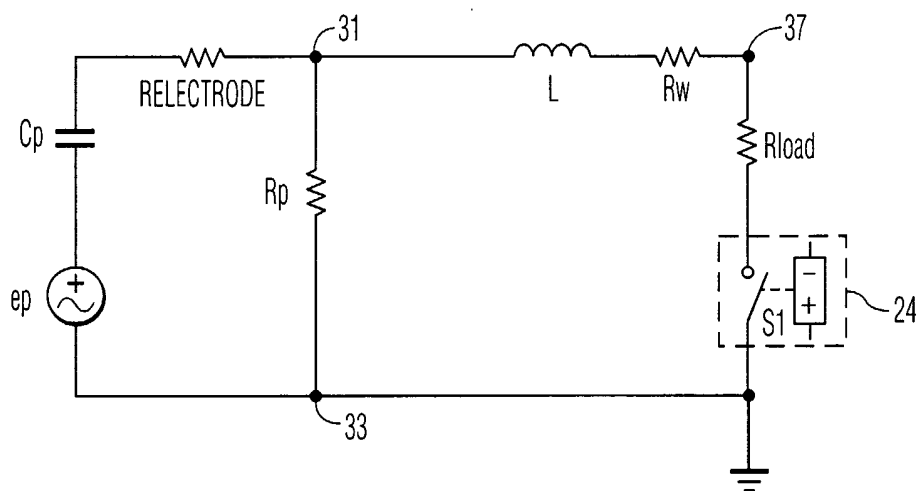


FIG. 3C

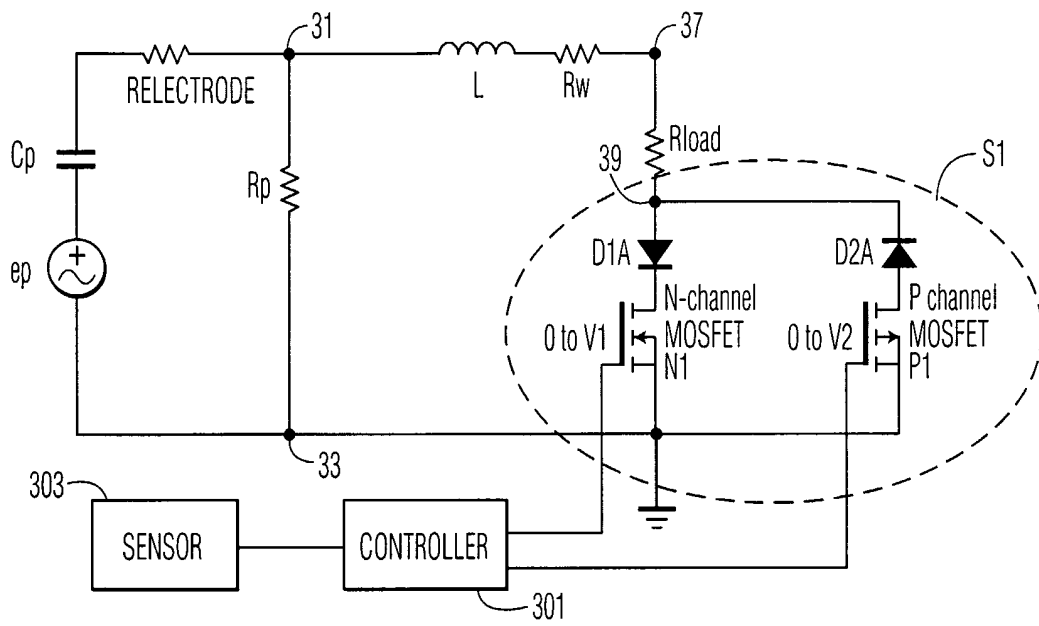


FIG. 3D

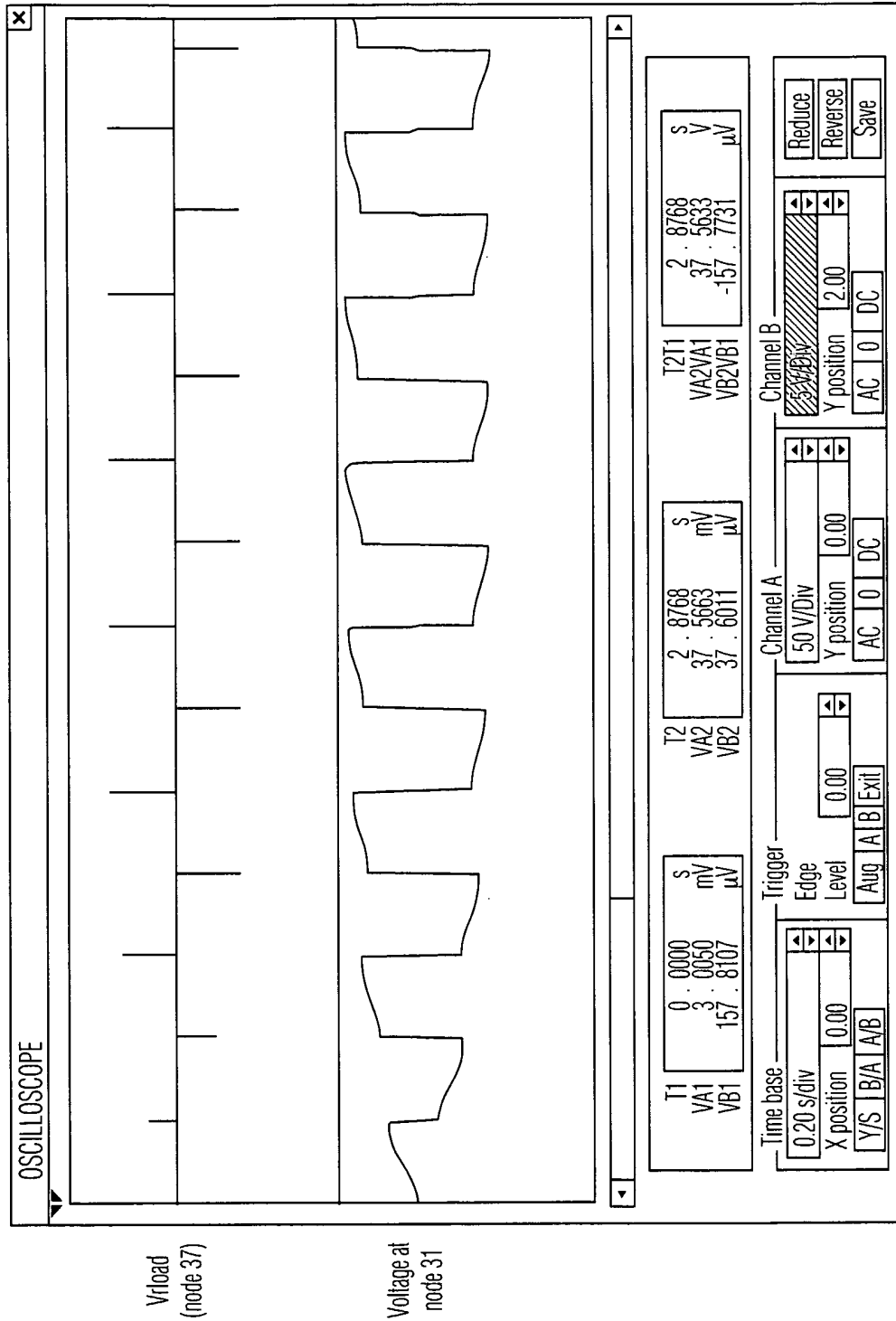


FIG. 4

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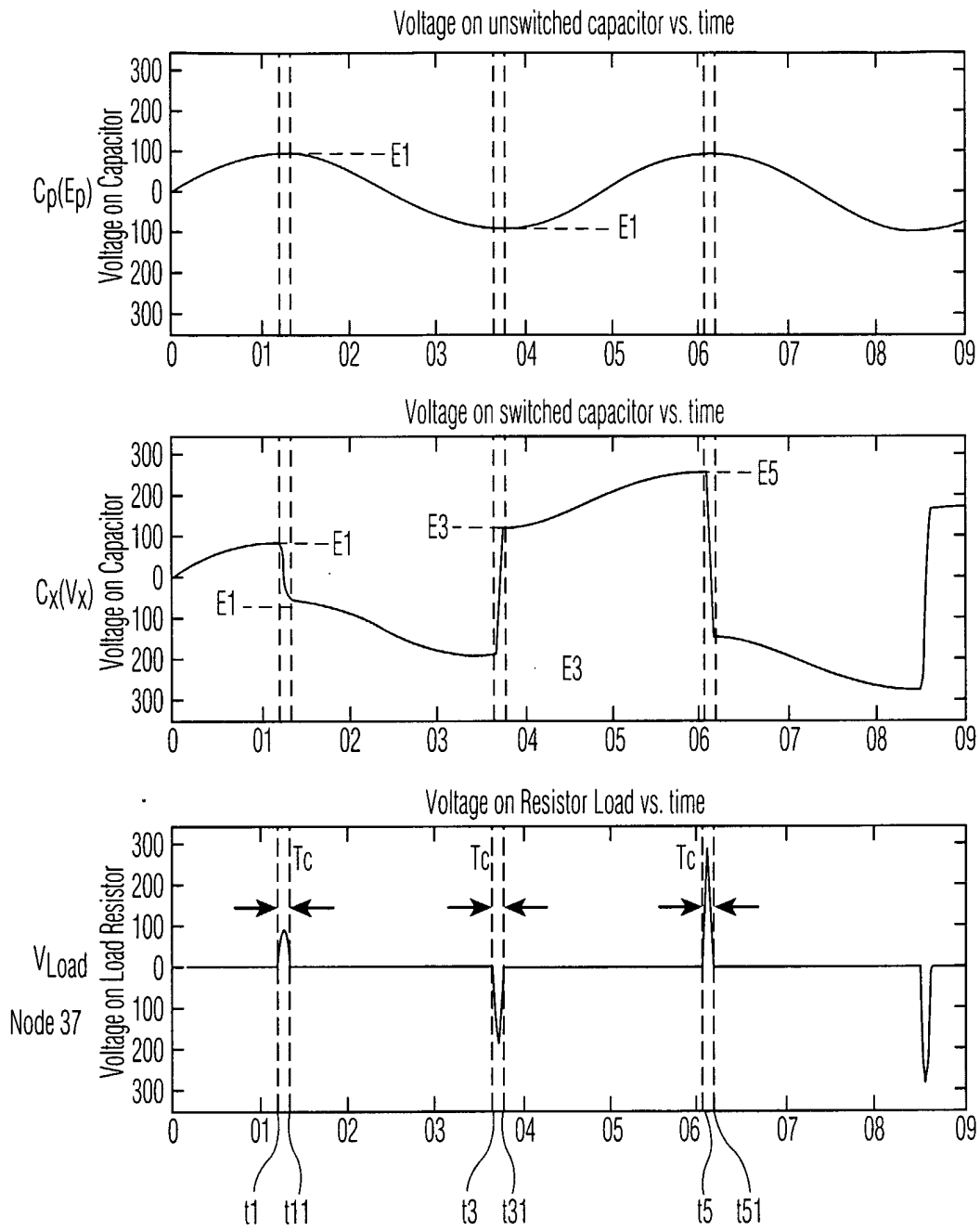


FIG. 5

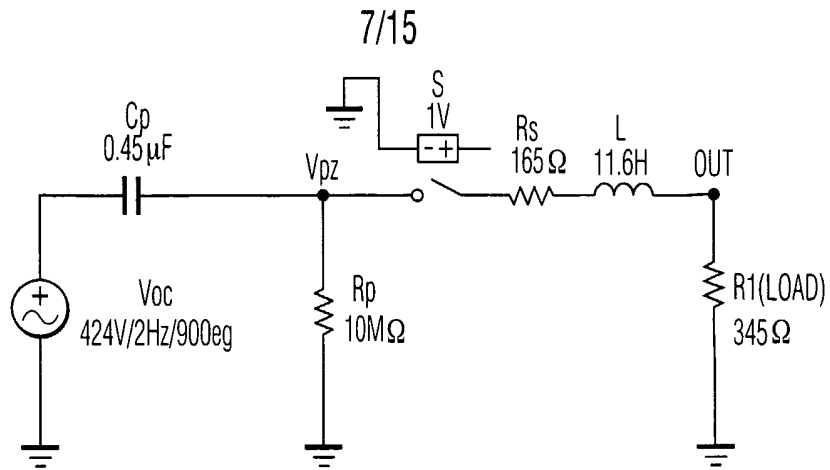


FIG. 6

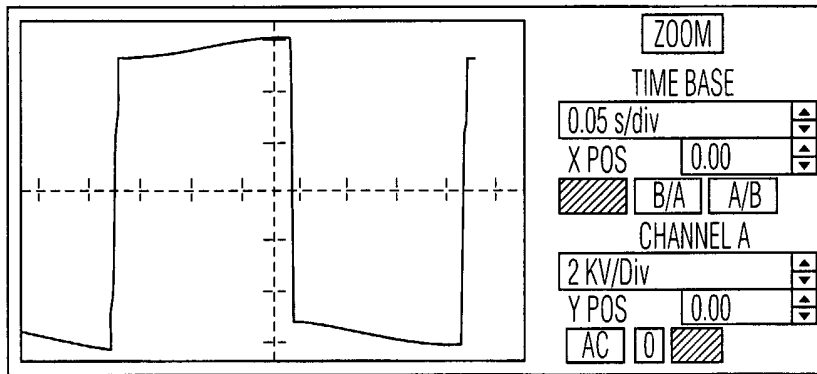


FIG. 7

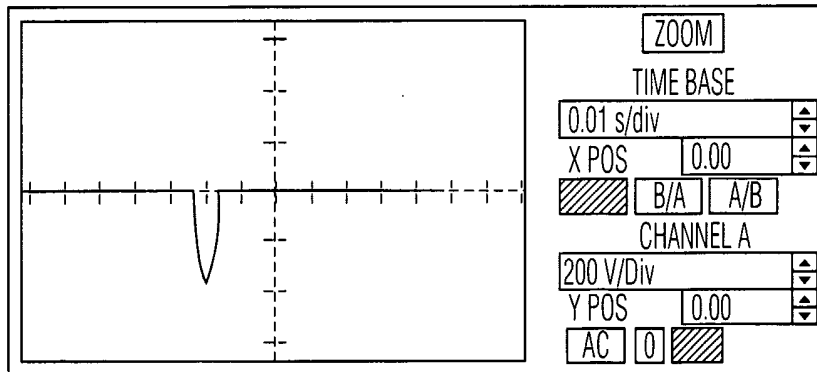


FIG. 8

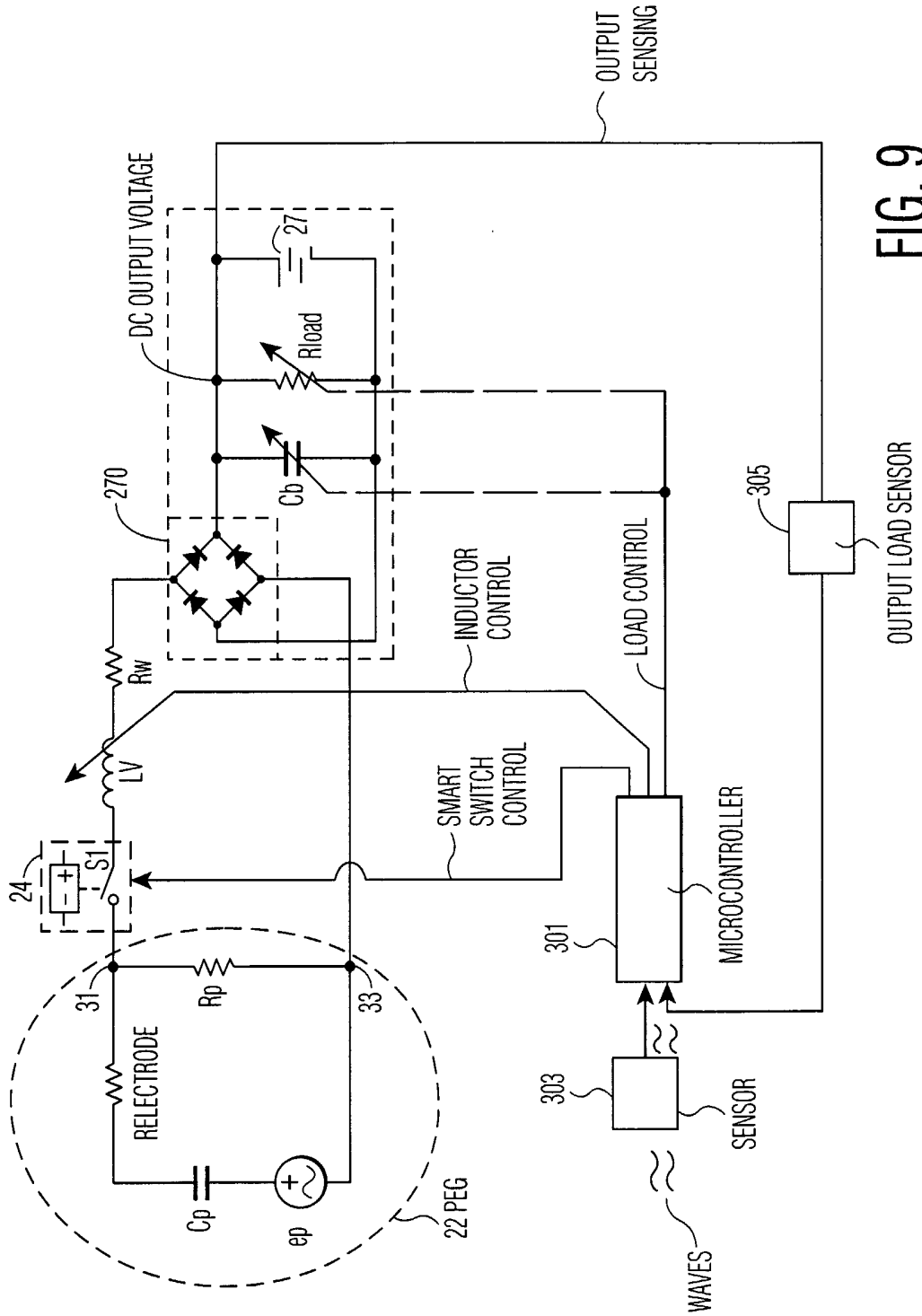


FIG. 9

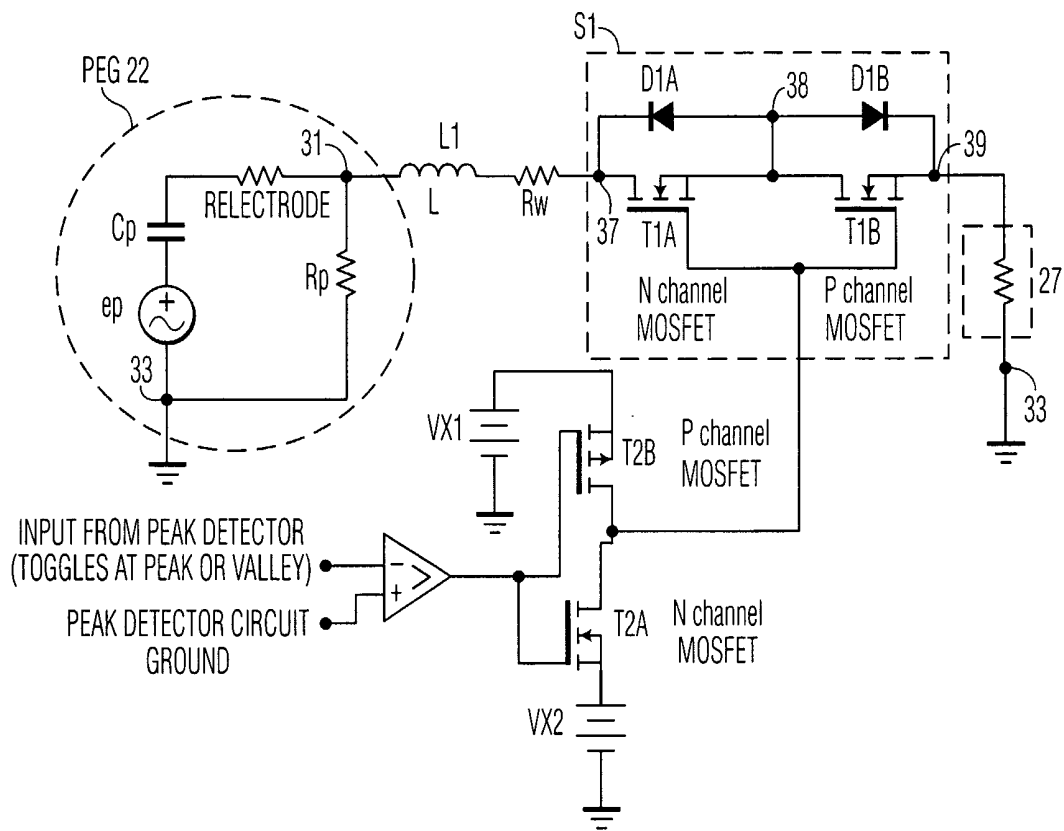


FIG. 10

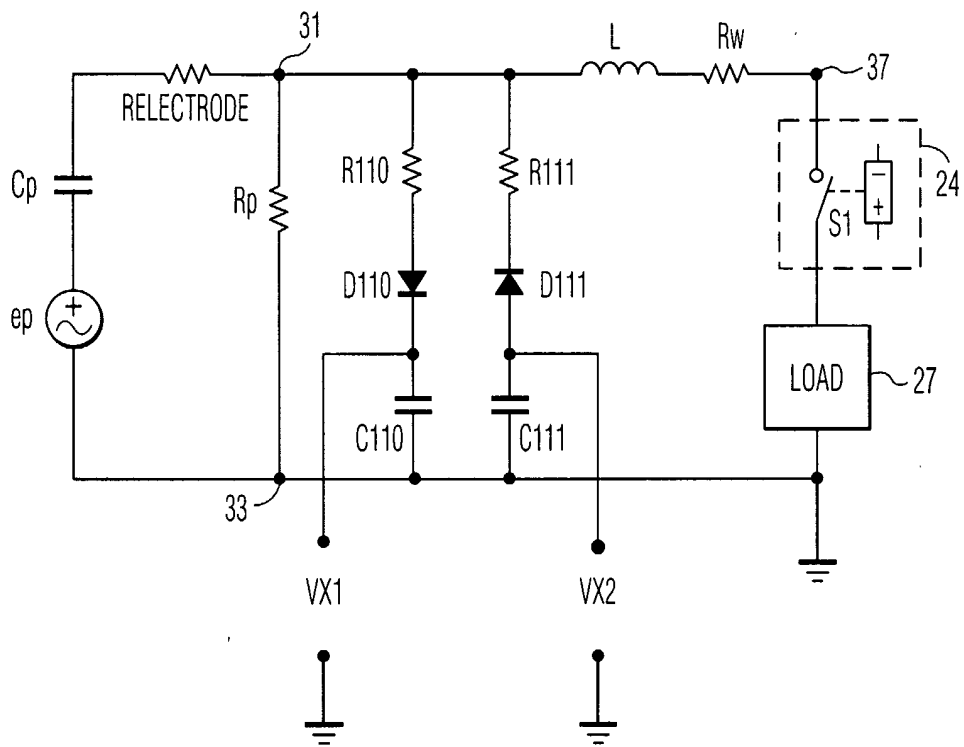


FIG. 11

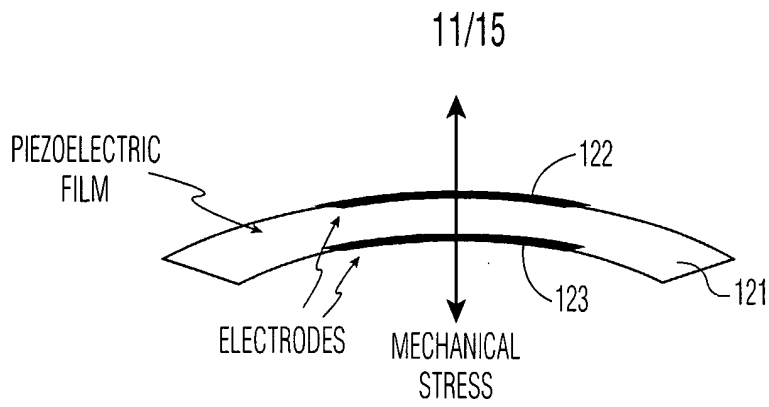


FIG. 12A

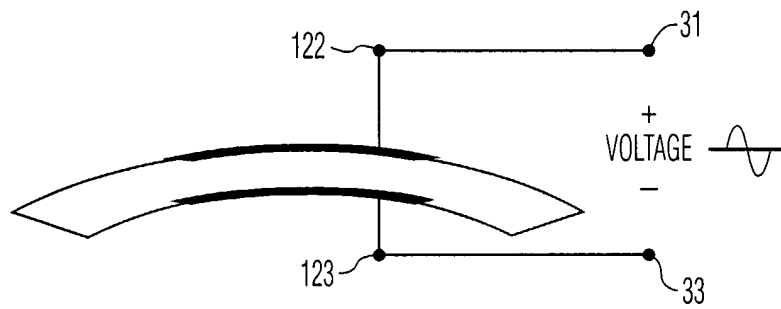


FIG. 12B

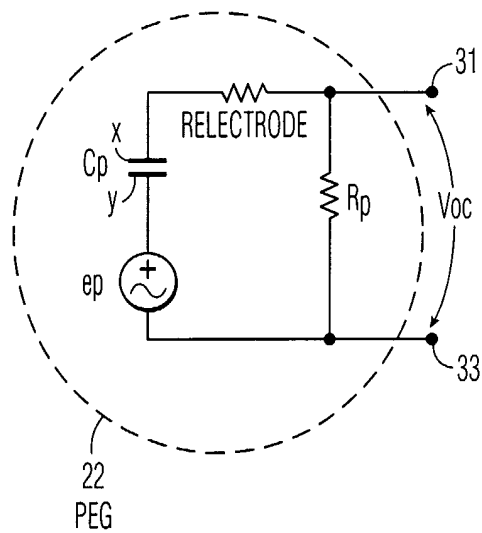


FIG. 12C

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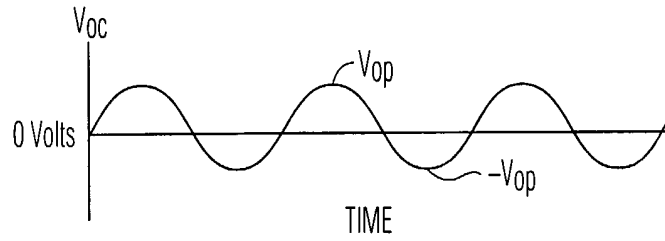


FIG. 12D

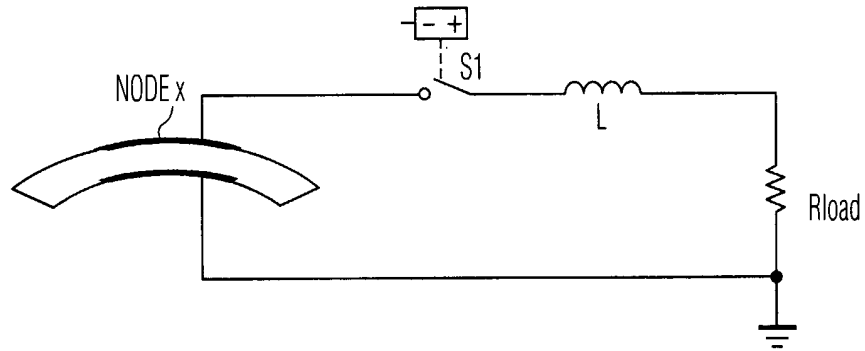


FIG. 12E

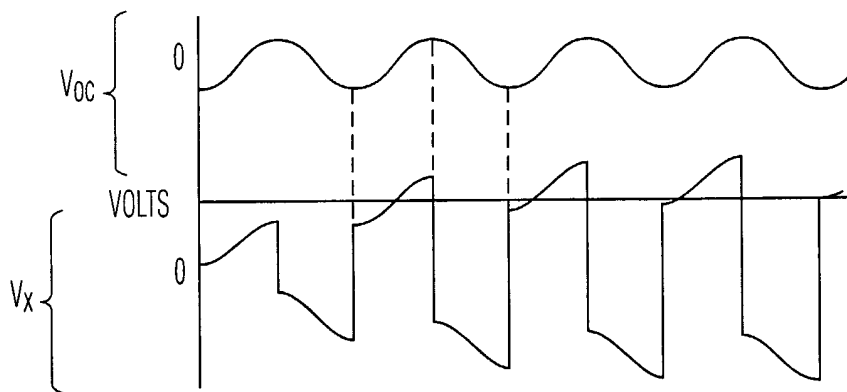


FIG. 12F

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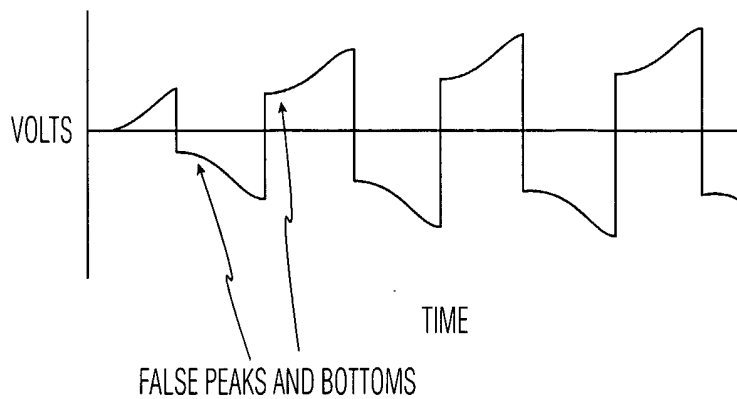


FIG. 12F1

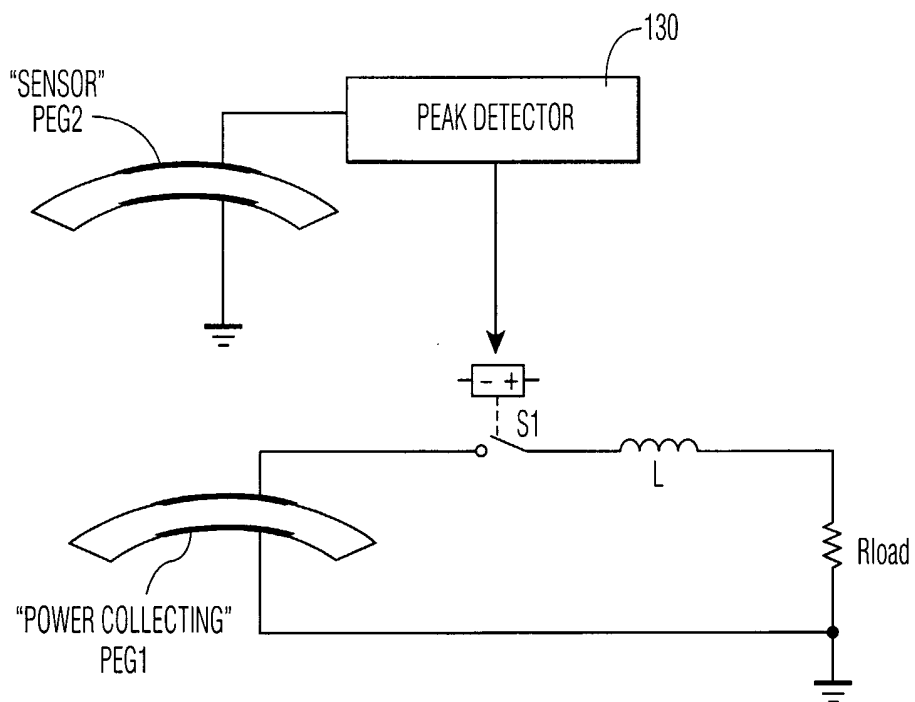
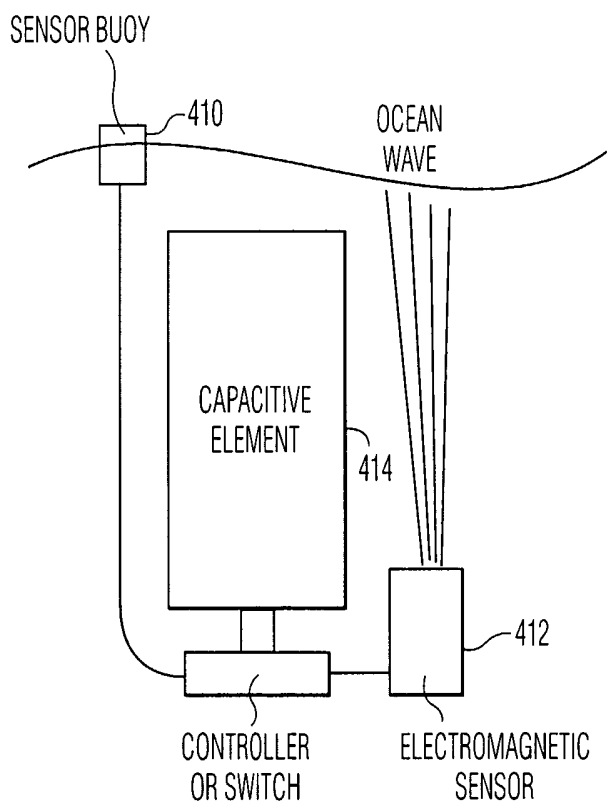
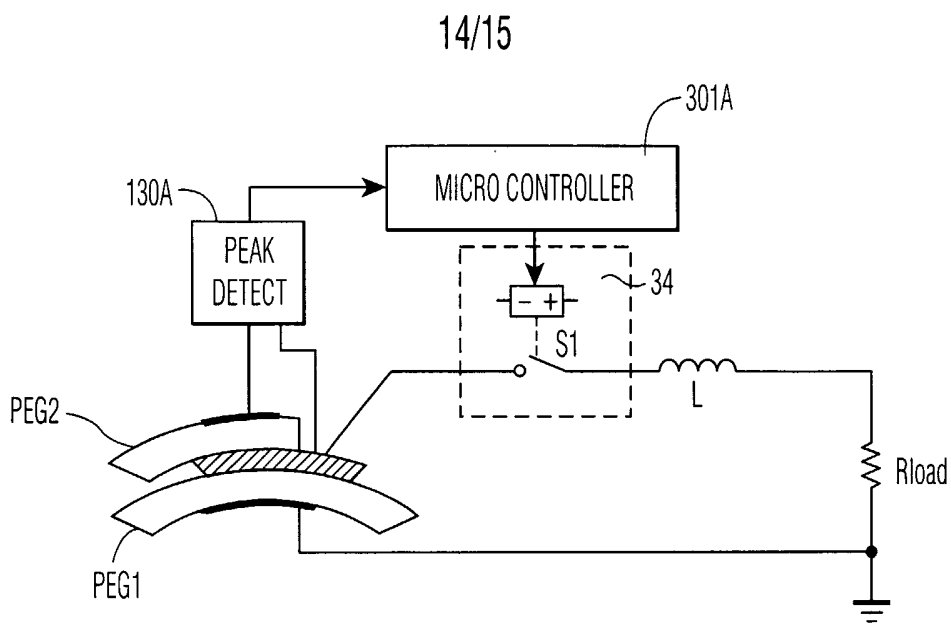


FIG. 12G



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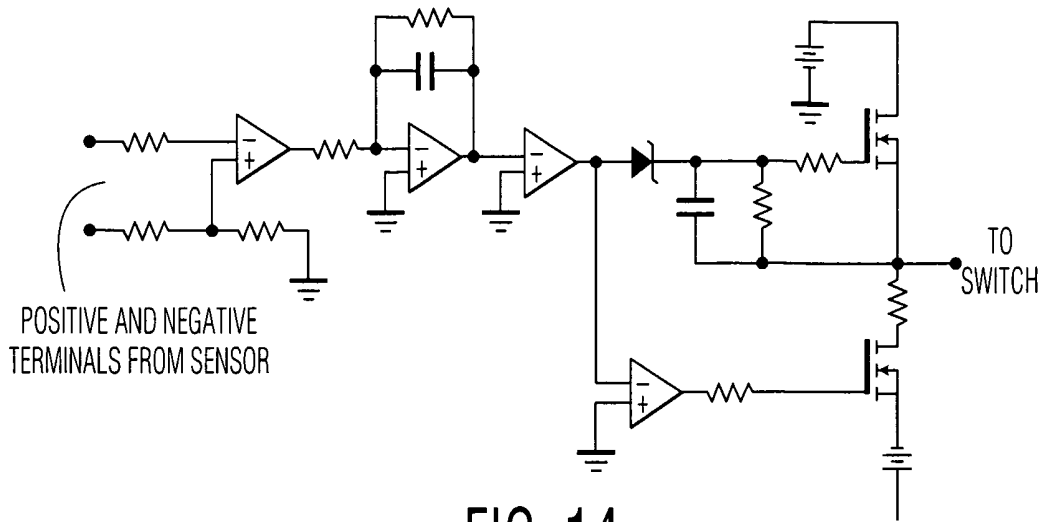


FIG. 14

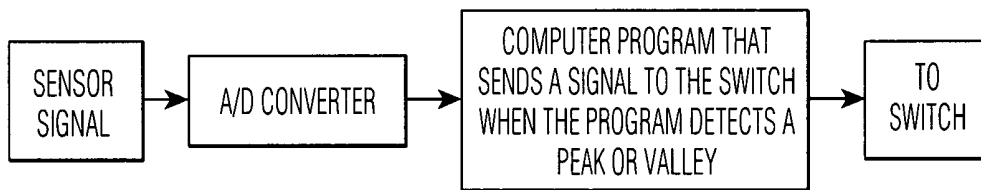


FIG. 15

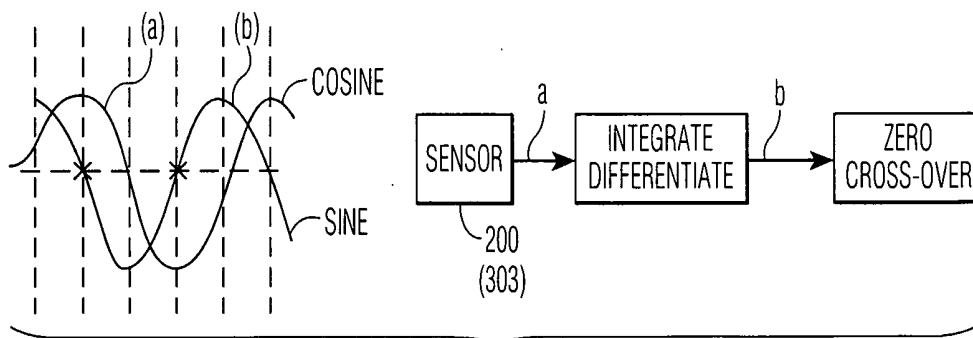


FIG. 16

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US02/23311

A. CLASSIFICATION OF SUBJECT MATTER
IPC(7) :HO1L 41/08
US CL :310/339
According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED
Minimum documentation searched (classification system followed by classification symbols)
U.S. : 310/339

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched
310/316.01,317,319,322,328,339

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 6,252,36 B1 (HALL) 26 JUNE 2001 (26/06/01) SEE ENTIRE DOCUMENT	1-16
A	US 5,801,475 A (KIMURA) 01 SEPTEMBER 1998 (01/09/98) SEE ENTIRE DOCUMENT	1-16
A	US 4,542,325 A (KOBAYASHI) 17 SEPTEMBER 1985 (17/09/85) SEE ENTIRE DOCUMENT	1-16
A	US 4,527,105 (SHIRAISHI) 02 JULY 1985 (02/07/85). SEE ENTIRE DOCUMENT	1-16

Further documents are listed in the continuation of Box C. See patent family annex.

"A"	document defining the general state of the art which is not considered to be of particular relevance	"T"	later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
"E"	earlier document published on or after the international filing date	"X"	document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
"L"	document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	"Y"	document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
"O"	document referring to an oral disclosure, use, exhibition or other means	"&"	document member of the same patent family
"P"	document published prior to the international filing date but later than the priority date claimed		

Date of the actual completion of the international search 23 AUGUST 2002	Date of mailing of the international search report 11 SEP 2002
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Name and mailing address of the ISA/US
Commissioner of Patents and Trademarks
Box PCT
Washington, D.C. 20231
Facsimile No. (703) 305-3230

Authorized officer
MARK BUDD *J Macaluso*
Telephone No. (703)308-3929