Title: METHOD FOR IMPROVING THE PRECISION OF A TEMPERATURE-SENSOR CIRCUIT

Abstract: The preferred embodiment described below provides a method and a memory device for improving the precision of a temperature sensor circuit (160). In one preferred embodiment, first (Vtemp1) and second (Vtemp2) temperature dependent reference voltages are generated and compared, and an operating condition of the memory array is controlled based on the result (TempTripl, TempTrip2) of the comparison.
**INTERNATIONAL SEARCH REPORT**

**A  CLASSIFICATION OF SUBJECT MATTER**

<table>
<thead>
<tr>
<th>IPC</th>
<th>USPC</th>
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<tr>
<td>G1C 7/04( 2006 01)</td>
<td>365/21 1, 189 07,207</td>
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According to International Patent Classification (IPC) or to both national classification and IPC

**B  FIELDS SEARCHED**

Minimum documentation searched (classification system followed by classification symbols)

- U $ 365/21 1, 189 07, 207

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

**EAST**

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

NONE

**C  DOCUMENTS CONSIDERED TO BE RELEVANT**

<table>
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<tr>
<th>Category</th>
<th>Citation of document, with indication, where appropriate, of the relevant passages</th>
<th>Relevant to claim No</th>
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<td>A</td>
<td>US 6,531,511 A (HSU et al) 11 March 2003</td>
<td>1-32</td>
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</table>

**D  Further documents are listed in the continuation of Box C**

- * Special categories of cited documents
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**Date of the actual completion of the international search**

21 May 2008 (21 05 2008)

**Name and mailing address of the ISA/US**

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- Commissioner for Patents
- P O Box 1450
- Alexandria, Virginia 22313-1450

**Facsimile No** (571) 273-3201

**Date of mailing of the international search report**

10 Jun 2008

**Authorized officer**

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