

COMMONWEALTH of AUSTRALIA
Patents Act 1952

634838

APPLICATION FOR A STANDARD PATENT

I/We

Sumitomo Electric Industries, Ltd.

of

5-33, Kitahama 4-chome, Chuo-ku, Osaka, Japan

hereby apply for the grant of a Standard Patent for an invention entitled:

Pickup method and the pickup apparatus for chip-type part

which is described in the accompanying complete specification.

Details of basic application(s):-

<u>Number</u>	<u>Convention Country</u>	<u>Date</u>
319327/1989	Japan	8 December 1989
151278/1990	Japan	8 June 1990
151279/1990	Japan	8 June 1990
151280/1990	Japan	8 June 1990

The address for service is care of DAVIES & COLLISON, Patent Attorneys, of 1 Little Collins Street, Melbourne, in the State of Victoria, Commonwealth of Australia.

DATED this SEVENTH day of DECEMBER 1990

To: THE COMMISSIONER OF PATENTS

Keith Collison

.....
a member of the firm of
DAVIES & COLLISON for
and on behalf of the
applicant(s)

Davies & Collison, Melbourne

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COMMONWEALTH OF AUSTRALIA

PATENTS ACT 1952

DECLARATION IN SUPPORT OF CONVENTION OR NON-CONVENTION APPLICATION FOR A PATENT

Insert title of invention.

In support of the Application made for a patent for an invention
entitled: PICKUP METHOD AND THE PICKUP APPARATUS FOR CHIP-TYPE PART

Insert full name(s) and address(es)
of declarant(s) being the appli-
cant(s) or person(s) authorized to
sign on behalf of an applicant
company.

I Tsuneo NAKAHARA (Representative Director of
~~XXX~~ SUMITOMO ELECTRIC INDUSTRIES, LTD.)
5-33, Kitahama 4-chome, Chuo-ku, Osaka, Japan

Cross out whichever of paragraphs
1(a) or 1(b) does not apply
1(a) relates to application made
by individual(s)
1(b) relates to application made
by company; insert name of
applicant company.

do solemnly and sincerely declare as follows:-

1. (a) ~~XXX~~ ~~the applicant~~ ~~for the patent~~
or (b) I am authorized by SUMITOMO ELECTRIC INDUSTRIES, LTD.

the applicant..... for the patent to make this declaration on ^{its} ~~their~~ behalf.

2. (a) ~~XXX~~ ~~the actual inventor~~ ~~of the invention~~
or (b) Masanori NISHIGUCHI
c/o Yokohama Works of Sumitomo Electric Industries, Ltd.
1, Taya-cho, Sakae-ku, Yokohama-shi, Kanagawa, Japan

~~is~~ the actual inventor..... of the invention and the facts upon which the applicant.....
~~are~~
~~is~~ entitled to make the application are as follows :-
~~are~~

The said applicant is the assignee of the actual inventor
in respect of the invention

State manner in which applicant(s)
derive title from inventor(s)

Cross out paragraphs 3 and 4
for non-convention applications.
For convention applications,
insert basic country(s) followed
by date(s) and basic applicant(s).

3. The basic application, S..... as defined by Section 141 of the Act ~~was~~ ^{were} made
in JAPAN on the 8th Dec. 1989
by SUMITOMO ELECTRIC INDUSTRIES, LTD.
in JAPAN on the 8th Jun. 1990
by SUMITOMO ELECTRIC INDUSTRIES, LTD.
in JAPAN on the 8th Jun. 1990
by SUMITOMO ELECTRIC INDUSTRIES, LTD.
in JAPAN on the 8th Jun. 1990
by SUMITOMO ELECTRIC INDUSTRIES, LTD.

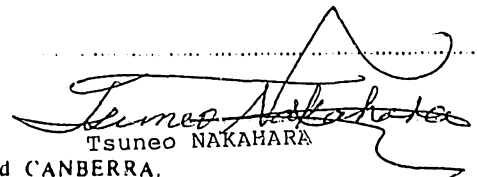
4. The basic application, S..... referred to in paragraph 3 of this Declaration ~~was~~ ^{were}
the first application, S..... made in a Convention country in respect of the invention the subject
of the application.

Insert place and date of signature.

Declared at Osaka this 15th day of Nov. 1990

Signature of declarant(s) (no
attestation required)

Note Initial all alterations


Tsuneo NAKAHARA

DAVIES & COLLISON, MELBOURNE and CANBERRA.



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- (56) Prior Art Documents
US 4720317
US 4718967
US 3973682
- (57) Claim

1. A method of picking up a chip part fixed on an adhesive layer formed on a tape member through which an energy beam is transmitted, the adhesive layer having an adhesive strength decreased upon radiation of the energy beam, comprising the steps of:

radiating the energy beam, through said tape member, onto only a predetermined portion of said adhesive layer to which said chip part is fixed, wherein said predetermined portion is less than the total surface area of the chip, for leaving the other portion thereof adhered; and

causing a collet to hold and peel off said chip part from the side of the tape member opposite the energy beam.

5. A method of picking up a chip part fixed to an adhesive layer formed on a tape member through which an energy beam is transmitted, the adhesive layer having an adhesive strength decreased upon radiation of the energy beam, comprising the steps of:

radiating the energy beam, through said tape member, onto only a predetermined portion of said adhesive layer to which said chip part is fixed, wherein said predetermined portion is less than the total surface area of the chip, for leaving the other portion thereof adhered; and

bringing a plurality of pins into contact with said tape member and pushing up the chip part from said tape member away from said adhesive layer by said plurality of pins; and

causing a collet to hold and peel off said chip part from the tape member.

8. An apparatus for picking up a chip part fixed to an adhesive layer formed on a tape member through which an energy beam is transmitted, said adhesive layer having an adhesive strength which is decreased upon radiation of the energy beam, comprising:

radiation means for radiating the energy beam, through said tape member, onto only a predetermined portion of the adhesive layer to which said chip part is fixed, wherein said predetermined portion is less than the total surface area of the chip, for leaving the other portion thereof adhered; and

a collet for holding said chip part and peeling off the chip part from the side of the tape opposite the energy beam.

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COMMONWEALTH OF AUSTRALIA
PATENTS ACT 1952
COMPLETE SPECIFICATION

NAME & ADDRESS
OF APPLICANT:

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NAME(S) OF INVENTOR(S):

Masanori NISHIGUCHI

ADDRESS FOR SERVICE:

DAVIES & COLLISON
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COMPLETE SPECIFICATION FOR THE INVENTION ENTITLED:

Pickup method and the pickup apparatus for chip-type part

The following statement is a full description of this invention, including the best method of performing it known to me/us:-

5 Background of the Invention

(Field of the Invention)

10 The present invention relates to a method of peeling off a chip type part such as chip condenser or semiconductor chip, etc., fixed on an adhesive tape or the like and the pickup apparatus.

(Related Background Art)

15 In the process for manufacturing semiconductor, a semiconductor wafer is adhered to an expanded tape and diced into chips on the expanded tape, the diced chip type parts are generally peeled off from the expanded tape one by one, and the peeled chip is die-bonded in a predetermined package and is stored in a predetermined tray.

20 The adhesive tape is an adhesive tape having an adhesive layer of an adhesive material on its surface. The adhesive material has a sufficient adhesive strength not to cause positional errors of the semiconductor devices during dicing of the semiconductor wafer into chips.

25 When the adhesive material which has a sufficient adhesive strength fixing a semiconductor having a back metal is used, the back metal may be peeled from the

1 chip type part upon peeling of the chip type part from
the adhesive tape. In the worst case, a deformation
such as cracks in a chip type part is present.

5 In order to prevent the back metal from peeling off
from the chip type part, the adhesive tape is irradiated
with the ultraviolet beam to decrease the adhesive
strength of the adhesive material before the chip type
part is peeled off from the adhesive tape.

10 Since the entire lower surface of the adhesive tape
is irradiated with the ultraviolet beam in a
conventional method, the adhesive strength of the entire
adhesive tape is decreased upon radiation of the
ultraviolet beam. The adhesive strength is naturally
slightly decreased with a lapse of time. For this
15 reason, assuming that the first half of chip type parts
are left on the adhesive tape for current use, while the
second half of chip type parts are stored on the
adhesive tape, the positions of the chip type parts are
shifted due to an external force acting on them during
20 storage. Therefore, storage of chip type parts on an
adhesive tape is not suitable for re-storage.

Summary of the Invention

25 It is an object of the present invention to provide
a pickup method and the pickup apparatus for
chip type part capable of storing the second half of
chip type parts kept adhered with a sufficient adhesive

strength on an adhesive layer such as an adhesive tape for a storage purpose while the first half of chip type parts is adhered on the adhesive layer for current use.

According to the present invention there is provided a method of picking up a
5 chip part fixed on an adhesive layer formed on a tape member through which an energy beam is transmitted, the adhesive layer having an adhesive strength decreased upon radiation of the energy beam, comprising the steps of:

radiating the energy beam, through said tape member, onto only a predetermined
portion of said adhesive layer to which said chip part is fixed, wherein said predetermined
10 portion is less than the total surface area of the chip, for leaving the other portion thereof adhered; and

causing a collet to hold and peel off said chip part from the side of the tape member opposite the energy beam.

15 The invention also provides a method of picking up a chip part fixed to an adhesive layer formed on a tape member through which an energy beam is transmitted, the adhesive layer having an adhesive strength decreased upon radiation of the energy beam, comprising the steps of:

radiating the energy beam, through said tape member, onto only a predetermined
20 portion of said adhesive layer to which said chip part is fixed, wherein said predetermined portion is less than the total surface area of the chip, for leaving the other portion thereof adhered; and

bringing a plurality of pins into contact with said tape member and pushing up the
chip part from said tape member away from said adhesive layer by said plurality of pins;
25 and

causing a collet to hold and peel off said chip part from the tape member.

The invention also provides an apparatus for picking up a chip part fixed to an
adhesive layer formed on a tape member through which an energy beam is transmitted,
30 said adhesive layer having an adhesive strength which is decreased upon radiation of the energy beam, comprising:

radiation means for radiating the energy beam, through said tape member, onto

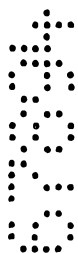


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only a predetermined portion of the adhesive layer to which said chip part is fixed, wherein said predetermined portion is less than the total surface area of the chip, for leaving the other portion thereof adhered; and

5 a collet for holding said chip part and peeling off the chip part from the side of the tape opposite the energy beam.

According to the pickup method for chip type part described above, the adhesive strength of the adhesive layer for each chip type part can be partially decreased.



1 With this operation, concentration of stresses
acting on a chip type part when it is pushed up can be
prevented, and the frequency of defects occurring in
chip type part when it is pushed can be reduced. In
5 addition, peeling of an adhesive tape starts from the
predetermined portion of a chip type part which is fixed
to a tape portion whose adhesive strength is decreased
upon radiation of an energy beam. This allows the
adhesive tape to be smoothly peeled off from the entire
bottom surface of the chip type part.

10 The present invention will become more fully
understood from the detailed description given
hereinbelow and the accompanying drawings which are
given by way of illustration only, and thus are not to
be considered as limiting the present invention.

15 Further scope of applicability of the present
invention will become apparent from the detailed
description given hereinafter. However, it should be
understood that the detailed description and specific
examples, while indicating preferred embodiments of the
20 invention, are given by way of illustration only, since
various changes and modifications within the spirit and
scope of the invention will become apparent to those
skilled in the art from this detailed description.

Brief Description of the Drawings

25 Fig. 1A is a perspective view showing a schematic

1 structure of the pick up apparatus for chip type part
and Fig. 1B is a vertical sectional view showing a state
wherein the chip type part is pushed up toward collet by
push-up pins,

5 Fig. 2 is a vertical sectional view showing
radiation of ultraviolet beam,

Figs. 3 and 4 are bottom end views showing radiation
areas of ultraviolet beams,

Fig. 5 is a schematic perspective view showing a
characteristic feature of a pickup apparatus to which
the present invention is applied,

Fig. 6 is a perspective view showing an arrangement
of push-up pins in relation to the bottom surface of a
semiconductor chip,

15 Fig. 7 is a vertical sectional view showing a state
wherein peeling of an adhesive tape starts from a corner
portion of a semiconductor chip,

Figs. 8A to 8C are bottom end views each showing a
portion of an adhesive area on which an ultraviolet beam
is radiated,

Figs. 9A and 9B are vertical sectional views showing
a state wherein an adhesive tape is peeled off from a
semiconductor chip, and

25 Fig. 10 is a vertical sectional view showing a state
wherein an adhesive tape is peeled off from a
semiconductor chip.

1 Description of the Preferred Embodiment

The first embodiment of the present invention will be described with reference to Figs. 1 to 4.

5 First, a schematic whole structure of pickup apparatus for chip type part according to one embodiment of the present invention will be explained in reference with Figs. 1A and 1B.

The pickup apparatus for chip type part comprises a wafer ring 11 and a push up stage 12. An adhesive tape 4 fixing plurality of semiconductor chips 5 thereon is fixed by the wafer ring 11. And the push up stage 12 is arranged underneath the wafer ring 11. An ultraviolet source 8 is arranged inside of the push up stage 12 and plurality of push up pins 6a, 6b, 6c and 6d are arranged in upper portion of the push up stage 12. The push up pins 6a, 6b, 6c and 6d are driven by cam mechanism to move up and down. A collet 7 is arranged over the wafer ring 11 and move up and down to pick up a semiconductor chip 5. This collet 7 has ~~an absorption~~ ^{a suction} opening 7a forming a pyramid dent in the bottom portion thereof.

20 The ~~absorption~~ ^{suction} opening 7a is communicated with a vacuum device (not shown) to absorb the semiconductor chip 5.

Further, a carrier device 14 having plurality of packages 13 forming a queue is arranged near the wafer ring 11. The semiconductor chip 5 ~~absorbed~~ ^{held} by the collet 7 is to be placed on the package 13 to be carried in certain direction. A TV camera 15 and lighting



1 device 16 are arranged over the carrier device 14. The
TV camera 15 is connected to a TV monitor 17 to allow an
operator to monitor the positioning between the package
13 and the semiconductor chip 5.

5 Fig. 2 shows a state in which the lower surface of
an adhesive tape having a plurality of chip type parts
fixed thereon is irradiated with an ultraviolet beam.

An adhesive layer is formed on a surface 4a of an
adhesive tape 4 and is made of an adhesive material
whose adhesion strength is decreased upon radiation of
the ultraviolet beam. A plurality of semiconductor
chips 5 are adhered and fixed to this adhesive layer.
When each chip 5 is to be peeled off from the adhesive
tape 4, the lower surface of the adhesive tape 4, i.e.,
the adhesive layer, is irradiated with the ultraviolet
beam from an ultraviolet source 8 to decrease the
adhesive strength of the adhesive layer. At this time,
in the pickup method for chip type part of the present
invention, an adhesive layer portion to which one chip
is fixed is irradiated with an ultraviolet beam every
few seconds. Each chip corresponding to the adhesive
layer portion which is irradiated with the ultraviolet
beam and whose adhesive strength is decreased is peeled
off from the adhesive tape. When the adhesive layer
portion to which one chip 5 is fixed is irradiated with
the ultraviolet beam, the ultraviolet beam is radiated
within a predetermined area inside its peripheral

1 portion by at least 0.1 mm. This ultraviolet radiation area is illustrated as a hatched portion in Fig. 2. The ultraviolet radiation area is limited inside the chip fixing portion due to the following reason.

5 More specifically, assume that the ultraviolet radiation area expands outside the portion which fixes the chip 5, as indicated by a hatched portion in Fig. 3.

Even if the radiation area does not reach the lower surface portion of an adjacent chip, the adhesive strength of the adhesive layer which fixes other adjacent chips 5 is decreased by propagation of a polymerization reaction of the adhesive material and oozing of the ultraviolet beam although the adhesive layer portions corresponding to these adjacent chips are not actually irradiated with the ultraviolet beam. For this reason, when chips 5 are to be stored while being kept adhered on the adhesive tape 4, the positions of the chips fixed near the portion irradiated with the ultraviolet rays tend to be shifted. According to the method of the present invention, however, a decrease in adhesive strength upon radiation of the ultraviolet beam does not occur except for the chip to be peeled off.

The ultraviolet source 8 may have a structure capable of radiating ultraviolet beam within the limited area, as described above. For example, there may be provided an arrangement capable of radiating an ultraviolet beam within the predetermined limited area

1 from an ultraviolet source through a lens and a
reflector or through a light guide.

5 The chip fixed to the portion whose adhesive
strength is decreased is pushed up from the adhesive
tape by push-up pins 6a to 6d. The peeled chip is
chucked in vacuum by a collet 7. Therefore, the chips
can be peeled off from the adhesive tape 4 one by one
(Refer to Fig. 1B).

10 An ultraviolet radiation area limited in the range
of the diameter of 0.8 mm inside the bottom surface
adhered by the adhesive tape of the chip, as shown in
Fig. 3, was compared with that expanding in the range of
the diameter of 1.5 mm outside the bottom surface of the
chip, as shown in Fig. 4, and changes in adhesion
15 strength values of the chips on the adhesive layer
portions upon radiation of ultraviolet beam were
measured. Test results are shown in the table below.
This table shows the adhesion strength obtained when a
chip having an area of 1 mm² is peeled off from an
20 adhesive tape which is expanded at an expansion ratio of
130%, and the adhesion strength values prior to
radiation of the ultraviolet beam are 15 g.

1

	UltraViolet Radiation Area	
	Within Chip Bottom Surface 0.8 mmø (Fig. 3)	Outside Chip Bottom Surface 1.5 mmø (Fig. 4)
Adhesion Strength of Target Chip	(15 g) → 3 g	(15 g) → 1 g or less
Adhesion Strength of Adjacent Chip	(15 g) → 15 g	(15 g) → 5 g

5

As is apparent from this table, the adhesion strength of the chip to be peeled off from the adhesive tape is decreased to 3 g upon radiation of the ultraviolet beam when the radiation area is limited within the bottom surface of the chip. However, this adhesion strength can facilitate peeling of the chip from the adhesive tape, thus posing no problem. To the contrary, when the adhesion strength of the chip adjacent to the target chip is taken into consideration, the adhesion strength is kept unchanged upon radiation of an ultraviolet beam within the radiation area falling within the bottom surface of the chip. No problem is presented to continuously adhere the chips on the adhesive tape. However, when the radiation range falls outside the bottom surface of the chip, the adhesion strength is decreased to 5 g. When this chip is kept adhered on the adhesive tape and is stored, a positional error tends to occur. This radiation range cannot be apparently suitable for chip re-storage.

15

25

1 The second embodiment of the present invention will
be described below with reference to Figs. 5 to 6.

Fig. 5 schematically shows a characteristic feature
of a pickup apparatus for semiconductor chips to which
the present invention is applied. This pickup apparatus
comprises push-up means having push-up pins 6a to 6d for
pushing up a plurality of semiconductor chips 5 adhered
and fixed to the adhesive surface of an adhesive tape 4
one by one from the lower surface side of the adhesive
tape 4 (the lower surface on which no semiconductor
chips 5 are adhered and fixed), a collet 7 for
chucking/holding the pushed semiconductor chips 5 from
the upper surface side of the tape 4 one by one so as to
peel them off from the tape 4 one by one, an ultraviolet
source 8 for radiating an ultraviolet beam on the
adhesive tape 4 from its lower surface side, and a mask
9, arranged between the ultraviolet source 8 and the
adhesive tape 4, for allowing the ultraviolet beam to be
transmitted through a predetermined portion. An
adhesive material whose adhesive strength is decreased
upon radiation of an ultraviolet beam is coated on the
upper surface of the adhesive tape 4. The adhesive tape
4 is made of an elastic resin or the like for
transmitting an ultraviolet beam therethrough. If,
therefore, an ultraviolet beam is radiated on the
adhesive tape 4 from its lower surface side, the
adhesive strength of a portion which receives the



1 ultraviolet beam is decreased. The mask 9 is designed
to allow an ultraviolet beam to be transmitted ^{through} ~~through~~
only portions corresponding to corner portions of the
semiconductor chips 5 fixed to the adhesive tape 4. For
5 this purpose, through holes 9a are formed in these
portions of the mask 9, or only these portions are made
of a material having transmissivity to ultraviolet
light. Therefore, when an ultraviolet beam is radiated
from the ultraviolet source 8 onto the adhesive tape 4
10 through the mask 9, the adhesive strength of only a
portion, of a surface portion of the adhesive tape 4 to
which each semiconductor chip 5 is fixed, to which a
corner portion of each semiconductor chip 5 is fixed is
decreased. That is, in this embodiment, the mask 9 and
15 the ultraviolet source 8 constitute an ultraviolet
radiation means for selectively radiating an ultraviolet
beam to only a portion to which corner portion of each
semiconductor chip 5 is fixed.

20 A push-up means for pushing up the semiconductor
chips 5 from the lower surface side of the adhesive tape
4 comprises the four push-up pins, i.e., the push-up
pins 6a to 6d, as described above. These push-up pins
are arranged to be brought into contact with the lower
surface of the adhesive tape 4 at four positions
25 respectively separated from the four corners of each
semiconductor chip 5, which is fixed to a tape surface
portion whose adhesive strength is decreased upon



1 radiation of an ultraviolet beam, by a distance of 5% or
more of the length of a diagonal line of the square
lower surface of the semiconductor chip 5, thereby
pushing up the semiconductor chip 5. Fig. 6 is an
5 enlarged view showing the arrangement of the push-up
pins 6a to 6d in relation to the bottom surface of each
semiconductor chip 5. Referring to Fig. 6, hatched
portions are portions on which an ultraviolet beam is
radiated. As shown in Fig. 6, in this embodiment, of
the four push-up pins 6a to 6d, the push-up pins 6a to
6c are arranged at positions where they are brought into
contact with corner portions of each semiconductor chip
5, which are fixed to portions receiving no ultraviolet
beam, from the lower surface side of the adhesive tape
15 4, whereas the push-up 6d is arranged at a position
where it is brought into contact with the lower surface
of the adhesive tape 4 at a position separated from a
corner portion of the semiconductor chip 5, which is
fixed to a portion on which the ultraviolet beam is
20 radiated, by a distance of 5% or more of the length of a
diagonal line of the lower surface of the semiconductor
chip 5 (a position closer to the center of the chip 5).

The way in which semiconductor chips on an adhesive
tape are picked up by the above-described pickup
25 apparatus will be described below.

When the adhesive tape 4 having the plurality of
semiconductor chips 5 adhered and fixed thereto is set

1 in the pickup apparatus, the mask 9 is positioned in
correspondence with the arrangement of the semiconductor
chips 5. An ultraviolet beam is then radiated from the
ultraviolet source 8 to only a corner portion of each
5 semiconductor chip 5. Upon radiation of the ultraviolet
beam, the mask 9 is removed, and each semiconductor chip
5 is pushed up by the push-up means having the push-up
pins 6a to 6d. When this push-up operation is
performed, since the semiconductor chip 5 is pushed up
by the plurality of push-up pins 6a to 6d, stresses
generated by the push-up operation are not concentrated
to one portion but are dispersed, thus reducing the
frequency of defects, such as cracks and chips,
occurring in the semiconductor chips 5. When the
15 semiconductor chip 5 is pushed up, peeling of the
adhesive tape 4 starts from the corner portion of the
semiconductor chip 5 which is fixed to the portion of
the adhesive tape 4 to which the ultraviolet beam is
radiated because its adhesive strength is decreased upon
radiation of the ultraviolet beam. Since no push-up
20 pins are present within a distance of 5% or less of the
length of a diagonal line of the semiconductor chip 5
from the corner portion of the chip 5, the adhesive tape
4 starts peeling off from the corner portion. As a
25 result, a portion of the adhesive tape 4 present within
a radius of at least 5% of the diagonal line length from
this corner portion is peeled off. Fig. 7 shows this

1 state. The pushed-up semiconductor chip 5 is chucked
and held by the collet 7 positioned above this
semiconductor chip 5. When the collet 7 is moved
upward, the semiconductor chip 5 is peeled off from the
5 adhesive tape 4 to be picked up. In this case, if a
portion of the peripheral portion of an adhesive area to
which the semiconductor chip 5 is adhered and fixed
(corresponding to the corner portion of the
semiconductor chip 5 fixed to the area on which the
ultraviolet beam is radiated) is peeled off, peeling of
this portion facilitates a smooth peeling operation of
the overall adhesive area even if the adhesive strength
thereof is not decreased. Therefore, even a large,
flattened semiconductor chip can be easily peeled off
from an adhesive tape to be picked up without increasing
15 the suction power of the collet 7.

In addition, assume that some of the semiconductor
chips 5 adhered and fixed to the adhesive tape 4 are
picked up, and the remaining semiconductor chips 5 are
stored while they are kept adhered and fixed to the
20 adhesive tape 4. Even in such a case, since radiation
areas of an ultraviolet beam are limited, and the
adhesive strength of the entire area to which the
semiconductor chips 5 are fixed is not decreased, the
25 remaining semiconductor chips on the adhesive tape 4 can
be re-stored while they are kept adhered and fixed to
the tape 4 with a large adhesive strength.

1 The above-described radiation of the ultraviolet
beam may be performed in the following manner. An
ultraviolet beam emitted from the ultraviolet source 8
may be focused and guided by a light guide or the like
5 so as to sequentially radiate a beam spot onto the
corresponding corner portions of the respective
semiconductor chips 5. Further, after radiation of an
ultraviolet beam, the semiconductor chip 5 can be
immediately chucked/held by the collet 7 and can be then
10 peeled off and picked up from the adhesive tape 4
without using the push-up means. ~~If, however, the~~

portions to which the corner portions of the respective
semiconductor chips 5 are fixed are irradiated with an
ultraviolet beam at once by using the mask 9 as in the
above embodiment, each semiconductor chip can be picked
15 up by only the two subsequent steps, i.e., the push-up
step by the push-up means and the pickup step by the
collet. Therefore, the time (tact time) required for
one pickup operation can be shortened.

20 An experiment result obtained by applying the
present invention to a pickup operation of a
semiconductor chip having an area of 5 mm² will be
described below.

25 The adhesive/fixing power of a semiconductor chip
was about 300 g prior to radiation of an ultraviolet
means. After an ultraviolet beam was radiated on a
portion to which a corresponding corner portion of the



1 semiconductor chip was fixed, the adhesive/fixing
strength was decreased to 250 g. When the
above-described pickup operation was performed in this
state, the frequency of defects, such as cracks and
5 chips of semiconductor chips, occurring when they were
pushed up was substantially equal to that of a
conventional method in which the adhesive strength was
reduced to 100 g by radiating an ultraviolet beam on the
entire adhesive area. In addition, since portions to
10 which an ultraviolet was radiated to decrease the
adhesive strength were limited, the remaining
semiconductor chips 5 on the adhesive tape 4 could be
re-stored with a sufficient adhesive strength.

The present invention is not limited to the
15 above-described embodiment, and various modifications of
the invention can be made.

In the above embodiment, the present invention is
applied to a case wherein the semiconductor chips 5
adhered and fixed to the adhesive tape 4 are picked up.
20 However, the present invention is not limited to this.
For example, the present invention can be applied to a
pickup operation of chip type parts such as chip
capacitors and chip resistors adhered and fixed to an
adhesive tape such as an adhesive tape.

25 Furthermore, in the above embodiment, an ultraviolet
beam is used as an energy beam. However, it is
preferable that an energy beam is selected in accordance

1 with an adhesive material used for an adhesive tape such
as an adhesive tape so as to efficiently decrease its
adhesive strength. For example, with respect to an
adhesive tape coated with an adhesive material whose
5 adhesive strength is efficiently decreased upon
radiation of an infrared beam, an infrared beam is
preferably used as an energy beam.

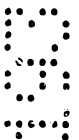
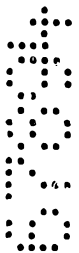
In addition, in the above embodiment, the push-up
means is constituted by the four push-up pins. However,
the push-up means need not have the push-up pins but may
have a flat portion instead, which is brought into
contact with an adhesive tape such as an adhesive tape.
Even in this case, the flat portion is brought into
contact with an adhesive tape at a plurality of portions
in a microscopic viewpoint. In this case, however, in
order to prevent the push-up means from coming contact
with an radius of 5% of the diagonal line length from a
corner of a semiconductor chip which is fixed to a
portion on which an ultraviolet beam is radiated, a
corresponding portion of the means must be notched or
any similar processing must be performed in advance.

Next, some modifications of the second embodiment
for the present invention will be explained in reference
to Fig. 5 and Figs. 8A to 8C. In the above-described
embodiment, as shown in Fig. 8A, a portion 10 (hatched
25 portion) of an adhesive area 9 to which an ultraviolet
beam is radiated corresponds to a portion to which a

corner portion of the semiconductor chip 5 is fixed. However, an ultraviolet beam may be radiated on a hatched portion 10 shown in Fig. 8B or 8C, wherein Fig. 8B shows the radiated portion to comprise one perimetral edge of the chip. In this case, the portion 10 on which an ultraviolet beam is radiated preferably includes a portion to which at least one of the corner portions of the semiconductor chip 5 is adhered and fixed. This is because peeling of the adhesive tape 4 tends to start from this corner portion. Note that such a modification of the portion 10 for receiving an ultraviolet beam can be made by only changing the mask 9 in accordance with the corresponding specifications.

10 Next, the third embodiment for the present invention will be explained below with reference to Figs. 9A and 9B. The difference between the third embodiment and the second embodiment is as follows. Of these pins, the push-up pin 6a has a distal end located farther from the adhesive tape 4 than the distal ends of the other push-up pins 6b to 6d. That is, the distal end of the push-up pin 6a, which is positioned to correspond to a diagonal corner portion with respect to a corner portion of the semiconductor chip 5 which is fixed to the portion on which the ultraviolet beam is radiated, is located farther from the adhesive tape 4 than the distal ends of the other push-up pins 6b to 6d.


20 Figs. 9A and 9B show a state wherein the semiconductor chip 5 is pushed up by the push-up pins 6a



1 to 6d, and the adhesive tape 4 is peeled off from the semiconductor chip 5.

As shown in Fig. 9A, when the semiconductor chip 5 is pushed up by the push-up pins 6b to 6d, since the adhesive power of a portion of the adhesive tape 4 on which an ultraviolet beam is radiated is decreased, peeling of the adhesive tape 4 easily occurs from this portion. Since no force for pulling the semiconductor chip 5 downward acts on the portion where the adhesive tape 4 is peeled off, the balance between the forces for pulling the semiconductor chip 5 downward which act on the two sides of the diagonal line connecting the push-up pins 6b and 6c is lost. Since the distal end of the push-up pin 6a is separated from the adhesive tape 4, the semiconductor chip 5 is tilted from the diagonal line as a center line, as shown in Fig. 9B. As a result, the peeled corner portion is separated from the pin 6d to float, and peeling of the adhesive tape 4 proceeds to the central portion of the semiconductor chip 5. The pushed-up semiconductor chip 5 is chucked and held by the collet positioned above this semiconductor chip 5. The upper surface of the semiconductor chip 5 is pressed by the collet 7, the chip 5 is tilted in the reverse direction (clockwise direction) to allow the chip 5 to close the chuck opening of the collet 7 (Refer Fig. 7). After that, the collet is moved upward, the semiconductor chip 5 is

1 peeled off from the adhesive tape 4 to be picked up. In
this case, if a portion of the peripheral portion of an
adhesive area to which the semiconductor chip 5 is
adhered and fixed (corresponding to the corner portion
5 of the semiconductor chip 5 fixed to the area on which
the ultraviolet beam is radiated) is peeled off, peeling
of this portion facilitates a smooth peeling operation
of the overall adhesive area even if the adhesive
strength thereof is not decreased. Therefore, even a
10 large, flattened semiconductor chip can be easily peeled
off from an adhesive tape to be picked up without
increasing the suction power of the collet 7.



15 In addition, assume that some of the semiconductor
chips 5 adhered and fixed to the adhesive tape 4 are
picked up, and the remaining semiconductor chips 5 are
stored while they are kept adhered and fixed to the
adhesive tape 4. Even in such a case, since radiation
areas of an ultraviolet beam are limited, and the
adhesive power of the entire area to which the
20 semiconductor chips 5 are fixed is not decreased, the
remaining semiconductor chips on the adhesive tape 4 can
be re-stored while they are kept adhered and fixed to
the tape 4 with an adhesive power large enough to store
them.

25 The fourth embodiment of the present invention,
which is different from the above-described embodiment,
will be described below with reference to Fig. 10.

1 In the embodiment described with reference to Figs.
9A to 9C, the distal end of the push-up pin 6a on the
side diagonal to the portion on which the ultraviolet
beam is radiated is separated from the adhesive tape 4.

5 In contrast to this, in the fourth embodiment, as shown
in Fig. 10, the distal end of a push-up pin 6d, which is
arranged at a position corresponding to a corner portion
of a semiconductor chip 5 which is fixed to a portion on
which an ultraviolet beam is radiated, is located

10 farther from an adhesive tape than other push-up pins 6a
to 6c. When the push-up pins 6a to 6c are brought into
contact with the adhesive tape 4 to push up the

semiconductor chip 5, peeling of the adhesive tape 4
starts from a portion with which no push-up pin is in
contact and has adhesive power decreased upon radiation
of an ultraviolet beam, and this peeling progresses to
the central portion of the semiconductor chip 5. The
semiconductor chip 5 is chucked and held by a collet 7.

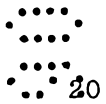
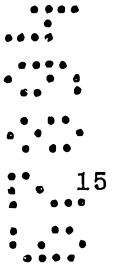
The semiconductor chip 5 is then peeled off from the
adhesive tape 4 to be picked up. In this case, since
the semiconductor chip 5 does not tilt as shown in Fig.
10, the semiconductor chip 5 can be easily chucked and
held by the collet 7. Therefore, this embodiment is
preferable to the previous embodiment.

25 From the invention thus described, it will be
obvious that the invention may be varied in many ways.
Such variations are not to be regarded as a departure

1 from the spirit and scope of the invention, and all such
modifications as would be obvious to one skilled in the
art are intended to be included within the scope of the
following claims.

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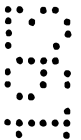
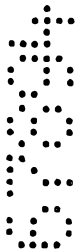
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THE CLAIMS DEFINING THE INVENTION ARE AS FOLLOWS:

1. A method of picking up a chip part fixed on an adhesive layer formed on a tape member through which an energy beam is transmitted, the adhesive layer having an adhesive strength decreased upon radiation of the energy beam, comprising the steps of:
5 radiating the energy beam, through said tape member, onto only a predetermined portion of said adhesive layer to which said chip part is fixed, wherein said predetermined portion is less than the total surface area of the chip, for leaving the other portion thereof adhered; and
10 causing a collet to hold and peel off said chip part from the side of the tape member opposite the energy beam.
2. A method according to claim 1, wherein when a polygonal bottom surface of the chip part is fixed to said adhesive layer, said predetermined portion of said adhesive layer
15 is an adhesive layer for fixing a corner of said polygonal bottom surface.
3. A method according to claim 1, wherein when a polygonal bottom surface of the chip part is fixed to said adhesive layer, said predetermined portion of said adhesive layer
20 is an adhesive layer for fixing one perimetral edge of said polygonal bottom surface.
4. A method according to claim 1, wherein when a polygonal bottom surface of the chip part is fixed to said adhesive layer, said predetermined portion of said adhesive layer
25 is an adhesive layer for fixing a portion inside an outer edge of said polygonal bottom surface.
5. A method of picking up a chip part fixed to an adhesive layer formed on a tape member through which an energy beam is transmitted, the adhesive layer having an adhesive strength decreased upon radiation of the energy beam, comprising the steps of:
30 radiating the energy beam, through said tape member, onto only a predetermined portion of said adhesive layer to which said chip part is fixed, wherein said predetermined portion is less than the total surface area of the chip, for leaving the other portion thereof adhered; and



bringing a plurality of pins into contact with said tape member and pushing up the chip part from said tape member away from said adhesive layer by said plurality of pins; and

causing a collet to hold and peel off said chip part from the tape member.

5

6. A method according to claim 5, wherein when a polygonal bottom surface of the chip part is fixed to said adhesive layer, a plurality of pins are brought into contact with the chip part wherein one of the pins is positioned separated from a corner of the chip part fixed on said predetermined portion of said adhesive layer by not less than a distance
10 corresponding to 5% of a diagonal line length of said polygonal bottom surface, and the chip part is pushed up.

7. A method according to claim 5, wherein when a polygonal bottom surface of the chip part is fixed to said adhesive layer, a distal end of at least one of the group of a pin
15 located at said predetermined portion of said adhesive layer and a pin located at a corner farthest from said predetermined portion, is brought into contact with said tape member with a lapse of time from contact between said tape member and distal ends of other pins.

8. An apparatus for picking up a chip part fixed to an adhesive layer formed on a
20 tape member through which an energy beam is transmitted, said adhesive layer having an adhesive strength which is decreased upon radiation of the energy beam, comprising:

radiation means for radiating the energy beam, through said tape member, onto only a predetermined portion of the adhesive layer to which said chip part is fixed, wherein said predetermined portion is less than the total surface area of the chip, for
25 leaving the other portion thereof adhered; and

a collet for holding said chip part and peeling off the chip part from the side of the tape opposite the energy beam.

9. An apparatus according to claim 8, wherein said radiation means includes an
30 energy source for generating the energy beam, and a mask member, located between said energy source and said adhesive tape, for causing the energy beam to transmit through only said predetermined portion of said adhesive layer.



10. An apparatus according to claim 8, wherein said mask member is made of a member which does not cause the energy beam to transmit therethrough and which has a through hole at said predetermined portion of said adhesive layer.

5 11. An apparatus according to claim 8, wherein said mask member is made of a material for causing the energy beam to transmit through only said predetermined portion of said adhesive layer.

12. An apparatus according to claim 8, wherein:
10 said chip part has a polygonal bottom surface fixed to said adhesive layer; and said predetermined portion of said adhesive layer is an adhesive layer for fixing a corner of said polygonal bottom surface.

13. An apparatus according to claim 8, wherein:
15 said chip part has a polygonal bottom surface fixed to said adhesive layer; and said predetermined portion of said adhesive layer is an adhesive layer for fixing one perimetral edge of said polygonal bottom surface.

14. An apparatus according to claim 8, wherein said predetermined portion of said
20 adhesive layer is an adhesive layer for fixing a portion of said polygonal bottom surface inside an outer edge thereof.

15. An apparatus according to claim 8, further comprising push-up means for pushing
up the chip part toward said collet through said tape member.

25 16. An apparatus according to claim 15, wherein:
said chip part has a polygonal bottom surface fixed to said adhesive layer; and
said push-up means comprises a plurality of pins, which are located at positions
corresponding to said corners of said polygonal bottom surface and distal ends of which
30 are brought into contact with said tape member from a side opposite to said adhesive layer, for pushing up said chip part.



17. An apparatus according to claim 16, wherein of said plurality of pins, a distal end of at least one of the group of a pin located at said predetermined portion of said adhesive layer and a pin located at a corner farthest from said predetermined portion, is farther separated from said adhesive tape than distal ends of other pins.

5

18. An apparatus according to claim 16, wherein said plurality of pins comprise a plurality of pins which are in contact with said tape member wherein one of the pins is positioned separated from a corner of the chip part fixed on said predetermined portion of said adhesive layer by not less than a distance corresponding to 5% of a diagonal line
10 length of said polygonal bottom surface.

19. An apparatus according to claim 15, wherein said push-up means is brought into contact with said tape member from a side opposite to said adhesive layer by means of a flat member obtained by notching said predetermined portion of said adhesive layer,
15 and said push-up means pushes up said chip part.

20. A method of picking up a chip part substantially as hereinbefore described with reference to the accompanying drawings.

20 21. An apparatus for picking up a chip part substantially as hereinbefore described with reference to the accompanying drawings.

25

DATED this 13th day of January, 1992

30 SUMITOMO ELECTRIC INDUSTRIES, LTD.

By its Patent Attorneys

DAVIES COLLISON CAVE



Fig. 1A

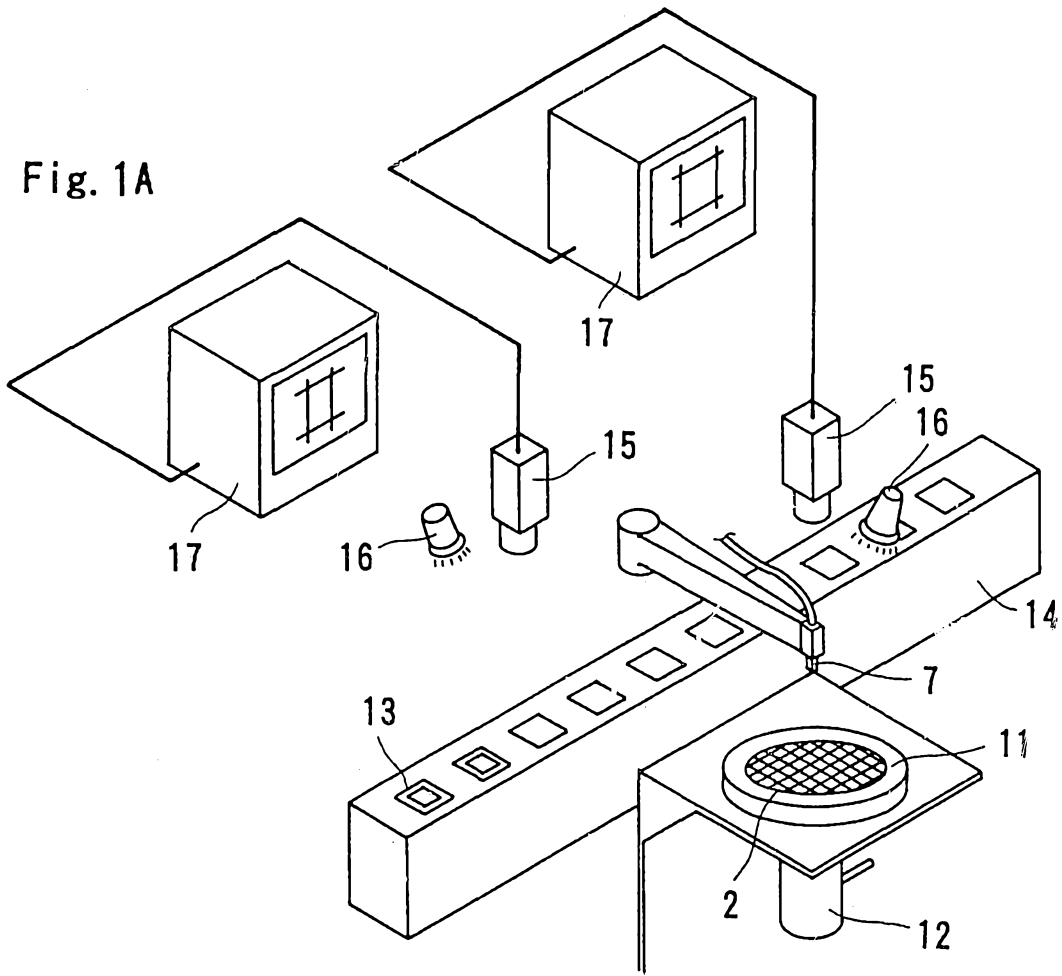


Fig. 1B

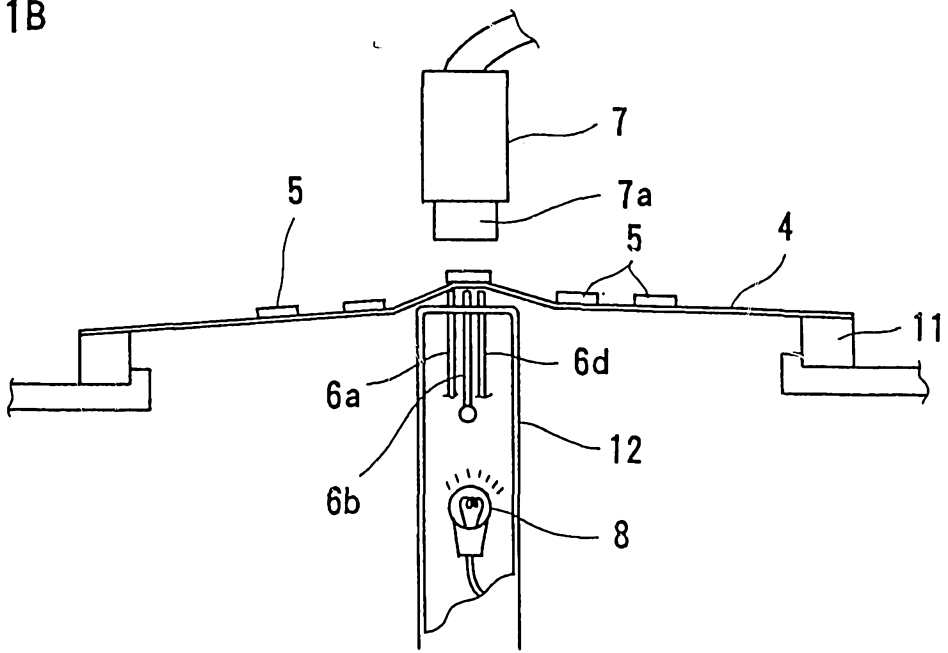


Fig. 2

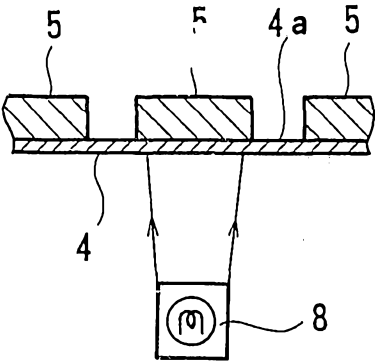


Fig. 3

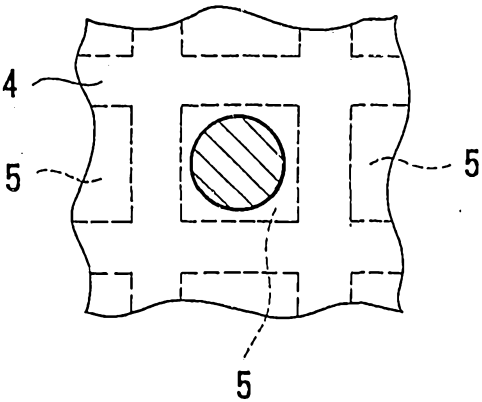


Fig. 4

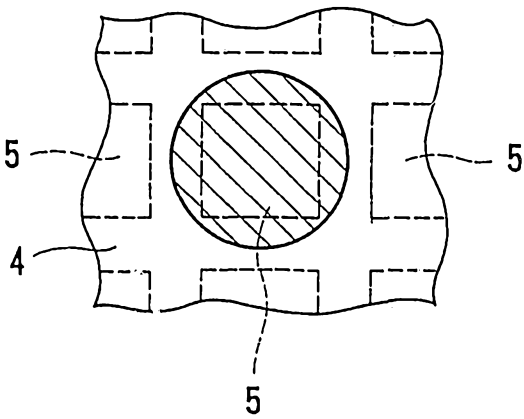
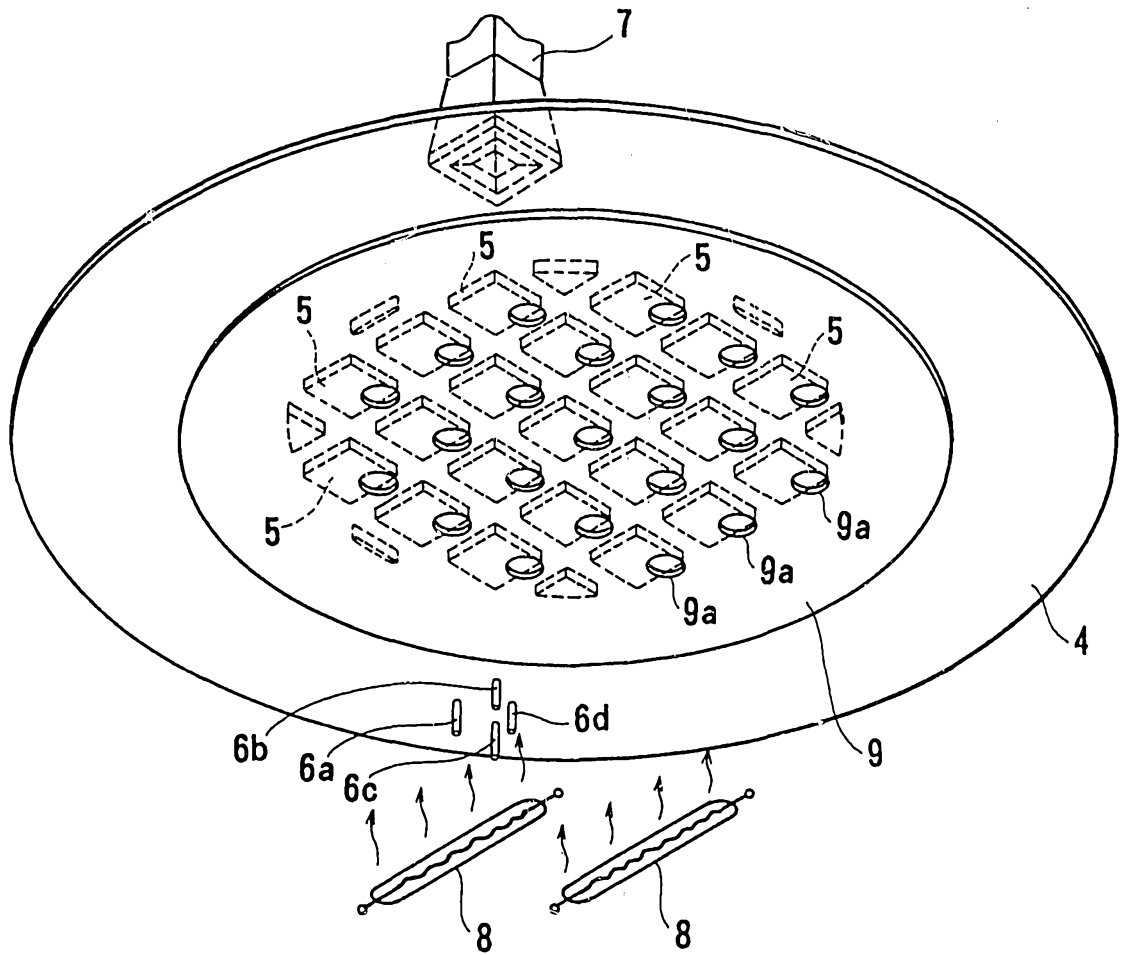


Fig. 5



7 12 90 67004

Fig. 6

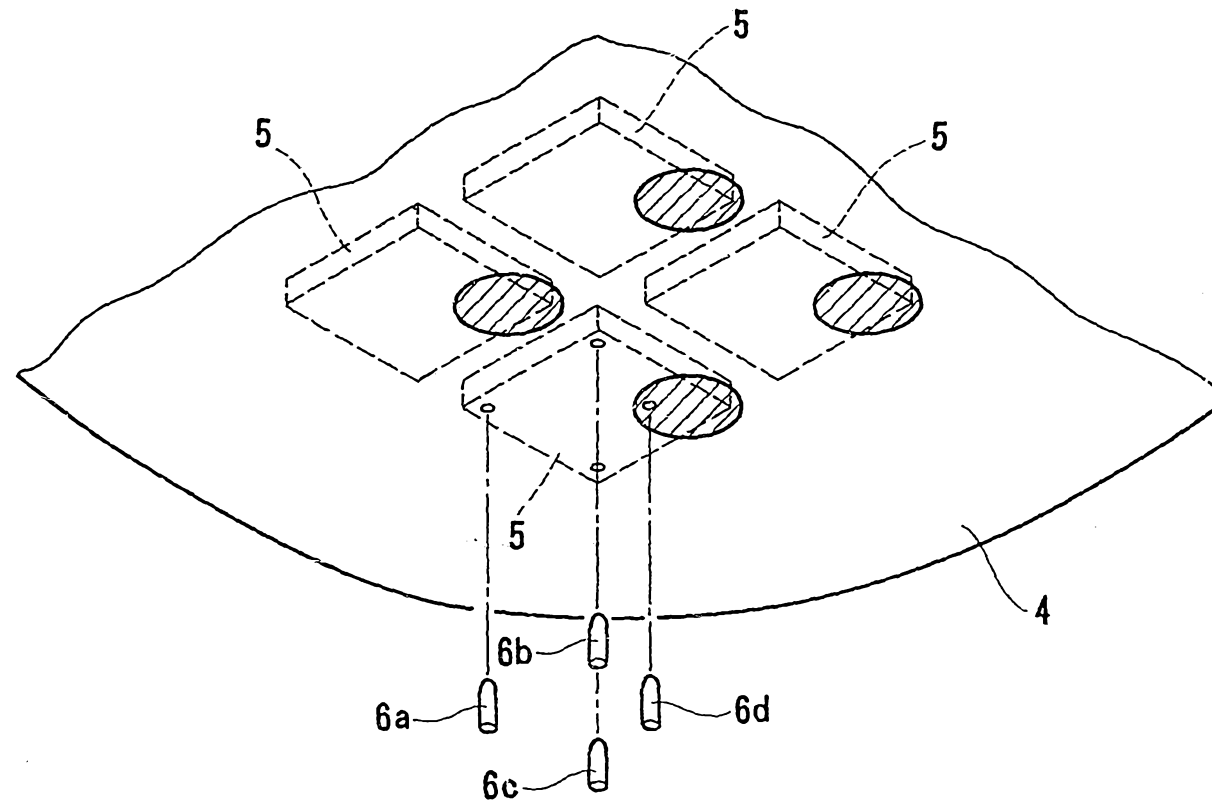


Fig. 7

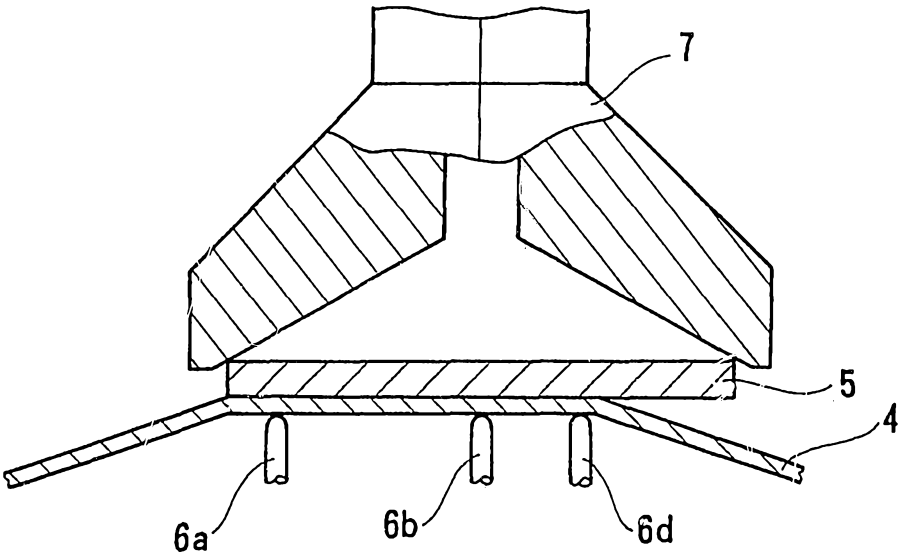


Fig. 8A

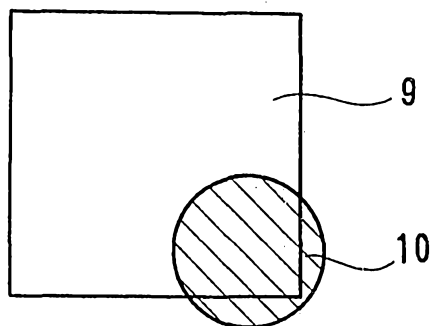


Fig. 8B

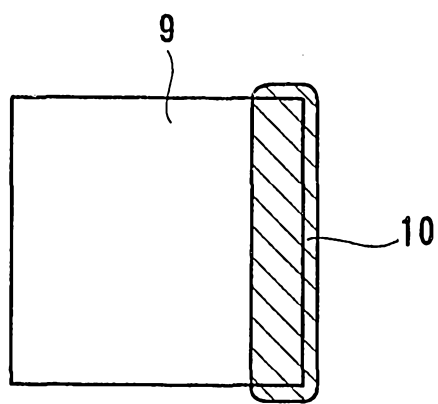


Fig. 8C

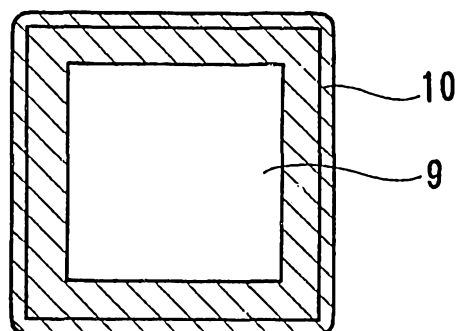


Fig. 9A

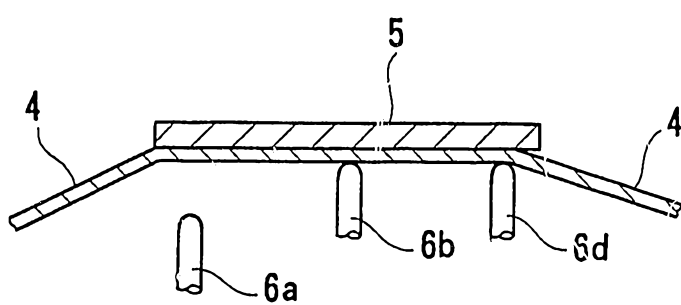


Fig. 9B

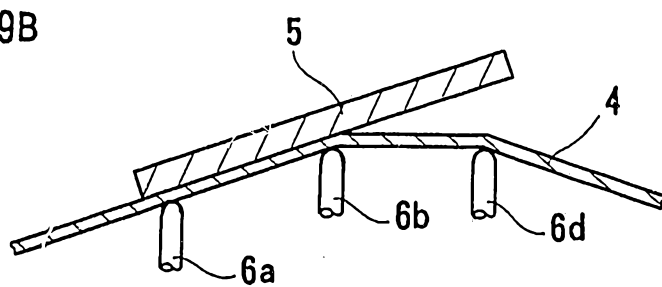


Fig. 10

