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Declarations under Rule 4.17:

— of inventorship (Rule 4.17(iv))

[Continued on next page]

(54) Title: MEMORY SYSTEM USING ASYMMETRIC SOURCE-SYNCHRONOUS CLOCKING

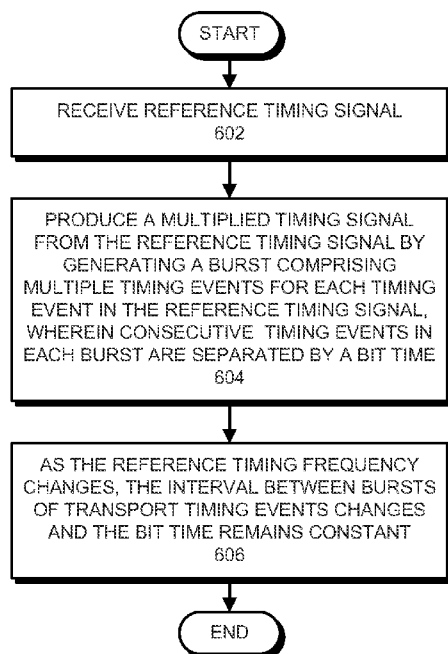


FIG. 6A

(57) Abstract: The disclosed embodiments relate to a memory system that generates a multiplied timing signal from a reference timing signal. During operation, the system receives a reference timing signal. Next, the system produces a multiplied timing signal from the reference timing signal by generating a burst comprising multiple timing events for each timing event in the reference timing signal, wherein consecutive timing events in each burst of timing events are separated by a bit time. Then, as the reference clock frequency changes, the interval between bursts of timing events changes while the bit time remains substantially constant.

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A. CLASSIFICATION OF SUBJECT MATTER*G11C 7/22(2006.01)i, G11C 7/10(2006.01)i*

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

G11C 7/22; G11C 7/00; G06F 1/04; G06F 12/00; G11C 8/18

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Korean utility models and applications for utility models

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Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

eKOMPASS(KIPO internal) & Keywords: controller, plural, change, reference, clock, frequency, timing, signal, rising, falling

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 2009-0109770 A1 (SUGISHITA) 30 April 2009 See abstract, paragraphs [0013],[0015],[0017],[0035],[0036], and figure 2.	1-54
A	US 2010-0205386 A1 (YAMASHITA) 12 August 2010 See abstract, paragraph [0013], and claim 1.	1-54
A	US 2007-0234100 A1 (BAKER et al.) 04 October 2007 See abstract, paragraphs [0034],[0035], and figures 2-3.	1-54
A	US 2002-0040424 A1 (KUMAZAWA) 04 April 2002 See abstract, paragraphs [0017],[0058], and figure 7.	1-54



Further documents are listed in the continuation of Box C.



See patent family annex.

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"&" document member of the same patent family

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INTERNATIONAL SEARCH REPORT

Information on patent family members

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