

Patent Number:

[11]

United States Patent [19]

[54] CONNECTING TERMINAL FOR CHIP

Merlin et al.

Date of Patent: ** May 23, 2000 [45]

Des. 425,519

	[54]	CARDS					
	[75]	Inventors:	Pierre Merlin, La Ciotat; Didier Elbaz; Stephane Ayala, both of Marseilles, all of France	ļ			
	[73]	Assignee:	Gemplus, Gemenos, France				
	[**]	Term:	14 Years				
				i			
	[21]	Appl. No.: 29/103,796					
	[22]	Filed:	Apr. 22, 1999				
Dolated U.S. Application Data							
	Related U.S. Application Data						
	[62]	Division of application No. 29/081,130, Dec. 23, 1997, Pat. No. Des. 412,164, which is a division of application No. 29/047,994, Dec. 18, 1995, Pat. No. Des. 393,458.					
	[30]	0] Foreign Application Priority Data					
	r .1		0 11				

[51] LOC (7) Cl. 14-02

[58]	Field of Search
	D13/182; 235/378-382, 382.5; 439/43,
	52, 64; 361/737–739

[56] **References Cited**

U.S. PATENT DOCUMENTS

D. 393,458	4/1998	Merlin et al	D14/117
D. 412.164	7/1999	Laviron et al	D14/117

Primary Examiner—Freda Nunn Attorney, Agent, or Firm-Roland Plottel

CLAIM

The ornamental design for a connecting terminal for chip cards, as shown and described.

DESCRIPTION

The single FIGURE is a top plan view of a connecting terminal for chip cards showing my new design; the bottom plan view is a mirror image and the terminal shown is of no substantial thickness.

1 Claim, 1 Drawing Sheet



