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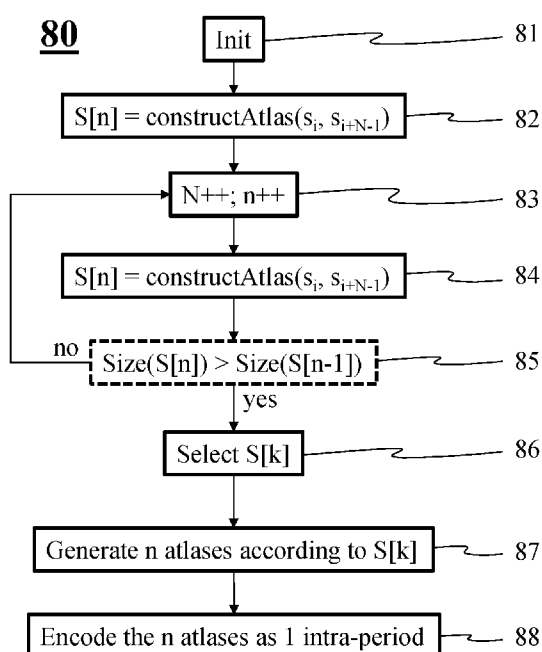


Figure 8

(57) Abstract: A method and a device are disclosed to encode volumetric video in a patch-based atlas format in intra-periods of varying length. A first atlas layout is built for a first sequence of 3D scenes. The number of 3D scenes in the sequence is chosen to fit the size of a GoP of the codec. A second sequence is iteratively set up by appending the next 3D scene of the sequence to encode while the number of patches of the layout built for this iterative second sequence is lower than or equal to the number of patches of the first layout. When iterations end, one of the layouts is selected to generate every atlas of the group. In such a way, size of metadata is decreased and compression is enhanced.

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A METHOD AND APPARATUS FOR ENCODING AND DECODING VOLUMETRIC VIDEO

1. Technical Field

The present principles generally relate to the domain of three-dimensional (3D) scene and volumetric video content. The present document is also understood in the context of the encoding, the formatting and the decoding of data representative of the texture and the geometry of a 3D scene for a rendering of volumetric content on end-user devices such as mobile devices or Head-Mounted Displays (HMD).

2. Background

The present section is intended to introduce the reader to various aspects of art, which may be related to various aspects of the present principles that are described and/or claimed below. This discussion is believed to be helpful in providing the reader with background information to facilitate a better understanding of the various aspects of the present principles. Accordingly, it should be understood that these statements are to be read in this light, and not as admissions of prior art.

Recently there has been a growth of available large field-of-view content (up to 360°). Such content is potentially not fully visible by a user watching the content on immersive display devices such as Head Mounted Displays, smart glasses, PC screens, tablets, smartphones and the like. That means that at a given moment, a user may only be viewing a part of the content. However, a user can typically navigate within the content by various means such as head movement, mouse movement, touch screen, voice and the like. It is typically desirable to encode and decode this content.

Immersive video, also called 360° flat video, allows the user to watch all around himself through rotations of his head around a still point of view. Rotations only allow a 3 Degrees of Freedom (3DoF) experience. Even if 3DoF video is sufficient for a first omnidirectional video experience, for example using a Head-Mounted Display device (HMD), 3DoF video may quickly become frustrating for the viewer who would expect more freedom, for example by experiencing

parallax. In addition, 3DoF may also induce dizziness because of a user never only rotates his head but also translates his head in three directions, translations which are not reproduced in 3DoF video experiences.

A large field-of-view content may be, among others, a three-dimension computer graphic imagery scene (3D CGI scene), a point cloud or an immersive video. Many terms might be used to design such immersive videos: Virtual Reality (VR), 360, panoramic, 4π steradians, immersive, omnidirectional or large field of view for example.

Volumetric video (also known as 6 Degrees of Freedom (6DoF) video) is an alternative to 3DoF video. When watching a 6DoF video, in addition to rotations, the user can also translate his head, and even his body, within the watched content and experience parallax and even volumes. Such videos considerably increase the feeling of immersion and the perception of the scene depth and prevent from dizziness by providing consistent visual feedback during head translations. The content is created by the means of dedicated sensors allowing the simultaneous recording of color and depth of the scene of interest. The use of rig of color cameras combined with photogrammetry techniques is a way to perform such a recording, even technical difficulties remain.

While 3DoF videos comprise a sequence of images resulting from the un-mapping of texture images (e.g. spherical images encoded according to latitude/longitude projection mapping or equirectangular projection mapping), 6DoF video frames embed information from several points of views. They can be viewed as a temporal series of point clouds resulting from a three-dimension capture. Two kinds of volumetric videos may be considered depending on the viewing conditions. A first one (i.e. complete 6DoF) allows a complete free navigation within the video content whereas a second one (aka. 3DoF+) restricts the user viewing space to a limited volume called viewing bounding box, allowing limited translation of the head and parallax experience. This second context is a valuable trade-off between free navigation and passive viewing conditions of a seated audience member.

A technical approach for the encoding of volumetric video is based on the projection of the 3D scene onto a multiplicity of 2D images, called patches, packed into atlases which can be further compressed using conventional video encoding standards (e.g., HEVC). Patches are packed in an atlas following the organization of a given layout. The atlas is encoded in the data stream in association with metadata describing its layout; that is a description of the location, the shape and

the size of each patch within the atlas. These metadata have a non-negligible size as an atlas may comprises several hundreds of patches. To limit the size of layout metadata, an approach consists in using a same layout for a given number of successive atlases corresponding to the projection of the same number of successive 3D scene of the volumetric sequence. This number is selected to fit the number of frames in a Group of Picture (GoP) of a chosen codec, for example 8 or 12. Even when dividing the number of layout metadata by this number, the size of these data remains important. A technique for reducing the size of layout data in an atlas based encoding of volumetric video without decreasing the quality of the encoded sequence is lacking.

3. Summary

The following presents a simplified summary of the present principles to provide a basic understanding of some aspects of the present principles. This summary is not an extensive overview of the present principles. It is not intended to identify key or critical elements of the present principles. The following summary merely presents some aspects of the present principles in a simplified form as a prelude to the more detailed description provided below.

The present principles relate a method comprising obtaining a first atlas layout for a first sequence of 3D scenes. An atlas layout defines an organization of at least one patch within an atlas that is image data representative of at least one patch of a same 3D scene. A patch is image data representative of a projection of a part of a 3D scene onto an image plane. So, different projections of different parts of a 3D scene provide a set of patches. These patches are packed in a bigger image called atlas. The organization of the patches within their atlas is called the atlas layout. The method also comprises obtaining a second atlas layout for a second sequence of 3D scenes. This second sequence is the first sequence to which the next 3D scene of the sequence of 3D scene to encode is appended. If the number of patches of the second atlas layout is greater than the number of patches of the first atlas layout, then, the method generates atlases for the first sequence of 3D scenes according to the first atlas layout. Otherwise, the method selects a layout among the first and the second layout and generates atlases for the second sequence of 3D scenes according to the selected layout.

In a particular embodiment, obtaining atlas layouts steps are iterated while the number of patches of the second atlas layout is lower than or equal to the number of patches of the first atlas

layout, the first sequence of 3D scenes becoming the second sequence of 3D scene of the preceding iteration. While iterating, the method stores the obtained first atlas layouts at each iteration. When iterations end, a layout is selected among the stored layouts and atlases for the last first sequence of 3D scenes are generated according to this selected atlas layout. In a variant, a given number is set to be the maximal number of atlases generated according to a same atlas layout. In this case, the iterations of the method end when the second sequence comprises more than this given number of 3D scenes.

According to a particular embodiment, the selecting of a layout among several is performed on the basis of a rate-distortion optimization criterion. According to a particular embodiment, generated atlases are encoded as one variable-size intra-period in a video data stream.

The present principles also relate to a device comprising a memory storing instructions to cause a processor to implement the steps of the method. The present principles also relate to a video data stream generated by such a device.

4. Brief Description of Drawings

The present disclosure will be better understood, and other specific features and advantages will emerge upon reading the following description, the description making reference to the annexed drawings wherein:

- **Figure 1** shows a three-dimension (3D) model of an object and points of a point cloud corresponding to the 3D model, according to a non-limiting embodiment of the present principles;
- **Figure 2** shows a non-limitative example of the encoding, transmission and decoding of data representative of a sequence of 3D scenes, according to a non-limiting embodiment of the present principles;
- **Figure 3** shows an example architecture of a device which may be configured to implement a method described in relation with figure 8, according to a non-limiting embodiment of the present principles;

- **Figure 4** shows an example of an embodiment of the syntax of a stream when the data are transmitted over a packet-based transmission protocol, according to a non-limiting embodiment of the present principles;
- **Figure 5** illustrates the patch atlas approach with an example of 4 projection centers, according to a non-limiting embodiment of the present principles;
- **Figure 6** shows an example of an atlas comprising the texture information of the points of a 3D scene, according to a non-limiting embodiment of the present principles;
- **Figure 7** shows an example of an atlas comprising the depth information of the points of the 3D scene of figure 6, according to a non-limiting embodiment of the present principles;
- **Figure 8** illustrates a method 80 of encoding a sequence of 3D scenes in a data stream according to a non-limitative embodiment of the present principles;
- **Figure 9** shows an example structure for intra-periods for atlas layouts of varying lifetime, according to a non-limitative embodiment of the present principles;

5. Detailed description of embodiments

The present principles will be described more fully hereinafter with reference to the accompanying figures, in which examples of the present principles are shown. The present principles may, however, be embodied in many alternate forms and should not be construed as limited to the examples set forth herein. Accordingly, while the present principles are susceptible to various modifications and alternative forms, specific examples thereof are shown by way of examples in the drawings and will herein be described in detail. It should be understood, however, that there is no intent to limit the present principles to the particular forms disclosed, but on the contrary, the disclosure is to cover all modifications, equivalents, and alternatives falling within the spirit and scope of the present principles as defined by the claims.

The terminology used herein is for the purpose of describing particular examples only and is not intended to be limiting of the present principles. As used herein, the singular forms "a", "an" and "the" are intended to include the plural forms as well, unless the context clearly indicates otherwise. It will be further understood that the terms "comprises", "comprising", "includes" and/or "including" when used in this specification, specify the presence of stated features, integers, steps, operations, elements, and/or components but do not preclude the presence or addition of one or

more other features, integers, steps, operations, elements, components, and/or groups thereof. Moreover, when an element is referred to as being "responsive" or "connected" to another element, it can be directly responsive or connected to the other element, or intervening elements may be present. In contrast, when an element is referred to as being "directly responsive" or "directly
5 connected" to other element, there are no intervening elements present. As used herein the term "and/or" includes any and all combinations of one or more of the associated listed items and may be abbreviated as "/".

It will be understood that, although the terms first, second, etc. may be used herein to describe various elements, these elements should not be limited by these terms. These terms are
10 only used to distinguish one element from another. For example, a first element could be termed a second element, and, similarly, a second element could be termed a first element without departing from the teachings of the present principles.

Although some of the diagrams include arrows on communication paths to show a primary direction of communication, it is to be understood that communication may occur in the opposite
15 direction to the depicted arrows.

Some examples are described with regard to block diagrams and operational flowcharts in which each block represents a circuit element, module, or portion of code which comprises one or more executable instructions for implementing the specified logical function(s). It should also be noted that in other implementations, the function(s) noted in the blocks may occur out of the order
20 noted. For example, two blocks shown in succession may, in fact, be executed substantially concurrently or the blocks may sometimes be executed in the reverse order, depending on the functionality involved.

Reference herein to "in accordance with an example" or "in an example" means that a particular feature, structure, or characteristic described in connection with the example can be
25 included in at least one implementation of the present principles. The appearances of the phrase in accordance with an example" or "in an example" in various places in the specification are not necessarily all referring to the same example, nor are separate or alternative examples necessarily mutually exclusive of other examples.

Reference numerals appearing in the claims are by way of illustration only and shall have no limiting effect on the scope of the claims. While not explicitly described, the present examples and variants may be employed in any combination or sub-combination.

5 According to the present principles, methods and devices for encoding and decoding volumetric video in an atlas-based format are disclosed herein. 3D scenes are projected onto a multiplicity of 2D images, called patches, packed into atlases according to a layout. According to the present principles, a same layout organizing patches in atlases is used for a varying number of successive atlases encoding successive 3D scenes of the sequence.

10 **Figure 1** shows a three-dimension (3D) model 10 of an object and points of a point cloud 11 corresponding to 3D model 10. 3D model 10 and the point cloud 11 may for example correspond to a possible 3D representation of an object of the 3D scene comprising other objects. Model 10 may be a 3D mesh representation and points of point cloud 11 may be the vertices of the mesh. Points of point cloud 11 may also be points spread on the surface of faces of the mesh.
15 Model 20 may also be represented as a splatted version of point cloud 11, the surface of model 10 being created by splatting the points of the point cloud 11. Model 10 may be represented by a lot of different representations such as voxels or splines. Figure 1 illustrates the fact that a point cloud may be defined with a surface representation of a 3D object and that a surface representation of a 3D object may be generated from a point of cloud. As used herein, projecting points of a 3D object
20 (by extension points of a 3D scene) onto an image is equivalent to projecting any representation of this 3D object, for example a point cloud, a mesh, a spline model or a voxel model.

A point cloud may be represented in memory, for instance, as a vector-based structure, wherein each point has its own coordinates in the frame of reference of a viewpoint (e.g. three-dimensional coordinates XYZ, or a solid angle and a distance (also called depth) from/to the
25 viewpoint) and one or more attributes, also called component. An example of component is the color component that may be expressed in various color spaces, for example RGB (Red, Green and Blue) or YUV (Y being the luma component and UV two chrominance components). The point cloud is a representation of a 3D scene comprising objects. The 3D scene may be seen from a given viewpoint or a range of viewpoints. The point cloud may be obtained by many ways, e.g.:

- from a capture of a real object shot by a rig of cameras, optionally complemented by depth active sensing device;
- from a capture of a virtual/synthetic object shot by a rig of virtual cameras in a modelling tool;
- 5 • from a mix of both real and virtual objects.

Figure 2 shows a non-limitative example of the encoding, transmission and decoding of data representative of a sequence of 3D scenes. The encoding format that may be, for example and at the same time, compatible for 3DoF, 3DoF+ and 6DoF decoding.

10 A sequence of 3D scenes 20 is obtained. As a sequence of pictures is a 2D video, a sequence of 3D scenes is a 3D (also called volumetric) video. A sequence of 3D scenes may be provided to a volumetric video rendering device for a 3DoF, 3Dof+ or 6DoF rendering and displaying.

Sequence of 3D scenes 20 is provided to an encoder 21. The encoder 21 takes one 3D scenes or a sequence of 3D scenes as input and provides a bit streams representative of the input.

15 The bit stream may be stored in a memory 22 and/or on an electronic data medium and may be transmitted over a network 22. The bit stream representative of a sequence of 3D scenes may be read from a memory 22 and/or received from a network 22 by a decoder 23. Decoder 23 is inputted by said bit stream and provides a sequence of 3D scenes, for instance in a point cloud format.

Encoder 21 may comprise several circuits implementing several steps. In a first step,

20 encoder 21 projects each 3D scene onto at least one 2D picture. 3D projection is any method of mapping three-dimensional points to a two-dimensional plane. As most current methods for displaying graphical data are based on planar (pixel information from several bit planes) two-dimensional media, the use of this type of projection is widespread, especially in computer graphics, engineering and drafting. Projection circuit 211 provides at least one two-dimensional

25 frame 2111 for a 3D scene of sequence 20. Frame 2111 comprises color information and depth information representative of the 3D scene projected onto frame 2111. In a variant, color information and depth information are encoded in two separate frames 2111 and 2112.

Metadata 212 are used and updated by projection circuit 211. Metadata 212 comprise information about the projection operation (e.g. projection parameters) and about the way color

and depth information is organized within frames 2111 and 2112 as described in relation to figures 5 to 7. According to the present principles, 3D scenes are projected onto a multiplicity of 2D images, called patches, packed into atlases. A same layout organizing patches in atlases is used for a varying number of successive atlases encoding successive 3D scenes of the sequence.

5 A video encoding circuit 213 encodes sequence of frames 2111 and 2112 as a video. Pictures of a 3D scene 2111 and 2112 (or a sequence of pictures of the 3D scene) is encoded in a stream by video encoder 213. Then video data and metadata 212 are encapsulated in a data stream by a data encapsulation circuit 214.

Encoder 213 is for example compliant with an encoder such as:

- 10 – JPEG, specification ISO/CEI 10918-1 UIT-T Recommendation T.81, <https://www.itu.int/rec/T-REC-T.81/en>;
- AVC, also named MPEG-4 AVC or h264. Specified in both UIT-T H.264 and ISO/CEI MPEG-4 Part 10 (ISO/CEI 14496-10), <http://www.itu.int/rec/T-REC-H.264/en>, HEVC (its specification is found at the ITU website, T recommendation, H series, h265, <http://www.itu.int/rec/T-REC-H.265-201612-I/en>);
- 15 – 3D-HEVC (an extension of HEVC whose specification is found at the ITU website, T recommendation, H series, h265, <http://www.itu.int/rec/T-REC-H.265-201612-I/en> annex G and I);
- VP9 developed by Google; or
- 20 – AV1 (AOMedia Video 1) developed by Alliance for Open Media.

The data stream is stored in a memory that is accessible, for example through a network 22, by a decoder 23. Decoder 23 comprises different circuits implementing different steps of the decoding. Decoder 23 takes a data stream generated by an encoder 21 as an input and provides a
 25 sequence of 3D scenes 24 to be rendered and displayed by a volumetric video display device, like a Head-Mounted Device (HMD). Decoder 23 obtains the stream from a source 22. For example, source 22 belongs to a set comprising:

- a local memory, e.g. a video memory or a RAM (or Random-Access Memory), a flash memory, a ROM (or Read Only Memory), a hard disk;

- a storage interface, e.g. an interface with a mass storage, a RAM, a flash memory, a ROM, an optical disc or a magnetic support;
- a communication interface, e.g. a wireline interface (for example a bus interface, a wide area network interface, a local area network interface) or a wireless interface (such as a
5 IEEE 802.11 interface or a Bluetooth® interface); and
- a user interface such as a Graphical User Interface enabling a user to input data.

Decoder 23 comprises a circuit 234 for extract data encoded in the data stream. Circuit 234 takes a data stream as input and provides metadata 232 corresponding to metadata 212 encoded in
10 the stream and a two-dimensional video. The video is decoded by a video decoder 233 which provides a sequence of frames. Decoded frames comprise color and depth information. In a variant, video decoder 233 provides two sequences of frames, one comprising color information, the other comprising depth information. A circuit 231 uses metadata 232 to un-project color and depth information from decoded frames to provide a sequence of 3D scenes 24. Sequence of 3D scenes
15 24 corresponds to sequence of 3D scenes 20, with a possible loss of precision related to the encoding as a 2D video and to the video compression.

Figure 3 shows an example architecture of a device 30 which may be configured to implement a method described in relation with figure 8. Encoder 21 and/or decoder 23 of figure 2
20 may implement this architecture. Alternatively, each circuit of encoder 21 and/or decoder 23 may be a device according to the architecture of Figure 3, linked together, for instance, via their bus 31 and/or via I/O interface 36.

Device 30 comprises following elements that are linked together by a data and address bus
31:

- a microprocessor 32 (or CPU), which is, for example, a DSP (or Digital Signal Processor);
- a ROM (or Read Only Memory) 33;
- a RAM (or Random Access Memory) 34;
- a storage interface 35;
- an I/O interface 36 for reception of data to transmit, from an application; and

- a power supply, e.g. a battery.

In accordance with an example, the power supply is external to the device. In each of mentioned memory, the word « register » used in the specification may correspond to area of small capacity (some bits) or to very large area (e.g. a whole program or large amount of received or decoded data). The ROM 33 comprises at least a program and parameters. The ROM 33 may store algorithms and instructions to perform techniques in accordance with present principles. When switched on, the CPU 32 uploads the program in the RAM and executes the corresponding instructions.

The RAM 34 comprises, in a register, the program executed by the CPU 32 and uploaded after switch-on of the device 30, input data in a register, intermediate data in different states of the method in a register, and other variables used for the execution of the method in a register.

The implementations described herein may be implemented in, for example, a method or a process, an apparatus, a computer program product, a data stream, or a signal. Even if only discussed in the context of a single form of implementation (for example, discussed only as a method or a device), the implementation of features discussed may also be implemented in other forms (for example a program). An apparatus may be implemented in, for example, appropriate hardware, software, and firmware. The methods may be implemented in, for example, an apparatus such as, for example, a processor, which refers to processing devices in general, including, for example, a computer, a microprocessor, an integrated circuit, or a programmable logic device. Processors also include communication devices, such as, for example, computers, cell phones, portable/personal digital assistants ("PDAs"), and other devices that facilitate communication of information between end-users.

In accordance with examples, the device 30 is configured to implement a method described in relation with figure 8, and belongs to a set comprising:

- a mobile device;
- a communication device;
- a game device;
- a tablet (or tablet computer);
- a laptop;

- a still picture camera;
- a video camera;
- an encoding chip;
- a server (e.g. a broadcast server, a video-on-demand server or a web server).

5

Figure 4 shows an example of an embodiment of the syntax of a stream when the data are transmitted over a packet-based transmission protocol. Figure 4 shows an example structure 4 of a volumetric video stream. The structure consists in a container which organizes the stream in independent elements of syntax. The structure may comprise a header part 41 which is a set of data common to every syntax elements of the stream. For example, the header part comprises some of metadata about syntax elements, describing the nature and the role of each of them. The header part may also comprise a part of metadata 212 of figure 2, for instance the coordinates of a central point of view used for projecting points of a 3D scene onto frames 2111 and 2112. The structure comprises a payload comprising an element of syntax 42 and at least one element of syntax 43. Syntax element 42 comprises data representative of the color and depth frames. Images may have been compressed according to a video compression method.

Element of syntax 43 is a part of the payload of the data stream and may comprise metadata about how frames of element of syntax 42 are encoded, for instance parameters used for projecting and packing points of a 3D scene onto frames. Such metadata may be associated with each frame of the video or to group of frames (also known as Group of Pictures (GoP) in video compression standards).

Figure 5 illustrates the patch atlas approach with an example of 4 projection centers. 3D scene 50 comprises a character. For instance, center of projection 51 is a perspective camera and camera 53 is an orthographic camera. Cameras may also be omnidirectional cameras with, for instance a spherical mapping (e.g. Equi-Rectangular mapping) or a cube mapping. The 3D points of the 3D scene are projected onto the 2D planes associated with virtual cameras located at the projection centers, according to a projection operation described in projection data of metadata. In the example of figure 5, projection of the points captured by camera 51 is mapped onto patch 52 according to a perspective mapping and projection of the points captured by camera 53 is mapped onto patch 54 according to an orthographic mapping.

The clustering of the projected pixels yields a multiplicity of 2D patches, which are packed in a rectangular atlas 55. The organization of patches within the atlas defines the atlas layout. In an embodiment, two atlases with identical layout: one for texture (i.e. color) information and one for depth information. Two patches captured by a same camera or by two distinct cameras may
5 comprise information representative of a same part of the 3D scene, like, for instance patches 54 and 56.

The packing operation produces a patch data for each generated patch. A patch data comprises a reference to a projection data (e.g. an index in a table of projection data or a pointer (i.e. address in memory or in a data stream) to a projection data) and information describing the
10 location and the size of the patch within the atlas (e.g. top left corner coordinates, size and width in pixels). Patch data items are added to metadata to be encapsulated in the data stream in association with the compressed data of the one or two atlases. The set of patch data items is also called the layout metadata in the present description.

Figure 6 shows an example of an atlas 60 comprising the texture information (e.g. RGB
15 data or YUV data) of the points of a 3D scene, according to a non-limiting embodiment of the present principles. As explained in relation to figure 5, an atlas is an image packing patches, a patch being a picture obtained by projecting a part of the points of the 3D scene. The layout of the atlas is metadata describing the location, the shape and the size of patches within the atlas. In an embodiment, the shape of a patch is a rectangle by default, so the shape has not to be described,
20 this information can be omitted in the layout metadata. In this embodiment, the location may be the top left coordinates of the patch rectangle in pixels of the atlas and its size described by a width and a height in pixels. In a variant, the size of a patch is described in arc degrees corresponding to the solid angle of the 3D space, pointing on a projection center, comprising points projected onto that patch. In other embodiments, patches are ellipses and/or polygons, described, for instance with
25 Scalable Vector Graphics (SVG) instructions.

In the example of figure 6, atlas 60 comprises a first part 61 comprising the texture information of the points of the 3D scene that are visible from a point of view and one or more second parts 62. The texture information of the first part 61 may for example be obtained according to an equirectangular projection mapping, an equirectangular projection mapping being an
30 example of spherical projection mapping. In the example of figure 6, the second parts 62 are

arranged at the left and right borders of the first part 61 but the second parts may be arranged differently. Second parts 62 comprise texture information of parts of the 3D scene that are complementary to the part visible from the point of view. The second parts may be obtained by removing from the 3D scene the points that are visible from the first viewpoint (the texture of which being stored in the first part) and by projecting the remaining points according to the same point of view. The latter process may be reiterated iteratively to obtain at each time the hidden parts of the 3D scene. According to a variant, the second parts may be obtained by removing from the 3D scene the points that are visible from the point of view, for example a central point of view, (the texture of which being stored in the first part) and by projecting the remaining points according to a point of view different from the first point of view, for example from one or more second point of view of a space of view centred onto the central point of view (e.g. the viewing space of a 3DoF rendering).

The first part 61 may be seen as a first large texture patch (corresponding to a first part of the 3D scene) and the second parts 62 comprises smaller textures patches (corresponding to second parts of the 3D scene that are complementary to the first part). Such an atlas has the advantage to be compatible at the same time with 3DoF rendering (when rendering only the first part 61) and with 3DoF+ / 6DoF rendering.

Figure 7 shows an example of an atlas 70 comprising the depth information of the points of the 3D scene of figure 6, according to a non-limiting embodiment of the present principles. Atlas 70 may be seen as the depth image corresponding to texture image 60 of figure 6.

Atlas 70 comprises a first part 71 comprising the depth information of the points of the 3D scene that are visible from the central point of view and one or more second parts 62. Atlas 70 may be obtained in a same way as atlas 60 but contains the depth information associated with the points of the 3D scene instead of the texture information.

For 3DoF rendering of the 3D scene, only one point of view, typically the central point of view, is considered. The user may rotate his head in three degrees of freedom around the first point of view to watch various parts of the 3D scene, but the user cannot move this unique point of view. Points of the scene to be encoded are points which are visible from this unique point of view, and only the texture information is needed to be encoded / decoded for the 3DoF rendering. There is

no need to encode points of the scene that are not visible from this unique point of view for a 3DoF rendering as the user cannot access to them.

With regard to 6DoF rendering, the user may move the viewpoint everywhere in the scene. In this case, it is required to encode every point (depth and texture) of the scene in the bitstream as every point is potentially accessible by a user who can move his/her point of view. At the
5 encoding stage, there is no means to know, a priori, from which point of view the user will observe the 3D scene.

With regard to 3DoF+ rendering, the user may move the point of view within a limited space around a central point of view. This enables to experience parallax. Data representative of
10 the part of the scene visible from any point of the space of view is to be encoded into the stream, including the data representative of the 3D scene visible according to the central point of view (i.e. the first parts 61 and 71). The size and shape of the space of view may for example be decided and determined at the encoding step and encoded in the bitstream. The decoder may obtain this
15 information from the bitstream and the renderer limits the space of view to the space determined by the obtained information. According to another example, the renderer determines the space of view according to hardware constraints, for example in relation to capabilities of the sensor(s) that detects the movements of the user. In such a case, if, at the encoding phase, a point visible from a
point within the space of view of the renderer has not been encoded in the bitstream, this point will not be rendered. According to a further example, data (e.g. texture and/or geometry) representative
20 of every point of the 3D scene is encoded in the stream without considering the rendering space of view. To optimize the size of the stream, only a subset of the points of the scene may be encoded, for instance the subset of points that may be seen according to a rendering space of view.

The parameters of the projection surfaces of figure 5 may change frequently over time, in
25 order to adapt to the pose and geometry changes between 3D scenes of the sequence. These parameters are selected by the projection algorithm as a function of criteria to obey, like the number of patch or a rate of redundancy in projected information. From one 3D scene to its successor in the sequence, these parameters may change leading to a modification of the number and/or the size of patches and, transitively modifying the atlas structure and the related layout
30 metadata. To limit these changes, the projection parameters are evaluated per small fixed-length

segments of N consecutive frames (typically N is equal to 8). The transmitted un-projection parameters, including the layout metadata (i.e. patch data items) are therefore regularly updated every N frames. Furthermore, the encoding structure of the video streams made up of the sequence of depth and texture patch atlases is adapted accordingly, with aligned fixed size, N-frame long, closed GOPs. Doing so, the encoding efficiency is optimized by resetting the temporal predictions at each video content changes (i.e. patch atlases structure updates, which create “scene cuts” in the atlas videos). Such a method of update of projection surface parameters at fixed time instants, every N frames, is sub-optimal in terms of transmission bit-rate, as a given projection surface (and so an atlas layout) could be valid for a much longer duration. Provided the 3D geometry of the scene do not change too fast:

- The metadata bit-rate may be decreased by avoiding un-necessary updates of un-projection parameters;
- The video compression efficiency of projected depth and texture atlases may be significantly improved by avoiding too frequent un-necessary scene changes (corresponding to atlases structure updates) and adapting the encoding parameters accordingly.

According to the present principles, instead of estimating the parameters of the patch-based projection surface best adapted to fixed-length groups of N successive point clouds (and so determining an atlas layout for N successive atlases), the number N of successive frames varies over time, depending on the temporal evolution of the scene geometry.

Figure 8 illustrates a method 80 of encoding a sequence of 3D scenes in a data stream according to a non-limitative embodiment of the present principles.

At a step 81, data are obtained from a source. For example, a sequence of 3D scenes is obtained. Variables are initialized. In particular, a first sequence of consecutive 3D scenes is selected. The size N of the first sequence may be set to the size of a GoP for the codec selected to encode the atlases representative of the 3D scenes, for instance N=8 for HEVC. A maximal size may be initialized too. So, 3D scenes of the first sequences goes from index i to index i+N-1, where i is the index of the first 3D scene of the first sequence. An index n is initialized to 0 for the purpose of the clarity of the present description.

At a step 82 an atlas layout is built for the first sequence according to a known method. A set of patches is generated by projecting points of the 3D scenes onto projection surfaces as illustrated in Figure 5. Patches are packed in N atlases according to a same layout. The number of patches packed in each atlas is called the size of the layout. The obtained layout is stored in a table S in a memory at index n. At a step 83, n is incremented. A second sequence of 3D scenes is built by appending the i+N scene of the sequence to the first sequence. That is, the upcoming 3D scene is added to the first sequence to form the second sequence. N is incremented also. At a step 84, a new atlas layout is built for the second sequence. This second layout is stored in the table of layouts at index n (which has been incremented).

At a step 85, the size of the second layout S[n] is compared to the size of the first layout S[n-1]. If the number of patches of the second layout is lower than or equal to the number of patches of the first layout, then the method iterates at step 83. N and N are incremented, the second sequence becomes the first sequence and a new second sequence is set up by appending the next 3D scene to the new first sequence. An atlas layout is built for this new second sequence and is stored in table S. The method iterates while the size of S[n] is lower than or equal to S[n-1]. In a variant, the iterations end when the second sequence comprises a number of 3D scenes which exceeds a given number, for example 9, 10, 128, 256 or 512. This given number may be defined in accordance to the maximal intra-period size of the codec selected to encode the atlases.

At a step 86, table S comprises n+1 layouts. The last one (i.e. the layout built for the last second sequence) is removed as its number of patches exceeds the number of patches of the first layout (or because the number of stored layouts exceeds the maximal intra period size by one in the variant). One of these layout is selected to be used to generate the n atlases for the last first sequence. The selected layout may be the last or the layout stored in the table. In another embodiment, when n multiple successive layouts $\{S[k]\}, k \in [1, K]$, have been estimated, the method selects the atlas layout of patches generating texture and depth atlas videos with best compression properties. First it is verified for each computed atlas layout whether all points from final point cloud segment $[1, N]$ can be paired with one of its patches, thus yielding a group of valid candidate patch sets $\{S[k]\}, k \in [1, K'], K' \leq K$. Then depth and texture atlas video for each incremental sequences of 3D scenes are encoded with same encoding parameters (i.e. in one single GOP) for each candidate patch set, and the patch set with best compression properties is chosen.

The bit-rate only is considered and the patch set yielding the smallest compressed atlases file size R is selected. In a variant, a rate distortion optimization (RDO; like in G. Sullivan and T. Wiegand, “Rate-distortion optimization for video compression”) approach is followed, by selecting the atlas layout S^* minimizing a distortion measure D , subject to a constraint R_c on the number of bits used

5 R as described by equation Eq1.

$$\text{Eq1} \quad S^* = \underset{S \in \{S^k\}, k \in [1, K']}{\operatorname{argmin}} \{D(S)\}, \text{ subject to } R < R_c$$

This optimization task can be solved using Lagrangian optimization where a distortion term is weighted against a rate term like in Eq2.

$$\text{Eq2} \quad S^* = \underset{S \in \{S^k\}, k \in [1, K']}{\operatorname{argmin}} \{D(S) + \lambda R\}$$

10 Each solution to equation Eq2 for a given value of the Lagrangian multiplier λ is a solution of equation Eq1 for a given constraint R_c .

A distortion measure D is therefore required. An image-based criterion based on rendered images on user viewport(s) is preferred to a point-to-point distortion of compressed point cloud, as being closer to the user experience. More precisely a pixel-to-pixel distance metrics is evaluated

15 between rendered frames before and after volumetric video compression, and averaged on those frames belonging to a path of pre-defined viewports. In other variants, other criteria may be considered according to the properties of the video stream which have to be guaranteed or optimized.

A pseudo code of method 80 may be:

```

20  int nb_seg = 1;           // initial number of point cloud segments
    int pcl = 1;             // index of first point cloud of current segment
    int pc2 = Nmin;          // index of last point cloud of current segment
    int n;                   // number of added point clouds (to minimum-size segment)
    int Nmin = 8;            // minimum length of a point cloud segment
25  int Nmax = 256;          // maximum length of a point cloud segment
    int P1, P[];             // number of patches in current patch set
    PatchSet S1, S[];        // candidate patch sets in current segment
    PatchSet SS[];           // selected patch sets (one per segment)
    float rdcrit;            // rate-distortion criterion to minimize

```

```

    for (;;) {
        // enlarge incrementally point cloud segment length
        for (n = 0;; n++, pc2++) {
5           (P1, S1) = ConstructAtlas(pc1, pc2);
            if ((n > 0) && (P1 > P[n-1]) {n--; break}; // patch number increases, exit
            P[n] = P1; S[n] = S1;                    // store current patch set and continue
            if (pc2-pc1+1 == Nmax) break;           // maximum segment length attained, exit
            if (pc2 == PCMAX) break;                // end of point cloud sequence, exit
10          }
            // select best candidate patch set (among n+1 per current segment)
            float rdcrit_min = EncodeVideoAtlases(S[n]);
            n_best = n;
            for (i = n-1; i >= 0; i--) {
15          segment    if (CheckAtlasValidity(S[n]) { // verify patch set is valid for entire
                        rdcrit = EncodeVideoAtlases(S[i]);
                        if (rdcrit < rdcrit_min) {
                            n_best = i;
20                        rdcrit_min = rdcrit;
                        }
                    }
                }
            SS[nb_seg-1] = S[n_best];
25          // jump to next point cloud segment
            if (pc2 > PCMAX) break;                // end of point cloud sequence, exit
            nb_seg++;                               // one more segment
            pc1 = pc2;
            pc2 = min(pc1 + Nmin -1, PCMAX);
30          }

```

The depth and texture atlas videos which are generated by the above described projection method can be encoded with any conventional video encoding standardized method, e.g. HEVC.

Those atlas videos have specific properties, however, which are inherent to their generation

process: in between two atlas structure updates, the video content is highly temporally correlated, as the frames consist of the same layout of patches (e.g. rectangular), comprising the partial projections of depth or texture. Atlas layout updates for a new group of input 3D scenes break the temporal consistency and can be described as “scene cuts”. Such property can be exploited for an optimized compression efficiency by setting accordingly the adequate encoding parameters.

A typical temporal organization of the different types (I, P, B) of encoded picture is based on Group of Pictures (GOP). Typically, a GOP is 8-picture long. An intra period is usually made of several GOPs.

To benefit from the temporal predictability of frames in atlas videos, the method according to the present principles aligns a variable GOP and intra period structure with the atlas updates, with a new intra period starting at each atlas update.

Figure 9 shows an example structure for intra-periods for atlas layouts of varying lifetime N . In this example, an intra-period begins with a first fixed-size k -picture long GOP. For example, k is equal to 8 or 10. $(\lfloor N / k \rfloor - 1)$ fixed-size GOPs follow. The last GOP of the intra-period is of shortened length N modulo the fixed size. In the example of Figure 9, a GOP contains 8 pictures. A first intra-period 93 contains a first GOP 91 of 8 atlases and a shortened GOP 92 of 3 atlases. GOP 92 has been shortened because appending the next atlas to this sequence would have increased the number of patches and so, modified the layout. Figure 9 shows another intra-period 96 comprising two GOP 94 and 95 of eight pictures. At this stage, intra-period 96 is not yet closed and will receive at least another GOP, maybe shortened.

Such a structure may be embedded in generic elements of ISO/BMMF syntax, for example, to transmit un-projection parameters metadata of varying duration described according to the present principles. The un-projection parameters for a given patch-based projection surface, including list of patches with their characteristics (i.e. patch data items) and associated atlas packing of patches (layout metadata), are defined as a metadata sample, with a structured sample format. The un-projection metadata samples are placed in a timed metadata track, with samples of varying duration. The two video tracks of atlas-based projection of depth and texture are combined in a track group. The un-projection metadata track is linked to the projected video track group through a track reference (i.e. with a 'cdtg' track reference).

The duration of timed metadata samples not matching the duration of associated video samples, the synchronizing of a projected depth / texture video frame and the associated un-projection metadata at rendering side are resolved by parsing the sample decoding times.

5 The implementations described herein may be implemented in, for example, a method or a process, an apparatus, a computer program product, a data stream, or a signal. Even if only discussed in the context of a single form of implementation (for example, discussed only as a method or a device), the implementation of features discussed may also be implemented in other forms (for example a program). An apparatus may be implemented in, for example, appropriate
10 hardware, software, and firmware. The methods may be implemented in, for example, an apparatus such as, for example, a processor, which refers to processing devices in general, including, for example, a computer, a microprocessor, an integrated circuit, or a programmable logic device. Processors also include communication devices, such as, for example, Smartphones, tablets, computers, mobile phones, portable/personal digital assistants ("PDAs"), and other devices that
15 facilitate communication of information between end-users.

Implementations of the various processes and features described herein may be embodied in a variety of different equipment or applications, particularly, for example, equipment or applications associated with data encoding, data decoding, view generation, texture processing, and other processing of images and related texture information and/or depth information.
20 Examples of such equipment include an encoder, a decoder, a post-processor processing output from a decoder, a pre-processor providing input to an encoder, a video coder, a video decoder, a video codec, a web server, a set-top box, a laptop, a personal computer, a cell phone, a PDA, and other communication devices. As should be clear, the equipment may be mobile and even installed in a mobile vehicle.

25 Additionally, the methods may be implemented by instructions being performed by a processor, and such instructions (and/or data values produced by an implementation) may be stored on a processor-readable medium such as, for example, an integrated circuit, a software carrier or other storage device such as, for example, a hard disk, a compact diskette ("CD"), an optical disc (such as, for example, a DVD, often referred to as a digital versatile disc or a digital video disc), a
30 random access memory ("RAM"), or a read-only memory ("ROM"). The instructions may form

an application program tangibly embodied on a processor-readable medium. Instructions may be, for example, in hardware, firmware, software, or a combination. Instructions may be found in, for example, an operating system, a separate application, or a combination of the two. A processor may be characterized, therefore, as, for example, both a device configured to carry out a process
5 and a device that includes a processor-readable medium (such as a storage device) having instructions for carrying out a process. Further, a processor-readable medium may store, in addition to or in lieu of instructions, data values produced by an implementation.

As will be evident to one of skill in the art, implementations may produce a variety of signals formatted to carry information that may be, for example, stored or transmitted. The
10 information may include, for example, instructions for performing a method, or data produced by one of the described implementations. For example, a signal may be formatted to carry as data the rules for writing or reading the syntax of a described embodiment, or to carry as data the actual syntax-values written by a described embodiment. Such a signal may be formatted, for example, as an electromagnetic wave (for example, using a radio frequency portion of spectrum) or as a
15 baseband signal. The formatting may include, for example, encoding a data stream and modulating a carrier with the encoded data stream. The information that the signal carries may be, for example, analog or digital information. The signal may be transmitted over a variety of different wired or wireless links, as is known. The signal may be stored on a processor-readable medium.

A number of implementations have been described. Nevertheless, it will be understood that
20 various modifications may be made. For example, elements of different implementations may be combined, supplemented, modified, or removed to produce other implementations. Additionally, one of ordinary skill will understand that other structures and processes may be substituted for those disclosed and the resulting implementations will perform at least substantially the same function(s), in at least substantially the same way(s), to achieve at least substantially the same
25 result(s) as the implementations disclosed. Accordingly, these and other implementations are contemplated by this application.

CLAIMS

1. A method (80) comprising:
 - obtaining a first atlas layout (82) of at least one patch for a first sequence of 3D scenes;
 - obtaining a second atlas layout (84) of at least one patch for a second sequence of 3D scenes,
5 said second sequence being the first sequence to which one 3D scene is appended;
 - on condition that the number of patches of the second atlas layout is greater than the number
of patches of the first atlas layout, generating atlases for the first sequence of 3D scenes
(87) according to the first atlas layout.
2. The method of claim 1, wherein obtaining steps are iterated while the number of patches of the
10 second atlas layout is lower than or equal to the number of patches of the first atlas layout, the
first sequence of 3D scenes being the second sequence of 3D scene of the preceding iteration;
and the method comprising:
 - storing the first atlas layout (82, 84) at each iteration; and
 - selecting a layout (86) among the stored layouts to generate (87) atlases for the first
15 sequence of 3D scenes when iterations end.
3. The method of claim 2, wherein iterations end when the second sequence comprises more than
a given number of 3D scenes.
4. The method of one of claims 2 to 3, wherein said selecting a layout is performed on the basis
of a rate-distortion optimization criterion.
- 20 5. The method of one of claims 1 to 4, wherein generated atlases are encoded (88) as one variable-
size intra-period in a video data stream.
6. The method of claim 5, wherein said group of pictures comprises at least one fixed-size group
of pictures comprising the number of atlases of the initial first sequence of 3D scenes.
7. A device (30) comprising a memory storing instructions to cause a processor to:
25 – obtain a first atlas layout of at least one patch for a first sequence of 3D scenes;

- obtain a second atlas layout of at least one patch for a second sequence of 3D scenes, said second sequence being the first sequence to which one 3D scene is appended;
- on condition that the number of patches of the second atlas layout is greater than the number of patches of the first atlas layout, generate atlases for the first sequence of 3D scenes according to the first atlas layout.

8. The device of claim 7, wherein said instructions are configured to cause the processor to iterate obtaining steps while the number of patches of the second atlas layout is lower than or equal to the number of patches of the first atlas layout, the first sequence of 3D scenes being the second sequence of 3D scene of the preceding iteration; and to:

- store the first atlas layout at each iteration in the memory; and
- select a layout among the stored layouts to generate atlases for the first sequence of 3D scenes when iterations end.

9. The device of claim 8, wherein iterations end when the second sequence comprises more than a given number of 3D scenes.

10. The device of one of claims 8 to 9, wherein said selecting a layout is performed on the basis of a rate-distortion optimization criterion.

11. The device of one of claims 7 to 10, wherein said instructions are configured to cause the processor to encode generated atlases as one variable-size intra-period in a video data stream.

12. The device of claim 11, wherein said group of pictures comprises at least one fixed-size group of pictures comprising the number of atlases of the initial first sequence of 3D scenes.

13. A video data stream (4) comprising data representative of pictures encoded as variable-size intra-periods, a picture being an atlas of at least one patch of a 3D scene.

14. The video data stream of claim 13, wherein a variable-size intra-period comprises at least one fixed-size group of pictures.

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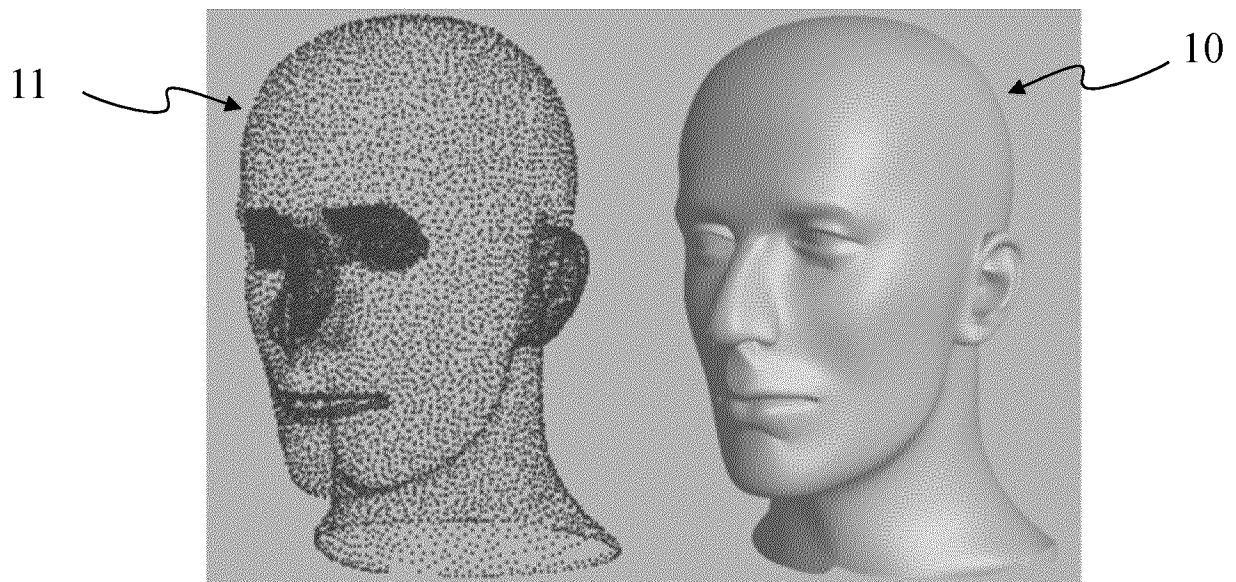


Figure 1

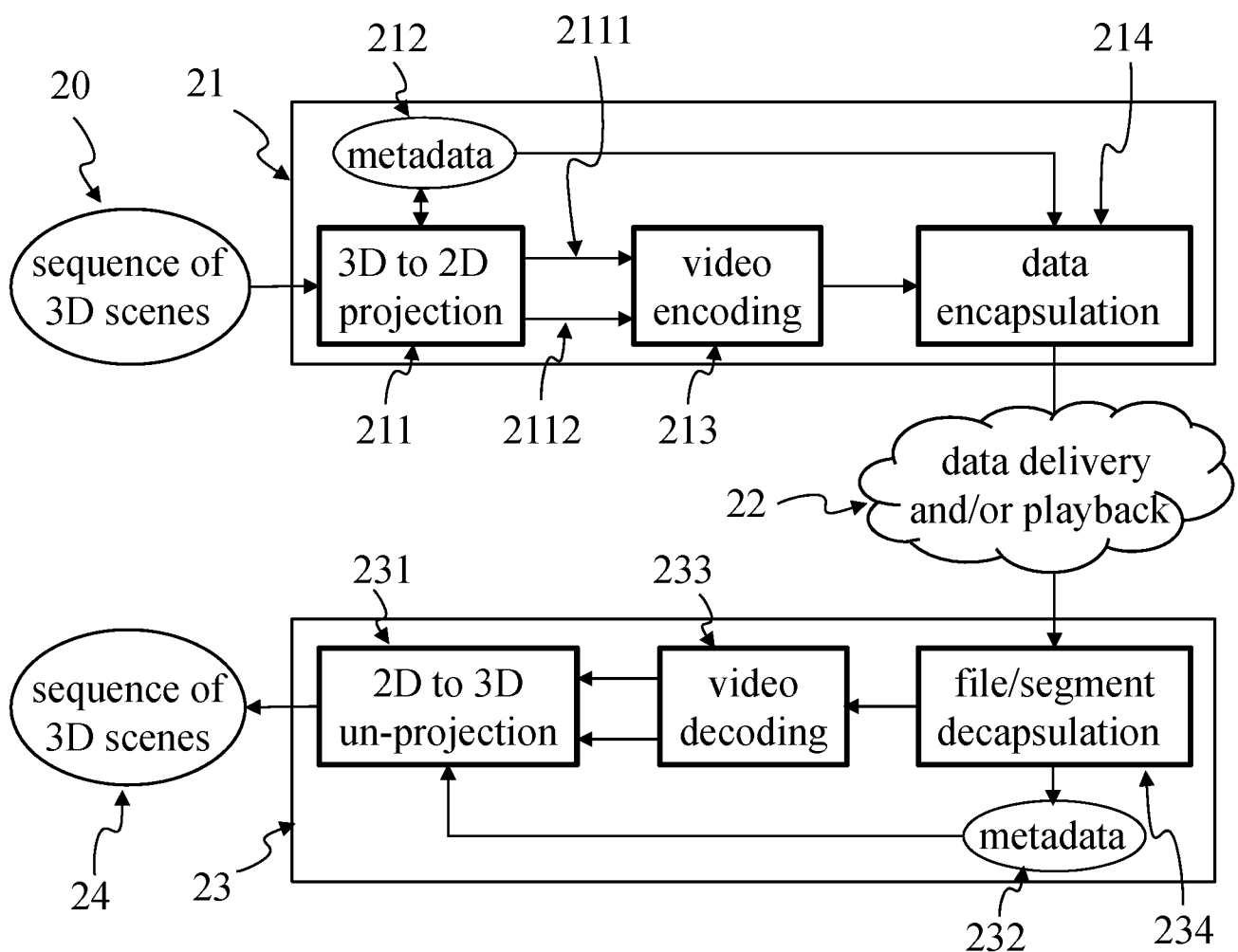


Figure 2

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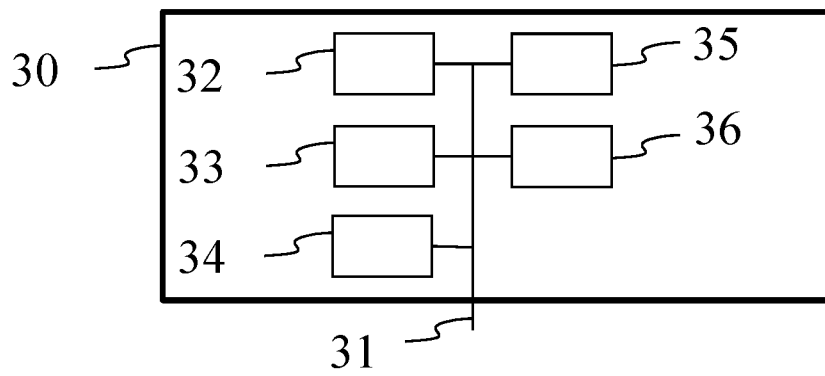


Figure 3

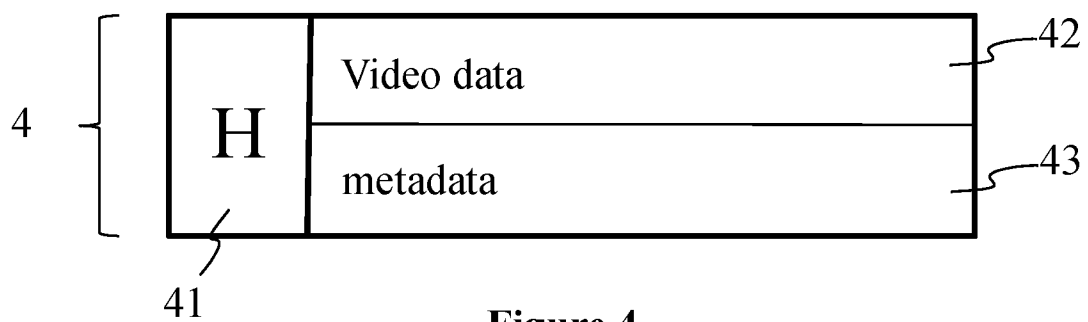


Figure 4

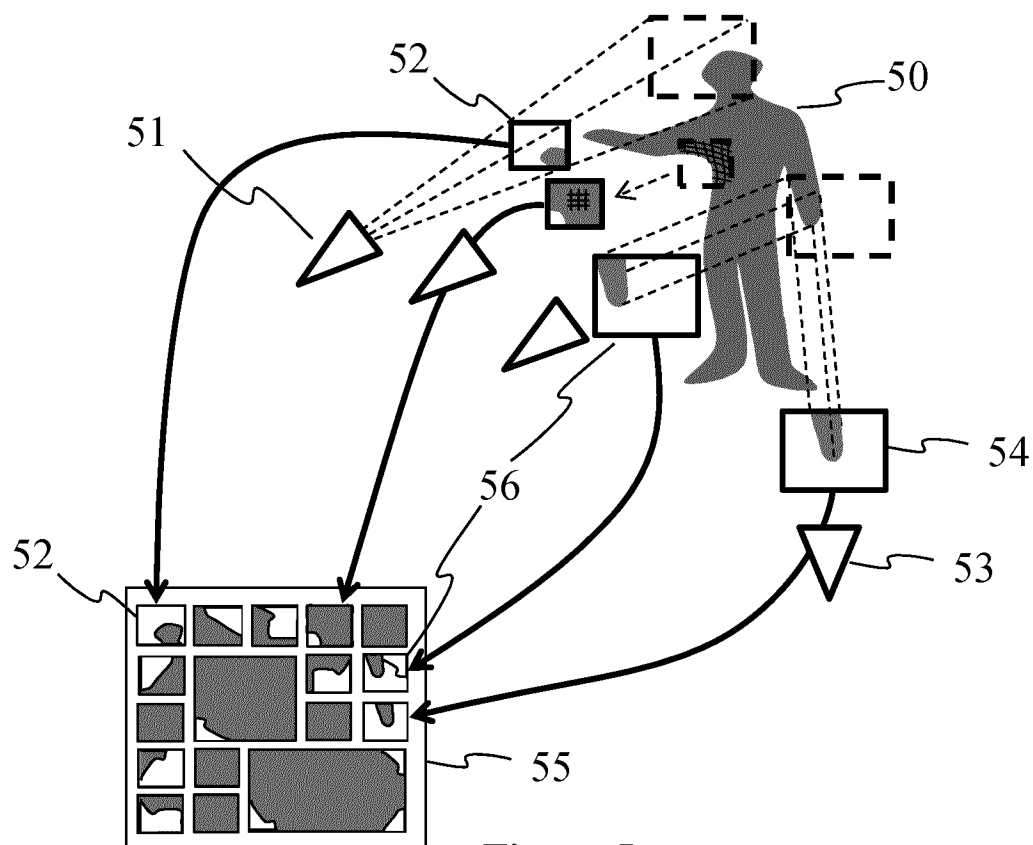


Figure 5

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Figure 6



Figure 7

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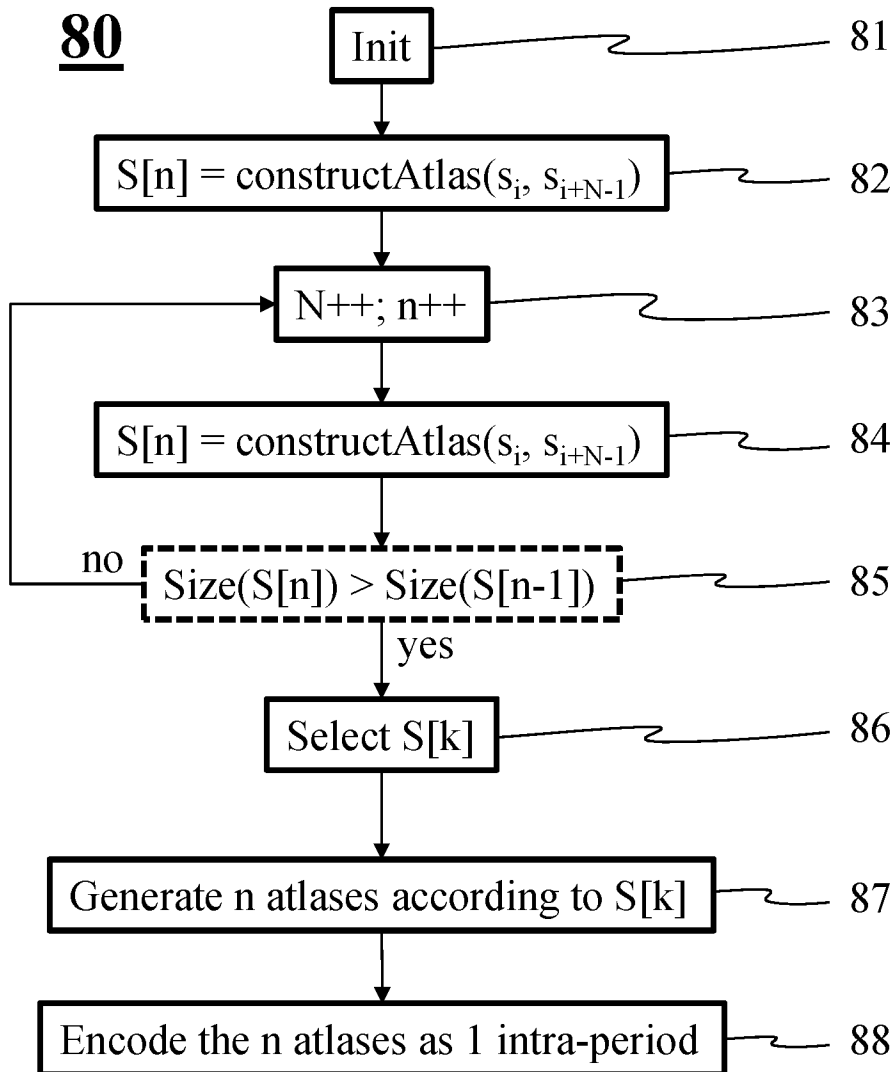


Figure 8

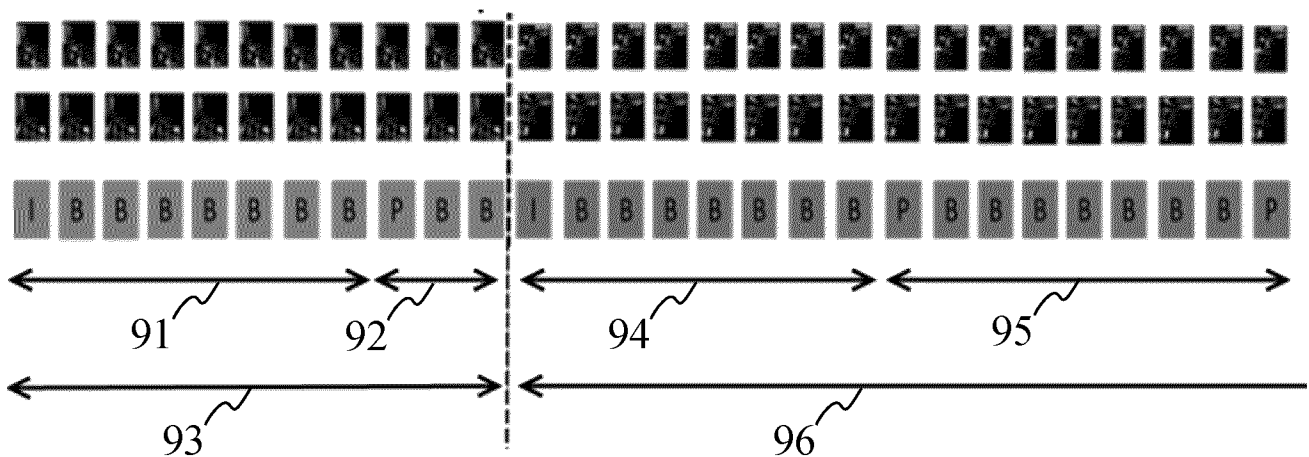


Figure 9

INTERNATIONAL SEARCH REPORT

International application No
PCT/EP2020/057700

A. CLASSIFICATION OF SUBJECT MATTER
INV. H04N19/597 G06T17/00
ADD.

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
H04N G06T

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EPO-Internal, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	EP 3 349 182 A1 (THOMSON LICENSING) 18 July 2018 (2018-07-18) paragraph [0025] - paragraph [0029] paragraph [0051] - paragraph [0058]; figure 5 paragraph [0060] - paragraph [0065]; figures 6,7 paragraph [0068] - paragraph [0075]; figure 12 paragraph [0077] - paragraph [0080]; figure 13 paragraph [0091] - paragraph [0096]; figure 11 ----- -/-	1-14



Further documents are listed in the continuation of Box C.



See patent family annex.

* Special categories of cited documents :

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"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

9 April 2020

Date of mailing of the international search report

20/04/2020

Name and mailing address of the ISA/

European Patent Office, P.B. 5818 Patentlaan 2
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Fax: (+31-70) 340-3016

Authorized officer

Moschetti, Fulvio

INTERNATIONAL SEARCH REPORT

International application No
PCT/EP2020/057700

C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	WO 2019/055389 A1 (INTERDIGITAL VC HOLDINGS INC [US]) 21 March 2019 (2019-03-21) page 1 - page 2 page 13, line 15 - page 15, line 23; figures 10,9,16 page 16, line 26 - page 17, line 16; figures 11,12 page 20, line 22 - page 21, line 30; figures 13,14 -----	1-14

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No

PCT/EP2020/057700

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
EP 3349182	A1	18-07-2018	CN 110383342 A 25-10-2019
			EP 3349182 A1 18-07-2018
			EP 3568834 A1 20-11-2019
			KR 20190105011 A 11-09-2019
			US 2019371051 A1 05-12-2019
			WO 2018130491 A1 19-07-2018

WO 2019055389	A1	21-03-2019	NONE
