



(51) International Patent Classification:

H04N 19/56 (2014.01) H04N 19/583 (2014.01)

(21) International Application Number:

PCT/US2018/049009

(22) International Filing Date:

31 August 2018 (31.08.2018)

(25) Filing Language:

English

(26) Publication Language:

English

(30) Priority Data:

17306130.0 01 September 2017 (01.09.2017) EP

(71) Applicant: **INTERDIGITAL VC HOLDINGS, INC.**  
[US/US]; Patent Operations, 200 Bellevue Parkway, Wilmington, Delaware 19809 (US).

(72) Inventors: **ROBERT, Antoine**; La Gommerais, F-35140 Mézières sur Couesnon (FR). **POIRIER, Tangi**; 64 Avenue de Cork, F-35200 Rennes (FR). **DANTEN, Simon**; 376 rue de la chapelle, F-80170 Rouvroy-en-santerre (FR).

(74) Agent: **KOLCZYNSKI, Ronald J.** et al.; 4 Research Way, Princeton, New Jersey 08540 (US).

(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DE, DJ, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN,

HR, HU, ID, IL, IN, IR, IS, JO, JP, KE, KG, KH, KN, KP, KR, KW, KZ, LA, LC, LK, LR, LS, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SA, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, ST, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, KM, ML, MR, NE, SN, TD, TG).

**Declarations under Rule 4.17:**

- as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii))
- as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii))

**Published:**

- with international search report (Art. 21(3))

(54) Title: REFINEMENT OF INTERNAL SUB-BLOCKS OF A CODING UNIT

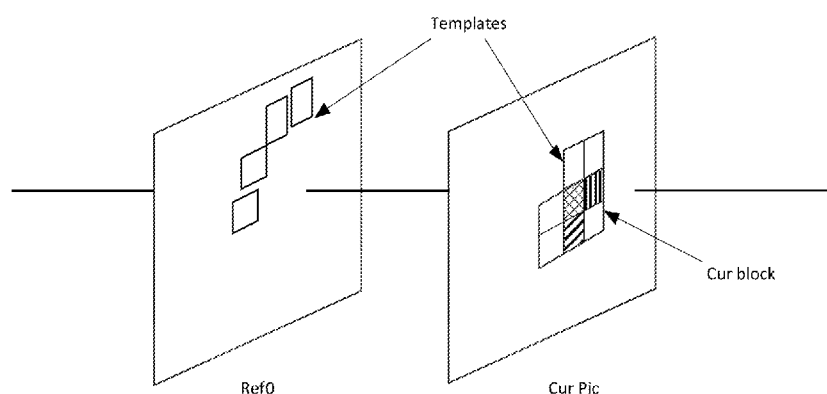


Figure 6

(57) Abstract: Motion information for an internal sub-block of a larger block can be derived for use in encoding or decoding the video block or a coding unit by using the motion information for sub-blocks on the left or top edge of the coding block. The left column of edge sub-blocks and the top row of sub-blocks has motion information, such as motion vectors, derived using such techniques as template matching. The motion vectors of these edge sub-blocks are used in deriving the motion vectors of internal sub-blocks, which leads to better prediction and improved coding efficiency. In another embodiment, other motion information for internal sub-blocks is derived from corresponding information of the edge sub-blocks.



## REFINEMENT OF INTERNAL SUB-BLOCKS OF A CODING UNIT

### FIELD OF THE INVENTION

The present principles relate to the field of video compression.

5

### BACKGROUND OF THE INVENTION

In the HEVC (High Efficiency Video Coding of International Telecommunication Union, ITU-T H.265) video compression standard, a picture is divided into so-called Coding Tree Units (CTU), whose size is typically 64x64, 128x128, or 256x256 pixels.

10

Each CTU is represented by a Coding Tree in the compressed domain. This is a quad-tree division of the CTU, where each leaf is called a Coding Unit (CU), as shown in Figure 1.

Each CTU is represented by a Coding Tree in the compressed domain. This is a quad-tree division of the CTU, where each leaf is a Coding Unit (CU), as shown in Figure

15

1.

Motion compensated temporal prediction is employed to exploit the redundancy that exists between successive pictures of a video. To do so, a motion vector is associated with each prediction unit (PU). Each CU is then given some Intra or Inter prediction parameters (Prediction Info) and is spatially partitioned into one or more Prediction Units (PUs), each PU being assigned some prediction information. The Intra or Inter coding mode is assigned on the CU level, as shown in Figure 2.

20

In HEVC, Coding Units are also divided into so-called transform units, in a recursive way, following a "transform tree". Thus, a transform tree is a quad-tree division of a coding unit, and transform units are the leaf of the transform tree. A transform unit encapsulates the square transform blocks of each picture component corresponding to a considered square spatial area. A transform block is a square block of samples in a single component, where the same transform is applied.

25

Exactly one Motion Vector is assigned to each PU in HEVC. This motion vector is used for motion compensated temporal prediction of the considered PU.

30

In the Joint Exploration Model (JEM) developed by the JVET (Joint Video Exploration Team) group, a CU is no longer divided into PU or TU, and some motion data is directly assigned to each CU. In this new codec design, a CU can be divided into sub-CU and a motion vector can be computed for each sub-CU.

Several new tools can further divide a CU into sub-blocks and derive one motion

vector for each of these sub-blocks. The FRUC and Affine tools use such sub-block motion derivation, and OBMC (Overlapped Block Motion Compensation) and BIO (Bi-directional Optical Flow) perform in quite the same way.

Some of these tools, or in some precise configurations, use neighboring encoded and reconstructed parts of the image. In that case, it is not possible for those tools to derive a motion vector for the internal sub-blocks, i.e. the sub-blocks not in the first row and/or column.

New emerging video compression tools include a Coding Tree Unit representation in the compressed domain to represent picture data in a more flexible way in the compressed domain. The advantage of this flexible representation of the coding tree is that it provides increased compression efficiency compared to the CU/PU/TU arrangement of the HEVC standard.

With these new video compression tools, there is no longer partitioning of a CU into predictions units or transform unit. In other words, each Coding Unit is systematically made of a single prediction unit (previously 2Nx2N prediction unit partition type) and a single transform unit (no division into a transform tree).

## SUMMARY OF THE INVENTION

These and other drawbacks and disadvantages of the prior art are addressed by at least one of the described embodiments, which are directed to a method and apparatus for deriving the motion vectors of internal sub-blocks of a coding unit coded with a tool using sub-block level and reconstructed neighboring information.

In at least one embodiment, it is proposed to improve motion derivation for internal sub-blocks by taking advantage of previously refined sub-blocks.

According to at least one general embodiment described herein, there is provided a method for coding a block of video data. The method comprises steps for deriving information for a block of video data from neighboring blocks previously encoded; refining the information for sub-blocks along a left or top edge of the block of video data by using the derived information and neighboring sub-blocks previously encoded; refining the information for internal sub-blocks of the block of video data by using the refined information from sub-blocks along a left or top edge of the block of video data; and, encoding the block of video data using said refined sub-blocks.

According to another aspect described herein, there is provided a method for decoding a block of data. The method comprises deriving information for a block of

video data from neighboring blocks previously encoded; refining the information for sub-blocks along a left or top edge of the block of video data by using the information and neighboring sub-blocks previously encoded; refining the information for internal sub-blocks of the block of video data by using the refined information from sub-blocks along a left or top edge of the block of video data; and, decoding the block of video data using said refined sub-blocks.

According to another aspect described herein, there is provided apparatus for coding or decoding a block of video data, comprising: a memory, and a processor, configured to perform: deriving information for a block of video data from neighboring blocks previously encoded; refining the information for sub-blocks along a left or top edge of the block of video data by using the information and neighboring sub-blocks previously encoded; refining the information for internal sub-blocks of the block of video data by using the refined information from sub-blocks along a left or top edge of the block of video data; and, encoding or decoding the block of video data. The apparatus can execute any of the aforementioned method embodiments with variations.

According to another aspect described herein, there is provided a nontransitory computer readable storage medium containing data content generated according to the method of any one of the aforementioned method embodiments, or by the apparatus of any one of the aforementioned apparatus embodiments for playback using a processor.

According to another aspect described herein, there is provided a signal comprising video data generated according to the method of any one of the aforementioned method embodiments for coding a block of video data, or by the apparatus of any one of the aforementioned apparatus embodiments for coding a block of video data, for playback using a processor.

According to another aspect described herein, there is provided a computer program product comprising instructions which, when the program is executed by a computer, cause the computer to carry out the method of any one of the aforementioned method embodiments.

These and other aspects, features and advantages of the present principles will become apparent from the following detailed description of exemplary embodiments, which is to be read in connection with the accompanying drawings.

## BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 shows an example of a coding tree unit and coding tree concepts to

represent a compressed HEVC picture.

Figure 2 shows an example of division of a coding tree unit into coding units, prediction units, and transform units.

Figure 3 shows one example of a generic video compression scheme to which the present embodiments can be applied.

Figure 4 shows one example of a generic video decompression scheme to which the present embodiments can be applied.

Figure 5 shows an example prior art FRUC Template matching process of an internal coding unit.

Figure 6 shows another example prior art FRUC Template matching process of sub-blocks of an internal coding unit.

Figure 7 shows an example OBMC propagation over the second row and column of a large coding unit.

Figure 8 shows an example of improved rows and columns of internal sub-blocks using average of refined neighbors (dashed) or whole refined coding unit (blank) using the present embodiments.

Figure 9 shows examples of two particular cases on the a) left or b) top border of the frame.

Figure 10 shows one embodiment of an encoding method using the aspects described.

Figure 11 shows one embodiment of a decoding method using the aspects described.

Figure 12 shows one embodiment of an apparatus for encoding or decoding using the aspects described.

## DETAILED DESCRIPTION

In the HEVC video compression standard, a picture is divided into so-called Coding Tree Units (CTU), with typical sizes of 64x64, 128x128, or 256x256 pixels.

Each CTU is represented by a Coding Tree in the compressed domain. This is a quad-tree division of the CTU, where each leaf is called a Coding Unit (CU), see Figure 1.

Each CU is then given some Intra or Inter prediction parameters (Prediction Info). To do so, it is spatially partitioned into one or more Prediction Units (PUs), each PU being assigned some prediction information. The Intra or Inter coding mode is assigned on the

CU level, see Figure 2.

According to the HEVC standard, Coding Units are also divided into so-called transform units, in a recursive way, following a “transform tree”. Thus, a transform tree is a quad-tree division of a coding unit, and transform units are the leaf of the transform tree.

5 A transform unit encapsulates the square transform blocks of each picture component corresponding to a considered square spatial area. A transform block is a square block of samples in a single component, where the same transform is applied.

New emerging video compression tools include a Coding Tree Unit representation in the compressed domain to represent picture data in a more flexible way in the compressed domain. The advantage of this flexible representation of the coding tree is  
10 that it provides increased compression efficiency compared to the CU/PU/TU arrangement of the HEVC standard.

The problem solved by the described embodiments is how to derive motion vectors for internal sub-blocks, to increase the overall compression performance of the  
15 considered video codec.

In the state of the art approach, Coding Units coded with a tool using sub-block motion derivation from neighboring reconstructed part of the image cannot address internal sub-blocks. The basic idea proposed herein is to derive the Motion Vector of an internal sub-block in an encoder/decoder.

20 The Frame Rate Up Conversion (FRUC) tool of the prior art derives motion vectors with some template matching technique. Such techniques allow extraction of the best motion vector within a set of candidates, and then refining it towards the best template matching. FRUC firstly performs at the entire CU level. The obtained refined motion vector is then used as a seed for the sub-block level where it repeats the process for each sub-  
25 block independently.

The first step consists in deriving the refined motion vector for the whole CU. FRUC collects motion vector candidates picked from some spatial and temporal neighbors. For each of those candidates, the corresponding template(s) as the spatial top and/or left neighboring image block(s) are picked within the associated reference frame as shown  
30 in Figure 5. The associated template matching cost is then the SAD (Sum of Absolute Differences) between the candidate template(s) and the current template(s). The candidate reaching the minimum template matching cost is defined as the best representative one. Then, this best candidate is refined towards the minimum template matching cost by adding small deltas to this motion vector (using diamond, square, cross,

patterns, for example, or some other pattern).

This FRUC process is then repeated at a sub-block level (of size 4x4 or 8x8 depending on the size of the entire CU). The refined motion vector of the whole CU is used as a candidate for the sub-blocks as well as some other potential candidates. For each sub-block, when a current template (as the spatial top and/or left neighboring image block(s)) is available, the best candidate is extracted according to the template matching cost, and then refined towards the minimum template matching cost. When no current template is available, the sub-block motion vector is affected to the whole CU refined one. For CUs which are not on the border of a frame, the first top-left sub-block can use top and left templates (crossed sub-block in Figure 6) while the remaining ones on the first row (respectively column) can only use top (resp. left) template (vertical (resp. horizontal) stripped sub-block in Figure 6). The other internal sub-blocks (white sub-block in Figure 6) cannot benefit of the template refinement and directly use the refined motion vector of the entire CU.

In quite the same way as FRUC, the improvement of the AMVP motion compensation (proposed in "Motion Compensation at a finer precision than Motion Vector Differential") allows refining at a sub-block level the incoming integer- or 1/4-pel AMVP motion vector to 1/16-pel accuracy using neighboring information.

The first row and column of sub-blocks can use spatial and/or temporal neighboring information directly, or with a FRUC process, to increase the precision of their motion vectors. For all other internal sub-blocks, such neighbors are not available, because they have not already been encoded and decoded. It is thus not possible to use the same process as for the border sub-blocks.

A main limitation of the prior art sub-block processes is that all internal sub-block information cannot be improved as with the other (i.e. border) sub-blocks, they can only be conformed to the incoming or refined motion vector of the whole CU.

This section describes the proposed method to derive information for internal sub-blocks of a CU.

The described embodiments aim at improving the motion derivation for internal sub-blocks of a CU by taking advantage of the already derived motion information.

In the FRUC tool, the first row and column of sub-blocks can be derived with the template matching as described in the state-of-the-art. The remaining internal sub-blocks are not derived as the other, they just use the refined value of the whole CU.

In a first embodiment, it is possible to improve the motion derivation for those sub-

blocks by taking advantage of first refined ones.

To do so, it is possible to use the neighboring refined motion vectors, spatial and/or temporal.

Only one neighbor can be considered: (i) the same as the best candidate, (ii) a predefined neighbor (for example, always the spatial left neighbor), (iii) the first encountered one having a different value than the incoming refined MV, (iv) the one having the closest MV to the refined incoming one, or some other candidate. In that case, the internal sub-block MV takes the value of the selected neighboring motion vector.

In a variant, it is also possible to consider several neighbors: (i) the set (or a subset) of (classical) spatial neighbors, (ii) the first 2, 3, ... encountered ones having a different value than the incoming refined motion vector, (iii) the 2, 3, ... having the closest motion vector to the incoming refined one, or some other. In this case, the internal sub-block motion vector is filled with the average, the median, or any other combination of the considered neighboring motion vectors.

In another variant, it is also possible to consider the incoming refined motion vector as part of a combination with one or several neighboring motion vectors.

In another variant, it is also possible to consider a more complex motion model involving all the sub-block refined motion vectors of the first row and column and the one of the whole CU. Such a model could be an affine model, a parametric one, some model based on another.

In a variant, it is also possible to use different motion derivation methods for different sub-blocks. For example, the first row and column of internal sub-blocks (second ones of the CU) can use several neighboring motion vectors and the whole CU refined MV, less neighboring MVs and the whole CU refined MV for the second column and row, and finally using the closest neighboring motion vector to the whole CU refined motion vector for all following sub-blocks.

Such motion derivation cannot only be applied to FRUC, but can also be applied on AMVP motion compensation.

The first embodiment above is dedicated to motion vector (as in the case of FRUC), but in a second embodiment, it can be applied on another kind of information used by other sub-block tool.

For example, OBMC performs like a motion smoothing across the CU boundaries. It just affects the first row and column of sub-blocks, and nothing is done on the internal sub-blocks. It can be useful to propagate the smoothing even more inside the CU, in



particular for very large CUs, in at least one row and column of internal sub-blocks as presented in Figure 7.

In a preferred embodiment, the same process is used to improve motion derivation for internal sub-blocks of the FRUC tool and the enhanced AMVP motion compensation.

5       The two first rows and columns of internal sub-blocks (second and third ones of the CU) are derived using the average of up to 4 spatially neighboring MVs (from left, top, top-left and top-right neighbors) and of the refined motion vector of the entire CU. And, the remaining sub-blocks use the refined motion vector of the whole CU as shown on Figure 8.

10       Figure 9 describes the particular cases where FRUC is not fully applicable, on left and top borders of the frame. On the left border (resp. top), FRUC can only be applied on the first row (resp. column) of sub-blocks, so the second and third rows (resp. columns) use the average of neighboring and whole CU refined motion vectors, and the remaining rows (resp. columns) use the whole CU refined motion vector.

15       Figure 10 shows one embodiment of a method 1000 under the aspects described. The method commences at Start block 1001 and control proceeds to block 1010 for deriving information for a block of video data from neighboring blocks previously encoded. Control proceeds from block 1010 to block 1020 for refining the information for sub-blocks along a left or top edge of the block of video data by using the derived information and  
20       neighboring sub-blocks previously encoded. Control then proceeds from block 1020 to block 1030 for refining the information for internal sub-blocks of the block of video data by using the refined information from sub-blocks along a left or top edge of the block of video data. Control then proceeds from block 1030 to block 1040 for encoding the block of video data using said refined sub-blocks.

25       Figure 11 shows one embodiment of a method 1100 under the aspects described. The method commences at Start block 1101 and control proceeds to block 1110 for deriving information for a block of video data from neighboring blocks previously encoded. Control proceeds from block 1110 to block 1120 for refining the information for sub-blocks along a left or top edge of the block of video data by using the derived information and  
30       neighboring sub-blocks previously encoded. Control then proceeds from block 1120 to block 1130 for refining the information for internal sub-blocks of the block of video data by using the refined information from sub-blocks along a left or top edge of the block of video data. Control then proceeds from block 1130 to block 1140 for decoding the block of video data using said refined sub-blocks.

Figure 12 shows one embodiment of an apparatus 1200 for coding or decoding a block of video data. The apparatus comprises Processor 1210 which has input and output ports and is in signal connectivity with Memory 1220, also having input and output ports. The apparatus can execute any of the aforementioned method embodiments, or variations.

The functions of the various elements shown in the figures can be provided using dedicated hardware as well as hardware capable of executing software in association with appropriate software. When provided by a processor, the functions can be provided by a single dedicated processor, by a single shared processor, or by a plurality of individual processors, some of which can be shared. Moreover, explicit use of the term “processor” or “controller” should not be construed to refer exclusively to hardware capable of executing software, and can implicitly include, without limitation, digital signal processor (“DSP”) hardware, read-only memory (“ROM”) for storing software, random access memory (“RAM”), and non-volatile storage.

Other hardware, conventional and/or custom, can also be included. Similarly, any switches shown in the figures are conceptual only. Their function can be carried out through the operation of program logic, through dedicated logic, through the interaction of program control and dedicated logic, or even manually, the particular technique being selectable by the implementer as more specifically understood from the context.

The present description illustrates the present ideas. It will thus be appreciated that those skilled in the art will be able to devise various arrangements that, although not explicitly described or shown herein, embody the present ideas and are included within its spirit and scope.

All examples and conditional language recited herein are intended for pedagogical purposes to aid the reader in understanding the present principles and the concepts contributed by the inventor(s) to furthering the art, and are to be construed as being without limitation to such specifically recited examples and conditions.

Moreover, all statements herein reciting principles, aspects, and embodiments of the present principles, as well as specific examples thereof, are intended to encompass both structural and functional equivalents thereof. Additionally, it is intended that such equivalents include both currently known equivalents as well as equivalents developed in the future, i.e., any elements developed that perform the same function, regardless of structure.

Thus, for example, it will be appreciated by those skilled in the art that the block

diagrams presented herein represent conceptual views of illustrative circuitry embodying the present principles. Similarly, it will be appreciated that any flow charts, flow diagrams, state transition diagrams, pseudocode, and the like represent various processes which can be substantially represented in computer readable media and so executed by a computer or processor, whether or not such computer or processor is explicitly shown.

In the claims herein, any element expressed as a means for performing a specified function is intended to encompass any way of performing that function including, for example, a) a combination of circuit elements that performs that function or b) software in any form, including, therefore, firmware, microcode or the like, combined with appropriate circuitry for executing that software to perform the function. The present principles as defined by such claims reside in the fact that the functionalities provided by the various recited means are combined and brought together in the manner which the claims call for. It is thus regarded that any means that can provide those functionalities are equivalent to those shown herein.

Reference in the specification to “one embodiment” or “an embodiment” of the present principles, as well as other variations thereof, means that a particular feature, structure, characteristic, and so forth described in connection with the embodiment is included in at least one embodiment of the present principles. Thus, the appearances of the phrase “in one embodiment” or “in an embodiment”, as well any other variations, appearing in various places throughout the specification are not necessarily all referring to the same embodiment.

In conclusion, an improved method of refinement of internal sub-blocks for encoding or decoding is provided. In the refinement, internal sub-blocks are refined using sub-block portions that have already been refined. This refinement can be motion information derived from the motion information of already refined sub-block portions, for example. Other embodiments provide other derivations of internal sub-blocks using other information. In addition, an embodiment uses other information to derive internal sub-blocks based on outer sub-block information.

CLAIMS

1. A method, comprising:

deriving information for a block of video data from neighboring blocks  
5 previously encoded;

refining said information for sub-blocks along a left or top edge of said block  
of video data by using said derived information and neighboring sub-blocks previously  
encoded;

refining said information for internal sub-blocks of said block of video data  
10 by using said refined information from sub-blocks along a left or top edge of said block of  
video data; and,

encoding said block of video data using said refined sub-blocks.

15 2. A method, comprising:

deriving information for a block of video data from neighboring blocks  
previously encoded;

refining said information for sub-blocks along a left or top edge of said block  
of video data by using said information and neighboring sub-blocks previously encoded;

20 refining said information for internal sub-blocks of said block of video data  
by using said refined information from sub-blocks along a left or top edge of said block of  
video data; and,

decoding said block of video data using said refined sub-blocks.

25 3. An apparatus for coding a block of video data, comprising:

a memory, and

a processor, configured to:

deriving information for a block of video data from neighboring blocks  
previously encoded;

30 refining said information for sub-blocks along a left or top edge of said block  
of video data by using said information and neighboring sub-blocks previously encoded;

refining said information for internal sub-blocks of said block of video data  
by using said refined information from sub-blocks along a left or top edge of said block of  
video data; and,

encoding said block of video data using said refined sub-blocks.

4. An apparatus for decoding a block of video data, comprising:  
a memory, and  
5 a processor, configured to:  
deriving information for a block of video data from neighboring blocks  
previously encoded;  
refining said information for sub-blocks along a left or top edge of said block  
of video data by using said information and neighboring sub-blocks previously encoded;  
10 refining said information for internal sub-blocks of said block of video data  
by using said refined information from sub-blocks along a left or top edge of said block of  
video data; and,  
decoding said block of video data using said refined sub-blocks.

15 5. The method of any of Claims 1 or 2, or the apparatus of any of claims 3 or  
4, wherein said deriving information uses template matching prediction.

6. The method or the apparatus of claim 5, wherein said template matching  
prediction is performed temporally.

20 7. The method or the apparatus of claim 5, wherein said template matching  
prediction is performed spatially.

8. The method of any of Claims 1 or 2, or the apparatus of any of claims 3 or  
25 4, wherein N number of spatial neighboring sub-blocks are used to refine information for  
a current sub-block.

9. The method of any of Claims 1 or 2, or the apparatus of any of claims 3 or  
4, wherein a motion vector is derived for a sub-block using some combination of  
30 neighboring spatial or temporal sub-blocks.

10. The method of any of Claims 1 or 2, or the apparatus of any of claims 3 or  
4, wherein a motion vector is derived for a sub-block using a motion model that uses at  
least one refined neighboring motion vector from a neighboring sub-block as well as a

motion vector for a block.

11. The method of any of Claims 1 or 2, or the apparatus of any of claims 3 or 4, wherein different motion derivation methods are used for different sub-blocks of the current block.

12. The method of any of Claims 1 or 2, or the apparatus of any of claims 3 or 4, wherein a second and third row and second and third column of a coding unit are derived using an average of up to four spatial neighboring motion vectors and of a refined motion vector of the coding unit.

13. A non-transitory computer readable medium containing data content generated according to the method of any one of claims 1 and 5 to 12, or by the apparatus of any one of claims 3 and 5 to 12, for playback using a processor.

14. A signal comprising video data generated according to the method of any one of claims 1 and 5 to 12, or by the apparatus of any one of claims 3 and 5 to 12, for playback using a processor.

15. A computer program product comprising instructions which, when the program is executed by a computer, cause the computer to carry out the method of any one of claims 2 and 5 to 12.

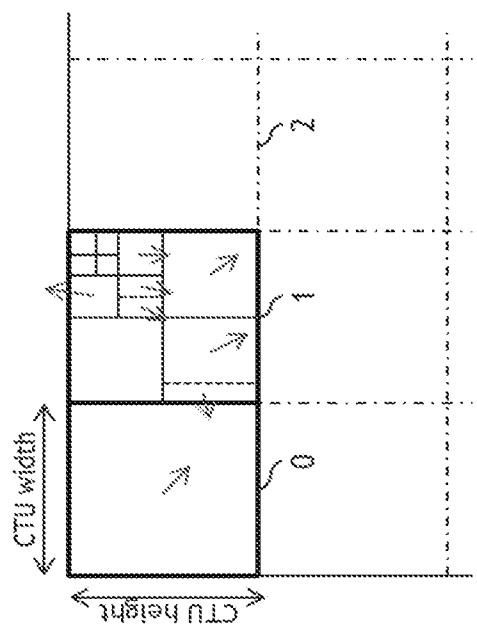


Figure 1

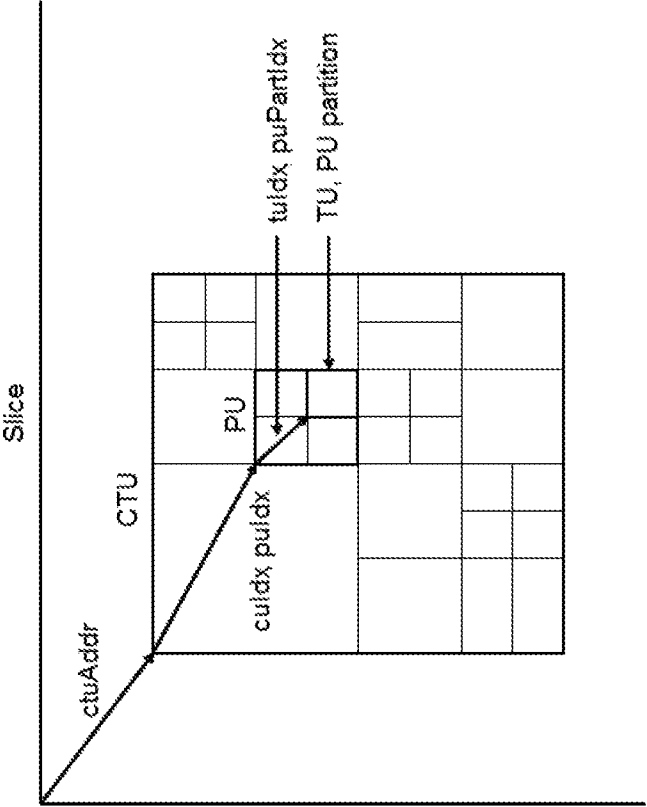


Figure 2



3/12

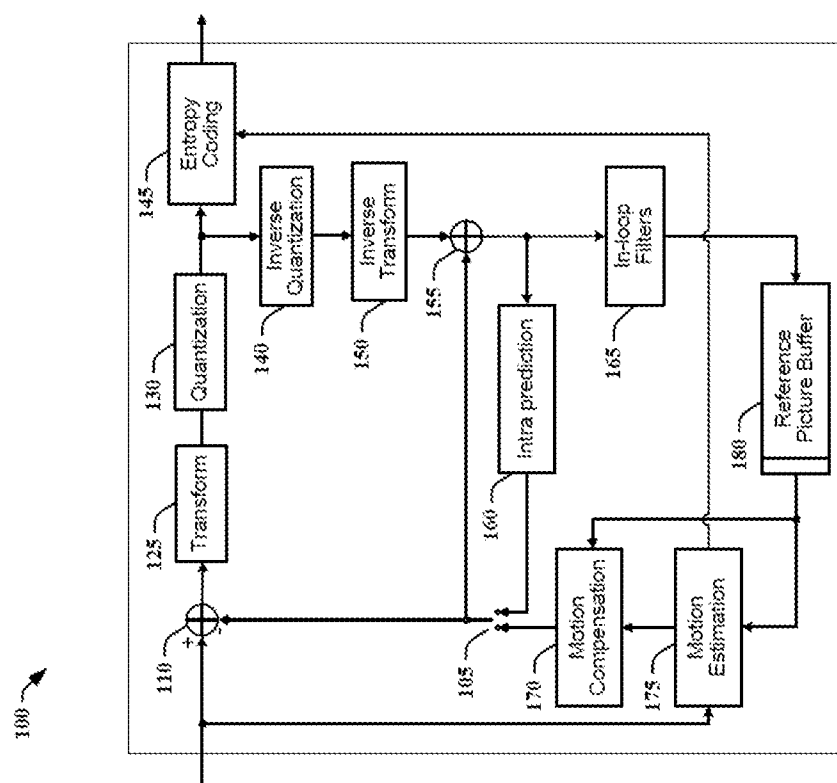


Figure 3

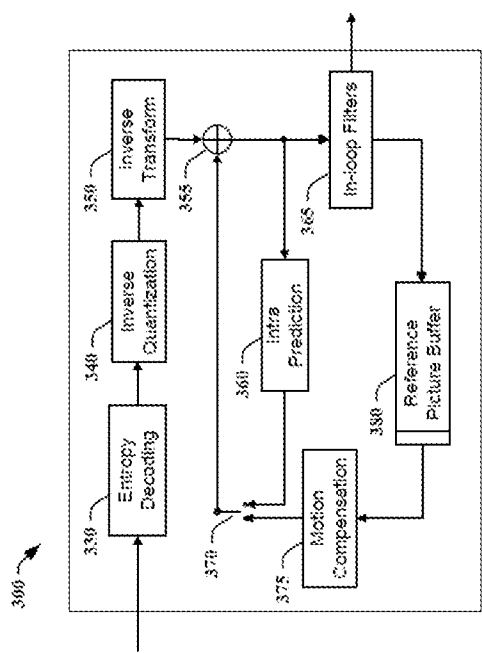


Figure 4

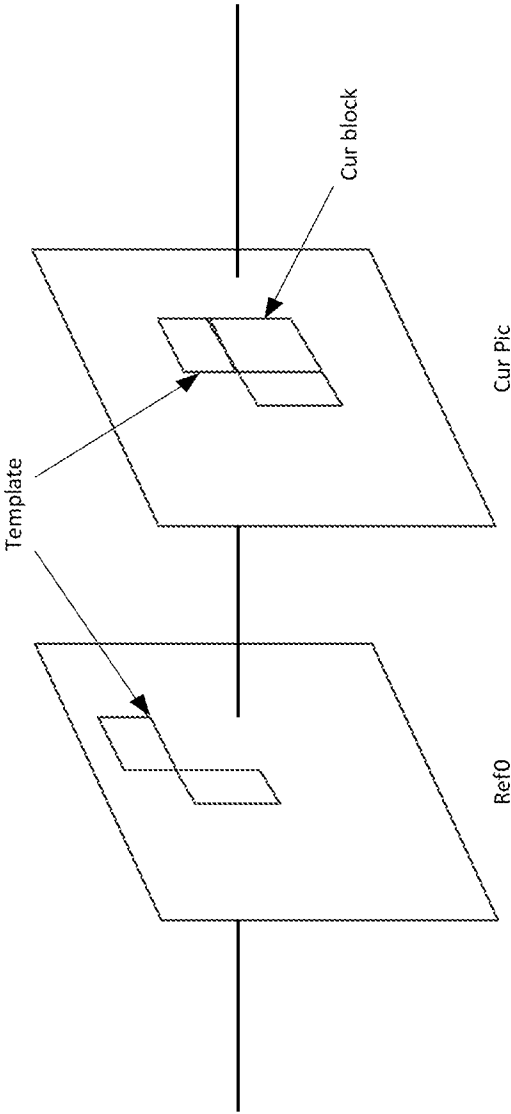


Figure 5

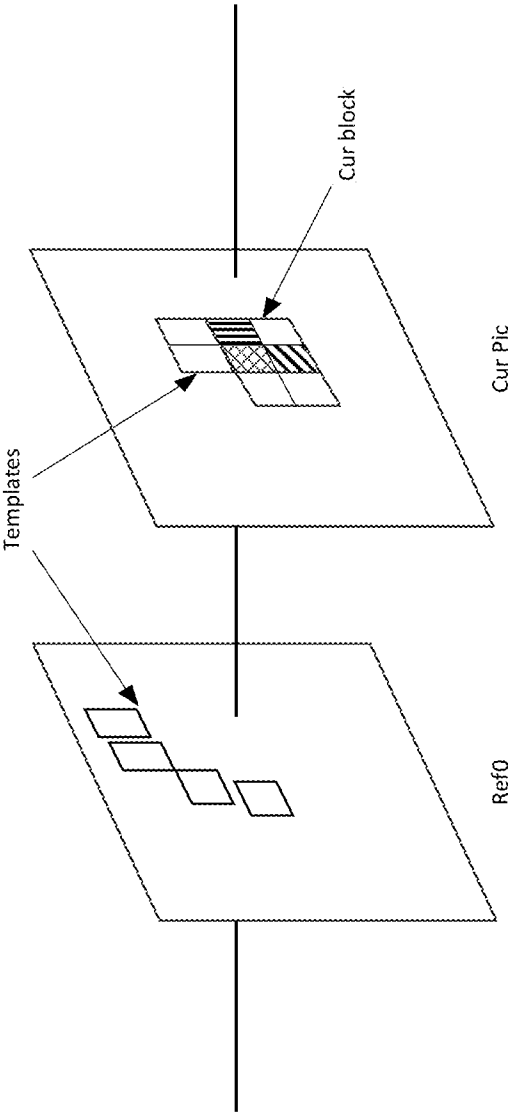


Figure 6

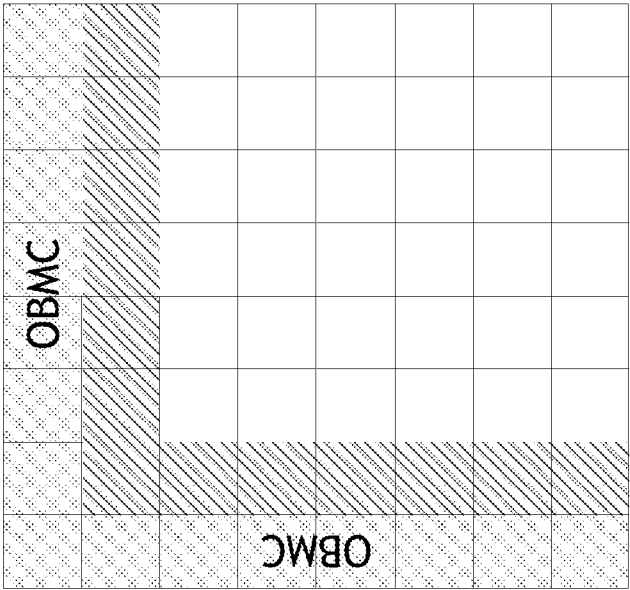


Figure 7

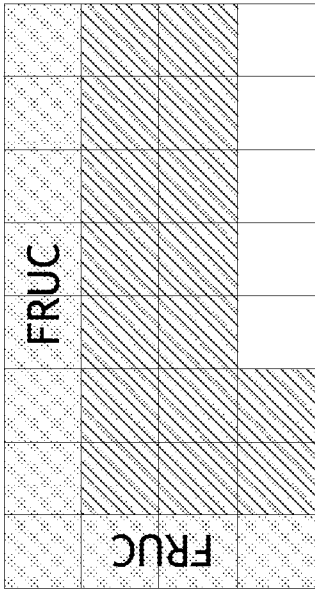


Figure 8

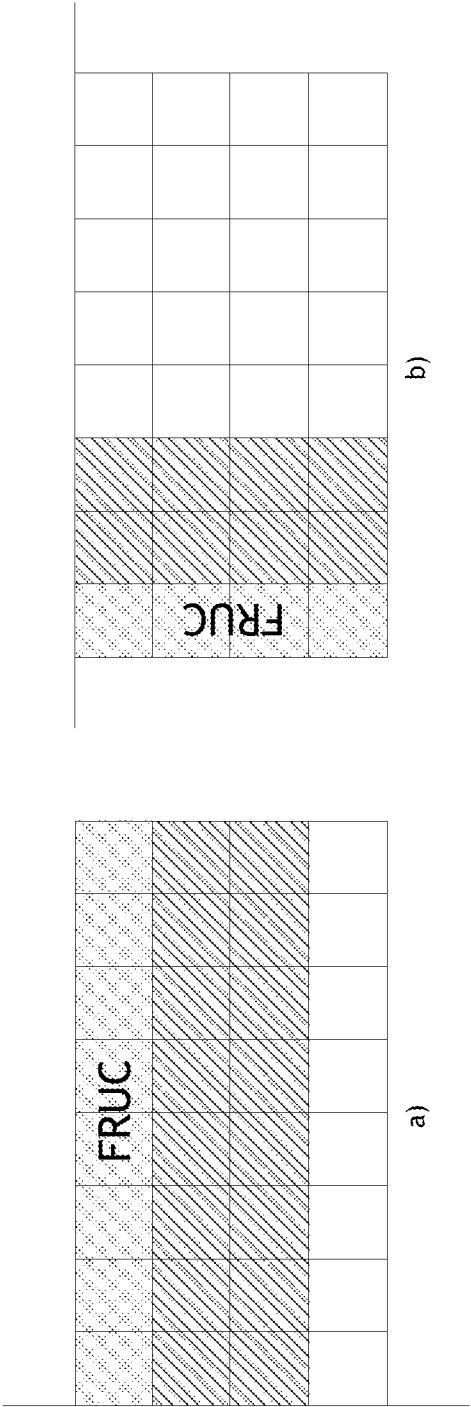


Figure 9

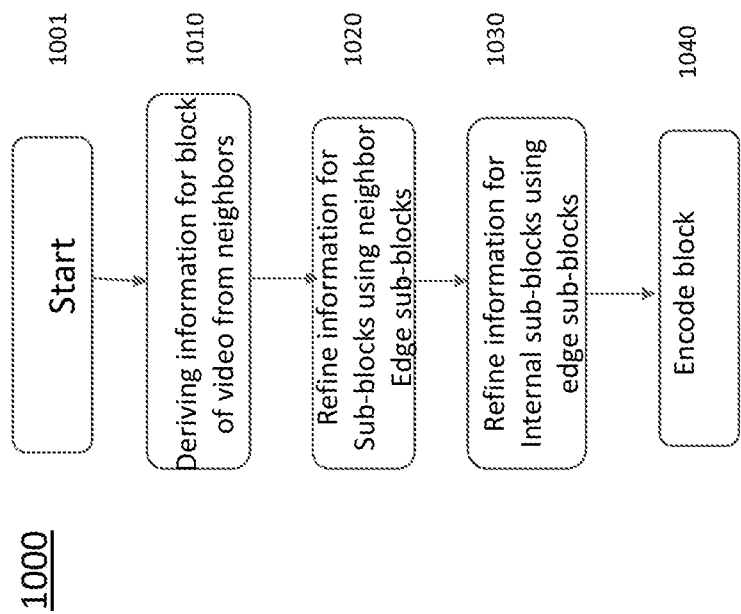


Figure 10



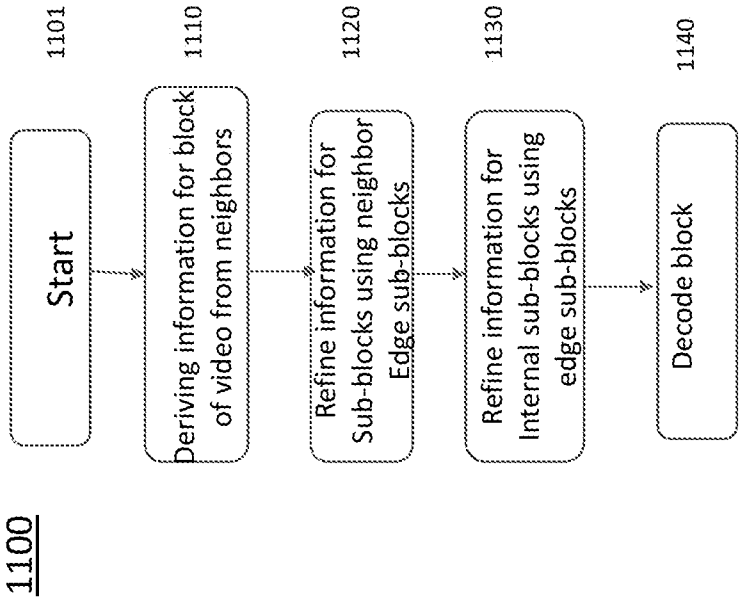


Figure 11

1200

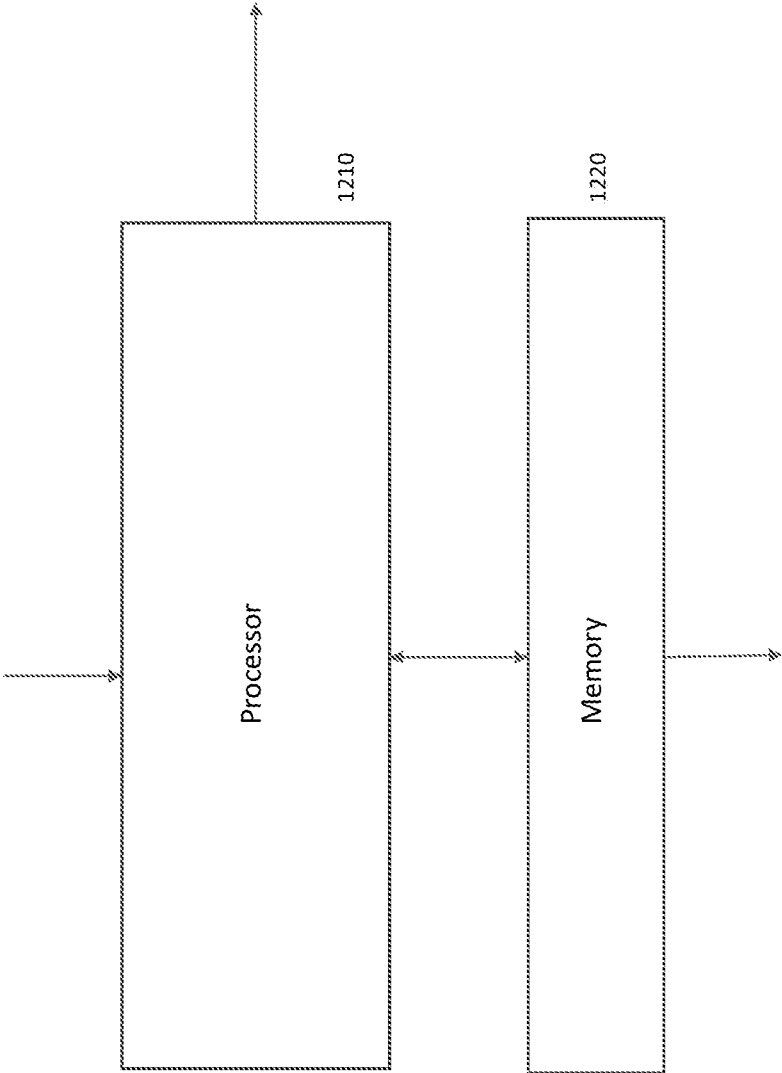


Figure 12

## INTERNATIONAL SEARCH REPORT

International application No

PCT/US2018/049009

## A. CLASSIFICATION OF SUBJECT MATTER

INV. H04N19/56 H04N19/583  
ADD.

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)  
H04N

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EPO-Internal, WPI Data

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	<p>CHEN J ET AL: "Algorithm description of Joint Exploration Test Model 6 (JEM6)", 6. JVET MEETING; 31-3-2017 - 7-4-2017; HOBART; (THE JOINT VIDEO EXPLORATION TEAM OF ISO/IEC JTC1/SC29/WG11 AND ITU-T SG.16); URL: HTTP://PHENIX.INT-EVRY.FR/JVET/, no. JVET-F1001, 31 May 2017 (2017-05-31), XP030150793, page 15, paragraph 2.3 - page 25, paragraph 2.3.8</p> <p style="text-align: center;">----- -/--</p>	1-8,11, 13-15



Further documents are listed in the continuation of Box C.



See patent family annex.

## \* Special categories of cited documents :

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier application or patent but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

4 December 2018

Date of mailing of the international search report

14/12/2018

Name and mailing address of the ISA/

European Patent Office, P.B. 5818 Patentlaan 2  
NL - 2280 HV Rijswijk  
Tel. (+31-70) 340-2040,  
Fax: (+31-70) 340-3016

Authorized officer

Le Guen, Benjamin

## INTERNATIONAL SEARCH REPORT

International application No  
PCT/US2018/049009

C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	<p>US 2016/286230 A1 (LI XIANG [US] ET AL) 29 September 2016 (2016-09-29)</p> <p>paragraph [0016] - paragraph [0020]  paragraph [0044] - paragraph [0046]  paragraph [0091]  paragraph [0194] - paragraph [0196]  paragraph [0214] - paragraph [0226]</p> <p>-----</p>	<p>1-4, 8-10, 12-15</p>
A	<p>WO 2016/201094 A1 (QUALCOMM INC [US]) 15 December 2016 (2016-12-15)</p> <p>paragraph [0006]  paragraph [0093] - paragraph [0134];  figures 11A-11B  paragraph [0189] - paragraph [0208];  figures 10-13</p> <p>-----</p>	<p>1-15</p>

# INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No

PCT/US2018/049009

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 2016286230	A1	29-09-2016	
		AU 2016243518 A1	31-08-2017
		AU 2016243520 A1	31-08-2017
		AU 2016243521 A1	31-08-2017
		BR 112017020627 A2	03-07-2018
		BR 112017020632 A2	26-06-2018
		BR 112017020635 A2	26-06-2018
		CA 2976818 A1	06-10-2016
		CA 2976827 A1	06-10-2016
		CA 2976828 A1	06-10-2016
		CL 2017002383 A1	15-06-2018
		CL 2017002384 A1	15-06-2018
		CL 2017002423 A1	13-07-2018
		CN 107409225 A	28-11-2017
		CN 107431820 A	01-12-2017
		CN 107534766 A	02-01-2018
		EP 3275186 A1	31-01-2018
		EP 3275187 A1	31-01-2018
		EP 3275188 A1	31-01-2018
		JP 2018512810 A	17-05-2018
		JP 2018513611 A	24-05-2018
		JP 2018513612 A	24-05-2018
		KR 20170131446 A	29-11-2017
		KR 20170131447 A	29-11-2017
		KR 20170131448 A	29-11-2017
		PH 12017501440 A1	15-01-2018
		PH 12017501470 A1	15-01-2018
		PH 12017501471 A1	29-01-2018
		SG 11201706534T A	30-10-2017
		SG 11201706537W A	30-10-2017
		SG 11201706539P A	30-10-2017
		TW 201639368 A	01-11-2016
		TW 201639369 A	01-11-2016
		TW 201639370 A	01-11-2016
		US 2016286229 A1	29-09-2016
		US 2016286230 A1	29-09-2016
		US 2016286232 A1	29-09-2016
		WO 2016160605 A1	06-10-2016
		WO 2016160608 A1	06-10-2016
		WO 2016160609 A1	06-10-2016
-----			
WO 2016201094	A1	15-12-2016	
		AU 2016274692 A1	30-11-2017
		BR 112017026588 A2	14-08-2018
		CN 107690809 A	13-02-2018
		EP 3308545 A1	18-04-2018
		JP 2018522468 A	09-08-2018
		KR 20180018535 A	21-02-2018
		TW 201711472 A	16-03-2017
		US 2016366435 A1	15-12-2016
		WO 2016201094 A1	15-12-2016
-----			