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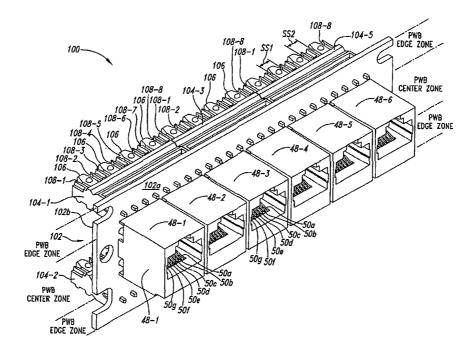
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(54) Title: PATCH PANEL WITH CROSSTALK REDUCTION SYSTEM AND METHOD



(57) Abstract: A insulation displacement connector (IDC) patch panel includes a circuit (PC) board with interdigitated capacitance for balancing out inherent capacitance found within IDCs of the panel. Unwanted cross-talk signals are reduced as a consequence.

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B. FIELDS SEARCHED			
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Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)			
C. DOCUMENTS CONSIDERED TO BE RELEVANT			
Category *	Citation of document, with indication, where a	ppropriate, of the relevant passages	Relevant to claim No.
P	US 2004/0092154 A(Doorhy et al.)13 May 2004 (1	3.05.2004) Figs. 1-17	1-11
P	US 6,729,899 (Aekins et al.) 04 May 2004 (04.05.2004) Figs. 5-11		12
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Further	documents are listed in the continuation of Box C.	See patent family annex.	
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