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[Continued on next page]

- (54) **Title:** ELECTRONIC PACKAGE THAT INCLUDES FINE POWDER COATING

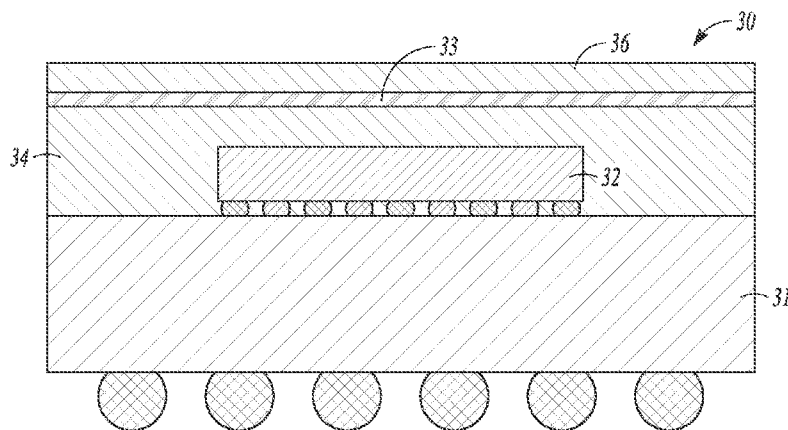


FIG. 4

- (57) **Abstract:** An electronic package that includes a substrate; an electronic component mounted to a surface of the substrate; and a porous coating that includes grains of metal powder formed onto the electronic component by melting the metal powder onto the electronic component. An electronic package that includes a substrate; an electronic component mounted to a surface of the substrate; and a porous coating that includes grains of metal powder formed onto the substrate by melting the metal powder onto the substrate. An electronic package that includes a substrate; an electronic component mounted to a surface of the substrate; an initial mold covering the electronic component; and a porous coating that includes grains of metal powder formed onto the initial mold by melting the metal powder onto the initial mold.





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ELECTRONIC PACKAGE THAT INCLUDES
FINE POWDER COATING

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CROSS-REFERENCE TO RELATED APPLICATION

[0001] This application is a continuation of U.S. Patent Application Serial No. 14/974,771, filed December 18, 2015, which is incorporated by reference herein in its entirety.

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Technical Field

[0002] Embodiments described herein generally relate to electronic packages, and more particularly to electronic packages that include fine powder coatings.

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Background

[0003] Two of the biggest challenges in developing next generation packaging technologies are package warpage reduction and package thickness reduction. These challenges are due to the ever increasing demand in the consumer microelectronic market for smaller and thinner devices.

20

[0004] In many conventional electronic packages an over-mold is used to try to control temperature-related warpage of electronic packages. There are several drawbacks that are associated with conventional over-mold electronic packaging solutions. These drawbacks are due to manufacturing considerations associated with using (i) relatively high temperatures during fabrication of the electronic packages; and (ii) smaller form factors for the electronic packages.

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[0005] Electronic packages may differ in size and/or form factor. One of the drawbacks with conventional electronic packages relates to finding a proper mold material for different types of electronic packages. One important consideration when over-molding is balancing the electronic component(s) that form the electronic package with the over-mold.

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[0006] As an example, a die area of the electronic package may have a very different stiffness and thermal expansion coefficient than a non-die area.

Therefore, when the electronic package geometry changes, the over-mold material and over-mold thickness may need to be reconfigured.

[0007] There is no universal over-mold solution for different types of electronic packages. The overlying mold that is typically used in electronic packages often cracks due to excessive mechanical stresses (especially in the edge regions of the mold). Changing over-mold materials for each type of electronic package does not promote efficient manufacturing of electronic packages.

[0008] Many conventional electronic packages typically include a separate solid stiffener that is mounted to the back side of a die using an adhesive. One of the drawbacks with using such a stiffener is that there is often failure due to delamination between the adhesive and the stiffener.

Brief Description of the Drawings

[0009] FIG. 1 shows a schematic section view of an example electronic package.

[0010] FIG. 2 shows a schematic section view of another example electronic package.

[0011] FIG. 3 shows a schematic section view of still another example electronic package.

[0012] FIG. 4 shows a schematic section view of the example electronic package shown in FIG. 3 where an additional mold has been added to the electronic package.

[0013] FIG. 5 is flow diagram illustrating an example method of fabricating an electronic package.

[0014] FIG. 6 is block diagram of an electronic apparatus that includes the electronic packages described herein.

Description of Embodiments

[0015] The following description and the drawings sufficiently illustrate specific embodiments to enable those skilled in the art to practice them. Other embodiments may incorporate structural, logical, electrical, process, and other changes. Portions and features of some embodiments may be included in, or

substituted for, those of other embodiments. Embodiments set forth in the claims encompass all available equivalents of those claims.

[0016] Orientation terminology, such as “horizontal,” as used in this application is defined with respect to a plane parallel to the conventional plane or surface of a wafer or substrate, regardless of the orientation of the wafer or substrate. The term “vertical” refers to a direction perpendicular to the horizontal as defined above. Prepositions, such as “on,” “side” (as in “sidewall”), “higher,” “lower,” “over,” and “under” are defined with respect to the conventional plane or surface being on the top surface of the wafer or substrate, regardless of the orientation of the wafer or substrate.

[0017] The electronic packages described herein may include a thin metal coating that is deposited within the electronic packages by melting fine powder during a coating process. The thin metal coating is applied to balance temperature-related warpage throughout the entire electronic package.

[0018] Pulverized metal may be deposited onto the electronic package by passing the pulverized metal through a plasma melting process (e.g., by using an Atmospheric Pressure Plasma Jet) in a carrier stream. The Atmospheric Pressure Plasma Jet both melts and accelerates metal particles towards the electronic package surface in order to deposit the metal particles on the electronic package surface.

[0019] In some forms, the electronic package may include a mold that is applied at two different times. As an example, a thin portion of the mold may be applied on the back of a silicon die before the thin metal coating is deposited onto the initial mold. Once the thin metal coating is deposited onto the first portion of the mold, additional molding material may be applied on top of the thin metal coating.

[0020] The initial mold is placed on the silicon die in part to ensure satisfactory bonding between the silicon and the mold as well as between the mold and the thin metal coating. Attaching the thin metal coating after the initial mold is applied, and before the subsequent additional mold is applied, may improve the reliability of the electronic package.

[0021] Depositing the thin metal layer to the initial mold (or directly to the electronic component or the substrate) may improve the metal adhesion and

surface bonding strength, especially when compared to conventional stiffeners. In addition, directly depositing the thin metal coating onto the initial mold in this manner may not induce additional stress in the over-mold because the metal coating edge morphology may have a porous structure.

5 **[0022]** The porous structure may provide improved substrate warpage control over conventional stiffeners. In addition, the porous metal coating may facilitate direct application of the porous metal coating to (i) the substrate; (ii) an electronic component; and/or (iii) a mold without the need for any additional bonding materials. Reducing the need for extra bonding materials may minimize
10 costs, increase production speed and reduce the possibility of metal delamination.

[0023] In addition, the entire surface of the thin metal coating may be activated for better adhesion, especially when using a plasma spray process to apply the thin metal coating. The thin metal coating may be activated by using
15 additives that improve adhesion of the coating to the substrate. Some example additives includes polymer-based and/or epoxy-based materials.

[0024] The molded electronic packages described herein may provide some form of temperature-related warpage control for electronic packages. Controlling the temperature-related warpage may reduce yield losses that can
20 occur when fabricating molded electronic packages that include stiffeners. The molded electronic packages that are described herein may also utilize cost effective electronic packaging technology in order to reduce the costs that are associated with fabricating molded electronic packages.

[0025] FIG. 1 shows a schematic section view of an example electronic
25 package 10. The electronic package 10 includes a substrate 11 and an electronic component 12 mounted to a surface of the substrate 11. The electronic package 10 further includes a coating 13 formed onto the electronic component 12 by melting powder onto the electronic component 12.

[0026] In some forms, the substrate 11 is a printed circuit board. The
30 type of substrate 11 that is included in the electronic package 10 will be depend in part on the type of electronic component 12 that is mounted to the substrate 11 as well as the application where the electronic package 10 is to be used (among other factors).

[0027] In some forms, the electronic component 12 may be a die. It should be noted that the electronic component 12 may be any active or passive component(s). In addition, the type of electronic component 12 that is included in the electronic package 10 will depend in part on the application where the electronic package 10 is to be used (among other factors).

[0028] As an example, the coating 13 may be between 5 and 40 microns thick. The thickness of the coating 13 will be depend in part on the degree of stiffening that is required of the coating 13 in the electronic package 10.

[0029] In some forms, the powder that is melted to form the coating includes one or more metals. As examples, the metals may be copper, nickel, stainless steel (among other types of metals and/or alloys).

[0030] In addition, the coating 13 may have a grain size that is less than 30 micrometers. The grain size of the coating 13 will depend in part on the type of material that is used for the coating 13 as well as the process that is used to apply the coating 13 to the electronic component (among other factors).

[0031] In the example form of the electronic package 10 shown in FIG. 1, the coating 13 entirely covers the electronic component 12. In other forms, the coating 13 may partially cover the electronic component 12.

[0032] As shown in FIG. 1, the electronic package 10 may further include a mold 14 that covers the electronic component 12. The overall size, type, and shape of the mold 14 may depend in part on the overall size, type and shape of the substrate 11 and the electronic component 12 (among other factors).

[0033] In addition, the types of materials that are utilized for the mold 14 will depend in part on the manufacturing processes that are used to fabricate the electronic package 10. In some forms, the mold 14 may be an organic polymer material (e.g., a thermoset epoxy material) (among other types of materials). As an example, the mold 14 may include 20-80% epoxy resin (among other types of materials) and be mixed with various other materials. In addition, the mold 14 may also be partially formed of silica.

[0034] It should be noted that molds 14 described herein may be formed as part of the electronic package 10 in any manner that is known now, or discovered in the future. Some example processes for forming the mold 14 include, but are not limited to, injection molding, compression molding, press-fit

configurations and semi-additive lithography processes (among other types of processes).

5 [0035] FIG. 2 shows a schematic section view of another example electronic package 20. The electronic package 20 includes a substrate 21 and an electronic component 22 mounted to a surface of the substrate 21.

[0036] The electronic package 20 further includes a coating 23 formed onto the substrate 21 by melting powder on to the substrate 21. It should be noted that the coating 23 may cover a portion (or all) of the substrate 21 that is not covered by the electronic component 22.

10 [0037] The type of substrate 21 that is included in the electronic package 10 may be similar to any of the substrates 11 described above. In addition, the type of electronic component 22 that is included in the electronic package 20 may be similar to any of the electronic components 12 described above. It should be noted that the coating 23 may be similar to any of the coatings 13
15 described above.

[0038] In some forms, the electronic package 20 may further include a mold (not shown in FIG. 2) that covers the substrate 21, coating 23 and the electronic component 22. It should be noted that the mold may be similar to any of the molds 14 described above.

20 [0039] FIG. 3 shows a schematic section view of still another example electronic package 30. FIG. 4 shows a schematic section view of the example electronic package 30 shown in FIG. 3 where an additional mold has been added to the electronic package 30.

[0040] The electronic package 30 includes a substrate 31 and an
25 electronic component 32 mounted to a surface of the substrate 31. As shown in FIG. 3, an initial mold 34 covers the electronic component 32. A coating 33 is formed onto the initial mold 34 by melting powder onto the initial mold 34.

[0041] It should be noted that the substrate 31 may be similar to any of the substrates 11 described above. In addition, the electronic component 32 may
30 be similar to any of the electronic components 12 described above. It should be noted that the coating 33 may be similar to any of the coatings 13 described above.

[0042] As shown in FIG. 4, the electronic package 30 may further include an additional mold 36 that covers the coating 33. The initial mold 34 and the additional mold 36 may be formed of the same (or different) materials. It should be noted that the initial mold 34 and the additional mold 36 may be similar to any of the molds 14 described above.

[0043] The powder may be melted on to the electronic components, substrates and/or molds in any manner that is known now, or discovered in the future. The type of process that is used to form the coating on the electronic package 10 will depend on a variety of manufacturing considerations.

10 [0044] One example process includes using an Atmospheric Pressure Plasma Jet to deposit pulverized metal powder onto the electronic package 10. As an example, fine powder metal particles may be injected into a plasma beam by a powder feeding system. The powder particles then absorb the thermal energy of the plasma beam. The powder particles then melt on the grain boundary and/or melt completely before being deposited on the substrate. It should be noted that the powder metal particles may be accelerated through the melting source in order to gain high kinetic energy while being directed toward the substrate surface.

[0045] In one example process, the area where the powder metal particles is to be deposited is cleaned. As an example, organic contaminants may be removed by using oxidation and/or evaporation.

[0046] The process may further include preparing the surface to be coated crating hydroxyl groups (and potentially other functional groups) on the surface of the substrate (e.g., by directing air-based plasma on the surface). In some forms, the surface of the substrate may be initially coated with precursors before a surface is coated with powder metal particles (e.g., silicon oxide).

[0047] The example electronic packages and methods described herein may provide advantages over conventional electronic packaging techniques in terms of yield improvement, package warpage control and/or z-height reduction.

30 [0048] FIG. 5 is flow diagram illustrating an example method [500] of fabricating an electronic package. The method [500] includes mounting an electronic component to a surface of a substrate. The method [510] further

includes [520] melting a powder onto a surface of the electronic package to form a coating on the surface of the electronic package.

[0049] It should be noted that the electronic component may be mounted to a surface of a substrate in any manner that is known now, or discovered in the future. In addition, the powder may be melted onto a surface of the electronic package to form a coating in any manner that is known now, or discovered in the future (e.g., by using a plasma spraying process).

[0050] As shown in FIG. 1, [520] melting a powder onto a surface of the electronic package 10 to form a coating 13 on the surface of the electronic package 10 may include melting a powder onto the substrate 11. As shown in FIG. 2, [520] melting a powder onto a surface of the electronic package 20 to form a coating 23 on the surface of the electronic package 20 may include melting a powder onto the electronic component 22.

[0051] As shown in FIG. 3, the method [500] may further include [530] forming an initial mold 34 over the electronic component 30 such that [520] melting a powder onto a surface of the electronic package 30 includes melting a powder onto the initial mold 34 to form a coating 33 on the initial mold 34. As shown in FIG. 4, the method [500] may further include [540] forming an additional mold 36 over the coating 33. It should be noted that the initial mold 34 and the additional mold 36 may be formed in any manner that is known now, or discovered in the future (e.g., by using injection molding, compression molding, press-fit configurations and semi-additive lithography processes).

[0052] FIG. 6 is a block diagram of an electronic apparatus 600 incorporating at least one electronic package and/or method described herein. Electronic apparatus 600 is merely one example of an electronic apparatus in which forms of the electronic packages 10, 20, 30 and methods [500] described herein may be used. Examples of an electronic apparatus 600 include, but are not limited to, personal computers, tablet computers, mobile telephones, game devices, MP3 or other digital music players, etc.

[0053] In this example, electronic apparatus 600 comprises a data processing system that includes a system bus 602 to couple the various components of the electronic apparatus 600. System bus 602 provides communications links among the various components of the electronic apparatus

600 and may be implemented as a single bus, as a combination of busses, or in any other suitable manner.

[0054] An electronic assembly 610 as describe herein may be coupled to system bus 602. The electronic assembly 610 may include any circuit or
5 combination of circuits. In one embodiment, the electronic assembly 610 includes a processor 612 which can be of any type. As used herein, “processor” means any type of computational circuit, such as but not limited to a microprocessor, a microcontroller, a complex instruction set computing (CISC) microprocessor, a reduced instruction set computing (RISC) microprocessor, a
10 very long instruction word (VLIW) microprocessor, a graphics processor, a digital signal processor (DSP), multiple core processor, or any other type of processor or processing circuit.

[0055] Other types of circuits that may be included in electronic assembly 610 are a custom circuit, an application-specific integrated circuit
15 (ASIC), or the like, such as, for example, one or more circuits (such as a communications circuit 614) for use in wireless devices like mobile telephones, tablet computers, laptop computers, two-way radios, and similar electronic systems. The IC can perform any other type of function.

[0056] The electronic apparatus 600 may also include an external
20 memory 620, which in turn may include one or more memory elements suitable to the particular application, such as a main memory 622 in the form of random access memory (RAM), one or more hard drives 624, and/or one or more drives that handle removable media 626 such as compact disks (CD), flash memory cards, digital video disk (DVD), and the like.

[0057] The electronic apparatus 600 may also include a display device
25 616, one or more speakers 618, and a keyboard and/or controller 630, which can include a mouse, trackball, touch screen, voice-recognition device, or any other device that permits a system user to input information into and receive information from the electronic apparatus 600.

[0058] To better illustrate the electronic packages and methods disclosed
30 herein, a non-limiting list of examples is provided here:

[0059] Example 1 includes an electronic package that includes a substrate; an electronic component mounted to a surface of the substrate; and a

porous coating that includes grains of metal powder, wherein the porous coating is formed onto the electronic component by melting the metal powder onto the electronic component.

5 [0060] Example 2 includes the electronic package of example 1, wherein the substrate is a printed circuit board, and wherein the electronic component is a die.

[0061] Example 3 includes the electronic package of any one of examples 1-2, wherein the porous coating is between 5 and 400 microns thick.

10 [0062] Example 4 includes the electronic package of any one of examples 1-3, wherein the metal powder is formed of copper.

[0063] Example 5 includes the electronic package of any one of examples 1-4, wherein the porous coating has a grain boundary size less than 30 micrometers.

15 [0064] Example 6 includes the electronic package of examples 1-5, wherein the porous coating partially covers the electronic component.

[0065] Example 7 includes the electronic package of any one of examples 1-6, and further including a mold covering the coating.

20 [0066] Example 8 includes an electronic package that includes a substrate; an electronic component mounted to a surface of the substrate; and a porous coating that includes grains of metal powder formed onto the substrate by melting the metal powder onto the substrate.

[0067] Example 9 includes the electronic package of example 8, wherein the substrate is a printed circuit board, and wherein the electronic component is a die.

25 [0068] Example 10 includes the electronic package of any one of examples 8-9, wherein the porous coating is between 5 and 400 microns thick.

[0069] Example 11 includes the electronic package of any one of examples 8-10, wherein the metal powder is formed of copper.

30 [0070] Example 12 includes the electronic package of any one of examples 8-11, wherein the porous coating has a grain boundary size less than 30 micrometers.

[0071] Example 13 includes the electronic assembly of examples 8-12, and further including a mold covering the substrate and the electronic component.

5 [0072] Example 14 includes an electronic package that includes a substrate; an electronic component mounted to a surface of the substrate; an initial mold covering the electronic component; and a porous coating that includes grains of metal powder formed onto the initial mold by melting the metal powder onto the initial mold.

10 [0073] Example 15 includes the electronic package of example 14, wherein the substrate is a printed circuit board, and wherein the electronic component is a die.

[0074] Example 16 includes the electronic package of any one of examples 14-15, wherein the porous coating is between 5 and 400 microns thick.

15 [0075] Example 17 includes the electronic package of any one of examples 14-16, wherein the metal powder is formed of copper.

[0076] Example 18 includes the electronic package of any one of examples 14-17, wherein the porous coating has a grain boundary size less than 30 micrometers.

20 [0077] Example 19 includes the electronic package of any one of examples 14-18, and further including an additional mold covering the porous coating.

[0078] Example 20 includes the electronic package of example 19, wherein the initial mold and the additional mold are formed of the same material.

25 [0079] Example 21 includes method of fabricating an electronic package. The method includes mounting an electronic component to a surface of a substrate; and melting a metal powder onto a surface of the electronic package to form a porous coating that includes grains of metal powder on the surface of the electronic package.

30 [0080] Example 22 includes the method of example 21, wherein melting a metal powder onto a surface of the electronic package includes melting the metal powder onto the substrate.

[0081] Example 23 includes the method of any one of examples 21-22, wherein melting a metal powder onto a surface of the electronic package includes melting the metal powder onto the electronic component.

[0082] Example 24 includes the method of any one of examples 21-23, and further including forming an initial mold over the electronic component, and wherein melting a metal powder onto a surface of the electronic package includes melting the metal powder onto the initial mold to form a porous coating on the initial mold.

[0083] Example 25 includes the method of any one of examples 21-24, and further including forming an additional mold over the coating.

[0084] These and other examples and features of the present electronic assemblies and electronic packages will be set forth in part in the detailed description.

[0085] This overview is intended to provide non-limiting examples of the present subject matter. It is not intended to provide an exclusive or exhaustive explanation. The detailed description is included to provide further information about the methods [800], electronic assemblies 30 and/or electronic packages 10 described herein.

[0086] The above detailed description includes references to the accompanying drawings, which form a part of the detailed description. The drawings show, by way of illustration, specific embodiments in which the invention can be practiced. These embodiments are also referred to herein as “examples.” Such examples can include elements in addition to those shown or described. However, the present inventors also contemplate examples in which only those elements shown or described are provided. Moreover, the present inventors also contemplate examples using any combination or permutation of those elements shown or described (or one or more aspects thereof), either with respect to a particular example (or one or more aspects thereof), or with respect to other examples (or one or more aspects thereof) shown or described herein.

[0087] In this document, the terms “a” or “an” are used, as is common in patent documents, to include one or more than one, independent of any other instances or usages of “at least one” or “one or more.” In this document, the term “or” is used to refer to a nonexclusive or, such that “A or B” includes “A

- but not B,” “B but not A,” and “A and B,” unless otherwise indicated. In this document, the terms “including” and “in which” are used as the plain-English equivalents of the respective terms “comprising” and “wherein.” Also, in the following claims, the terms “including” and “comprising” are open-ended, that is, a system, device, article, composition, formulation, or process that includes elements in addition to those listed after such a term in a claim are still deemed to fall within the scope of that claim. Moreover, in the following claims, the terms “first,” “second,” and “third,” etc. are used merely as labels, and are not intended to impose numerical requirements on their objects.
- 10 **[0088]** The above description is intended to be illustrative, and not restrictive. For example, the above-described examples (or one or more aspects thereof) may be used in combination with each other. Other embodiments can be used, such as by one of ordinary skill in the art upon reviewing the above description.
- 15 **[0089]** The Abstract is provided to allow the reader to quickly ascertain the nature of the technical disclosure. It is submitted with the understanding that it will not be used to interpret or limit the scope or meaning of the claims.
- 20 **[0090]** Also, in the above Detailed Description, various features may be grouped together to streamline the disclosure. This should not be interpreted as intending that an unclaimed disclosed feature is essential to any claim. Rather, inventive subject matter may lie in less than all features of a particular disclosed embodiment. Thus, the following claims are hereby incorporated into the Detailed Description, with each claim standing on its own as a separate embodiment, and it is contemplated that such embodiments can be combined with each other in various combinations or permutations. The scope of the invention should be determined with reference to the appended claims, along with the full scope of equivalents to which such claims are entitled.
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Claims

1. An electronic package, comprising:
a substrate;
an electronic component mounted to a surface of the substrate; and
5 a porous coating that includes grains of metal powder, wherein the porous coating is formed onto the electronic component by melting the metal powder onto the electronic component.
2. The electronic package of claim 1, wherein the substrate is a printed
10 circuit board, and wherein the electronic component is a die.
3. The electronic package of claim 1, wherein the porous coating is between
5 and 400 microns thick.
- 15 4. The electronic package of claim 1, wherein the metal powder is formed of copper.
5. The electronic package of claim 1, wherein the porous coating has a
grain boundary size less than 30 micrometers.
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6. The electronic package of claim 1, wherein the porous coating partially
covers the electronic component.
7. The electronic package of claim 1, further comprising a mold covering
25 the coating.
8. An electronic package, comprising:
a substrate;
an electronic component mounted to a surface of the substrate; and
30 a porous coating that includes grains of metal powder formed onto the substrate by melting the metal powder onto the substrate.

9. The electronic package of claim 8, wherein the substrate is a printed circuit board, and wherein the electronic component is a die.
10. The electronic package of claim 8, wherein the porous coating is between
5 5 and 400 microns thick.
11. The electronic package of claim 8, wherein the metal powder is formed of copper.
- 10 12. The electronic package of claim 11, wherein the porous coating has a grain boundary size less than 30 micrometers.
13. The electronic package of claim 8, further comprising a mold covering the substrate and the electronic component.
- 15 14. An electronic package, comprising:
a substrate;
an electronic component mounted to a surface of the substrate;
an initial mold covering the electronic component; and
20 a porous coating that includes grains of metal powder formed onto the initial mold by melting the metal powder onto the initial mold.
15. The electronic package of claim 14, wherein the substrate is a printed circuit board, and wherein the electronic component is a die.
- 25 16. The electronic package of claim 14, wherein the porous coating is between 5 and 400 microns thick.
17. The electronic package of claim 14, wherein the metal powder is formed
30 of copper.
18. The electronic package of claim 14, wherein the porous coating has a grain boundary size less than 30 micrometers.

19. The electronic package of claim 14, further comprising an additional mold covering the porous coating.

5 20. The electronic package of claim 19, wherein the initial mold and the additional mold are formed of the same material.

21. A method of fabricating an electronic package, comprising:
mounting an electronic component to a surface of a substrate; and
10 melting a metal powder onto a surface of the electronic package to form a porous coating that includes grains of metal powder on the surface of the electronic package.

22. The method of claim 21, wherein melting a metal powder onto a surface
15 of the electronic package includes melting the metal powder onto the substrate.

23. The method of claim 21, wherein melting a metal powder onto a surface of the electronic package includes melting the metal powder onto the electronic component.

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24. The method of claim 21, further comprising forming an initial mold over the electronic component, and wherein melting a metal powder onto a surface of the electronic package includes melting the metal powder onto the initial mold to form a porous coating on the initial mold.

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25. The method of claim 24, further comprising forming an additional mold over the coating.

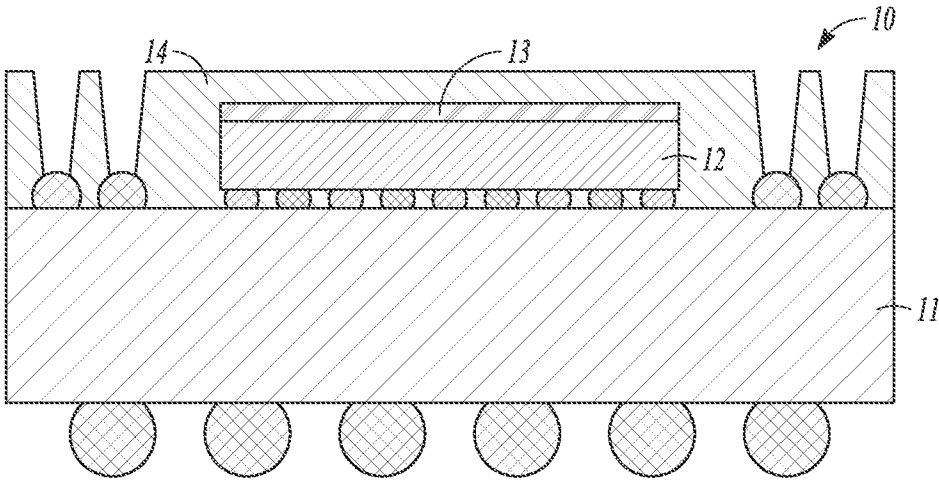


FIG. 1

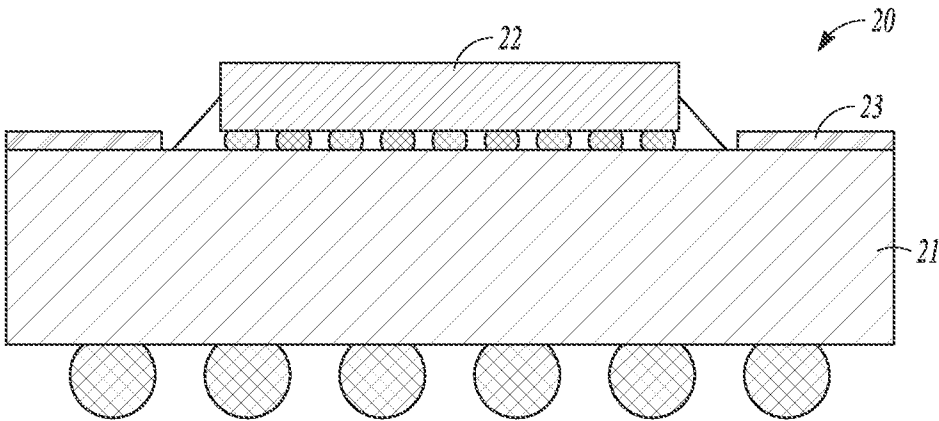


FIG. 2

2/3

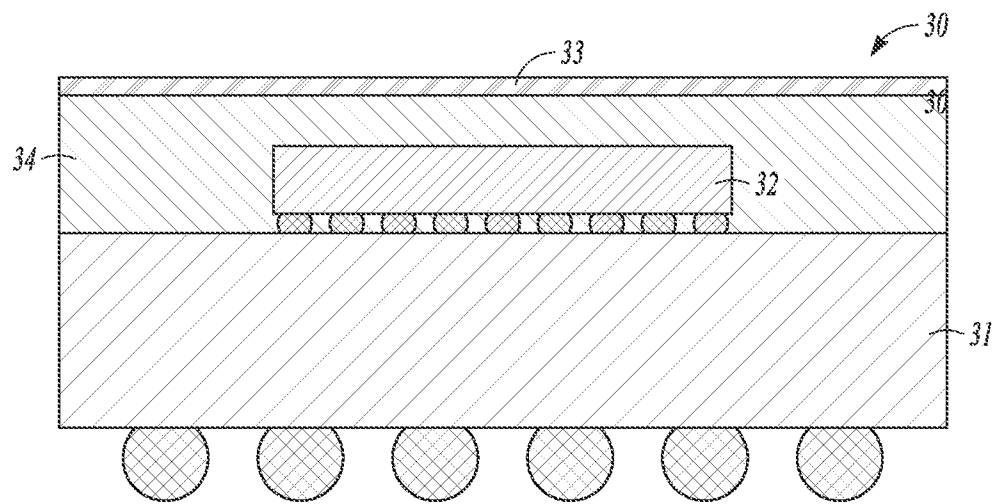


FIG. 3

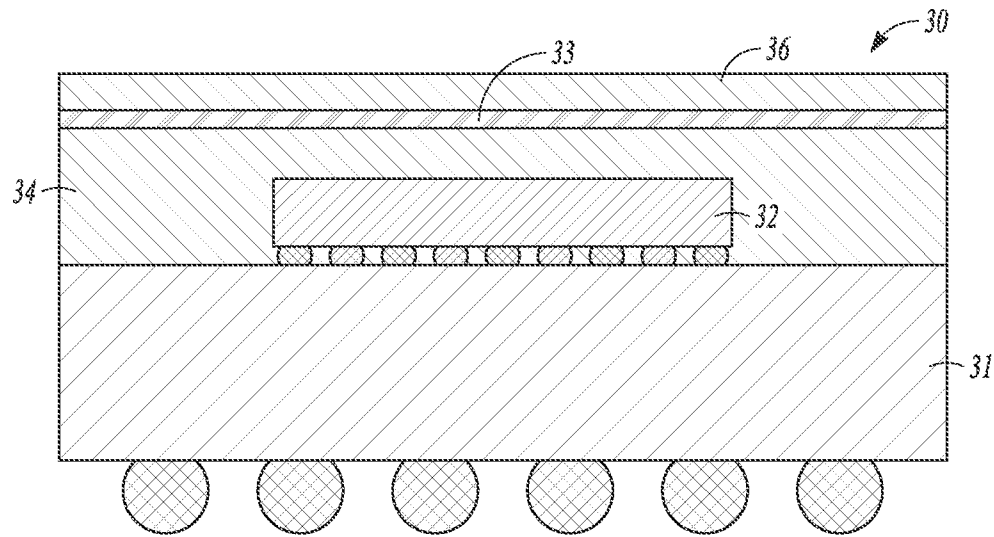


FIG. 4

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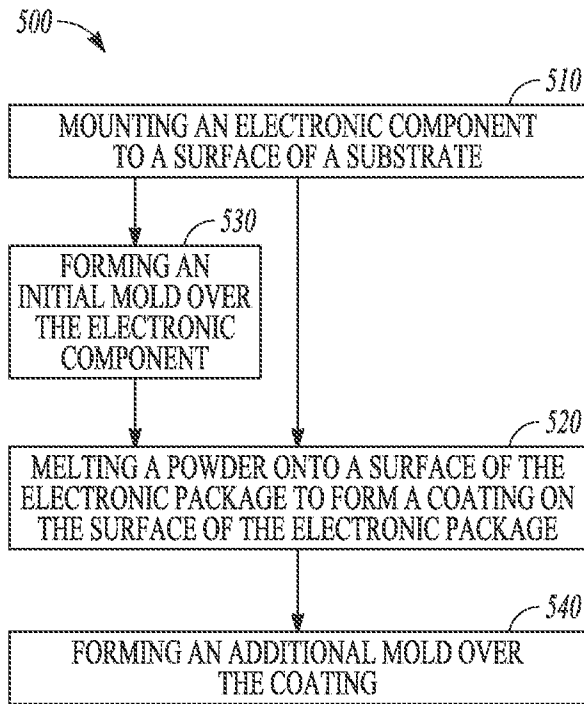


FIG. 5

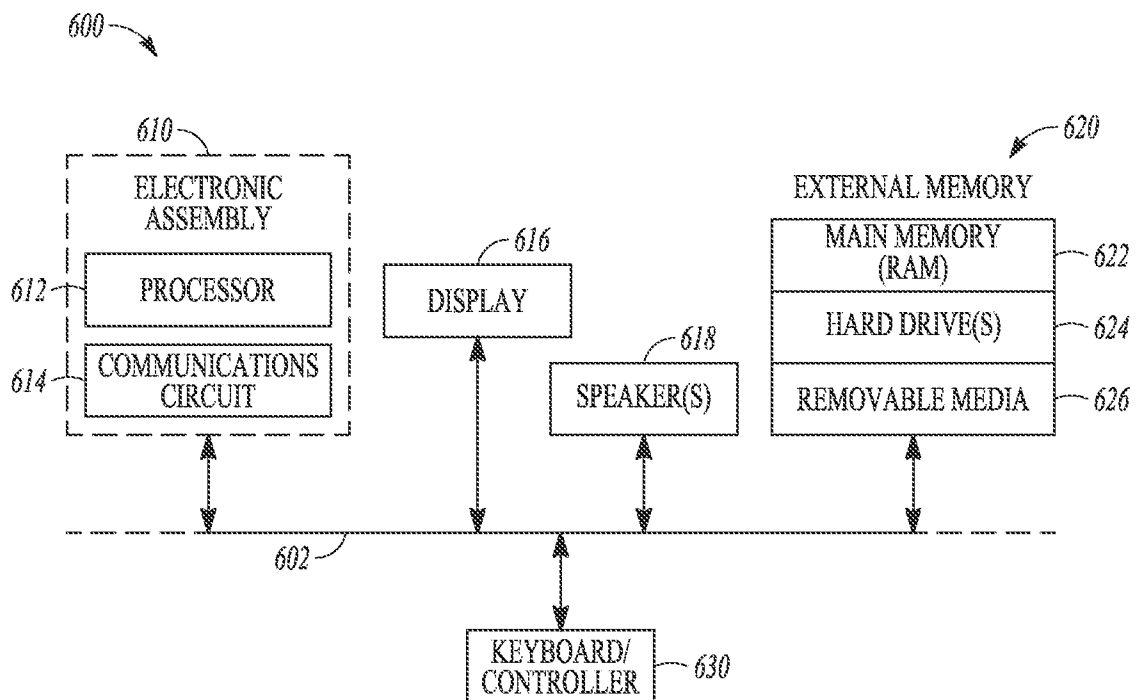


FIG. 6

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US2016/062741**A. CLASSIFICATION OF SUBJECT MATTER****H01L 23/29(2006.01)i, H01L 23/31(2006.01)i**

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H01L 23/29; H01L 23/373; H01L 23/34; H01L 33/44; H01L 23/28; H01L 33/62; H01L 33/50; H01L 23/31

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Korean utility models and applications for utility models

Japanese utility models and applications for utility models

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

eKOMPASS(KIPO internal) & keywords: package, porous, coating, metal, powder

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2005-0184405 A1 (JIN-CHUNG BAI et al.) 25 August 2005 See paragraphs [0028]-[0032] and figures 1-6.	1-25
A	US 2011-0169035 A1 (TOMIO INOUE et al.) 14 July 2011 See paragraphs [0052]-[0083] and figures 1A-2D.	1-25
A	US 2004-0135247 A1 (KOUICHI TAKASHIMA et al.) 15 July 2004 See paragraphs [0086]-[0093] and figures 8-9.	1-25
A	US 2012-0112236 A1 (SATOSHI HIGUMA et al.) 10 May 2012 See paragraphs [0048]-[0057] and figures 1-3.	1-25
A	US 2012-0280382 A1 (YUNHYEOK IM et al.) 08 November 2012 See paragraphs [0038]-[0060] and figures 1-2G.	1-25



Further documents are listed in the continuation of Box C.



See patent family annex.

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"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

28 February 2017 (28.02.2017)

Date of mailing of the international search report

28 February 2017 (28.02.2017)

Name and mailing address of the ISA/KR

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INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/US2016/062741

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