



(51) International Patent Classification:
H04B 7/024 (2017.01) *H04B 7/06* (2006.01)

(21) International Application Number:
PCT/US2017/032341

(22) International Filing Date:
12 May 2017 (12.05.2017)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
62/336,483 13 May 2016 (13.05.2016) US

(71) Applicant: INTEL CORPORATION [US/US]; 2200
Mission College Boulevard, Santa Clara, California 95054
(US).

(72) Inventor: DAVYDOV, Alexei Vladimirovich; Lenin av.
28/11-40, Nizhny Novgorod, 603132 (RU).

(74) Agent: PERDOK, Monique M. et al.; P.O. Box 2938, Min-
neapolis, Minnesota 55402 (US).

(81) Designated States (*unless otherwise indicated, for every
kind of national protection available*): AE, AG, AL, AM,
AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ,
CA, CH, CL, CN, CO, CR, CU, CZ, DE, DJ, DK, DM, DO,
DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN,
HR, HU, ID, IL, IN, IR, IS, JP, KE, KG, KH, KN, KP, KR,
KW, KZ, LA, LC, LK, LR, LS, LU, LY, MA, MD, ME, MG,
MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM,
PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SA, SC,
SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR,
TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(84) Designated States (*unless otherwise indicated, for every
kind of regional protection available*): ARIPO (BW, GH,

(54) Title: INTER EVOLVED NODEB COORDINATED BEAMFORMING

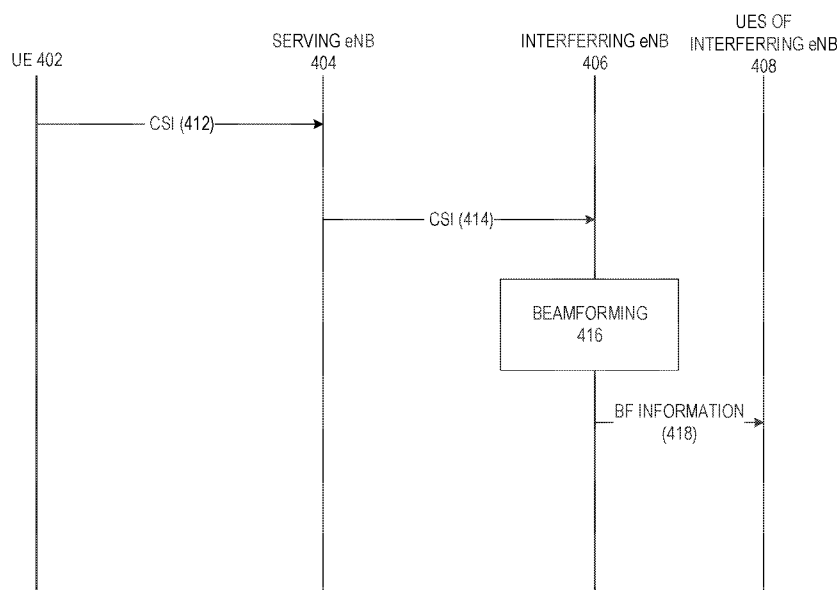


FIG. 4

(57) Abstract: Methods and apparatus are described by using channel state information (CSI) associated with a CSI interference measurement from. The CSI is generated by user equipment. The CSI interference measurement is associated with an aggressor enhance Node B (eNB). The CSI is transmitted to the serving eNB. The CSI is then transmitted to the aggressor eNB over an X2 interface. The aggressor eNB determines beamforming restriction information based upon the CSI. The beamforming restriction information may then be transmitted to user equipment served by the aggressor eNB. The user equipment served by the aggressor eNB may use the beamforming restriction information in selecting channel state information reference signal resource indicator index or a precoding matrix indicator.

GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, ST, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, KM, ML, MR, NE, SN, TD, TG).

Published:

- *with international search report (Art. 21(3))*
- *before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments (Rule 48.2(h))*

INTER EVOLVED NODEB COORDINATED BEAMFORMING**PRIORITY CLAIM**

[0001] This application claims priority to United States Provisional Patent
5 Application Serial No. 62/336,486 filed May 13, 2016, entitled "X2
SIGNALLING TO SUPPORT INTER EVOLVED NODEB COORDINATED
SCHEDULING/COORDINATED BEAMFORMING FECOMP" which is
incorporated herein by reference in its entirety.

10 FIELD OF THE DISCLOSURE

[0002] Various embodiments generally may relate to the field of wireless
communications.

BACKGROUND

[0003] Co-channel interference from the neighboring cells in a cellular network may
15 be one of the most dominant limiting factors for achieving higher network efficiency
especially for user equipment (UE) at cell edges. One way inter-cell interference
problems have been addressed is by using coordinated multi-point (CoMP)
techniques.

20 BRIEF DESCRIPTION OF THE DRAWINGS

[0004] The present disclosure is illustrated by way of example and not limitation in
the figures of the accompanying drawings, in which like references indicate similar
elements and in which:

[0005] FIG. 1 is a block diagram of a system that uses inter-cell inference
25 mitigation according to some embodiments described herein.

[0006] FIG. 2 is a block diagram illustrating channel state information (CSI) for a
coordinated multipoint (CoMP) transmission according to some embodiments
described herein.

[0007] FIG. 3 is a block diagram illustrating interference in a CoMP environment
30 according to some embodiments described herein.

[0008] FIG. 4 is a diagram of messaging regarding beamforming at an interfering
eNB according to some embodiments described herein.

[0009] FIG. 5 is a block diagram of a User Equipment (UE) in accordance with
some embodiments.

[0010] FIG. 6 illustrates a block diagram of a communication device such as an eNB or a UE, in accordance with some embodiments.

[0011] The following detailed description refers to the accompanying drawings.

The same reference numbers may be used in different drawings to identify the same

5 or similar elements. In the following description, for purposes of explanation and not limitation, specific details are set forth such as particular structures, architectures, interfaces, techniques, etc. in order to provide a thorough understanding of the various aspects of various embodiments. However, it will be apparent to those skilled in the art having the benefit of the present disclosure that the various aspects of the various embodiments may be practiced in other examples that depart from these specific details. In certain instances, descriptions of well-known devices, circuits, and methods are omitted so as not to obscure the description of the various embodiments with unnecessary detail.

[0012] The following description and the drawings sufficiently illustrate specific 15 embodiments to enable those skilled in the art to practice them. Other embodiments may incorporate structural, logical, electrical, process, and other changes. Portions and features of some embodiments may be included in, or substituted for, those of other embodiments. Embodiments set forth in the claims encompass all available equivalents of those claims.

20

DESCRIPTION

[0013] CoMP techniques help avoid interference by coordinating the transmitting nodes. FIG. 1 is a block diagram of a system 100 that uses inter-cell inference mitigation according to some embodiments described herein.

25 Coordination schemes require evolved NodeB (eNB) architecture with multiple remote radio heads (RRH) 102 and 104, a central processing unit (CPU) 120 responsible for signal processing and coordination of RRHs and a high capacity and low latency backhaul link 130 that connects the RRHs 102 and 104, and the CPU 120. Not all long term evolution advanced (LTE-A) deployments have such an eNB implementation with close-to-ideal backhaul links. Without the proper 30 deployment, the usage of the downlink (DL) CoMP schemes are limited to the specific scenarios.

[0014] In the scenarios where ideal backhaul link is not available, a CoMP non-ideal backhaul link (NIB) may be used to mitigate inter-cell co-channel interference.

In these scenarios, channel quality information (CQI) for the links corresponding to interfering channels or eNB to assist coordinated scheduling (CS) decisions such as muting.

- [0015] The CSI report for inter eNB signaling may support the wideband and
- 5 subband CQI reports calculated by the UEs.

IE/Group Name	Presence	Range	IE type and reference	Semantics description
CSI Report per Cell		1 .. <maxUEReport>		
>UE ID	M		BIT String (SIZE(16))	ID assigned by eNB2 for the IE
>> CSI Process Configuration Index	M		Integer (1..7, ...)	Indicates one of the possible CSI Process configurations in the serving cell.
>> CSI Report per CSI Process Item		1 .. <maxUEReport>		
>>>RI	M		Integer(1..8,...)	The RI corresponding to the CQI being reported for the CSI process item. Value defined in TS 36.213
>>>Wideband CQI	M		9.2.80	
>>>Subband Size	M		ENUMERATED(2, 3, 4, 6, 8, ..)	Corresponds to a value of subband size k defined in TS 36.213 for the system bandwidth N_{RB}^{DL}
>>>Subband CQI List		0 .. <maxSubband>		
>>>>Subband CQI	M		9.2.80	
Subband Index	M		Integer(0..27,...)	

Range bound	Explanation
maxUEReport	Maximum number of UE. Value is 128
maxCSIProcess	Maximum number of CSI processes per UE. The value is 4.
maxCSIReport	Maximum number of CSI Reports per CSI Process. The value is 2.
maxSubband	Maximum number of subbands. The value is 14.

5

[0016] It should be noted that the above CSI reporting only considers CQI signaling and, therefore, cannot support coordinated beamforming DL CoMP schemes.

Additional enhancements for inter eNB signaling may be considered for LTE-A to support coordinated scheduling (CS)/coordinated beamforming (CB) CoMP

10 schemes. For example, CSI reporting for X2 may be extended to include Channel State Information Reference Signal Index (CRS), precoding matrix indicator (PMI) or rank indicator (RI) calculated by the UE for interfering links. The information at a serving eNB can be used to perform interference mitigation using beamforming coordination with an aggressor/interfering eNB. The beamforming coordination
 15 may be provided by configuring codebook subset restriction to power offsets for CSI reference signals (CSI-RS) with different beamforming for the served UEs based on the received CSI information.

[0017] The PMI/RI calculated by UEs for a CSI process corresponding to an interfering eNB conveys some directional information about the interfering link.

20 This direction information may be used by the interfering eNB as part of the eNB's beamforming. Specifically, the beamforming may be done to reduce the interference at the UE from the interfering link of the interfering/aggressor eNB. CQI calculations by the same UE may not provide sufficient accuracy to allow the interfering eNB to make beamforming decisions.

25 [0018] FIG. 2 is a block diagram illustrating channel state information (CSI) for a coordinated multipoint (CoMP) transmission according to some embodiments described herein. A UE 210 is located at a cell edge of coverage 222 of a serving eNB 202 and also at a cell edge of coverage 224 of an interfering eNB 204. In the

figure, the serving eNB 202 and the interfering eNB 224 are coordinating with one another. For example, information regarding the UE 210 is shared between the eNBs 202 and 204. In an example, the information may be shared over a backhaul link (not shown).

5 [0019] The UE may be configured with two CSI processes where each process is associated with multiple non-zero point (NZP) CSI-RS resources, such as 232 and 234, corresponding to vertical beams. The CQI reported by the UE for the interfering link will reflect the link quality of the interfering link, but not the link corresponding to the serving eNB 202. The NZP CSI-RS resources corresponding to the interfering link may be used as interfering sources for a CQI calculation
10 corresponding to the serving link. In this case, the CQI reported by the UE would reflect the expected link quality of the channel under different interfering hypothesis.

[0020] Full-dimension multiple-input, and multiple-output (FD-MIMO) may support additional orthogonal demodulation reference signal (DM-RS) antenna
15 ports to support multi-user, multiple-input, and multiple-output (MU-MIMO). The orthogonal DM-RS antenna port multiplexing may be provided by scheduling the DM-RS antenna ports for a physical downlink shared channel (PDSCH) transmission to different UEs and indicating in the downlink control indicator (DCI)
20 the co-schedule DM-RS port assumptions. FIG. 3 is a block diagram illustrating interference in a CoMP environment according to some embodiments described herein. A first UE 312 may communicate with a transmission point (TP) such as an eNB 302. Signals, beams, links, etc., from another eNB 304 may provide interference to the UE 312. Additionally, the system may also include another UE
25 314 that is being served by the UE 304. Signals, beams, links, etc., from the eNB 302 may interfere with the UE 314. Using orthogonal DM-RS antenna ports may reduce this interference. For example, the UE 312 may use DM-RS ports 9 and 11, while the UE 314 may use DM-RS ports 7. In a multi-point scenario with coordinated scheduling (CS) and coordinated beamforming (BCB), the orthogonal
30 DM-RS antenna ports may be used to reduce interference on the received DM-RS antenna ports.

[0021] The X2 interface may be used for signaling for CQI and rank indicator (RI) information corresponding to interfering links. The CQI and RI information may be used to indicate the expected interference level from the neighboring cells to assist

CS operations. For full dimension multiple input, multiple output (FD-MIMO) scenarios with non-ideal backhaul, a similar enhancement may be considered for CS/CB schemes. It has been found that vertical beamforming may have a noticeable impact on system performance due to inter transmission point (TP) interference.

- 5 More specifically, depending on the antenna height and the deployment scenario the vertical beamforming at the TP with the elevations angles close to horizon may create server interference to the neighboring cells. To provide interference information for different elevation beamforming on the neighboring TPs, the CQI reporting for different CSI-RS resource indicator (CRI) indices of the neighboring
- 10 TPs may be considered. Such information may be exchanged over an X2 interface between the neighboring eNBs. The interference information may be used to assist the beamforming restriction configurations (*e.g.* selection of a power offset (Pc) for a non-zero power (NZP) channel state information reference signal (CSI-RS) with different vertical beamforming). In an example, the PMI information may be
- 15 exchanged, *e.g.*, over an X2 interface, between eNBs. The PMI information may include only W1 (beamforming) component of the PMI. In addition, the information on NZP CSI-RS resources may be exchanged between eNBs to support channel measurements by the neighboring UEs.

- [0022] To assist an interfering eNB's beamforming selection, the interfering eNB
- 20 may utilize PMI, RI or CRI corresponding to the interference channels reported by the victim UEs. Such CSI information may be periodically obtained by the neighboring eNB from the served UEs and signaled to the interfering eNB via the backhaul link. The interfering eNB then may adjust the beamforming based on the provided information. In an example, the beamforming restrictions may be
- 25 performed using codebookSubsetRestriction parameter which is provided by the interfering eNB. The interfering eNB may provide the beamforming restrictions to the UEs served by the interfering eNB. The UEs served by the interfering eNB may then select PMI or CRI that are not restricted based upon the received beamforming restrictions..

- 30 [0023] FIG. 4 is a diagram of messaging regarding beamforming at an interfering eNB according to some embodiments described herein. An interfering eNB 406 may be a macro eNB that may perform interference avoidance in the spatial domain towards a cell-edge UE 402. A serving eNB 404 may serve the cell-edge UE 402. The server eNB 404 may be a small cell eNB. The UE 402 may provide CSI

feedback 412 that corresponds to one or more interfering channels to the serving eNB 404. The interfering channels may originate from the interfering eNB 406. The CSI feedback 412 may include a channel state information reference signal resource index (CRI), precoding matrix indicator (PMI), a channel quality indicator (CQI), a rank indicator (RI), or a precoding type indicator (PTI).

The serving eNB 404 may provide the CSI 414 to the interfering eNB 406 over an X2 interface. The interfering eNB 406 may then make beamforming 416 decisions, such as beamforming restrictions, based upon the victims UE's CSI. The interfering eNB 406 may inform its served UEs 408 about beamforming restrictions 416. For example, the beamforming restrictions may be communicated to the UE 408 using codebookSubsetRestriction parameter or by assigning different power offsets (Pc) to different NZP CSI-RS resources of Class B FD-MIMO with different beamforming. The interfering eNB 408 may then perform user scheduling by considering the semi-static beamforming restrictions on the specific time frequency resources. In an example the beamforming restriction 416 can be calculated by the serving eNB 404 and communicated to interfering eNB 406 using X2 interface. The interfering eNB 404 may inform its served UEs 408 about restriction in the beamforming using the codebookSubsetRestriction parameter or by assigning different power offsets (Pc) to different NZP CSI-RS resources of Class B FD-MIMO with different beamforming. The served UEs 408 may then use the beamforming restriction information received from the interfering eNB 406 to select PMI and CRI. The beamforming restriction information may indicate which PMI and/or CRI the UEs 408 should avoid.

[0024] As used herein, the term "circuitry" may refer to, be part of, or include an Application Specific Integrated Circuit (ASIC), an electronic circuit, a processor (shared, dedicated, or group), and/or memory (shared, dedicated, or group) that execute one or more software or firmware programs, a combinational logic circuit, and/or other suitable hardware components that provide the described functionality. In some embodiments, the circuitry may be implemented in, or functions associated with the circuitry may be implemented by, one or more software or firmware modules. In some embodiments, circuitry may include logic, at least partially operable in hardware.

[0025] Embodiments described herein may be implemented into a system using any suitably configured hardware and/or software. Fig. 5 illustrates, for one

embodiment, example components of a User Equipment (UE) device 500. The example components may also be used in an eNB. In some embodiments, the UE device 500 may include application circuitry 502, baseband circuitry 504, Radio Frequency (RF) circuitry 506, front-end module (FEM) circuitry 508 and one or more antennas 510, coupled together at least as shown.

[0026] The application circuitry 502 may include one or more application processors. For example, the application circuitry 502 may include circuitry such as, but not limited to, one or more single-core or multi-core processors. The processor(s) may include any combination of general-purpose processors and dedicated processors (e.g., graphics processors, application processors, etc.). The processors may be coupled with and/or may include memory/storage and may be configured to execute instructions stored in the memory/storage to enable various applications and/or operating systems to run on the system.

[0027] The baseband circuitry 504 may include circuitry such as, but not limited to, one or more single-core or multi-core processors. The baseband circuitry 504 may include one or more baseband processors and/or control logic to process baseband signals received from a receive signal path of the RF circuitry 506 and to generate baseband signals for a transmit signal path of the RF circuitry 506. Baseband processing circuitry 504 may interface with the application circuitry 502 for generation and processing of the baseband signals and for controlling operations of the RF circuitry 506. For example, in some embodiments, the baseband circuitry 504 may include a second generation (2G) baseband processor 504a, third generation (3G) baseband processor 504b, fourth generation (4G) baseband processor 504c, and/or other baseband processor(s) 504d for other existing generations, generations in development or to be developed in the future (e.g., fifth generation (5G), 6G, etc.). The baseband circuitry 504 (e.g., one or more of baseband processors 504a-d) may handle various radio control functions that enable communication with one or more radio networks via the RF circuitry 506. The radio control functions may include, but are not limited to, signal modulation/demodulation, encoding/decoding, radio frequency shifting, etc. In some embodiments, modulation/demodulation circuitry of the baseband circuitry 504 may include Fast-Fourier Transform (FFT), precoding, and/or constellation mapping/demapping functionality. In some embodiments, encoding/decoding circuitry of the baseband circuitry 504 may include convolution, tail-biting

convolution, turbo, Viterbi, and/or Low Density Parity Check (LDPC) encoder/decoder functionality. Embodiments of modulation/demodulation and encoder/decoder functionality are not limited to these examples and may include other suitable functionality in other embodiments.

5 [0028] In some embodiments, the baseband circuitry 504 may include elements of a protocol stack such as, for example, elements of an evolved universal terrestrial radio access network (EUTRAN) protocol including, for example, physical (PHY), media access control (MAC), radio link control (RLC), packet data convergence protocol (PDCP), and/or radio resource control (RRC) elements. A central
10 processing unit (CPU) 504e of the baseband circuitry 504 may be configured to run elements of the protocol stack for signaling of the PHY, MAC, RLC, PDCP and/or RRC layers. In some embodiments, the baseband circuitry may include one or more audio digital signal processor(s) (DSP) 504f. The audio DSP(s) 504f may be
15 include elements for compression/decompression and echo cancellation and may include other suitable processing elements in other embodiments. Components of the baseband circuitry may be suitably combined in a single chip, a single chipset, or disposed on a same circuit board in some embodiments. In some embodiments, some or all of the constituent components of the baseband circuitry 504 and the application circuitry 502 may be implemented together such as, for example, on a
20 system on a chip (SOC).

[0029] In some embodiments, the baseband circuitry 504 may provide for communication compatible with one or more radio technologies. For example, in some embodiments, the baseband circuitry 504 may support communication with an evolved universal terrestrial radio access network (EUTRAN) and/or other wireless
25 metropolitan area networks (WMAN), a wireless local area network (WLAN), a wireless personal area network (WPAN). Embodiments in which the baseband circuitry 504 is configured to support radio communications of more than one wireless protocol may be referred to as multi-mode baseband circuitry.

[0030] RF circuitry 506 may enable communication with wireless networks using
30 modulated electromagnetic radiation through a non-solid medium. In various embodiments, the RF circuitry 506 may include switches, filters, amplifiers, etc. to facilitate the communication with the wireless network. RF circuitry 506 may include a receive signal path which may include circuitry to down-convert RF signals received from the FEM circuitry 508 and provide baseband signals to the

baseband circuitry 504. RF circuitry 506 may also include a transmit signal path which may include circuitry to up-convert baseband signals provided by the baseband circuitry 504 and provide RF output signals to the FEM circuitry 508 for transmission.

- 5 [0031] In some embodiments, the RF circuitry 506 may include a receive signal path and a transmit signal path. The receive signal path of the RF circuitry 506 may include mixer circuitry 506a, amplifier circuitry 506b and filter circuitry 506c. The transmit signal path of the RF circuitry 506 may include filter circuitry 506c and mixer circuitry 506a. RF circuitry 506 may also include synthesizer circuitry 506d
- 10 for synthesizing a frequency for use by the mixer circuitry 506a of the receive signal path and the transmit signal path. In some embodiments, the mixer circuitry 506a of the receive signal path may be configured to down-convert RF signals received from the FEM circuitry 508 based on the synthesized frequency provided by synthesizer circuitry 506d. The amplifier circuitry 506b may be configured to
- 15 amplify the down-converted signals and the filter circuitry 506c may be a low-pass filter (LPF) or band-pass filter (BPF) configured to remove unwanted signals from the down-converted signals to generate output baseband signals. Output baseband signals may be provided to the baseband circuitry 504 for further processing. In some embodiments, the output baseband signals may be zero-frequency baseband
- 20 signals, although this is not a requirement. In some embodiments, mixer circuitry 506a of the receive signal path may comprise passive mixers, although the scope of the embodiments is not limited in this respect.

- [0032] In some embodiments, the mixer circuitry 506a of the transmit signal path may be configured to up-convert input baseband signals based on the synthesized
- 25 frequency provided by the synthesizer circuitry 506d to generate RF output signals for the FEM circuitry 508. The baseband signals may be provided by the baseband circuitry 504 and may be filtered by filter circuitry 506c. The filter circuitry 506c may include a low-pass filter (LPF), although the scope of the embodiments is not limited in this respect.

- 30 [0033] In some embodiments, the mixer circuitry 506a of the receive signal path and the mixer circuitry 506a of the transmit signal path may include two or more mixers and may be arranged for quadrature downconversion and/or upconversion respectively. In some embodiments, the mixer circuitry 506a of the receive signal path and the mixer circuitry 506a of the transmit signal path may include two or

more mixers and may be arranged for image rejection (e.g., Hartley image rejection). In some embodiments, the mixer circuitry 506a of the receive signal path and the mixer circuitry 506a may be arranged for direct downconversion and/or direct upconversion, respectively. In some embodiments, the mixer circuitry 506a of the receive signal path and the mixer circuitry 506a of the transmit signal path may be configured for super-heterodyne operation.

[0034] In some embodiments, the output baseband signals and the input baseband signals may be analog baseband signals, although the scope of the embodiments is not limited in this respect. In some alternate embodiments, the output baseband signals and the input baseband signals may be digital baseband signals. In these alternate embodiments, the RF circuitry 506 may include analog-to-digital converter (ADC) and digital-to-analog converter (DAC) circuitry and the baseband circuitry 504 may include a digital baseband interface to communicate with the RF circuitry 506.

[0035] In some dual-mode embodiments, a separate radio IC circuitry may be provided for processing signals for each spectrum, although the scope of the embodiments is not limited in this respect.

[0036] In some embodiments, the synthesizer circuitry 506d may be a fractional-N synthesizer or a fractional $N/N+1$ synthesizer, although the scope of the embodiments is not limited in this respect as other types of frequency synthesizers may be suitable. For example, synthesizer circuitry 506d may be a delta-sigma synthesizer, a frequency multiplier, or a synthesizer comprising a phase-locked loop with a frequency divider.

[0037] The synthesizer circuitry 506d may be configured to synthesize an output frequency for use by the mixer circuitry 506a of the RF circuitry 506 based on a frequency input and a divider control input. In some embodiments, the synthesizer circuitry 506d may be a fractional $N/N+1$ synthesizer.

[0038] In some embodiments, frequency input may be provided by a voltage controlled oscillator (VCO), although that is not a requirement. Divider control input may be provided by either the baseband circuitry 504 or the applications processor 502 depending on the desired output frequency. In some embodiments, a divider control input (e.g., N) may be determined from a look-up table based on a channel indicated by the applications processor 502.

[0039] Synthesizer circuitry 506d of the RF circuitry 506 may include a divider, a delay-locked loop (DLL), a multiplexer and a phase accumulator. In some embodiments, the divider may be a dual modulus divider (DMD) and the phase accumulator may be a digital phase accumulator (DPA). In some embodiments, the DMD may be configured to divide the input signal by either N or N+1 (e.g., based on a carry out) to provide a fractional division ratio. In some example embodiments, the DLL may include a set of cascaded, tunable, delay elements, a phase detector, a charge pump and a D-type flip-flop. In these embodiments, the delay elements may be configured to break a VCO period up into Nd equal packets of phase, where Nd is the number of delay elements in the delay line. In this way, the DLL provides negative feedback to help ensure that the total delay through the delay line is one VCO cycle.

[0040] In some embodiments, synthesizer circuitry 506d may be configured to generate a carrier frequency as the output frequency, while in other embodiments, the output frequency may be a multiple of the carrier frequency (e.g., twice the carrier frequency, four times the carrier frequency) and used in conjunction with quadrature generator and divider circuitry to generate multiple signals at the carrier frequency with multiple different phases with respect to each other. In some embodiments, the output frequency may be a LO frequency (f_{LO}). In some embodiments, the RF circuitry 506 may include an IQ/polar converter.

[0041] FEM circuitry 508 may include a receive signal path which may include circuitry configured to operate on RF signals received from one or more antennas 510, amplify the received signals and provide the amplified versions of the received signals to the RF circuitry 506 for further processing. FEM circuitry 508 may also include a transmit signal path which may include circuitry configured to amplify signals for transmission provided by the RF circuitry 506 for transmission by one or more of the one or more antennas 510.

[0042] In some embodiments, the FEM circuitry 508 may include a TX/RX switch to switch between transmit mode and receive mode operation. The FEM circuitry may include a receive signal path and a transmit signal path. The receive signal path of the FEM circuitry may include a low-noise amplifier (LNA) to amplify received RF signals and provide the amplified received RF signals as an output (e.g., to the RF circuitry 506). The transmit signal path of the FEM circuitry 508 may include a power amplifier (PA) to amplify input RF signals (e.g., provided by RF circuitry

506), and one or more filters to generate RF signals for subsequent transmission (e.g., by one or more of the one or more antennas 510).

[0043] In some embodiments, the UE device 500 may include additional elements such as, for example, memory/storage, display, camera, sensor, and/or input/output (I/O) interface.

[0044] Example Machine Description

[0045] Fig. 6 illustrates a block diagram of an example machine 600 upon which any one or more of the techniques (e.g., methodologies) discussed herein may perform. In alternative embodiments, the machine 600 may operate as a standalone device or may be connected (e.g., networked) to other machines. In a networked deployment, the machine 600 may operate in the capacity of a server machine, a client machine, or both in server-client network environments. In an example, the machine 600 may act as a peer machine in peer-to-peer (P2P) (or other distributed) network environment. The machine 600 may be a user equipment (UE), evolved Node B (eNB), Wi-Fi access point (AP), Wi-Fi station (STA), personal computer (PC), a tablet PC, a set-top box (STB), a personal digital assistant (PDA), a mobile telephone, a smart phone, a web appliance, a network router, switch or bridge, or any machine capable of executing instructions (sequential or otherwise) that specify actions to be taken by that machine. Further, while only a single machine is illustrated, the term “machine” shall also be taken to include any collection of machines that individually or jointly execute a set (or multiple sets) of instructions to perform any one or more of the methodologies discussed herein, such as cloud computing, software as a service (SaaS), other computer cluster configurations.

[0046] Examples, as described herein, may include, or may operate on, logic or a number of components, modules, or mechanisms. Modules are tangible entities (e.g., hardware) capable of performing specified operations and may be configured or arranged in a certain manner. In an example, circuits may be arranged (e.g., internally or with respect to external entities such as other circuits) in a specified manner as a module. In an example, the whole or part of one or more computer systems (e.g., a standalone, client or server computer system) or one or more hardware processors may be configured by firmware or software (e.g., instructions, an application portion, or an application) as a module that operates to perform specified operations. In an example, the software may reside on a machine readable

medium. In an example, the software, when executed by the underlying hardware of the module, causes the hardware to perform the specified operations.

[0047] Accordingly, the term “module” is understood to encompass a tangible entity, be that an entity that is physically constructed, specifically configured (e.g.,
5 hardwired), or temporarily (e.g., transitorily) configured (e.g., programmed) to operate in a specified manner or to perform part or all of any operation described herein. Considering examples in which modules are temporarily configured, each of the modules need not be instantiated at any one moment in time. For example, where the modules comprise a general-purpose hardware processor configured
10 using software, the general-purpose hardware processor may be configured as respective different modules at different times. Software may accordingly configure a hardware processor, for example, to constitute a particular module at one instance of time and to constitute a different module at a different instance of time.

[0048] Machine (e.g., computer system) 600 may include a hardware processor 602
15 (e.g., a central processing unit (CPU), a graphics processing unit (GPU), a hardware processor core, or any combination thereof), a main memory 604 and a static memory 606, some or all of which may communicate with each other via an interlink (e.g., bus) 608. The machine 600 may further include a display unit 610, an alphanumeric input device 612 (e.g., a keyboard), and a user interface (UI)
20 navigation device 614 (e.g., a mouse). In an example, the display unit 610, input device 612 and UI navigation device 614 may be a touch screen display. The machine 600 may additionally include a storage device (e.g., drive unit) 616, a signal generation device 618 (e.g., a speaker), a network interface device 620, and one or more sensors, such as a global positioning system (GPS) sensor, compass,
25 accelerometer, or other sensor. The machine 600 may include an output controller 628, such as a serial (e.g., universal serial bus (USB), parallel, or other wired or wireless (e.g., infrared (IR), near field communication (NFC), etc.) connection to communicate or control one or more peripheral devices (e.g., a printer, card reader, etc.).

[0049] The storage device 616 may include a machine readable medium 622 on
30 which is stored one or more sets of data structures or instructions 624 (e.g., software) embodying or utilized by any one or more of the techniques or functions described herein. The instructions 624 may also reside, completely or at least partially, within the main memory 604, within static memory 606, or within the

hardware processor 602 during execution thereof by the machine 600. In an example, one or any combination of the hardware processor 602, the main memory 604, the static memory 606, or the storage device 616 may constitute machine readable media.

- 5 [0050] While the machine readable medium 622 is illustrated as a single medium, the term "machine readable medium" may include a single medium or multiple media (e.g., a centralized or distributed database, and/or associated caches and servers) configured to store the one or more instructions 624.

- [0051] The term "machine readable medium" may include any medium that is
10 capable of storing, encoding, or carrying instructions for execution by the machine 600 and that cause the machine 600 to perform any one or more of the techniques of the present disclosure, or that is capable of storing, encoding or carrying data structures used by or associated with such instructions. Non-limiting machine readable medium examples may include solid-state memories, and optical and
15 magnetic media. Specific examples of machine readable media may include: non-volatile memory, such as semiconductor memory devices (e.g., Electrically Programmable Read-Only Memory (EPROM), Electrically Erasable Programmable Read-Only Memory (EEPROM)) and flash memory devices; magnetic disks, such as internal hard disks and removable disks; magneto-optical disks; Random Access
20 Memory (RAM); and CD-ROM and DVD-ROM disks. In some examples, machine readable media may include non-transitory machine readable media. In some examples, machine readable media may include machine readable media that is not a transitory propagating signal.

- [0052] The instructions 624 may further be transmitted or received over a
25 communications network 626 using a transmission medium via the network interface device 620 utilizing any one of a number of transfer protocols (e.g., frame relay, internet protocol (IP), transmission control protocol (TCP), user datagram protocol (UDP), hypertext transfer protocol (HTTP), etc.). Example communication networks may include a local area network (LAN), a wide area network (WAN), a
30 packet data network (e.g., the Internet), mobile telephone networks (e.g., cellular networks), Plain Old Telephone (POTS) networks, and wireless data networks (e.g., Institute of Electrical and Electronics Engineers (IEEE) 802.11 family of standards known as Wi-Fi®, IEEE 802.16 family of standards known as WiMax®, IEEE 802.16.4 family of standards, a Long Term Evolution (LTE) family of standards, a

Universal Mobile Telecommunications System (UMTS) family of standards, peer-to-peer (P2P) networks, among others. In an example, the network interface device 620 may include one or more physical jacks (e.g., Ethernet, coaxial, or phone jacks) or one or more antennas to connect to the communications network 626. In an example, the network interface device 620 may include a plurality of antennas to wirelessly communicate using at least one of single-input multiple-output (SIMO), multiple-input multiple-output (MIMO), or multiple-input single-output (MISO) techniques. In some examples, the network interface device 620 may wirelessly communicate using Multiple User MIMO techniques. The term “transmission medium” shall be taken to include any intangible medium that is capable of storing, encoding or carrying instructions for execution by the machine 600, and includes digital or analog communications signals or other intangible medium to facilitate communication of such software.

[0053] Additional notes and examples:

[0054] Example 1 is an apparatus of an interfering evolved Node B (eNB), the apparatus comprising: memory; and processing circuitry, to: receive channel state information (CSI), over an X2 interface from a serving eNB, associated with a CSI reference signal (CSI-RS) from a user equipment (UE) served by the serving eNB, the CSI-RS associated with the interfering eNB; determine beamforming restrictions for UE served by the interfering eNB based upon the CSI from the serving eNB; and encode the beamforming restriction information for transmission to the UE served by the interfering eNB.

[0055] In Example 2, the subject matter of Example 1 optionally includes the CSI comprising a precoding matrix indicator (PMI).

[0056] In Example 3, the subject matter of any one or more of Examples 1–2 optionally include the CSI comprising a channel quality indicator (CQI).

[0057] In Example 4, the subject matter of any one or more of Examples 1–3 optionally include the CSI comprising a rank indicator (RI).

[0058] In Example 5, the subject matter of any one or more of Examples 1–4 optionally include the CSI comprising a precoding type indicator (PTI).

[0059] In Example 6, the subject matter of any one or more of Examples 1–5 optionally include the CSI comprising a CSI-RS resource indicator.

[0060] In Example 7, the subject matter of any one or more of Examples 1–6 optionally include component of a precoding matrix indicator (PMI) from the

second eNB 7 is missing parent: 8. The apparatus of Example 1, the processing circuitry to determine non-zero power (NZP) power offsets (Pc) of the CSI-RS.

[0061] In Example 8, the subject matter of Example undefined optionally includes , the processing circuitry to prepare to transmit the NZP Pc to the UE.

- 5 [0062] In Example 9, the subject matter of any one or more of Examples 1–8 optionally include the beamforming restriction information comprising power offsets assigned to different non-zero power (NZP) CSI-RS.

- [0063] In Example 10, the subject matter of Example 9 optionally includes the beamforming restriction information comprising NZP CSI-RS parameters, the
10 NZP CSI-RS parameters comprising one or more of a number of antenna ports, a subframe configuration, a resource configuration, or a scrambling identity.

[0064] In Example 11, the subject matter of any one or more of Examples 1–10 optionally include the beamforming restriction information comprising a codebookSubsetRestriction parameter.

- 15 [0065] Example 12 is a computer-readable medium comprising instructions to cause an interfering enhanced node B (eNB), upon execution of the instructions by processing circuitry of the serving eNB, to: receive channel state information (CSI), over an X2 interface from a serving eNB, associated with a CSI reference signal (CSI-RS) from a user equipment (UE) served by the serving eNB, the CSI-
20 RS associated with the interfering eNB; determine beamforming restrictions for UE served by the interfering eNB based upon the CSI from the serving eNB; and encode the beamforming restriction information for transmission to the UE served by the interfering eNB.

- [0066] In Example 13, the subject matter of Example 12 optionally includes
25 the CSI comprising one of a precoding matrix indicator (PMI), a channel quality indicator (CQI), a rank indicator (RI), a precoding type indicator (PTI), or a CSI-RS resource indicator (CRI).

- [0067] In Example 14, the subject matter of any one or more of Examples 12–13 optionally include component of a precoding matrix indicator (PMI) from the
30 second eNB.

[0068] In Example 15, the subject matter of any one or more of Examples 12–14 optionally include the processing circuitry to determine non-zero power (NZP) power offsets (Pc) of the CSI-RS.

[0069] In Example 16, the subject matter of any one or more of Examples 12–15 optionally include the beamforming restriction information comprising power offsets assigned to different non-zero power (NRP) CSI-RS.

5 [0070] In Example 17, the subject matter of any one or more of Examples 12–16 optionally include the beamforming restriction information comprising a codebookSubsetRestriction parameter.

[0071] Example 18 is an apparatus of a serving evolved Node B (eNB), the apparatus comprising: memory; and processing circuitry, to: receive channel state information (CSI) from a user equipment (UE), the CSI associated with a CSI resource (CSI-RS), the CSI-RS associated with an interfering eNB; determine beamforming restriction for the interfering eNB information based on the CSI; and transmit the beamforming restriction information to the interfering eNB over an X2 interface.

15 [0072] In Example 19, the subject matter of Example 18 optionally includes the CSI comprising one of a precoding matrix indicator (PMI), a channel quality indicator (CQI), a rank indicator (RI), a precoding type indicator (PTI), or a CSI-RS resource indicator (CRI).

[0073] In Example 20, the subject matter of any one or more of Examples 18–19 optionally include component of a precoding matrix indicator (PMI) to the serving eNB.

[0074] In Example 21, the subject matter of any one or more of Examples 18–20 optionally include the beamforming restriction information comprising power offsets assigned to different non-zero power (NRP) CSI-RS.

25 [0075] In Example 22, the subject matter of Example 21 optionally includes the beamforming restriction information comprising NRP CSI-RS parameters, the NRP CSI-RS parameters comprising one or more of a number of antenna ports, a subframe configuration, a resource configuration, or a scrambling identity.

[0076] In Example 23, the subject matter of any one or more of Examples 18–22 optionally include the beamforming restriction information comprising a codebookSubsetRestriction parameter.

30 [0077] Example 24 is a computer-readable medium comprising instructions to cause an interfering enhanced node B (eNB), upon execution of the instructions by processing circuitry of the aggressor eNB, to: receive channel state information (CSI) from a user equipment (UE), the CSI associated with a CSI resource (CSI-

RS), the CSI-RS associated with an interfering eNB; determine beamforming restriction for the interfering eNB information based on the CSI; and transmit the beamforming restriction information to the interfering eNB over an X2 interface.

5 [0078] In Example 25, the subject matter of Example 24 optionally includes the CSI comprising one of a precoding matrix indicator (PMI), a channel quality indicator (CQI), a rank indicator (RI), a precoding type indicator (PTI), or a CSI-RS resource indicator (CRI).

10 [0079] In Example 26, the subject matter of any one or more of Examples 24–25 optionally include the processing circuitry to prepare to transmit non-zero power (NZP) power offsets (P_c) of the CSI-RS from to the serving eNB.

[0080] In Example 27, the subject matter of any one or more of Examples 24–26 optionally include the beamforming restriction information comprising power offsets assigned to different non-zero power (NZP) CSI-RS.

15 [0081] In Example 28, the subject matter of any one or more of Examples 24–27 optionally include the beamforming restriction information comprising a codebookSubsetRestriction parameter.

[0082] Example 29 is an apparatus of an interfering evolved Node B (eNB), comprising means for performing the functions performed by the memory and processing circuitry of any of Examples 1 through 11.

20 [0083] Example 30 is an apparatus of a serving evolved Node B (eNB), comprising means for performing the functions performed by the memory and processing circuitry of any of Examples 18 through 23.

[0084] The above detailed description includes references to the accompanying drawings, which form a part of the detailed description. The drawings show, by way of illustration, specific embodiments that may be practiced. These
25 embodiments are also referred to herein as “examples.” Such examples may include elements in addition to those shown or described. However, also contemplated are examples that include the elements shown or described. Moreover, also contemplated are examples using any combination or permutation of those elements
30 shown or described (or one or more aspects thereof), either with respect to a particular example (or one or more aspects thereof), or with respect to other examples (or one or more aspects thereof) shown or described herein.

[0085] Publications, patents, and patent documents referred to in this document are incorporated by reference herein in their entirety, as though individually

incorporated by reference. In the event of inconsistent usages between this document and those documents so incorporated by reference, the usage in the incorporated reference(s) are supplementary to that of this document; for irreconcilable inconsistencies, the usage in this document controls.

[0086] In this document, the terms “a” or “an” are used, as is common in patent documents, to include one or more than one, independent of any other instances or usages of “at least one” or “one or more.” In this document, the term “or” is used to refer to a nonexclusive or, such that “A or B” includes “A but not B,” “B but not A,” and “A and B,” unless otherwise indicated. In the appended claims, the terms “including” and “in which” are used as the plain-English equivalents of the respective terms “comprising” and “wherein.” Also, in the following claims, the terms “including” and “comprising” are open-ended, that is, a system, device, article, or process that includes elements in addition to those listed after such a term in a claim are still deemed to fall within the scope of that claim. Moreover, in the following claims, the terms “first,” “second,” and “third,” etc. are used merely as labels, and are not intended to suggest a numerical order for their objects.

[0087] The embodiments as described above may be implemented in various hardware configurations that may include a processor for executing instructions that perform the techniques described. Such instructions may be contained in a machine-readable medium such as a suitable storage medium or a memory or other
5 processor-executable medium.

[0088] The embodiments as described herein may be implemented in a number of environments such as part of a wireless local area network (WLAN), 3rd Generation Partnership Project (3GPP) Universal Terrestrial Radio Access Network (UTRAN), or Long-Term-Evolution (LTE) or a Long-Term-Evolution (LTE) communication
10 system, although the scope of the disclosure is not limited in this respect. An example LTE system includes a number of mobile stations, defined by the LTE specification as User Equipment (UE), communicating with a base station, defined by the LTE specifications as an eNB.

[0089] Antennas referred to herein may comprise one or more directional or
15 omnidirectional antennas, including, for example, dipole antennas, monopole antennas, patch antennas, loop antennas, microstrip antennas or other types of antennas suitable for transmission of RF signals. In some embodiments, instead of two or more antennas, a single antenna with multiple apertures may be used. In

these embodiments, each aperture may be considered a separate antenna. In some multiple-input multiple-output (MIMO) embodiments, antennas may be effectively separated to take advantage of spatial diversity and the different channel characteristics that may result between each of antennas and the antennas of a transmitting station. In some MIMO embodiments, antennas may be separated by up to 1/10 of a wavelength or more.

[0090] In some embodiments, a receiver as described herein may be configured to receive signals in accordance with specific communication standards, such as the Institute of Electrical and Electronics Engineers (IEEE) standards including IEEE 802.11 standards and/or proposed specifications for WLANs, although the scope of the disclosure is not limited in this respect as they may also be suitable to transmit and/or receive communications in accordance with other techniques and standards. In some embodiments, the receiver may be configured to receive signals in accordance with the IEEE 802.16-2004, the IEEE 802.16(e) and/or IEEE 802.16(m) standards for wireless metropolitan area networks (WMANs) including variations and evolutions thereof, although the scope of the disclosure is not limited in this respect as they may also be suitable to transmit and/or receive communications in accordance with other techniques and standards. In some embodiments, the receiver may be configured to receive signals in accordance with the Universal Terrestrial Radio Access Network (UTRAN) LTE communication standards. For more information with respect to the IEEE 802.11 and IEEE 802.16 standards, please refer to "IEEE Standards for Information Technology -- Telecommunications and Information Exchange between Systems" - Local Area Networks - Specific Requirements - Part 11 "Wireless LAN Medium Access Control (MAC) and Physical Layer (PHY), ISO/IEC 8802-11: 1999", and Metropolitan Area Networks - Specific Requirements - Part 16: "Air Interface for Fixed Broadband Wireless Access Systems," May 2005 and related amendments/versions. For more information with respect to UTRAN LTE standards, see the 3rd Generation Partnership Project (3GPP) standards for UTRAN-LTE, including variations and evolutions thereof.

[0091] The above description is intended to be illustrative, and not restrictive. For example, the above-described examples (or one or more aspects thereof) may be used in combination with others. Other embodiments may be used, such as by one of ordinary skill in the art upon reviewing the above description. The Abstract is to

allow the reader to quickly ascertain the nature of the technical disclosure. It is submitted with the understanding that it will not be used to interpret or limit the scope or meaning of the claims. Also, in the above Detailed Description, various features may be grouped together to streamline the disclosure. However, the claims may not set forth every feature disclosed herein as embodiments may feature a subset of said features. Further, embodiments may include fewer features than those disclosed in a particular example. Thus, the following claims are hereby incorporated into the Detailed Description, with a claim standing on its own as a separate embodiment. The scope of the embodiments disclosed herein is to be determined with reference to the appended claims, along with the full scope of equivalents to which such claims are entitled.

CLAIMS

1. An apparatus of an interfering evolved Node B (eNB), the apparatus comprising:
5 memory to store beamforming restriction information; and
processing circuitry, to:
receive channel state information (CSI), over an X2 interface from a
serving eNB, associated with a CSI reference signal (CSI-RS) from a
user equipment (UE) served by the serving eNB, the CSI-RS associated
10 with the interfering eNB;
determine beamforming restriction information for a UE served by
the interfering eNB based upon the CSI from the serving eNB; and
encode the beamforming restriction information for transmission to
the UE served by the interfering eNB.
- 15 2. The apparatus of claim 1, the CSI comprising a precoding matrix
indicator (PMI).
3. The apparatus of claim 1, the CSI comprising a channel quality indicator
(CQI).
4. The apparatus of claim 1, the CSI comprising a rank indicator (RI).
- 20 5. The apparatus of claim 1, the CSI comprising a precoding type indicator
(PTI).
6. The apparatus of claim 1, the CSI comprising a CSI-RS resource
indicator.
7. The apparatus of claim 1, the CSI comprising a W1 component of a
25 precoding matrix indicator (PMI) from the second eNB
8. The apparatus of claim 1, the processing circuitry to determine non-zero
power (NZP) power offsets (Pc) of the CSI-RS.

9. The apparatus of claim 8, the processing circuitry to prepare to transmit the NZP Pc to the UE.
10. The apparatus of claim 1, the beamforming restriction information comprising power offsets assigned to different non-zero power (NZP) CSI-RS.
11. The apparatus of claim 10, the beamforming restriction information comprising NZP CSI-RS parameters, the NZP CSI-RS parameters comprising one or more of a number of antenna ports, a subframe configuration, a resource configuration, or a scrambling identity.
12. The apparatus of claim 1, the beamforming restriction information comprising a codebookSubsetRestriction parameter.
13. An apparatus of a serving evolved Node B (eNB), the apparatus comprising:
memory to store beamforming restriction information; and
processing circuitry, to:
 receive channel state information (CSI) from a user equipment (UE),
 the CSI associated with a CSI resource (CSI-RS), the CSI-RS associated with an interfering eNB;
 determine beamforming restriction information for the interfering eNB information based on the CSI; and
 encode the beamforming restriction information for transmission to the interfering eNB over an X2 interface.
14. The apparatus of claim 13, the CSI comprising one of a precoding matrix indicator (PMI), a channel quality indicator (CQI), a rank indicator (RI), a precoding type indicator (PTI), or a CSI-RS resource indicator (CRI).
15. The apparatus of claim 13, the CSI comprising a W1 component of a precoding matrix indicator (PMI) to the serving eNB.

16. The apparatus of claim 13, the beamforming restriction information comprising power offsets assigned to different non-zero power (NZP) CSI-RS.
17. The apparatus of claim 13, the beamforming restriction information comprising NZP CSI-RS parameters, the NZP CSI-RS parameters comprising one or more of a number of antenna ports, a subframe configuration, a resource configuration, or a scrambling identity.
18. The apparatus of claim 13, the beamforming restriction information comprising a codebookSubsetRestriction parameter.
19. An apparatus of an interfering evolved Node B (eNB), comprising means for performing the functions performed by the memory and processing circuitry of any of claims 1 through 12.
20. An apparatus of a serving evolved Node B (eNB), comprising means for performing the functions performed by the memory and processing circuitry of any of claims 13 through 18.

100

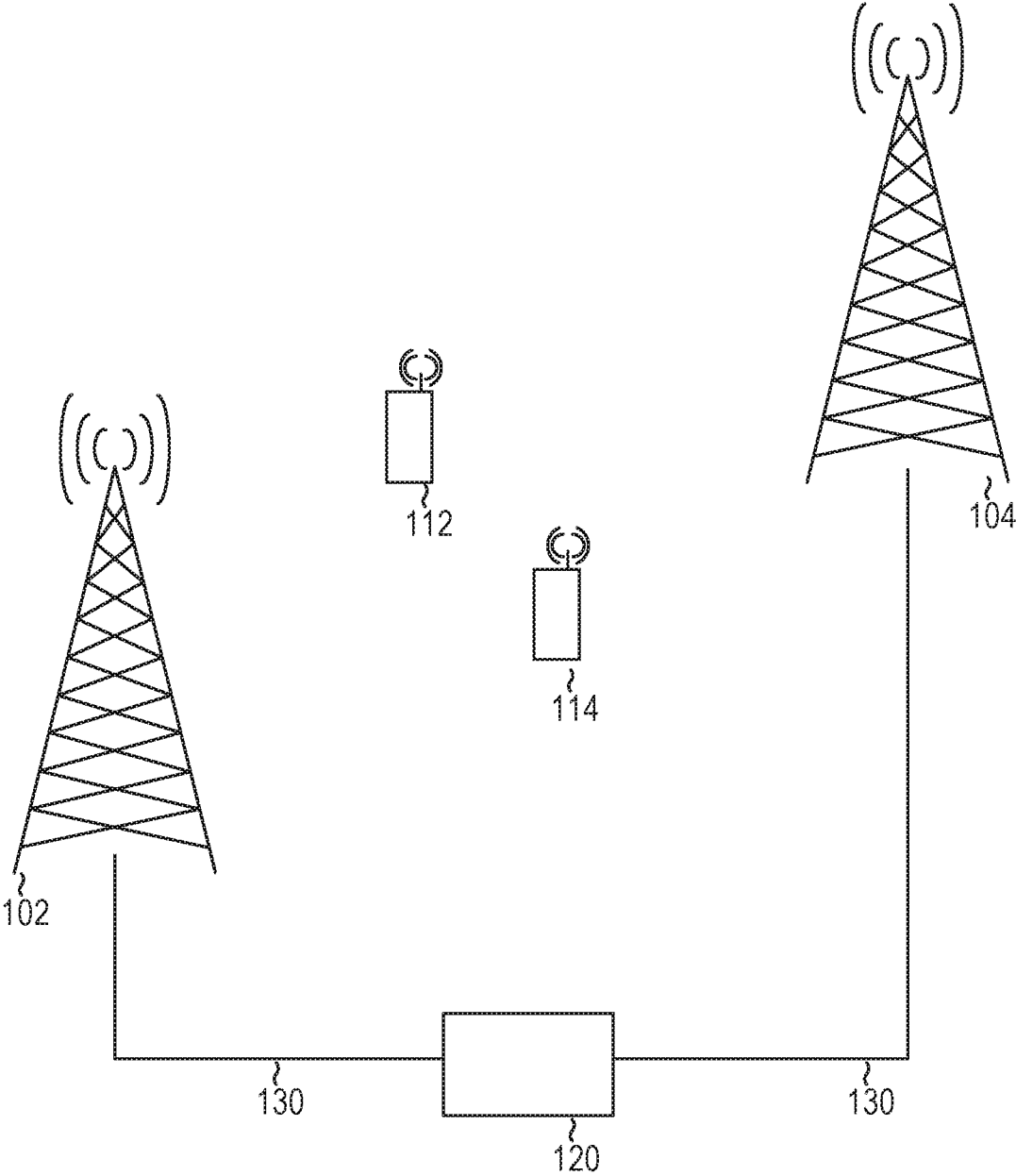


FIG. 1

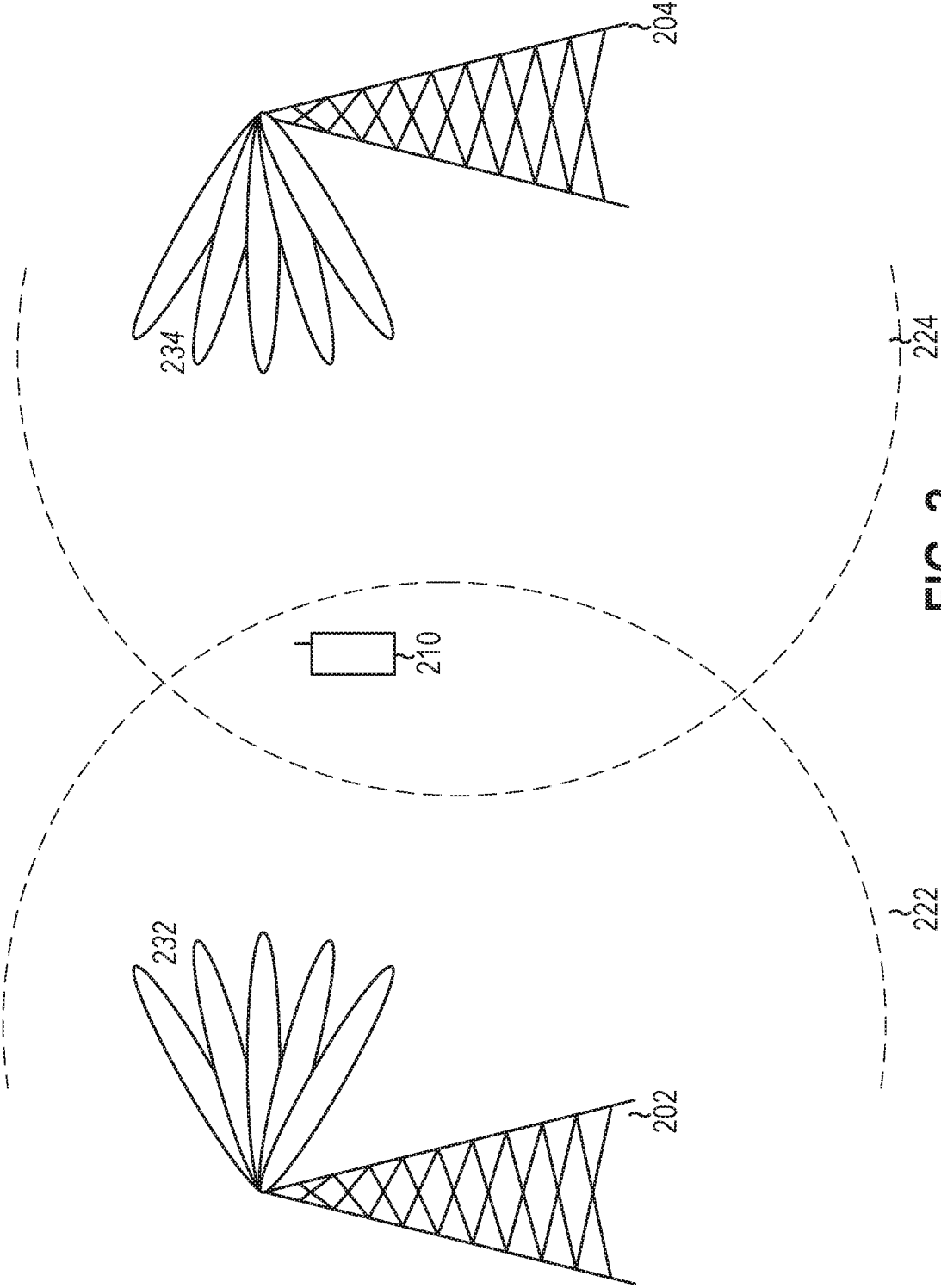


FIG. 2

3/6

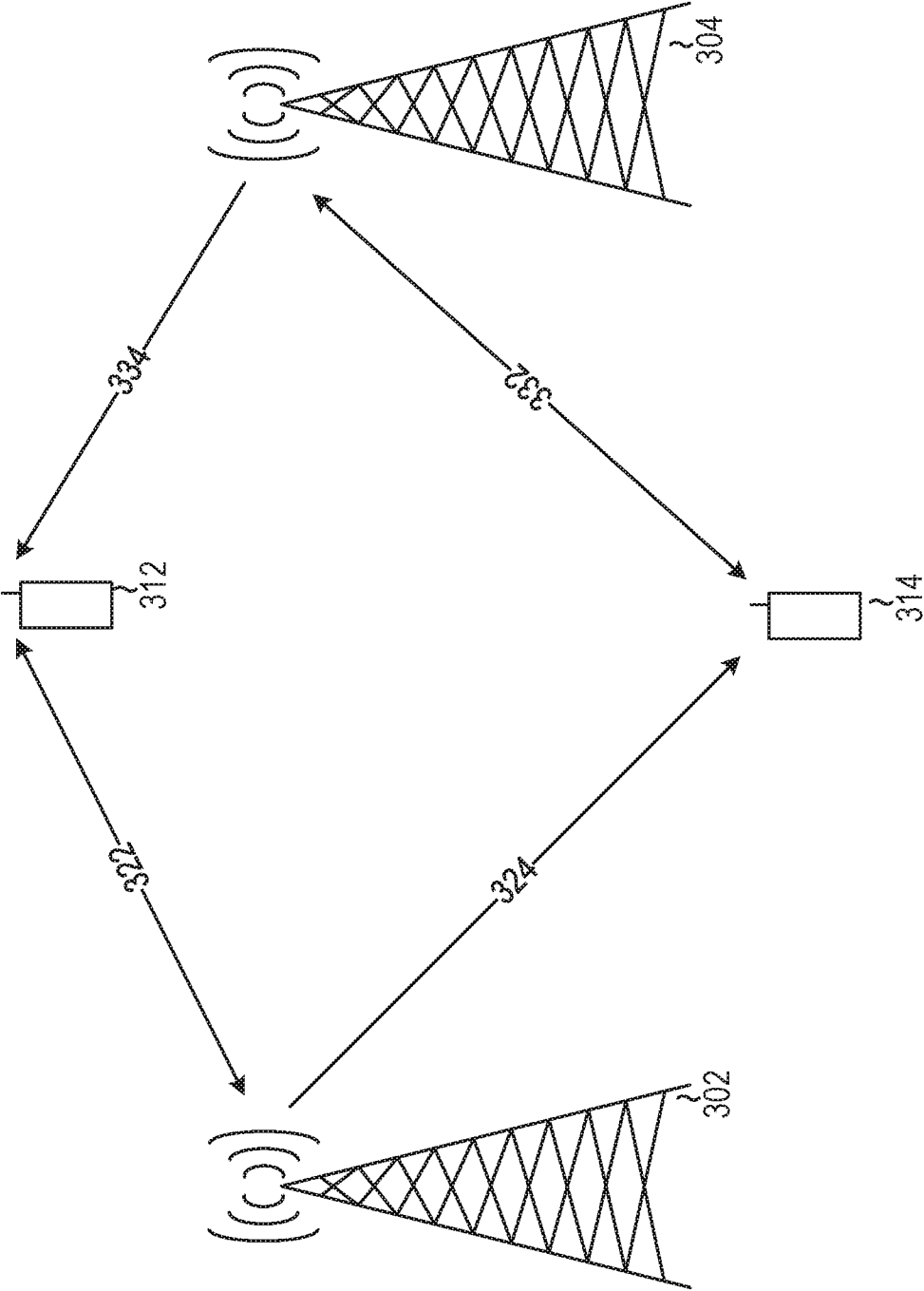


FIG. 3

4/6

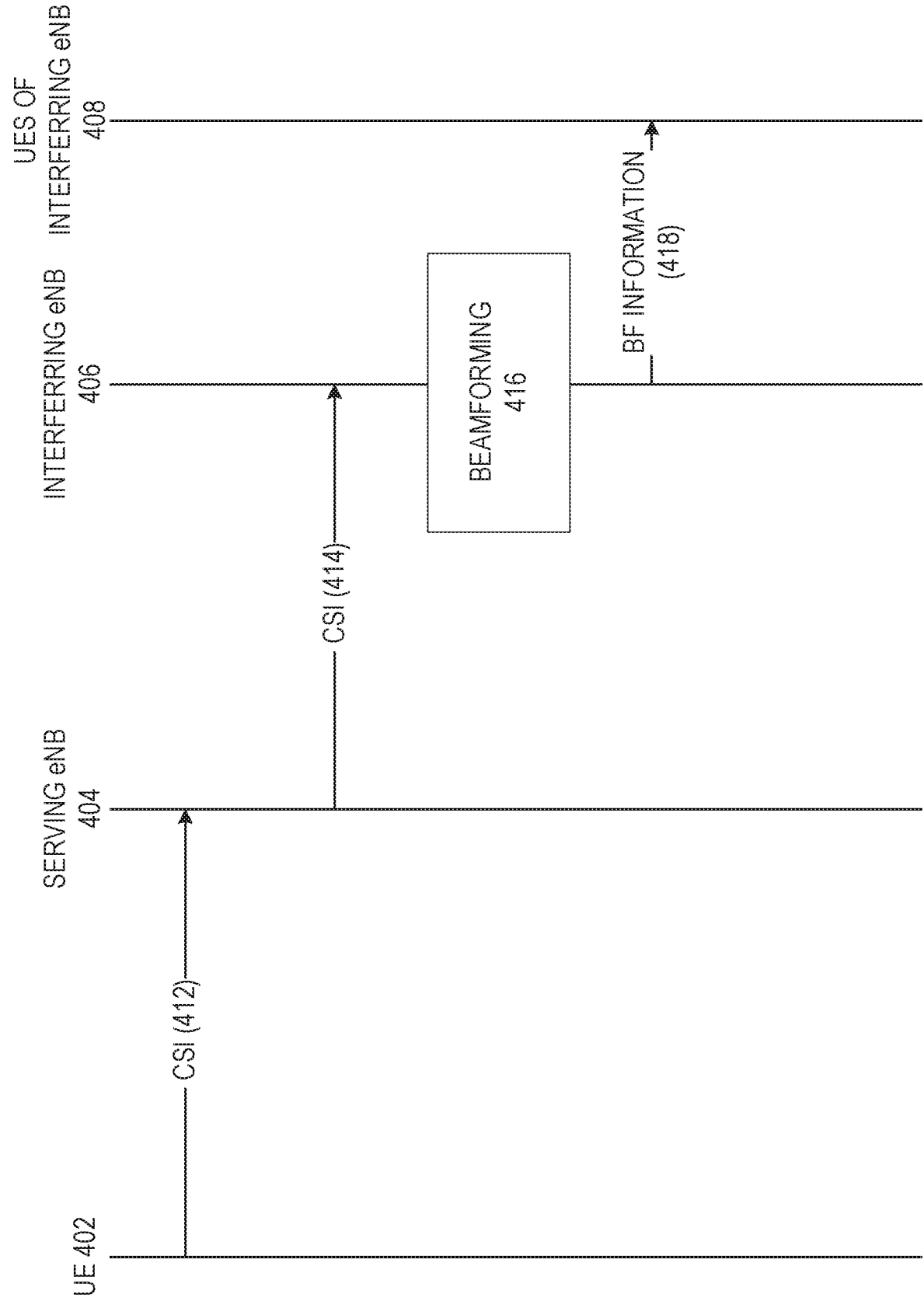


FIG. 4

500

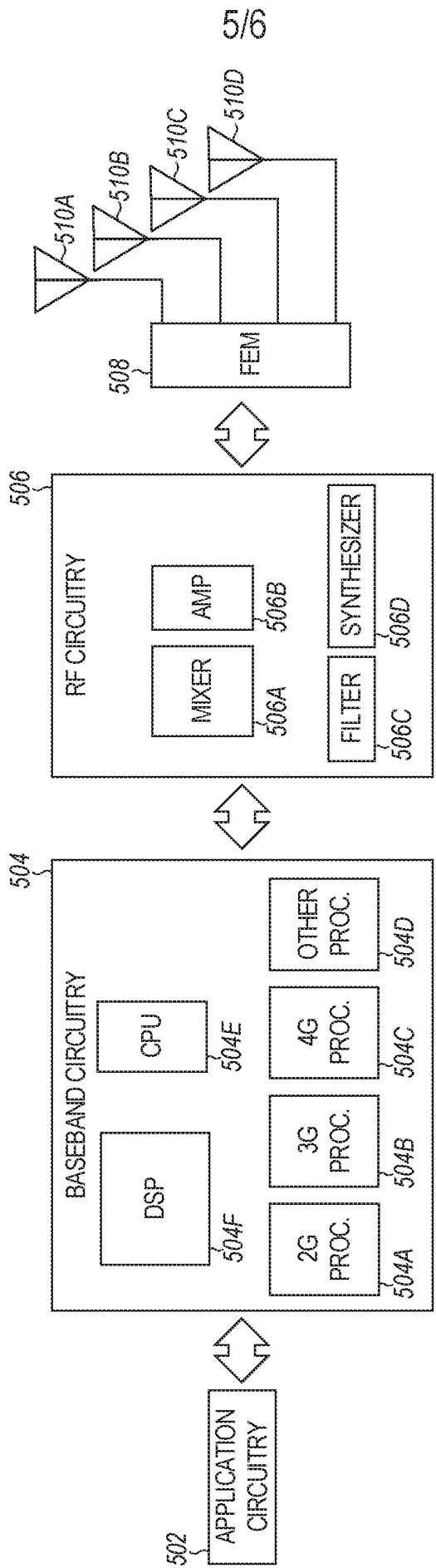


FIG. 5

6/6

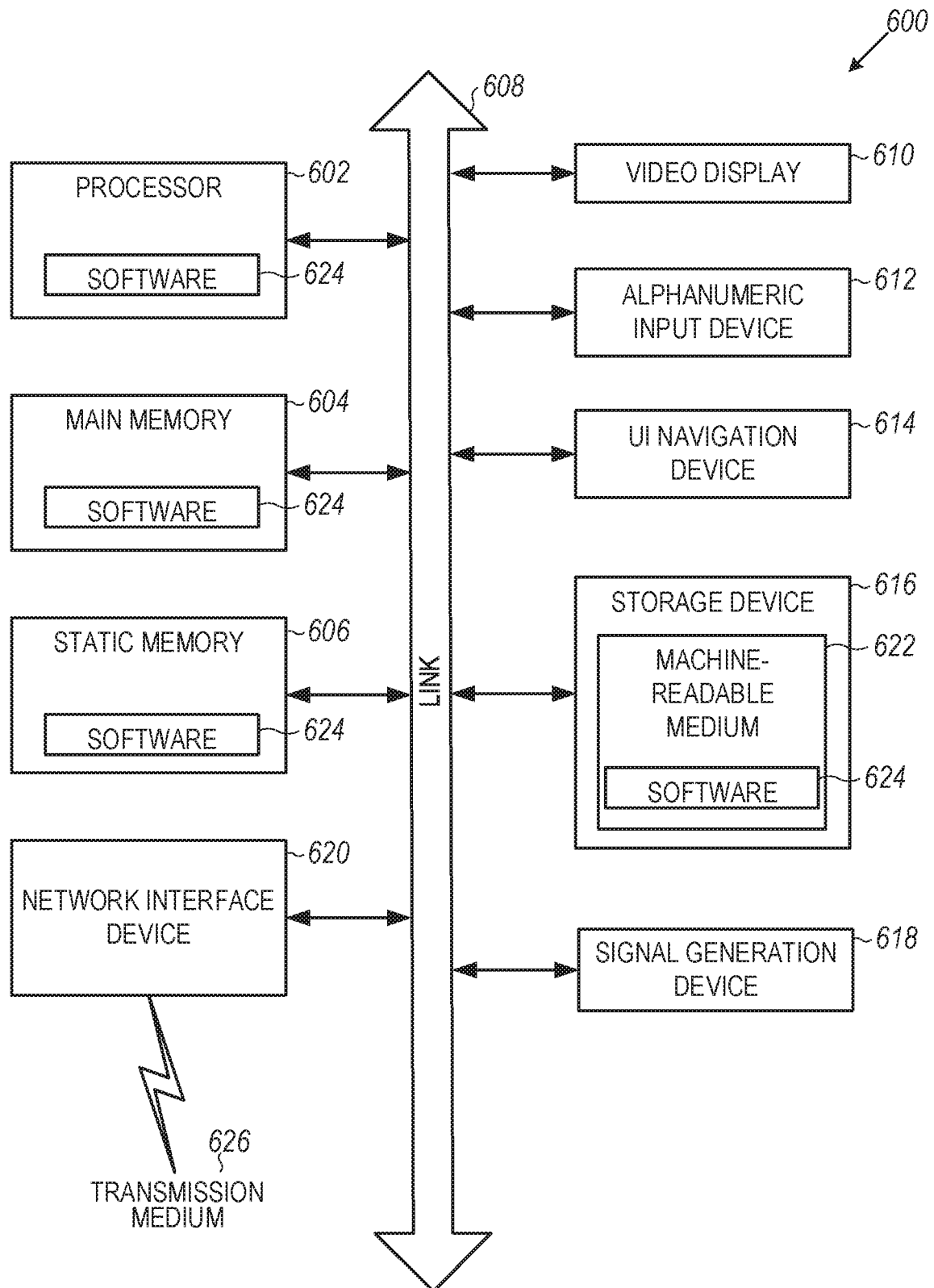


FIG. 6

A. CLASSIFICATION OF SUBJECT MATTER**H04B 7/024(2017.01)i, H04B 7/06(2006.01)i**

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H04B 7/024; H04B 7/06; H04B 7/02; H04B 7/08; H04W 72/04; H04W 72/08

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Korean utility models and applications for utility models

Japanese utility models and applications for utility models

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

eKOMPASS(KIPO internal) & keywords: beamforming restriction, eNB, channel state information (CSI), reference signal, X2 interface, non-zero power (NZP), power offset**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2015-0215934 A1 (INTEL CORPORATION) 30 July 2015	1-7, 12-15, 18-20
Y	See paragraphs [0067]-[0069], [0099], [0128]-[0131]; and claims 1-21.	8-11, 16-17
Y	US 2014-0086084 A1 (FUTUREWEI TECHNOLOGIES, INC.) 27 March 2014	8-11, 16-17
	See paragraphs [0048]-[0059]; and claim 24.	
A	US 2009-0016460 A1 (IN-SOO HWANG et al.) 15 January 2009	1-20
	See paragraphs [0050]-[0094]; and claims 1-12.	
A	US 2016-0105230 A1 (REGENTS OF THE UNIVERSITY OF MINNESOTA) 14 April 2016	1-20
	See paragraphs [0035]-[0041]; and claims 1-10.	
A	US 7676007 B1 (JIHOON CHOI et al.) 09 March 2010	1-20
	See column 12, line 49 - column 13, line 33; and claims 1-18.	



Further documents are listed in the continuation of Box C.



See patent family annex.

* Special categories of cited documents:

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier application or patent but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

29 August 2017 (29.08.2017)

Date of mailing of the international search report

01 September 2017 (01.09.2017)

Name and mailing address of the ISA/KR

International Application Division

Korean Intellectual Property Office

189 Cheongsa-ro, Seo-gu, Daejeon, 35208, Republic of Korea



Facsimile No. +82-42-481-8578

Authorized officer

KANG, Hee Gok

Telephone No. +82-42-481-8264



INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/US2017/032341

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 2015-0215934 A1	30/07/2015	CN 105850225 A EP 3100584 A1 TW 201542017 A WO 2015-116393 A1	10/08/2016 07/12/2016 01/11/2015 06/08/2015
US 2014-0086084 A1	27/03/2014	CN 104662969 A EP 2893753 A1 JP 2015-534396 A WO 2014-052926 A1	27/05/2015 15/07/2015 26/11/2015 03/04/2014
US 2009-0016460 A1	15/01/2009	CN 101369837 A CN 101369837 B EP 1998470 A2 EP 1998470 A3 EP 1998470 B1 JP 04652430 B2 JP 2008-301494 A KR 10-1446700 B1 KR 10-2008-0104999 A US 8284863 B2	18/02/2009 06/02/2013 03/12/2008 02/07/2014 22/02/2017 16/03/2011 11/12/2008 07/10/2014 03/12/2008 09/10/2012
US 2016-0105230 A1	14/04/2016	US 9647745 B2	09/05/2017
US 7676007 B1	09/03/2010	None	