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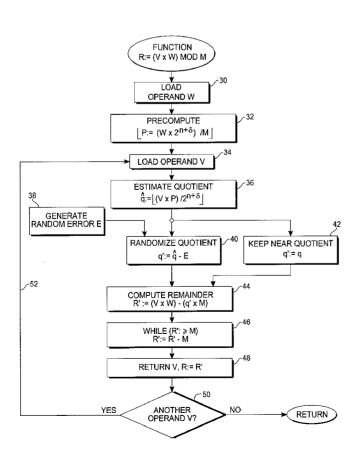
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(54) Title: MODULAR MULTIPLICATION METHOD WITH PRECOMPUTATION USING ONE KNOWN OPERAND



(57) Abstract: A modular multiplication method implemented in an electronic digital processing system takes advantage of the case where one of the operands W is known in advance or used multiple times with different second operands V to speed calculation. The operands V and W and the modulus M may be integers or polynomials over a variable X. A possible choice for the type of polynomials can be polynomials of the binary finite field GF (2^N). Once operand W is loaded (30; 60) into a data storage (12) location, a value $P = Lw-X^{n+\delta}/M$ J is pre-computed (32; 62) by the processing system (10). Then when a second operand V is loaded (34; 64), the quotient q for the product V.W being reduced modulo M is quickly estimated (36; 66), $q^{A} = L_{A}v - P/X^{n-1}$ optionally randomized (40; 70), $q' = q^{\Lambda} - E$, and can be used to obtain (44; 74) the remainder r' = V.W - q'-M, which is congruent to (V.W) mod M. A final reduction (46; 76) can be carried out, and the later steps repeated (52; 82) with other second operands V.



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Declarations under Rule 4.17:

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C. DOCUMENTS CONSIDERED TO BE RELEVANT			
Category*	Citation of document, with indication, where ap	ppropriate, of the relevant passages	Relevant to claim No.
Υ .	US 2005/0149595 A1 (Fischer et al.) 07 July 2005 (07 [0022],[0037],[0009].	.07.2005), para [0014]-[0015],	1-16
Υ .	US 2006/0061795 A1 (Walmsley) 23 March 2006 (23.0 para [0526],[0554],[0559],[0560], [0621],[0653],[0664],		1-16
А	US 2004/0066934 A1 (Chen) 08 April 2004 (08.04.2004), entire document.		1-16
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