EUROPEAN PATENT APPLICATION

Circuit and method for driving light emitting diodes

A LED driver comprises a microcontroller that is adapted to perform at least two of the following functions: LED current regulation; dimming control; thermal management; input power absorption control; input current absorption control; alarms and fault conditions management; and LED light optical feedback.
The invention relates to LED drivers and to a method to drive a LED system, in particular for use with high flux / high power LED systems.

In high flux / high power LED systems, often several functions / functionalities have to be performed to properly operate the LED system, e. g. to perform a LED current regulation, thermal management etc. Up to date, these functions are implemented using analog circuitry or integrated circuits dedicated to each functionality. The implementation requires a relatively large area on a respective printed circuit board (PCB), adds to the complexity of the system and reduces solution flexibility. Also, bill of materials (BOM) and manufacturing / quality costs are expensive.

It is thus the object of the present invention to overcome or improve at least some of the above stated problems.

The object is reached by the LED driver according to claim 1 and the method according to claim 8. Preferred embodiments are especially stated in the dependent claims.

The LED driver comprises a digital microcontroller that is adapted to perform at least two of the following functions:

- LED current regulation;
- dimming control;
- thermal management;
- input power absorption control;
- input current absorption control;
- alarms and fault conditions management; and
- LED light optical feedback.

For this, the microcontroller is either programmed accordingly or hardwired, or both. The use of a single microcontroller gives the advantage of only needing a relatively small PCB area and only needing few components (e. g., omitting many auxiliary parts that are typical for an analog solution). Further, a programmable microcontroller achieves a higher flexibility of operation and reduced adaptation costs which is especially advantageous for the use in different platforms including different power ratings, LED source types, dimming requirements etc. Also, a smart management of auxiliary functions (e.g., optimized structure vs. RFI, active thermal management, alarms and fault condition management etc.) is easily implemented. Additionally, a simpler manufacturing and quality assurance can be achieved.

Advantageously, the LED driver further comprises a connection to a LED current sensor of the overall lighting system and comprises a current converter for converting an input current into an LED (source) current. To drive the converter, the microcontroller comprises a power stage controller. Thus, based on a reading of the LED current sensor, the power stage controller of the microcontroller is enabled to adjust the current converter to set the LED current. This is advantageously done by implementing the LED control components as a control loop. By using the microcontroller, current levels may be flexibly changed or adjusted on demand.

Advantageously, the dimming control includes a 3-wire dimming capability. This can be achieved by accessing a special function of the power stage controller of the microprocessor.

Advantageously, the LED driver further comprises a connection to at least one thermal sensor sensing a temperature related to the LED source(s). The microcontroller comprises a driver circuit for at least one fan. The microcontroller, based on a reading of the thermal sensor(s), is enabled to adjust the operating condition of the fan(s). The temperature sensing design advantageously comprises an analog digital converter device of the microcontroller which advantageously may only need one external resistor divider with a thermistor, for example. Further, only a simple driver circuit for the fan(s) may be implemented. This function may be implemented by dedicated software within the microcontroller.

Advantageously, the microcontroller, for input power absorption control, comprises at least one analog digital converter to sense an input voltage, an output current, and an output voltage, and the microcontroller setting an input power assuming a statistical efficiency value. The sensing may be performed by the ADC devices of the microcontroller.

Input current absorption from the power supply is important for a RFI point of view. To improve input current absorption, advantageously, the microcontroller is adapted to perform a sweep of the DC-DC converter switching frequency to spread noise along a wide range instead of a narrow distribution. This sweep may be implemented into the microcontroller by software.

Advantageously, the alarms and fault conditions management is achieved using interrupts and programming suitable operation logic components.

In a preferred embodiment, a LED driver further comprises a connection to an optical sensor; wherein the microcontroller in turn comprises a power stage controller such that, based on a reading of the optical sensor, the microcontroller is enabled to drive the power stage controller of the microcontroller in order to adjust an intensity of light emitted from at least one LED.

The method of driving a LED system is being performed by a microcontroller and comprises at least two of the following steps:

- regulating a LED current;
- controlling a dimming function;
- performing a thermal management;
- controlling an input power absorption;
- controlling an input current absorption;
- performing an alarms and fault conditions management; and
- controlling the light emitted from the LED by means of optical feedback.
The invention is schematically described in more detail in the following exemplary embodiment. Identical or similar components are denoted by the same reference numerals, where applicable.

FIG 1 shows a block scheme diagram of a lighting system comprising a LED driver according to the invention;

FIG 2 shows a schematic diagram of the driver according to the invention;

FIG 3 shows a layout drawing of the driver according to the invention;

FIG 4 shows a plot of RFI results in a first frequency range;

FIG 5 shows a plot of RFI results in a second frequency range;

FIGs 6-10 show a software embodiment of a LED driver comprising several functions.

FIG 1 shows a block scheme diagram of components of a lighting system comprising at least parts of a LED driver 1 for three LED sources. At the beginning of the operation of the lighting system (activation), a system input voltage \( U = 24 \text{ V} \) is applied to a filter stage 2 after that the input voltage is input into a DC/DC converter 3 that transforms the input voltage of 24 V into a LED current suitable to operate the multiple LED sources 4. The LED current of about 600 mA is sensed by a LED current sense circuitry 5. Subsequently, the sensed current values are transmitted to a comparator 6 of a microcontroller 7. The comparator 6 compares the LED current with a reference current IREF. The result of this comparison, that uses this value to set / drive the DC/DC converter 3 to control the LED current, thus being part of a LED current control loop. The comparator 6 compares the LED current with a reference current value \( I_{REF} \). The result of this comparison, e.g. a value for the current difference, is fed back to a power stage controller section 9 of the microcontroller 7 to control the LED current, thus being part of a LED current control loop.

The LED driver 1 further comprises an input voltage sense circuit 10 fed by the filter stage 2, and an output voltage sense circuit 11 fed by the LED 4. The voltage sense circuits 10, 11 are input into a current and power absorption control section 12.

The microprocessor 7 further, via a thermal control section 13, senses temperature values from the LEDs by means of a temperature sensor 14. The thermal control section 13 further controls a fan driver 14 that in turn drives a fan 15 (e.g., a fan speed) to cool the LED sources 4 to - in turn - adjust their temperature. This can be regarded as a LED source temperature control loop.

The microcontroller 7 also comprises a dimming control section 17 fed by a dimming input 18 and a alarms / fault conditions management section 19. Additionally, a start-up control may be included (not shown).

FIG 2 shows a schematic diagram of the LED driver 1 with a relatively small number of components and a small occupied space. Additionally to a combined filter stage 2 and input voltage sense circuit 10 there is shown the DC/DC converter 3, the LED current sense circuitry 5, the microcontroller 7, the output voltage sense circuit 11, the thermal sense circuitry 14, the fan driver 15, the dimming input 18, LED connections 20, and external connections 21.

FIG 3 shows a layout drawing of the LED driver 1. In addition to the filter stage 2, the DC/DC converter 3, the LED current sense circuitry 5, the microcontroller 7, the input voltage sense circuit 10 the output voltage sense circuit 11, the thermal sense circuitry 14, the fan driver 15, and the dimming input 18, there are shown receiving areas 22 for the LEDs. This layout is very compact due to the relatively small number of components needed to drive the lighting system.

FIG 4 shows RFI results of the lighting system in a plot of dB in \( \mu \text{V} \) over f in MHz in a first frequency range of 9 to 30 MHz with attached testing parameters.

FIG 5 shows RFI results of the lighting system in a plot of dB in \( \mu \text{V} \) over f in MHz in a second frequency range of 30 to 300 MHz.

FIGs 6 to 10 show control functions of a LED driver in a software embodiment. FIG 6 shows the respective flow diagram on a high level, including a query S1 for a dimming / no dimming routine, followed by a power control segment S2 and a frequency sweep segment S3. FIG 7 shows a more detailed view on the no-dim routine S4 that is followed by a power control routine S5 and, on FIG 8, a frequency sweep routine S6. FIG 9 shows the dim routine S7 in greater detail while FIG 10 shows a current control routine S8, followed by a sweep routine S9.

The invention is not limited to the shown exemplary embodiment. For example, the number of light sources could be other than three, e.g., one, two, four, or more. Also, the type of light sources is not limited LEDs but may, for example, also comprise laser diodes. Further, the microcontroller may comprise fewer functions than shown, or more, like an optical intensity control.

List of reference numbers

[0027] LED driver
[0028] Filter stage
[0029] DC/DC converter
[0030] LEDs
[0031] LED current sense circuitry
[0032] Comparator
[0033] Microcontroller
[0034] Current control section
Claims

1. A LED driver (1), comprising a microcontroller (7) that is adapted to perform at least two of the following functions:
   - LED current regulation;
   - dimming control;
   - thermal management;
   - input power absorption control;
   - input current absorption control;
   - alarms and fault conditions management; and
   - LED light optical feedback.

2. The LED driver (1) according to claim 1,
   - further comprising a connection to a LED current sensor; and
   - further comprising a current converter (3) for converting an input voltage into an LED current; and
   - the microcontroller (7) comprising a power stage controller (9),
   - such that, based on a reading of the LED current sensor, the power stage controller (9) of the microcontroller (7) is enabled to adjust the current converter (3) to set the LED current.

3. LED driver (1) according to claim 1 or 2, wherein the dimming control includes a 3-wire dimming capability.

4. The LED driver (1) according to any of the preceding claims,
   - further comprising a connection to a thermal sensor; and
   - the microcontroller (7) comprising a driver circuit (15) for a fan (16),
   - such that, based on a reading of the thermal sensor, the microcontroller (7) is enabled to adjust the operating condition of the fan (16).

5. The LED driver (1) according to any of the preceding claims, wherein the microcontroller (7), for input power absorption control, comprises at least one analog digital converter (10, 11) to sense an input voltage, an output current, and an output voltage, and the microcontroller (7) setting an input power assuming a statistical efficiency value.

6. The LED driver (1) according to any of the preceding claims, wherein the microcontroller (7), for input current absorption control providing reduced RFI, is adapted to perform a sweep of the DC-DC converter switching frequency.

7. The LED driver (1) according to any of the preceding claims:
   - further comprising a connection to an optical sensor; and
   - the microcontroller (7) comprising a power stage controller (9)
   - such that, based on a reading of the optical sensor, the microcontroller (7) is enabled to drive the power stage controller (9) in order to adjust an intensity of light emitted from at least one LED (4).

8. A method of driving a LED system, the method being performed by a microcontroller (6) and comprising at least two of the following steps:
   - regulating a LED current;
   - controlling a dimming function;
   - performing a thermal management;
   - controlling an input power absorption;
   - controlling an input current absorption;
   - performing an alarms and fault conditions management; and
   - controlling the LED emitted light by means of optical feedback

9. The method according to claim 8, wherein the step of regulating a LED current comprises:
   - reading the actual LED current;
   - converting an input voltage into a LED current based on the reading of the actual LED current.
10. The method according to claim 8 or 9, wherein the step of performing a thermal management comprises:

- reading a temperature; and,
- based on the reading of the temperature, adjusting the operating condition of a fan.

11. The method according to any of the claims 8 to 10, wherein the step of controlling the dimming function comprises:

- receiving an external dimming signal; and
- halting the power stage controller (9) for a time defined by the dimming signal.

12. The method according to any of the claims 8 to 11, wherein the step of controlling an input power absorption comprises:

- sensing an input voltage, an output current, and an output voltage; and
- setting an input power assuming a statistical efficiency value.

13. The method according to any of the claims 8 to 12, wherein the step of controlling an input current absorption providing reduced RFI comprises performing a sweep of the DC-DC converter switching frequency.

Amended claims in accordance with Rule 137(2) EPC.

1. A LED driver (1), comprising a microcontroller (7) that is adapted to perform at least the following functions:

- LED current regulation;
- dimming control; and
- thermal management;

wherein

- the LED driver (1) further comprises a connection to a thermal sensor; and
- the microcontroller (7) comprises a driver circuit (15) for a fan (16),
- such that, based on a reading of the thermal sensor, the microcontroller (7) is enabled to adjust the operating condition of the fan (16).

2. The LED driver (1) according to claim 1, comprising an analog-to-digital converter for use with the thermal management function.

3. The LED driver (1) according to claim 1 or 2,

- further comprising a connection to a LED current sensor; and
- further comprising a current converter (3) for converting an input voltage into an LED current; and
- the microcontroller (7) comprising a power stage controller (9),
- such that, based on a reading of the LED current sensor, the power stage controller (9) of the microcontroller (6) is enabled to adjust the current converter (3) to set the LED current.

4. LED driver (1) according to any of the preceding claims, wherein the dimming control includes a 3-wire dimming capability.

5. The LED driver (1) according to any of the preceding claims, the microcontroller (7) further comprising the function of input power absorption control.

6. The LED driver (1) according to claim 5, wherein the microcontroller (7), for input power absorption control, comprises at least one analog digital converter (10, 11) to sense an input voltage, an output current, and an output voltage, and the microcontroller (7) setting an input power assuming a statistical efficiency value.

7. The LED driver (1) according to any of the preceding claims, the microcontroller (7) further comprising the function of input current absorption control.

8. The LED driver (1) according claim 7, wherein the microcontroller (7), for a input current absorption control providing reduced RFI, is adapted to perform a sweep of the DC-DC converter switching frequency.

9. The LED driver (1) according to any of the preceding claims, the microcontroller (7) further comprising the function of alarms and fault conditions management.

10. The LED driver (1) according to any of the preceding claims, the microcontroller (7) further comprising the function of LED light optical feedback.

11. The LED driver (1) according claim 10:

- further comprising a connection to an optical sensor; and
- the microcontroller (7) comprising a power stage controller (9)
- such that, based on a reading of the optical sensor, the microcontroller (7) is enabled to drive the power stage controller (9) in order to
adjust an intensity of light emitted from at least one LED (4).

12. A method of driving a LED system, the method being performed by a microcontroller (6) and comprising at least the following steps:

- regulating a LED current;
- controlling a dimming function; and
- performing a thermal management;

wherein the step of performing a thermal management comprises:

- reading a temperature; and,
- based on the reading of the temperature, adjusting the operating condition of a fan.

13. The method according to claim 12, wherein the step of regulating a LED current comprises:

- reading the actual LED current;
- converting an input voltage into a LED current based on the reading of the actual LED current.

14. The method according to any of the claims 12 to 13, wherein the step of controlling the dimming function comprises:

- receiving an external dimming signal; and
- halting the power stage controller (9) for a time defined by the dimming signal.

15. The method according to any of the claims 12 to 14, further comprising the step of:

- controlling an input power absorption.

16. The method according to claim 15, wherein the step of controlling an input power absorption comprises:

- sensing an input voltage, an output current, and an output voltage; and
- setting an input power assuming a statistical efficiency value.

17. The method according to any of the claims 12 to 16, further comprising the step of:

- controlling an input current absorption.

18. The method according to claim 17, wherein the step of controlling an input current absorption providing reduced RFI comprises performing a sweep of the DC-DC converter switching frequency.

19. The method according to any of the claims 12 to 18, further comprising the step of:

- performing an alarms and fault conditions management.

20. The method according to any of the claims 12 to 19, further comprising the step of:

- controlling the LED emitted light by means of optical feedback.

21. The method according to claim 20, wherein the step of controlling the LED emitted light by means of optical feedback comprises:

- reading of the optical sensor; and
- based on that reading, driving the power stage controller (9) in order to adjust an intensity of light emitted from at least one LED (4).
FIG 1
EUT: 03 digital lst v.
Manuf: OSRAM-TV
Op Cond: 24V DC
Operator: a.brieda
Test Spec: cavi connessione r/n
Comment: Primo software
Date: 08. Mar 07 09:40

Final Measurement Results:

Indicated Phase/PE shows Configuration of max. Emission

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* limit exceeded

FIG 4
FIG 5
BEGIN

INIT

PSC START

DIM?

Y

DUTY-CYCLE LINEAR DRIFT (TO DCM MODE)

WAIT...

N

DUTY-CYCLE LINEAR RISE (BACK TO NORMAL MODE)

ADC START

I0 SENSE

> REF?

N

Y

DEC DUTY-CYCLE

INC DUTY-CYCLE

POWER CONTROL

S2

FREQ SWEEP

S3

FIG 6
MAIN LOOP

PSC_INPUT

\( n = 1 \)?

\( N \) NO-DIM ROUTINE

\( Y \) DIM ROUTINE

\( n = 0 \) ADC START

I0 SENSE

\( \Rightarrow \text{REF?} \)

\( Y \) DEC DUTY-CYCLE

\( N \) INC DUTY-CYCLE

INC \( n \)

\( n = k \)?

\( N \)

\( Y \) POWER CONTROL ROUTINE

S4

S5

TO NEXT PAGE

FIG 7
FIG 8
FIG 9
## DOCUMENTS CONSIDERED TO BE RELEVANT

<table>
<thead>
<tr>
<th>Category</th>
<th>Citation of document with indication, where appropriate, of relevant passages</th>
<th>Relevant to claim</th>
<th>CLASSIFICATION OF THE APPLICATION (IPC)</th>
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<tr>
<td>X</td>
<td>WO 2007/019663 A (TIR SYSTEMS LTD [CA]; JUNGNIRTH PAUL [CA]; ROBINSON SHANE P [CA]; SPI) 22 February 2007 (2007-02-22) * paragraphs [0007], [0028], [0036] - [0043], [0047], [0051]; figure 1 *</td>
<td>1,2,7-9</td>
<td>INV. H03K19/0175 H05B33/08</td>
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<td>X</td>
<td>US 2004/160199 A1 (MORGAN FREDERICK M [US] ET AL) 19 August 2004 (2004-08-19) * paragraphs [0055], [0154] - [0180], [0233], [0234], [0283], [0296], [0298]; figures 1,11 *</td>
<td>1,5,8,12</td>
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<td>X</td>
<td>US 2006/261752 A1 (LEE SANG Y [KR]) 23 November 2006 (2006-11-23) * paragraphs [0007], [0015], [0017], [0036], [0041]; figures 2,3 *</td>
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<td>US 2003/214242 A1 (BERG-JOHANSEN ROAR [US]) 20 November 2003 (2003-11-20) * paragraphs [0028], [0031] - [0034], [0042]; figure 1C *</td>
<td>1,4,7,8</td>
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The present search report has been drawn up for all claims.
## Documents Considered to Be Relevant

<table>
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<tr>
<td>X</td>
<td>WO 2006/056066 A (TIR SYSTEMS LTD [CA]; ASHDOWN IAN [CA]; JUNGWIRTH PAUL [CA]; ROBINSON) 1 June 2006 (2006-06-01) * paragraphs [0044] - [0046], [0058], [0064], [0065], [0071] - [0074], [0080], [0081], [0093], [0103]; figure 1 *</td>
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<td>X</td>
<td>DE 102 01 053 A1 (VISTEON GLOBAL TECH INC [US]) 1 August 2002 (2002-08-01) * paragraphs [0005], [0012], [0013], [0016] - [0036]; figures 1,2 *</td>
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<td>X</td>
<td>US 2003/057888 A1 (ARCHENHOLD GEOFFREY HOWARD GIL [GB] ET AL) 27 March 2003 (2003-03-27) * paragraphs [0037], [0038], [0043] - [0050]; figures 2,9,10 *</td>
<td>1,2,8,9</td>
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### Technical Fields Searched (IPC)
- 1,2,7-9

The present search report has been drawn up for all claims.

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**Place of search:** Munich  
**Date of completion of the search:** 7 November 2007  
**Examiner:** Moll, Peter

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**Category of Cited Documents**
- X: particularly relevant if taken alone  
- Y: particularly relevant if combined with another document of the same category  
- A: technological background  
- D: document cited in the application  
- L: document cited for other reasons  
- T: theory or principle underlying the invention  
- E: earlier patent document, but published on, or after the filing date  
- P: intermediate document  
- F: member of the same patent family, corresponding document
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<tr>
<td>X</td>
<td>US 2006/245174 A1 (ASHDOWN IAN [CA] ET AL) 2 November 2006 (2006-11-02) * paragraphs [0011], [0027], [0029], [0063] - [0069], [0089]; figure 1 *</td>
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**TECHNICAL FIELDS SEARCHED** (IPC)

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The present search report has been drawn up for all claims.

**Place of search**: Munich

**Date of completion of the search**: 7 November 2007

**Examiner**: Moll, Peter

**CATEGORY OF CITED DOCUMENTS**

- T: theory or principle underlying the invention
- E: earlier patent document, but published on, or after the filing date
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- L: document cited for other reasons
- O: non-written disclosure
- A: technological background
- M: member of the same patent family, corresponding document
- P: intermediate document

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CLAIMS INCURRING FEES

The present European patent application comprised at the time of filing more than ten claims.

☐ Only part of the claims have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims and for those claims for which claims fees have been paid, namely claim(s):

☐ No claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims.

LACK OF UNITY OF INVENTION

The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

see sheet B

☐ All further search fees have been paid within the fixed time limit. The present European search report has been drawn up for all claims.

☐ As all searchable claims could be searched without effort justifying an additional fee, the Search Division did not invite payment of any additional fee.

☒ Only part of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the inventions in respect of which search fees have been paid, namely claims:

see annex

☐ None of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims, namely claims:
1. claims: 1-13
   LED driver and method of driving an LED system, comprising a microcontroller that is adapted to perform the functions LED current regulation and dimming control.

2. claims: 1-13
   LED driver and method of driving an LED system, comprising a microcontroller that is adapted to perform the functions LED current regulation and thermal management.

3. claims: 1-13
   LED driver and method of driving an LED system, comprising a microcontroller that is adapted to perform the functions LED current regulation and input power absorption control.

4. claims: 1-13
   LED driver and method of driving an LED system, comprising a microcontroller that is adapted to perform the functions LED current regulation and input current absorption control.

5. claims: 1-13
   LED driver and method of driving an LED system, comprising a microcontroller that is adapted to perform the functions LED current regulation and alarms and fault conditions management.

6. claims: 1-13
   LED driver and method of driving an LED system, comprising a microcontroller that is adapted to perform the functions LED current regulation and LED light optical feedback.

7. claims: 1-13
   LED driver and method of driving an LED system, comprising a microcontroller that is adapted to perform the functions dimming control and thermal management.
The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

LED driver and method of driving an LED system, comprising a microcontroller that is adapted to perform the functions dimming control and input power absorption control.

9. claims: 1-13

LED driver and method of driving an LED system, comprising a microcontroller that is adapted to perform the functions dimming control and input current absorption control.

10. claims: 1-13

LED driver and method of driving an LED system, comprising a microcontroller that is adapted to perform the functions dimming control and alarms and fault conditions management.

11. claims: 1-13

LED driver and method of driving an LED system, comprising a microcontroller that is adapted to perform the functions dimming control and LED light optical feedback.

12. claims: 1-13

LED driver and method of driving an LED system, comprising a microcontroller that is adapted to perform the functions thermal management and input power absorption control.

13. claims: 1-13

LED driver and method of driving an LED system, comprising a microcontroller that is adapted to perform the functions thermal management and input current absorption control.

14. claims: 1-13

LED driver and method of driving an LED system, comprising a microcontroller that is adapted to perform the functions thermal management and alarms and fault conditions management.

15. claims: 1-13
LED driver and method of driving an LED system, comprising a microcontroller that is adapted to perform the functions thermal management and LED light optical feedback.

16. claims: 1-13

LED driver and method of driving an LED system, comprising a microcontroller that is adapted to perform the functions input power absorption control and input current absorption control.

17. claims: 1-13

LED driver and method of driving an LED system, comprising a microcontroller that is adapted to perform the functions input power absorption control and alarms and fault conditions management.

18. claims: 1-13

LED driver and method of driving an LED system, comprising a microcontroller that is adapted to perform the functions input power absorption control and LED light optical feedback.

19. claims: 1-13

LED driver and method of driving an LED system, comprising a microcontroller that is adapted to perform the functions input current absorption control and alarms and fault conditions management.

20. claims: 1-13

LED driver and method of driving an LED system, comprising a microcontroller that is adapted to perform the functions input current absorption control and LED light optical feedback.

21. claims: 1-13

LED driver and method of driving an LED system, comprising a microcontroller that is adapted to perform the functions alarms and fault conditions management and LED light optical feedback.
The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

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**07-11-2007**

<table>
<thead>
<tr>
<th>Patent document cited in search report</th>
<th>Publication date</th>
<th>Patent family member(s)</th>
<th>Publication date</th>
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</thead>
<tbody>
<tr>
<td>WO 2007019663 A</td>
<td>22-02-2007</td>
<td>NONE</td>
<td></td>
</tr>
<tr>
<td>US 2004160199 A1</td>
<td>19-08-2004</td>
<td>NONE</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>JP 2005173523 A</td>
<td>30-06-2005</td>
</tr>
<tr>
<td></td>
<td></td>
<td>KR 2005005577 A</td>
<td>13-06-2005</td>
</tr>
<tr>
<td></td>
<td></td>
<td>US 2007114949 A1</td>
<td>24-05-2007</td>
</tr>
<tr>
<td></td>
<td></td>
<td>KR 20060119018 A</td>
<td>24-11-2006</td>
</tr>
<tr>
<td></td>
<td></td>
<td>WO 03098585 A1</td>
<td>27-11-2003</td>
</tr>
<tr>
<td>US 2004121321 A1</td>
<td>28-10-2004</td>
<td>NONE</td>
<td></td>
</tr>
<tr>
<td>WO 2006056066 A</td>
<td>01-06-2006</td>
<td>CA 2589238 A1</td>
<td>01-06-2006</td>
</tr>
<tr>
<td></td>
<td></td>
<td>EP 1839463 A1</td>
<td>03-10-2007</td>
</tr>
<tr>
<td>US 2007103086 A1</td>
<td>10-05-2007</td>
<td>NONE</td>
<td></td>
</tr>
<tr>
<td>DE 10201053 A1</td>
<td>01-08-2002</td>
<td>GB 2374715 A</td>
<td>23-10-2002</td>
</tr>
<tr>
<td></td>
<td></td>
<td>US 2002130786 A1</td>
<td>19-09-2002</td>
</tr>
<tr>
<td></td>
<td></td>
<td>GB 2369730 A</td>
<td>05-06-2002</td>
</tr>
<tr>
<td></td>
<td></td>
<td>WO 03022009 A1</td>
<td>13-03-2003</td>
</tr>
<tr>
<td></td>
<td></td>
<td>JP 2005502167 T</td>
<td>20-01-2005</td>
</tr>
<tr>
<td></td>
<td></td>
<td>WO 2006014473 A1</td>
<td>09-02-2006</td>
</tr>
<tr>
<td>US 2006245174 A1</td>
<td>02-11-2006</td>
<td>NONE</td>
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</tr>
<tr>
<td>US 6150774 A</td>
<td>21-11-2000</td>
<td>AT 222013 T</td>
<td>15-08-2002</td>
</tr>
</tbody>
</table>

For more details about this annex: see Official Journal of the European Patent Office, No. 12/82
ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

07-11-2007

<table>
<thead>
<tr>
<th>Patent document cited in search report</th>
<th>Publication date</th>
<th>Patent family member(s)</th>
<th>Publication date</th>
</tr>
</thead>
<tbody>
<tr>
<td>US 6150774 A</td>
<td></td>
<td>AU 757000 B2</td>
<td>30-01-2003</td>
</tr>
<tr>
<td></td>
<td></td>
<td>AU 9206098 A</td>
<td>16-03-1999</td>
</tr>
<tr>
<td></td>
<td></td>
<td>CA 2302227 A1</td>
<td>04-03-1999</td>
</tr>
<tr>
<td></td>
<td></td>
<td>DE 69807092 D1</td>
<td>12-09-2002</td>
</tr>
<tr>
<td></td>
<td></td>
<td>DE 69807092 T2</td>
<td>08-05-2003</td>
</tr>
<tr>
<td></td>
<td></td>
<td>ES 2182358 T3</td>
<td>01-03-2003</td>
</tr>
<tr>
<td></td>
<td></td>
<td>HK 1025416 A1</td>
<td>01-11-2002</td>
</tr>
<tr>
<td></td>
<td></td>
<td>JP 2001514432 T</td>
<td>11-09-2001</td>
</tr>
<tr>
<td></td>
<td></td>
<td>JP 2004006253 A</td>
<td>08-01-2004</td>
</tr>
<tr>
<td></td>
<td></td>
<td>WO 9910867 A1</td>
<td>04-03-1999</td>
</tr>
<tr>
<td></td>
<td></td>
<td>US 6016038 A</td>
<td>18-01-2000</td>
</tr>
</tbody>
</table>

For more details about this annex: see Official Journal of the European Patent Office, No. 12/82

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