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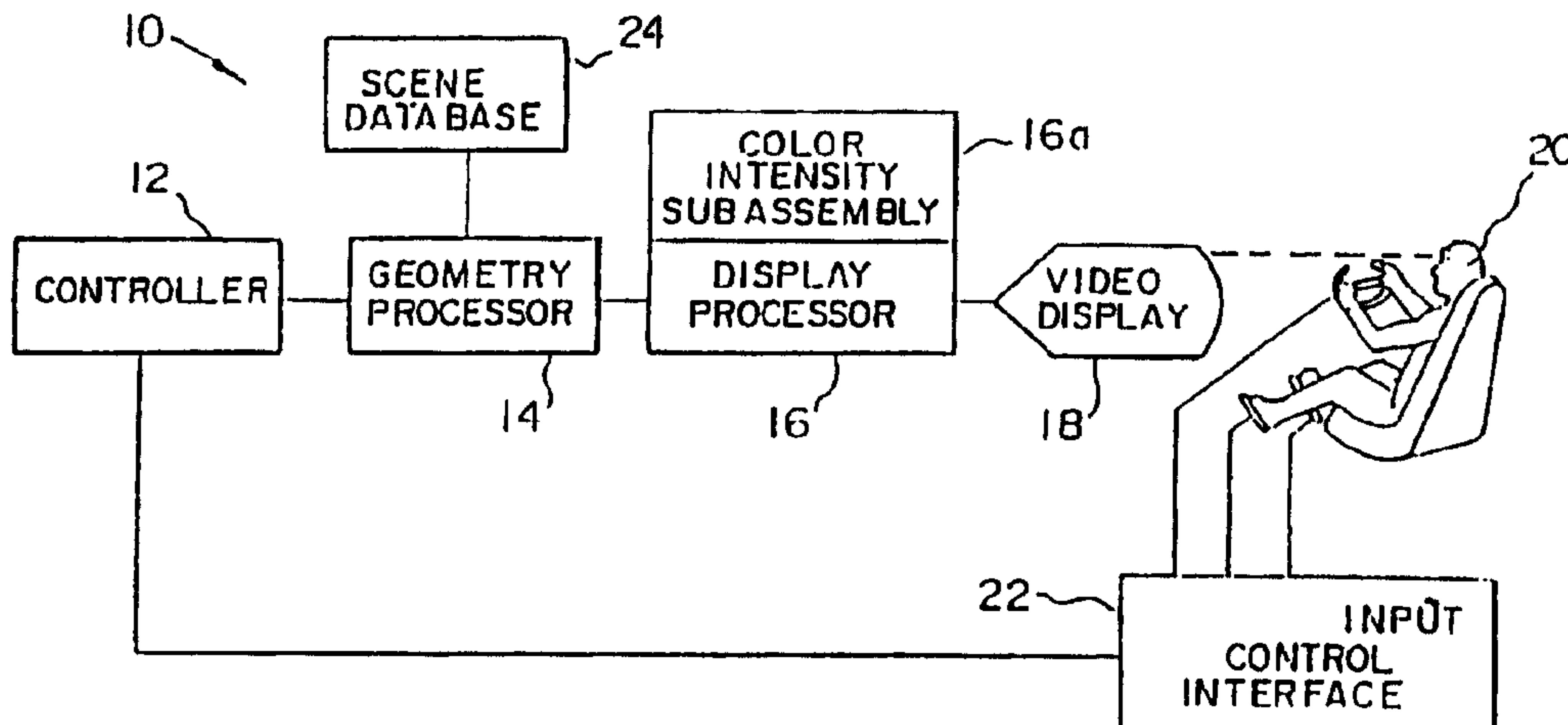
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(54) IMPROVED S-BUFFER ANTI-ALIASING METHOD



(57) A method and apparatus, for use in a computer image generation system (10) wherein polygons are displayed on a video display (18), for encoding data representing intersections of the polygons and the pixels. The method comprises the steps of receiving input data signals characterizing a set of coordinates of each of at least three vertices of each polygon to be displayed through an input control interface (22), each different pair of the vertices of each polygon defining a different edge of the polygon; and generating, through a geometry processor (14), responsive to the received vertex data signals, edge data signals describing the intersection, if any, of the polygon edges with each pixel to be displayed.

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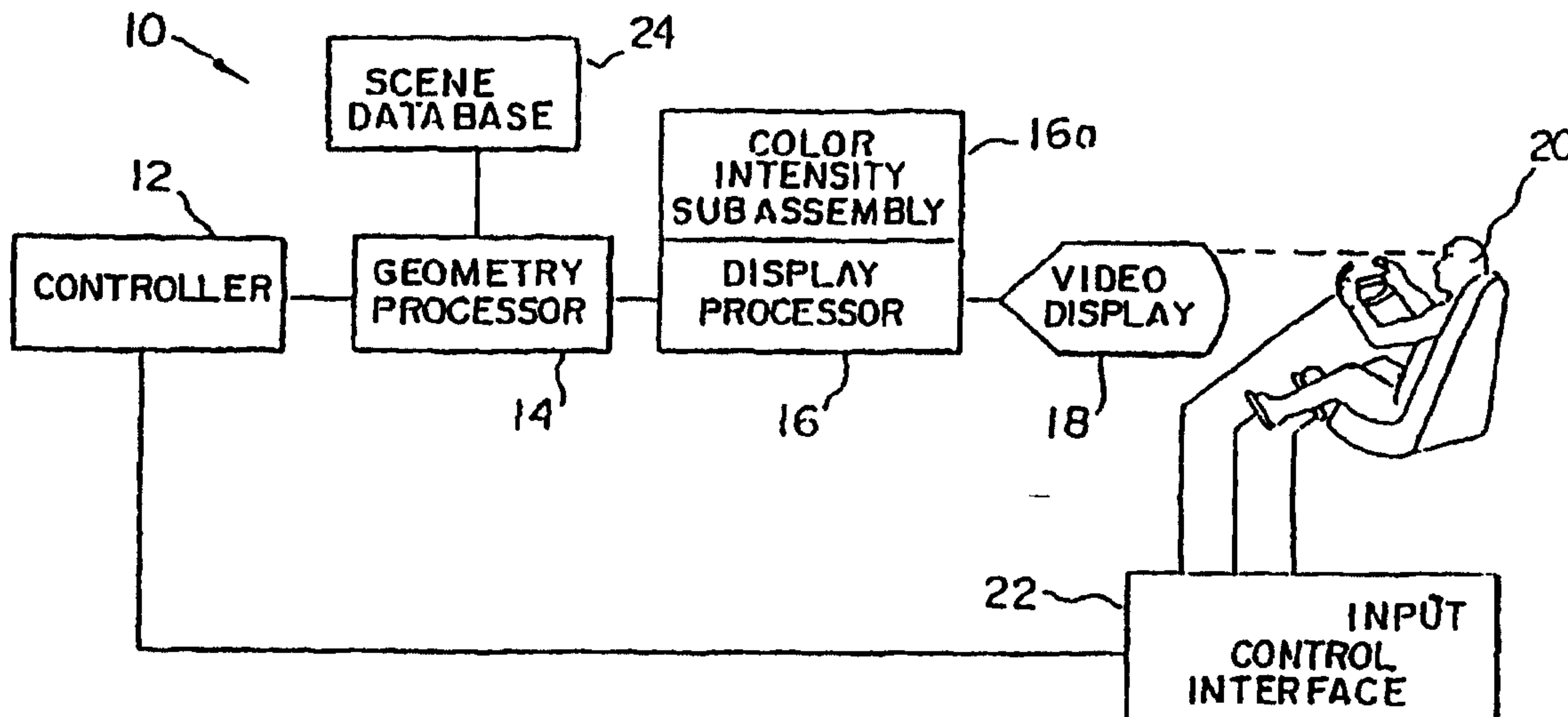
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(54) Title: IMPROVED S-BUFFER ANTI-ALIASING METHOD



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(57) Abstract: A method and apparatus, for use in a computer image generation system (10) wherein polygons are displayed on a video display (18), for encoding data representing intersections of the polygons and the pixels. The method comprises the steps of receiving input data signals characterizing a set of coordinates of each of at least three vertices of each polygon to be displayed through an input control interface (22), each different pair of the vertices of each polygon defining a different edge of the polygon; and generating, through a geometry processor (14), responsive to the received vertex data signals, edge data signals describing the intersection, if any, of the polygon edges with each pixel to be displayed.

IMPROVED S-BUFFER ANTI-ALIASING METHOD

FIELD OF THE INVENTION

This invention relates to computer image generation (CIG) systems, and, more particularly, to a novel color determination means for use in a display processor and to novel methods for processing image data describing object faces, for display of two-dimensional representations of those objects in a real-time imaging system.

BACKGROUND OF THE INVENTION

Real-time computer image systems are being designed to provide realistic image reproduction for a variety of video graphic systems, such as video games and video training and simulation devices. In these systems, it is very advantageous to provide the user with video display realism.

In a typical real-time computer image generation system, such as a video game, image generation is done in three sequential stages: Controller, Geometry Processor, and Display Processor. Each of these three processing stages or sections each work independently on data representing or corresponding to one of three consecutive scenes to be displayed. Each of these processing stages or sections processes data on a scene or image for a fixed time, which may be either a field time of 16.67 milliseconds or a frame time of 33.3 milliseconds, usually the former. These particular field and frame times corresponds to those of a typical television format display, i.e., two interlaced fields per frame with thirty frames per second.

For each time interval, all three processors are kept busy so that, at any given time, the processors are working on three separate scenes. Each section processes inputs and generates outputs during the 5 interval, so data flows through the three stages in a sequential manner from the controller, to the Geometry Processor, and then to the Display Processor. The computational load is spread out among the specialized processor sections, new scenes can be displayed each 10 field or frame time, even though the results are delayed three field or frame times.

For example, in a video game that simulates a flight system, user, or player, inputs are received by the Controller; and after being processed sequentially by 15 the Controller, the Geometry Processor and Display Processor, the output is sent as a color scene to video displays observed by the player. The data seen by the player starts out as simple control inputs applied to an environment definition and is converted to pixel video 20 display information. In the controller, the player inputs are used to calculate the new position and orientation of the simulated aircraft, and from this, a mathematical rotation matrix is calculated that is used 25 to rotate objects from a reference coordinate system to a display coordinate system. Other information dealing with the aircraft, with the position of targets and with other aircraft is also calculated in the Controller. In some applications, the Controller is a general purpose computer.

30 The Geometry Processor reads, from a database, descriptions of objects that are potentially visible in the stored three-dimensional digital representation of

the scene, and the objects that are read are rotated into display coordinates using the rotation matrices calculated in the Controller. The Geometry Processor mathematically projects the three-dimensional data onto the two-dimensional display window. In addition, the Geometry Processor calculates (as by use of a depth-buffer or the like) which objects are in front or behind other objects and stores this information in a priority list.

10 Each object processed is comprised of individual faces, where each face is in the form of a polygon bounded by straight edges. The priority list contains the order of all faces in the scene, with the first face in the list as the highest priority face, and the last face in the list as the lowest priority face. Whenever two faces overlap on the display, the higher priority face will be visible, and the lower priority face will be obscured. Lastly, the Geometry Processor calculates the display end points for the edges that bound the two-dimensional faces, and also calculates the coefficients of the line equations that describe these bounding edges.

15 The Display Processor receives the two-dimensional face descriptions, along with additional face related data and uses this information to output data describing to a color display each pixel in the scene. Each color display in the system is typically made up of 1024 lines of video information, with each line having 1024 individual color dots or pixels. The Display Processor has to resolve, for each pixel, what the color of the pixel should be, given that many faces can be present in a pixel, and that certain portions of faces

may be covered by other faces. Since values for all of these pixels need to be calculated in 1/60th of a second, the processing load in the Display Processor is very high. To calculate video for a multi-display system (a single system has contained as many as 14 displays), the Display Processor must perform in excess of 10 billion computations per second.

One persistent problem has been to process pixel color intensity information in an anti-aliasing manner, which allows for suitable high-definition edge effects to be visible while preventing undesirable video data components ("distortion") from taking on the identity of a desired component, so as to appear as the wrong color, or degree of translucency, in that pixel.

SUMMARY OF THE INVENTION

An object of this invention is to provide an improved anti-aliasing procedure for computer image generation systems.

Another object of the present invention is to provide an improved process for encoding edge intersection data for use in an anti-aliasing procedure in a computer image generation system.

These and other objectives are attained with a method and apparatus, for use in a computer image generation system wherein polygons are displayed on an array of pixels, for encoding data representing intersections of the polygons and the pixels. The method comprises the steps of receiving input data signals characterizing a set of coordinates of each of at least three vertices of each polygon to be displayed, each different pair of the vertices of each polygon defining a

different edge of the polygon; and generating, responsive to the received vertex data signals, edge data signals describing the intersection, if any, of the polygon edges with each pixel to be displayed.

5 The generating step includes, for each pixel to be displayed, i) identifying in a first field of one of the edge data signals, and for each side of the pixel, whether or not the edges of a selected one of the polygons intersect said side of the pixel, and ii) if the edges of the selected polygon intersects any side of the pixel, storing, in a second field of said one of the edge data signals, an estimate of the location along the side or sides of the pixel at which the polygon edges intersect the pixel side.

10 15 Further benefits and advantages of the invention will become apparent from a consideration of the following detailed description, given with reference to the accompanying drawings, which specify and show preferred embodiments of the invention.

20 **BRIEF DESCRIPTION OF THE DRAWINGS**

25 FIG. 1 is a schematic block diagram of a computer image generation (CIG) system in which the apparatus and methods of the present invention may be practiced;

30 FIG. 2 is a spatial diagram illustrating the relationship between the object face polygon, in object space, and a corresponding image face polygon, in image space, and the picture elements (pixels) of the display raster upon which the image face will appear;

FIG. 2a is a magnified portion of the image display of FIG. 2 in the vicinity of the desired polygon to be displayed;

5 FIG. 3a is a view of an array of display pixels, upon which a pair of face polygons are imaged;

FIG. 3b is a view of an array of display pixels, upon which a face polygon is positioned, and shows various pixel edges E and edge distances D that may be used to determine pixel color data.

10 FIG. 4 illustrates a polygon that intersects several pixel boundaries;

FIG. 5 shows a table that identifies the data bits of edge crossing signals;

15 FIG. 6 illustrates a procedure for resolving edge crossings; and

FIG. 7 schematically depicts a subpixel filter.

DESCRIPTION OF AN ILLUSTRATIVE EMBODIMENT

Referring initially to FIG. 1, a functional block diagram of a computer image generation system 10, such as an aircraft cockpit simulation system, is illustrated. The reader is referred to the existing CIG art, including such publications as U.S. Patent No. 4,727,365, issued Feb. 23, 1988 to Bunker et al., for "Advanced Video Object Generator"; U.S. Patent No. 4,825,391, issued April 25, 1989 to D. Merz, for "Depth Buffer Priority Processing For Real Time Computer Image Generating Systems"; and U.S. Patent 5,668,940 issued to W. Steiner and M. Morgan, for "Method and Apparatus For Anti-Aliasing Polygon Edges In a Computer Imaging System." These three U.S. Patents 4,727,365; 4,825,391

and 5,668,940 are incorporated herein in their entireties by reference.

5 The system 10 includes a Controller unit 12, a Geometry Processor unit 14, and a Display Processor unit 16. The Display Processor unit 16 displays information to a display window 18 to simulate the movement of the aircraft over a scene. An operator 20 is linked to the computer image generation system 10 through input control interface 22. The operator's maneuvering action is fed to input control interface 22. The input control interface converts the appropriate data such as aircraft position, aircraft roll, pitch, and yaw attitude, from the operator to the Controller 12.

10 In the Controller 12, the operator inputs are used to calculate a new position and orientation of the aircraft, and from this, a mathematical rotation matrix is calculated that is used to rotate objects from the reference coordinate system to a display coordinate system, which is well known in the art. Other 15 information dealing with the scene, the position of targets, or other aircraft is calculated in the Controller. In some computer image generation systems, the Controller unit 12 is a general purpose computer.

20 The Controller unit 12 is updated periodically 25 by the most recent aircraft orientation. This orientation includes the aircraft roll, pitch, yaw, and position from the operator via the input control interface 22, to the Controller 12. The Controller 12 works on a scene for a field or frame time and then 30 passes data to the Geometry Processor 14.

The Geometry Processor 14 reads from a scene database memory 24, descriptions of objects such as

5 towers, roads, rivers, hangars, etc., that are potentially visible in the stored three dimensional digital representation of the earth. The objects are read from the scene database and are rotated into display coordinates using the rotation matrix calculated in the Controller 12. The Geometry Processor 14 clips all edges which extend beyond the view window boundaries in object space, and the processor 14 mathematically projects the three-dimensional object data onto the two-dimensional 10 display window.

15 Object faces which extend beyond display window boundaries are clipped in two-dimensional image space. The two dimensional objects include points, lines, closed convex polygons, or combinations thereof. Each closed convex polygon is known as a face. Thus, each object processed is made up of individual faces, where each face is bounded by straight edges. Once the faces are 20 computed from the three-dimensional objects, in the Geometry Processor 14, the data is passed to the Display Processor 16. In addition, the Geometry Processor 14 calculates, in known manner which faces are in front or behind other faces and stores this information in a priority list.

25 This priority list contains the order of all faces in the scene where the first face in the list is the highest priority face, and the last face in the list is the lowest priority face. Whenever two faces overlap on a display, the higher priority face will be visible and the overlapped portion of the lower priority face 30 will be obscured. After calculating the priority list, the Geometry Processor 14 calculates the display endpoints of the line segments that bound the two

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dimensional faces and calculates the coefficients of the line equations that describe the bounding edges. The Geometry Processor 14 calculates the endpoints of each edge and the slope of each edge that describe a bounded face. These data are then passed on to the Display Processor 16.

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FIG. 1, shows Display Processor 16 that is used to drive the video display 18. In some systems, a single Controller 12 and Geometry Processor 14 is used to drive more than one Display Processor. The Display Processor is responsible for processing information to be displayed on video display 18. One particular task done by the Display Processor is to resolve for each pixel in the video display 18 what the color of the pixel should be, given the many faces that can be present in a pixel, and those portions of faces that may be covered by other faces. To perform this function, the Display Processor 16 thus receives two-dimensional face descriptions from the Geometry Processor 14, along with the face color, and a face priority list, and uses this information to output the scene to color video display 18. Each color display in a system can be made up of 1,024 or more lines of video information, and on each line there typically are 1,204 individual color dots or pixels (picture elements). For each pixel the display may receive a 12 bit red data signal, a 12 bit green data signal, and a 12 bit blue data signal as part of intensity information that describes the color of the pixel.

With reference to FIG. 2, the eyes 20' of the observer are at a viewpoint VP and look at a sample face polygon 30 within the view window in object space 30'. Only the face 30 closest to viewer 20', along a view line

32, is seen; and the visible face is determined by comparison of range distance R between view point VP and a face point Pf, using depth buffer techniques well known in the art (see, for example, the aforementioned U.S. Pat. No. 4,825,391 and the references cited therein). Thus, visible polygon 30 (here, a triangular face identified by its vertices Va, Vb and Vc) is that polygon having the minimum range R_{min} along view line 32. The object space 30' is projected into a display window 34 in image space 34'; and face polygon 30 is projected as display polygon 36, with respective vertices V1, V2 and V3 corresponding to vertices Va, Vb and Vc, respectively.

The display window is often the raster of the display with each pixel 38 scanned in sequential fashion along a horizontal line (in the J direction) from the left edge ($J=J_o$) to the right edge ($J=J_{max}$) of the raster/window, and for lines with increasing distance (in the I direction) down from the raster/window top edge (where $I=I_o$) to the bottom edge ($I=I_{max}$). Thus, the path line Lp is scanned from a first (left-hand) pixel P1 to a last (right-hand) pixel Pr. A pixel Pp is identified by the view line 32 passing through the window plane 34, so that pixel line Lp includes an image point projecting, and corresponding, to polygon point Pf. The polygon 36 will, along with line Lp, include pixels from a leftmost pixel Po, identified by the J_L crossing at the left line location, to a rightmost pixel Pq, identified by the J_R crossing at the right line location.

With reference to FIG. 2a, a row of the pixels 38 along the scan line 34" is sequentially scanned in the J direction, starting at leftmost pixel 38h. The next pixel 38i is then operated upon, and so forth, until a

pixel 38l is reached on the left edge of the polygon for that line and partially covered by the polygon 36. This pixel is identified as a leftmost crossing J_L of a polygon edge and the J line 34". Continuing along this line, through other pixels 38 completely covered by the polygon, a pixel 38r is reached on the right edge of the polygon and partially covered by the polygon 36. This pixel is identified as a rightmost crossing J_R of a polygon edge. Further travel along the J line 34" eventually reaches the rightmost pixel 38v of that scan line.

It will be seen that, without consideration of translucency and feedthrough of chroma from a more-distant polygon, the attributes of the pixels 38 that are completely covered by, and therefore totally within, the polygon 36 have exactly the polygon face attributes (color, intensity, translucency, etc.). In contrast, the pixels (e.g. pixels 38l and 38r) that are partially covered by the polygon 36 have the attributes of that particular polygon only for the pixel portion actually covered, so that the attributes for the remainder of the pixel must be obtained from other information and blended according to the various percentages of the pixel covered by each polygon/source of color information.

This may be understood by reference to FIG. 3a, wherein a first polygon 36a is ordered (as by any of the well-known depth-buffering processes) in front of another polygon 36b, as seen in image space. Polygon 36 has a plurality of vertices (e.g. four vertices V1, V2, V3 and V4 in FIG. 3b) connected by a like plurality of polygon edges 39 (e.g. as shown in FIG. 3b, edges 39a, 39b, 39c and 39d) which cross pixel boundaries at crossings 40.

5 The image screen is formed of a rectangular array of pixels, arranged in vertical columns (e.g. columns..., P-2, P-1, P, P+1, P+2,...) sequentially along horizontal lines (e.g. lines..., L-2, L-1, L, L+1, L+2,...) with horizontal edges E at the lower demarcation of each line L of pixels with the next-higher-numbered (next-to-be-scanned) pixel line. Each pixel can thus be ordered by its rowline L and column P designation. It will be seen that some pixels, such as pixel (L,P), will be completely covered by only one polygon (polygon 36a) and thus have only the attributes of that polygon, while other pixels have a sharing/blending of the attributes of plural polygons.

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15 With reference to FIG. 3b, color attributes may be determined by consideration of the entire pixel and do not need to process color information for a plurality of subportions of each pixel (or subpixels), even if the depth-buffering processing architecture (with or without use of span/subspan processing methodology) of the 20 aforementioned U.S. Pat. No. 4,825,391 is used. It should be understood that each item of information processed is a signal word of electronic digital data. While we may discuss these signals by shorthand 25 designations (e.g. corners, edges, polygons, etc.), we nevertheless mean these terms to signify the data signals used for each of these respective geometrical concepts. For instance, "corner" signifies the data word specifying the two-dimensional pixel corner location, "edge" is the data signal set representing the line of the associated 30 polygon edge, "polygon" means the digital data structure equivalent of a particular polygon, etc. The end result of the process is to provide a final sequence of video

signals (digital video, or analog video derived from digital) which can cause the color and intensity of the individual pixels of the video display means 18 to be illuminated and rendered visible to observer 20.

5 The extent to which a polygon, that partially covers a pixel, contributes to the final pixel attributes is determined on the basis of the intersection or intersections between the edges of the polygon and the pixel boundaries. One procedure for 10 determining the pixel attributes in this way is described in detail in the above-identified U.S. Patent 5,668,940.

15 Generally, in accordance with this procedure, input data signals are provided characterizing the display space coordinates of each of at least three vertices of each face polygon to be displayed. Then, 20 this vertex data is utilized to determine which polygon edges, if any, cut one of the sequence of pixels forming the display raster, and then the location of the cut along the associated pixel edge is determined. The edge crossing data signals for all polygons affecting each pixel are stored on a per-pixel basis in a frame buffer, 25 along with color data for each of the polygons occupying any portion of that pixel. For each pixel to be displayed, a constellation of several edges of selected adjacent pixels are processed for resolving edge relationships and mixing polygon color intensity data to determine the final, observable color of each display pixel.

30 With reference to Figure 4, each pixel 50 is a sample point that samples the polygon. The location where the polygon edge crosses between adjacent pixel sample points is computed. For each pixel sample point,

5 a top, left, right, and bottom edge crossing is computed. If the edge crossing is beyond the next pixel sample point, the edge crossing is flagged as covered which means that the polygon covers, or lies on, the adjacent pixel sample point. For example in Figure 4, the right crossing 52a for the lower left pixel sample point, and the left crossing 52b for the lower right pixel sample point would be flagged as covered.

10 Each polygon edge can be flagged as anti-aliased, or not anti-aliased. Also, each polygon edge can be flagged as mesh anti-aliased. In this mode, the polygon edge is anti-aliased if mesh anti-aliasing is enabled for every polygon edge crossing of the pixel sample point, and the edge is not shared with another polygon, and the edge is flagged as anti-aliased. If mesh anti-aliasing is disabled, the edge is anti-aliased unless the edge is flagged as not anti-aliased.

15 20 If the pixel is part of an image rather than a polygon, the pixel is flagged as conditionally anti-aliased. In this case, if a polygon edge lies on the image, the polygon edge is anti-aliased with the image; otherwise, the image is not anti-aliased.

25 When using alpha buffer operations (source and destination blend modes) that blend the color in the frame buffer (destination) with the color that is being put in the frame buffer (source), the edge crossings are combined as follows:

30 The alpha buffer blend equation is: blend factor,* source pixel+blend factor_d*destination pixel. If the blend factor_s is zero, the source pixel has no edge crossings. If the blend factor_d is zero, the destination pixel has no edge crossings. If the source

pixel and the destination pixel do not have edge crossings, nothing is done. If the source pixel has edge crossings, and the destination pixel does not, the source pixel edge crossings are used. If the source pixel does not have edge crossings, and the destination pixel does, the destination pixel edge crossings are used. If the source pixel has edge crossings, and the destination pixel does as well, the source and destination pixel edge crossings are averaged.

The edge crossing can be compressed to 8 or 9 bits to fit into the frame buffer as indicated in the Table of Figure 5.

The crossing flags, if set, indicate that there is an edge crossing, and if not set, indicates that the polygon lies on the adjacent pixels.

If mesh anti-aliasing is desired, nine bits are needed, otherwise, eight bits are needed. The mesh flag can replace the LSB of the blue color in the frame buffer. This will have the least impact on the image. The divide by 3 can be approximated. The edge crossings may be stored in the frame buffer in the alpha component. This restricts any blend mode that uses destination alpha.

The edge crossing data is read from the frame buffer during output time. The encoded edge crossing data can then be decoded back into the original edge crossing data.

With reference to Figure 6, four adjacent pixel sample points 50a, 50b, 50c and 50d are fetched from the frame buffer. These pixel sample points become the four corners of a pixel. Each sample point has four edge crossings and several flags. The edge crossings from

each of the pixel sample points are resolved to form a single edge crossing for each side of the pixel. For example, the top left pixel sample point 50a has a right crossing, and the top right pixel sample point 50b has a left crossing. These two crossing are resolved to form one edge crossing for the topside of the pixel.

5 The rules for edge resolution are as follows:

If for any pixel sample point, the mesh flag is not set, then select no mesh anti-aliasing.

10 For each side of the pixel, select the two sample points.

i) If there is one edge crossing, then set the valid edge crossing flag to zero, and set the edge crossing flag to a default value (.5).

15 ii) If there is one edge crossing from sample point A, and no edge crossing from the other sample point, then set the valid edge crossing flag to one, and set edge crossing equal to the edge crossing of sample point A.

20 iii) If there are two edge crossings and no mesh anti-aliasing, then set the valid edge crossing flag to one, and the edge crossing is the average of the 2 edge crossings.

25 iv) If there are two edge crossings and mesh anti-aliasing, then set the valid edge crossing flag to zero, and set the edge crossing flag to the default value (.5).

If the pixel is to be anti-aliased, the pixel corner colors must be blended together as follows:

30 Note that edge crossings are scaled between 0 and 1.0 and increase numerically from left to right and from top to bottom.

Area_br=(2-bot xing - right xing)/4

The final blended pixel color is determined by the equation:

5 Color = top left pixel corner color * Area_tl + top right pixel corner color * Area_tr + bot left pixel corner color * Area_bl + bot right pixel corner color * Area_br

10 With reference to Figure 7, if 2 x 2 S buffered subpixels per pixel 60 are desired for better sampling, the pixel resolution can be increased by a factor of 4, and then a post filter 62 applied to filter the pixels down to the display resolution.

15 Subpixel 11, 12, 21, and 22 are the subpixels that lie on the pixel to be displayed. The surrounding twelve subpixels are fetched, then a convolution filter is used to compute the pixel color according to the equation:

$$20 \text{ pixel color} = W \left(\frac{\text{subpixel 11} + \text{subpixel 12} + \text{subpixel 21} + \text{subpixel 22}}{4} \right) \\ + (1 - W) \left(\frac{\text{all other subpixels}}{12} \right)$$

25 If the weight W is 1.0, then no surrounding subpixels are used. If W is .75, then a pyramid shaped filter is produced. If W is .5, then a uniform filter is produced.

30 While it is apparent that the invention herein disclosed is well calculated to fulfill the objects previously stated, it will be appreciated that numerous modifications and embodiments may be devised by those

skilled in the art, and it is intended that the appended claims cover all such modifications and embodiments as fall within the true spirit and scope of the present invention.

CLAIMS

5 1. A method, for use in a computer image generation system wherein polygons are displayed on an array of pixels, for encoding data representing intersections of the polygons and the pixels, the method comprising:

10 receiving input data signals characterizing a set of coordinates of each of at least three vertices of each polygon to be displayed, each different pair of the vertices of each polygon defining a different edge of the polygon;

15 generating, responsive to the received vertex data signals, edge data signals describing the intersection, if any, of the polygon edges with each pixel to be displayed, and wherein the generating step includes

20 for each pixel to be displayed

25 i) identifying in a first field of one of the edge data signals, and for each side of the pixel, whether or not the edges of a selected one of the polygons intersect said side of the pixel, and

30 ii) if the edges of the selected polygon intersects any side of the pixel, storing, in a second field of said one of the edge data signals, an estimate of the location along the side or sides of the pixel at which the polygon edges intersect the pixel side.

2. A method according to Claim 1, wherein, the storing step includes the step of

35 if the edges of the selected polygon intersect two sides of the pixel, then

17 i) storing in a first subfield of the second field an estimate of the distance along a first of said two sides of the pixel at which the polygon edges intersect said first side, and

5 ii) storing in a second subfield of the second field an estimate of the distance along a second of said two sides of the pixel at which the polygon edges intersect said second side.

10 3. A method according to Claim 2, wherein:

the second field consists of a predetermined number of bit locations;

the first subfield consists of one half of said predetermined number of bit locations; and

15 the second subfield also consists of one half of said predetermined number of bit locations.

4. A method according to Claim 2, wherein:

20 the second field consists of four bit locations;

the first subfield consists of a first and a second of said four bit locations; and

the second subfield consists of a third and a fourth of said four bit locations.

25

5. A method according to Claim 1, wherein the storing step includes the step of:

30 if the edges of the selected polygon intersect three sides of the pixel, then

 storing in the second field an average of the distances along said three sides of the pixel at which the polygon edges intersect said three sides.

6. A method according to Claim 1, wherein the storing step includes the step of:

if the edges of the selected polygon intersect four sides of the pixel, then

5 storing in the second field an average of the distances along said four sides of the pixel at which the polygon edges intersect said four sides.

10 7. A apparatus, for use in a computer image generation system wherein polygons are displayed on an array of pixels, for encoding data representing intersections of the polygons and the pixels, the apparatus comprising:

15 means for receiving input data signals characterizing a set of coordinates of each of at least three vertices of each polygon to be displayed, each different pair of the vertices of each polygon defining a different edge of the polygon;

20 means for generating, responsive to the received vertex data signals, edge data signals describing the intersection, if any, of the polygon edges with each pixel to be displayed, and wherein the generating means includes

25 i) means to identify, for each pixel to be displayed, in a first field of one of the edge data signals, and for each side of the pixel, whether or not the edges of a selected one of the polygons intersect said side of the pixel, and

30 ii) means to store in a second field of said one of the edge data signals, and if the edges of the selected polygon intersect any side of the pixel, an estimate of the location along the side or sides of the

pixel at which the polygon edges intersect the pixel side.

5 8. An apparatus according to Claim 7, wherein the means to store includes means to store in first and second subfields of the second field, and if the edges of the selected polygon intersect two sides of the pixel, estimates of the distances along, respectively, a first and a second of said two sides of the pixel at which the 10 polygon edges intersect said first and second sides.

15 9. An apparatus according to Claim 8, wherein:
the second field consists of a predetermined number of bit locations;
the first subfield consists of one half of said predetermined number of bit locations; and
the second subfield also consists of one half of said predetermined number of bit locations.

20 10. An apparatus according to Claim 7, wherein:
the second field consists of four bit locations;

25 the first subfield consists of a first and a second of said four bit locations; and
the second subfield consists of a third and a fourth of said four bit locations.

30 11. An apparatus according to Claim 6, wherein the storing means includes means to store in the second field, and if the edges of the selected polygon intersect three sides of the pixel, an average of the distances

along said three sides of the pixel at which the polygon edges intersect said three sides.

12. An apparatus according to Claim 6, wherein the storing means includes means to store in the second field, and if the edges of the selected polygon intersect four sides of the pixel, an average of the distances along said four sides of the pixel at which the polygon edges intersect said four sides.

FIG. 1

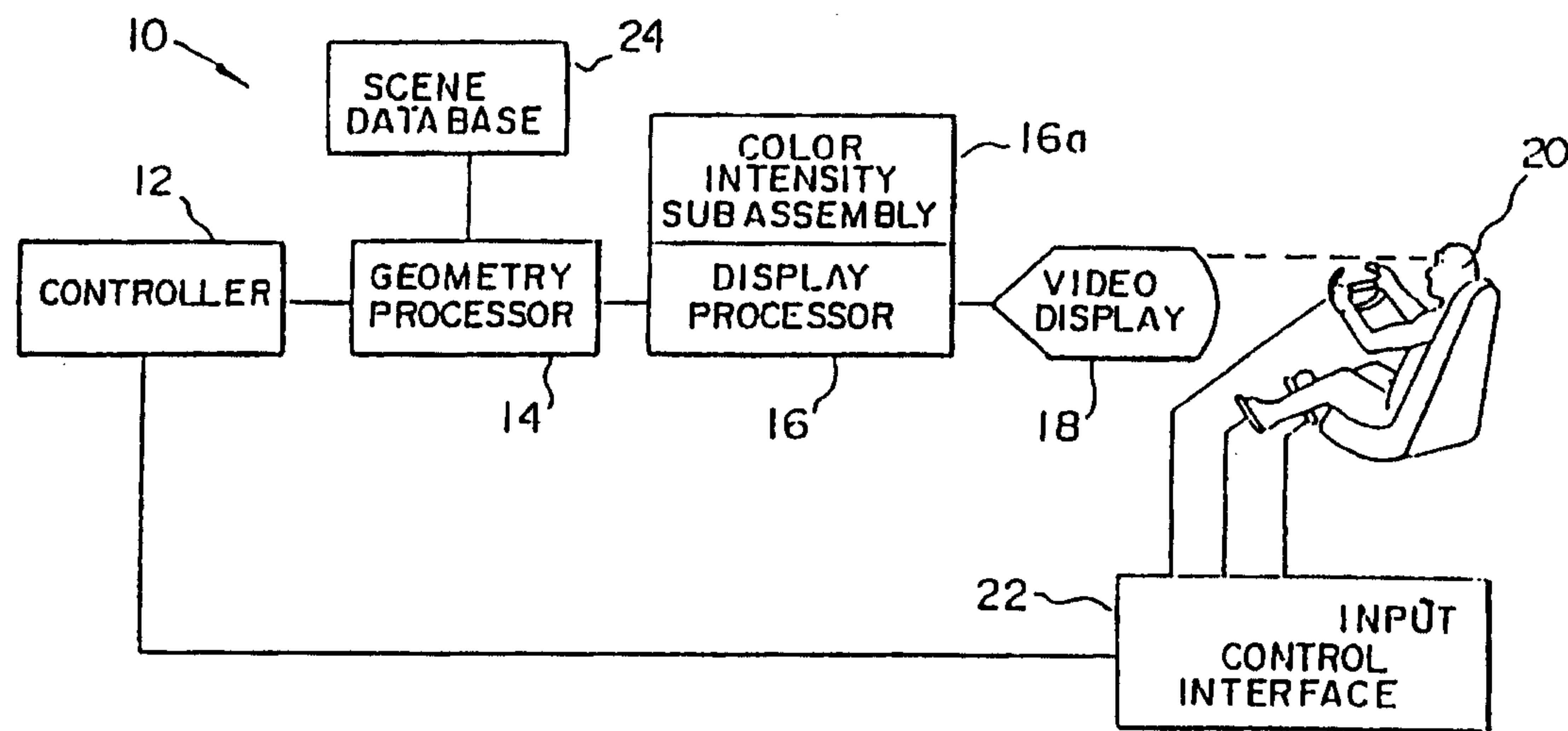
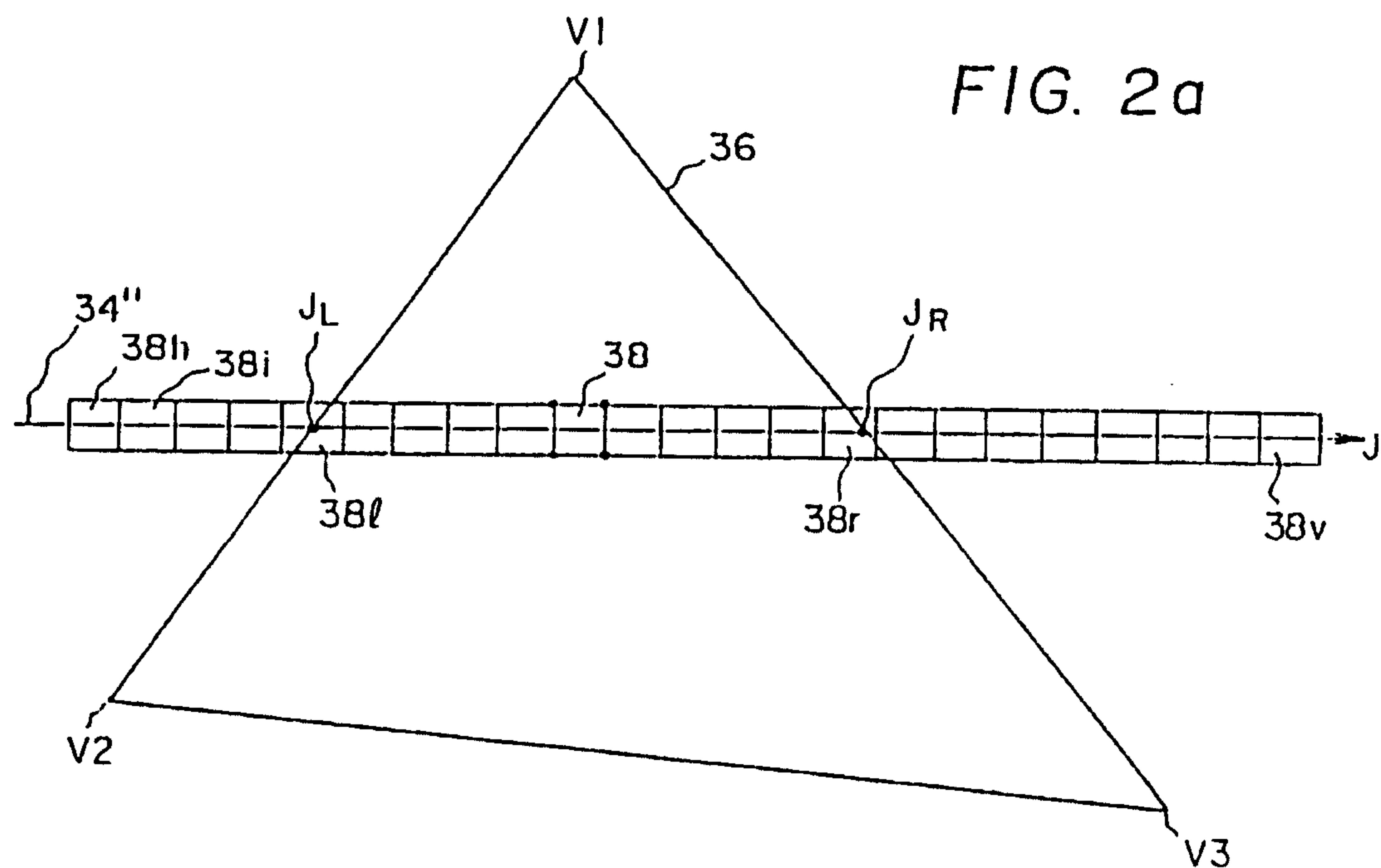


FIG. 2a



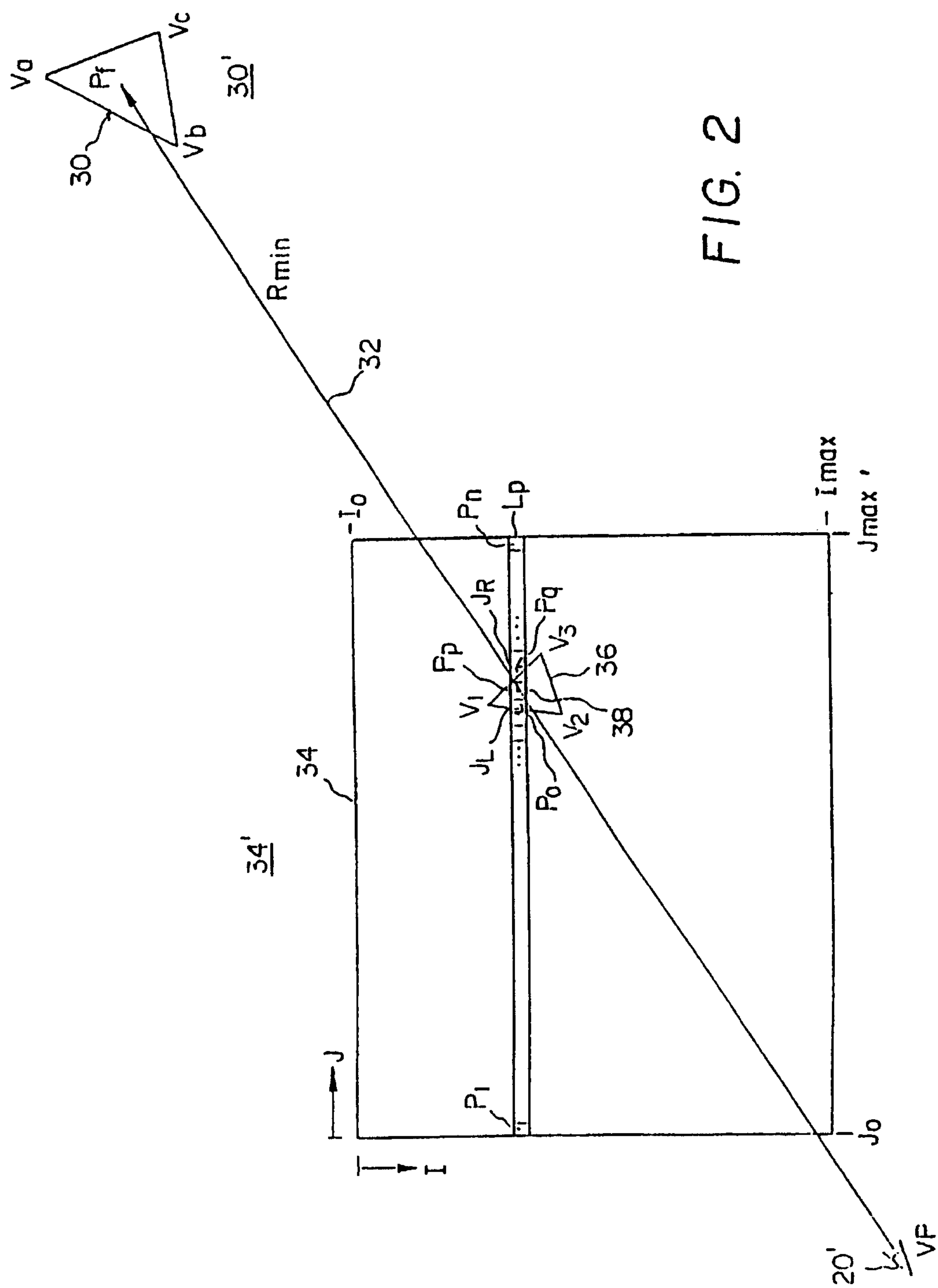


FIG. 2

FIG. 3a

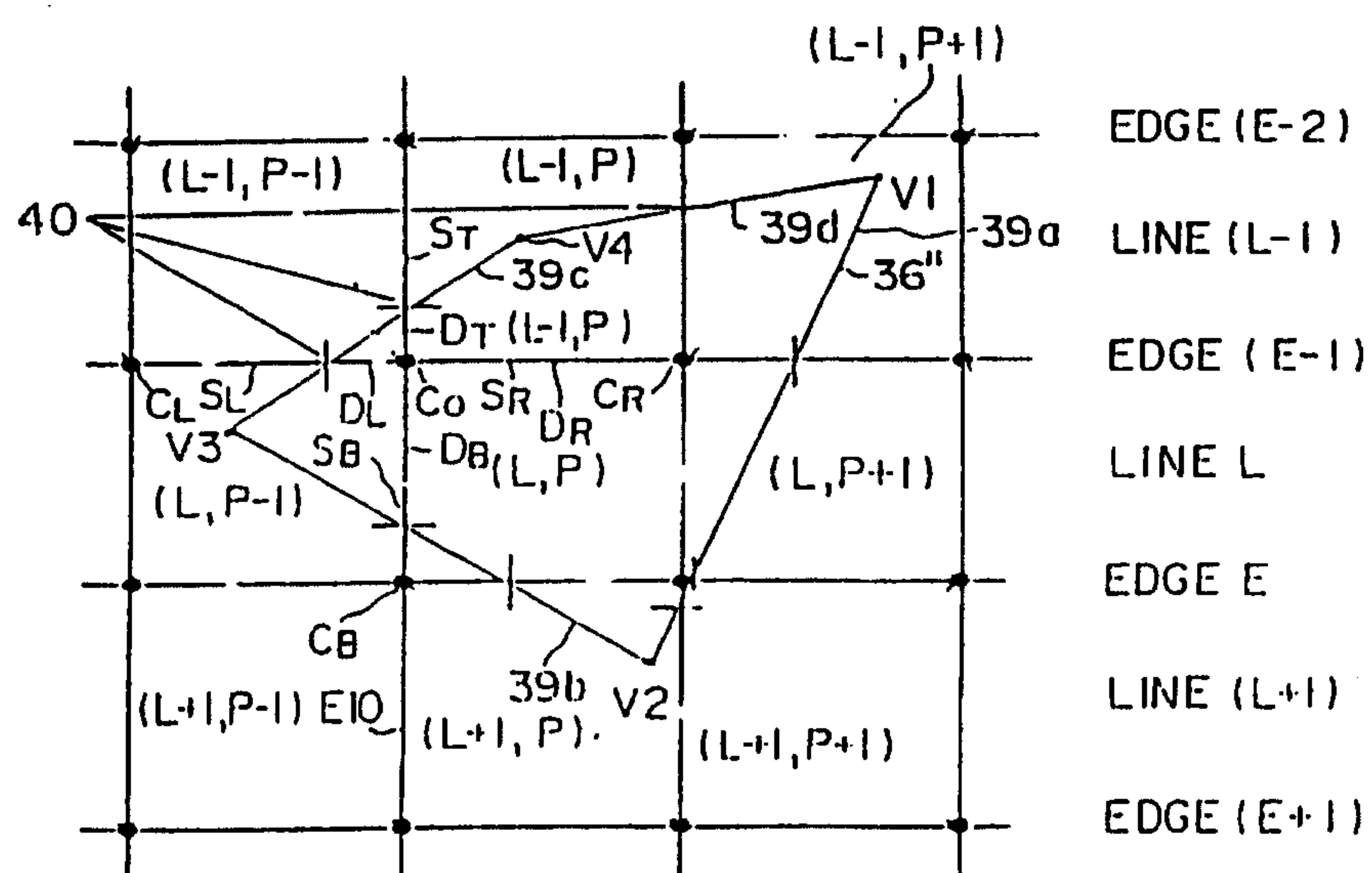
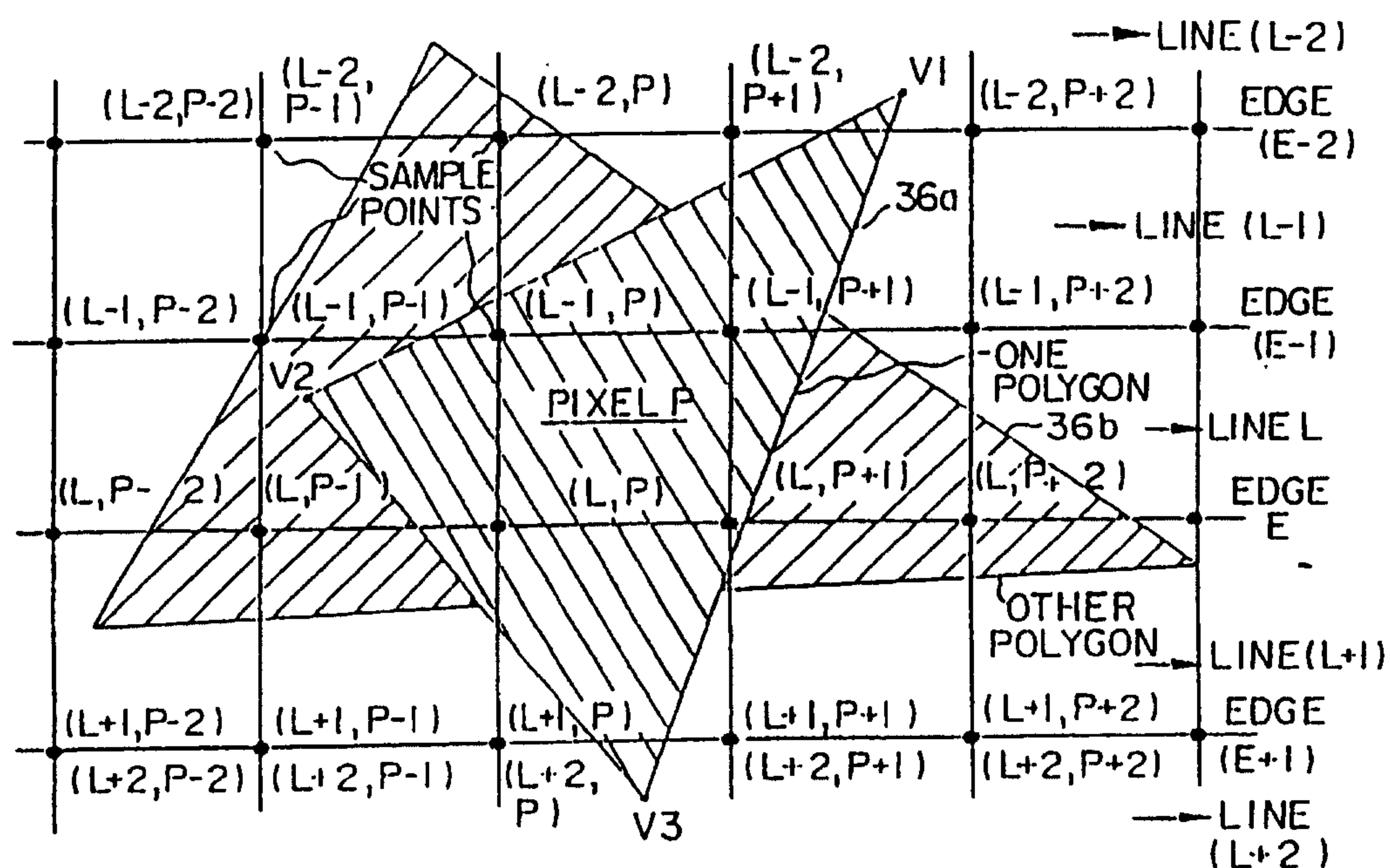


FIG. 3b

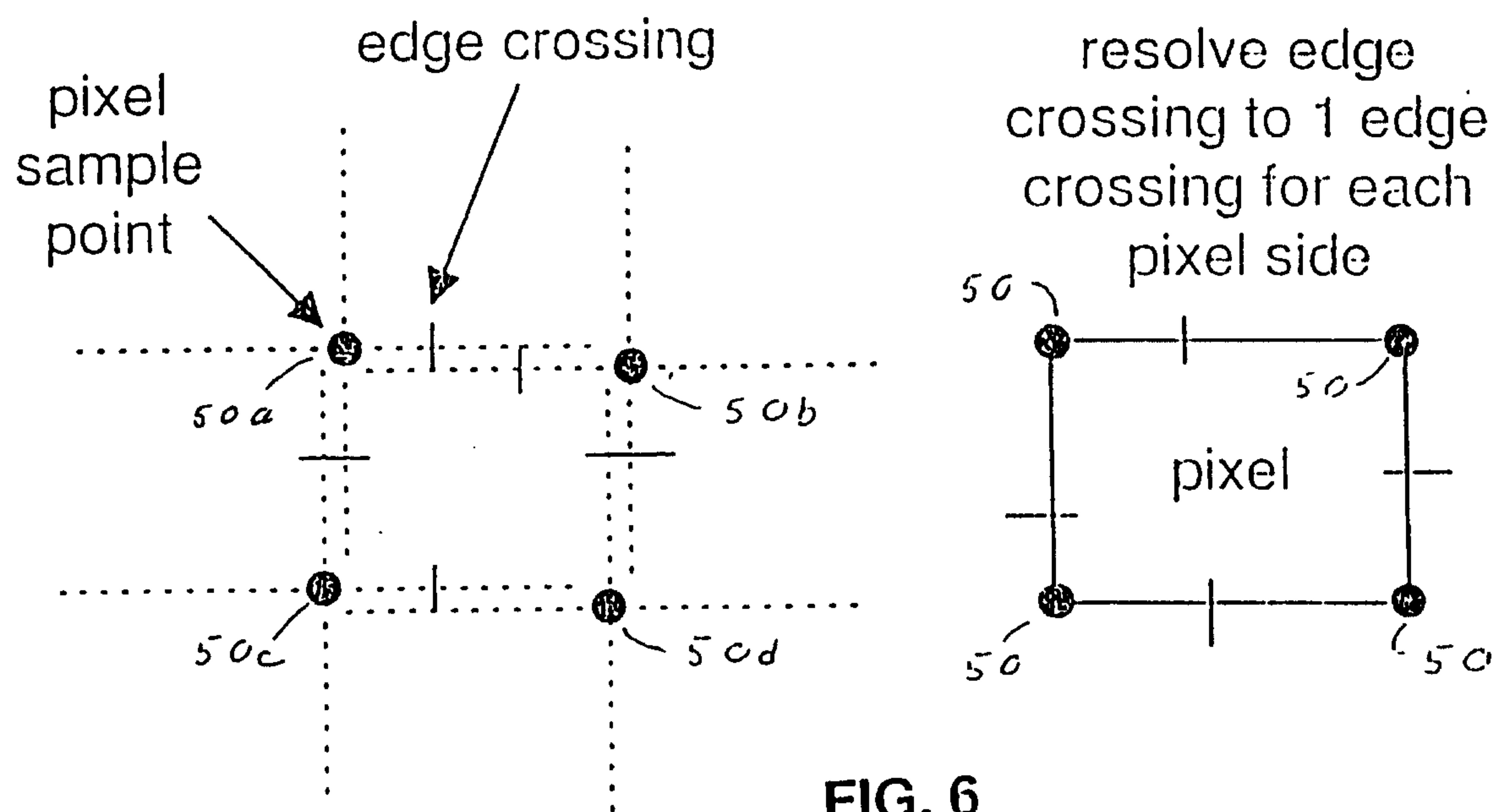
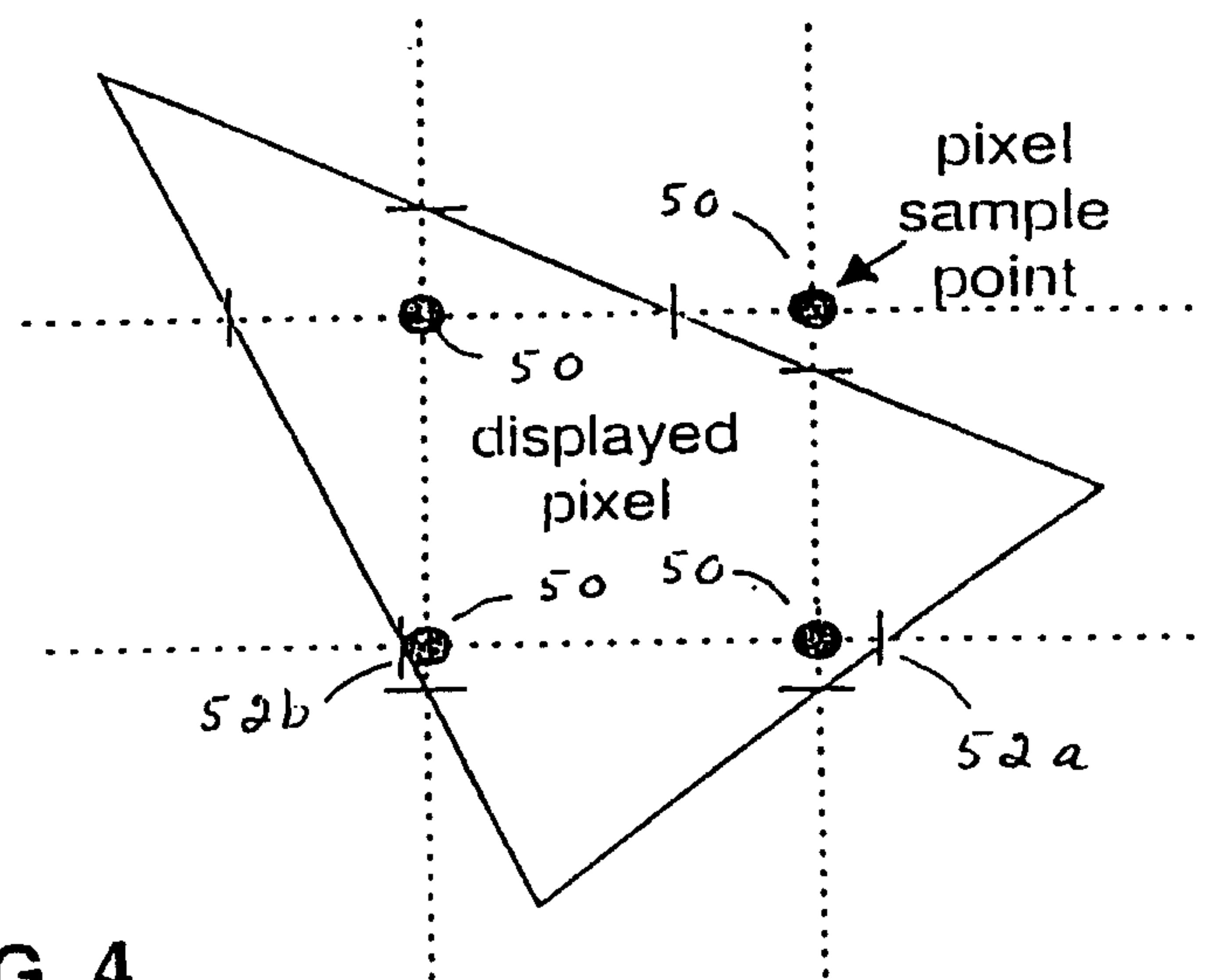


FIG. 5

Top Crossing Flag	Bot Crossing Flag	Left Crossing Flag	Right Crossing Flag	Edge crossing <4 bits>	Mesh Flag	description
0	0	0	0	1	x	No anti-aliasing
0	0	0	0	0	x	Conditionally no anti-aliasing
0	0	0	0	2	mesh	Anti-aliasing
0	0	0	0	3 to 7	mesh	spare
0	0	0	1	Right xing	mesh	Right crossing
0	0	1	0	Left xing	mesh	Left crossing
0	0	1	1	Left right	mesh	2 bits of left and right xing
0	1	0	0	Bottom xing	mesh	Bottom crossing
0	1	0	1	Right bot	mesh	2 bits of bot and right xing
0	1	1	0	Left bot	mesh	2 bits of left and bot xing
0	1	1	1	(Right + left + bot)/3	mesh	Average of crossings
1	0	0	0	Top xing	mesh	Top crossing
1	0	0	1	top right	mesh	2 bits of top and right xing
1	0	1	0	Left top	mesh	2 bits of left and top xing
1	0	1	1	(left + top + right)/3	mesh	Average of crossings
1	1	0	0	top bot	mesh	2 bits of top and bot xing
1	1	0	1	(top + bot + right)/3	mesh	Average of crossings
1	1	1	0	(top + bot + left)/3	mesh	Average of crossings
1	1	1	1	(left right + top + bot)/4	mesh	Average of crossings

Table 1. Encoding edge crossings

FIG. 7

