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(54) **Terminals for circuit board**

Anschlüsse für Leiterplatte

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(56) References cited:
EP-A- 0 571 093 **WO-A-98/52762**
US-A- 6 000 773 **US-A- 6 161 915**
US-A1- 2001 019 343

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EP 1 219 437 B1

Description

BACKGROUND OF THE INVENTION

Field of the Invention

[0001] The present invention relates to a circuit board for a marking material receptacle according to the preamble of independent claims 1 and 2.

[0002] Such circuit board is known from US 6 000 773 A.

Related Art

[0003] EP 0 571 093 A describes a flex tab circuit on a printhead of an ink jet printer, wherein ground pads are arranged on said flex tab circuit at the edges thereof.

[0004] In recent years, there has been developed ink cartridges of a type equipped with a circuit board that has a storage device for storing data relating to the ink contained in the ink cartridge. Such circuit boards are equipped with terminals for receiving power, data for storage, etc. from a printer, or for sending stored data to a printer.

[0005] The following description of a conventional terminal arrangement makes reference to Fig. 12. Circuit board 500 has a first terminal row with a ground terminal 510 in its center, a data I/O terminal 520 to a first side thereof, and a read/write signal terminal 530 to a second side thereof; and a second terminal row, located above the first terminal row, with a power terminal 540 in its center, a clock signal terminal 550 to a first side thereof, and a chip select signal terminal 560 to a second side thereof.

[0006] Printer 580 is provided with contact pins 570 corresponding to each of these terminals 510 -560; when the ink cartridge is installed in the mounting portion of the printer, terminals 510 -560 come into contact with the corresponding contact pins 570 to enable power, data, etc. to be exchanged between the circuit board 500 and the printer 580. The ground terminal 510 is used to allow the printer to determine if the ink cartridge is correctly installed; ink cartridge installation is detected by sensing contact (electrical continuity) between the ground terminal pin of the printer 580 and the ground terminal 510.

[0007] However, in the past there has been the problem that in some instances electrical continuity is sensed, and ink cartridge consequently determined to be installed, even where terminals 510 -560 and contact pins 570 are not in contact due to misalignment of the circuit board 500, etc. A resulting problem in such instances is that despite the determination that the ink cartridge is installed, the data stored in the storage device cannot be read.

SUMMARY OF THE INVENTION

[0008] In view of this problem, it is the object of the present invention to provide a terminal arrangement for a circuit board which permits accurate detection of contact with the terminals of the circuit board and to prevent short-circuiting between a power supply terminal and a ground terminal arranged on the circuit board. This object is achieved by the circuit board according to independent claims 1 and 2. Advantageous embodiments are described in the dependent claims.

[0009] According to an example a circuit board comprises a storage device for storing data relating to a marking material for print. The circuit board according to this example comprises: at least two ground terminals arranged on said circuit board at two edges thereof that are located on one axis thereof; and a plurality of terminals arranged on said circuit board, for read/write operations on said data relating to a marking material.

[0010] According to this example, there are provided at least two ground terminals arranged on said circuit board at two edges thereof that are located on one axis thereof, thereby affording accurate detection of contact with the terminals on the circuit board.

[0011] In the circuit board of this example, said plurality of terminals and said ground terminals may be arranged in a single row, with two of said at least two ground terminals being located at the outermost ends of said row. With this arrangement, contact with the terminals on the circuit board can be detected accurately.

[0012] In the circuit board of this example, said plurality of terminals may be arranged to form a plurality of rows, with two of said at least two ground terminals being located at the outermost ends of one of said plurality of rows. Said plurality of terminals may include a clock signal terminal, with said clock signal terminal being located between two of said at least two ground terminals. With this arrangement, the clock signal can be stabilized. Further, said plurality of terminals may include a power supply terminal, with two of said at least two ground terminals being located at the outermost ends of a row different than the row that contains said power supply terminal. This arrangement prevents short-circuiting between the power supply terminal and the ground terminals.

[0013] In the circuit board of this example, said plurality of terminals may include a power supply terminal and a control signal terminal, and said at least two ground terminals are not the terminals in closest proximity to said power supply terminal. This arrangement prevents short-circuiting between the power supply terminal and the ground terminals.

[0014] In the circuit board of this example, said plurality of terminals may include a data I/O terminal, a power supply terminal, a select signal terminal, a read/write control signal terminal, and a clock signal terminal, and said plurality of rows may have

a first row wherein said data I/O terminal and said

select signal terminal are located to either side of said power supply terminal, and

a second row wherein said read/write control signal terminal and said clock signal terminal are located between said two ground terminals,

said first row being located closer to the center of said circuit board than is said second row, and with said terminals in said first row and said terminals in said second row arranged in alternating fashion. This arrangement prevents short-circuiting between terminals, and also prevents unwanted contact of terminals.

[0015] In the circuit board of this example, said plurality of terminals may be arranged at intervals of approximately 1 mm in the direction of formation of said rows.

[0016] According to a second example, a circuit board comprises a storage device for storing data relating to a marking material, and provided to a marking material cartridge having a substantially rectangular shape. The circuit board according to this second example comprises: two ground terminals arranged on said circuit board at two edges thereof along one side thereof; and a plurality of terminals arranged parallel to one side of said circuit board, and used for storing said data.

[0017] According to the circuit board of the second example, two ground terminals are arranged on the circuit board at two edges thereof, thereby affording accurate detection of contact with the terminals on the circuit board.

[0018] In the circuit board of the second example, said plurality of terminals may form a plurality or rows parallel to one side of said circuit board, and said two ground terminals may be arranged at the outermost ends of one of said plurality of rows. Said plurality of terminals include a clock signal terminal, a control signal terminal, and a power supply terminal, with said two terminals being located at the outermost ends of a row that is different from the row that includes said power supply terminal, and that includes said clock signal terminal. This arrangement affords working effects similar to the circuit board pertaining to the first example.

[0019] According to a third example, a marking material receptacle is configured to be used with a printing device. The marking material receptacle pertaining to this third example comprises: a receptacle chamber for housing said marking material; and a circuit board having a storage device for storing data relating to the marking material housed in said receptacle chamber, said circuit board having a plurality of terminals arranged thereon along one axis thereof, and at least two ground terminals arranged on said circuit board at two edges thereof that are located on one axis thereof.

[0020] In the marking material receptacle pertaining to the third example, there is provided a circuit board having arranged thereon at least two ground terminals, located at two edges thereof that are located on one axis thereof, thereby affording accurate detection of whether the marking material receptacle has been installed. The

marking material is used for printing texts and/or images.

[0021] In the marking material receptacle pertaining to the third example, said plurality of terminals and said terminals may be arranged in a single row, with two of said at least two ground terminals being located at the outermost ends of said row. Said plurality of terminals may be arranged to form a plurality of rows, with two of said at least two ground terminals being located at the outermost ends of one of said plurality of rows. These arrangements as well afford accurate detection of whether the marking material receptacle has been installed.

[0022] In the marking material receptacle pertaining to the third example, said plurality of terminals may include a power supply terminal, control signal terminal, and clock signal terminal, with two of said at least two ground terminals being located at the outermost ends of a row that is different from the row that includes said power supply terminal, and in the same row as said clock signal terminal. This arrangement stabilizes the clock signal, and prevents short circuiting between the ground terminal and power supply terminal.

[0023] In the marking material receptacle pertaining to the third example, said plurality of terminals may include a data I/O terminal, a power supply terminal, a select signal terminal, a read/write control signal terminal, and a clock signal terminal; and said plurality of rows may have a first row wherein said data I/O terminal and said select signal terminal are located to either side of said power supply terminal, and a second row wherein said read/write control signal terminal and said clock signal terminal are located between said two ground terminals, with said first row being located closer to the center of said circuit board than is said second row, and with said terminals in said first row and said terminals in said second row arranged in alternating fashion. This arrangement prevents short-circuiting between terminals, and also prevents unwanted contact of terminals.

[0024] In the marking material receptacle pertaining to the third example, said plurality of terminals may be arranged at intervals of approximately 1 mm in the direction of formation of said rows. While incorrect installation of the marking material receptacle may result in faulty contact of terminals on the circuit board, with this arrangement, any faulty contact of terminals on the circuit board can be detected appropriately by means of situating the ground terminals at both edges or the outermost edge of the circuit board.

[0025] In the marking material receptacle pertaining to the third example, said marking material receptacle may be an ink cartridge, or a toner cartridge. In either case, accurate detection of cartridge installation will be required.

BRIEF DESCRIPTION OF THE DRAWINGS

[0026]

Figure 1 is an illustrative diagram of an exemplary circuit board terminal arrangement pertaining to a first embodiment.

Figure 2 is a side view of the circuit board shown in Fig. 1.

Figure 3 is an illustrative diagram depicting contact of the terminals on a circuit board pertaining to a first embodiment with the contact pins of a printer.

Figure 4 is a block diagram depicting schematically the circuit arrangement of the circuit board pertaining to the first embodiment.

Figure 5 is a perspective view of the general scheme of an ink cartridge comprising the circuit board of the first embodiment.

Figure 6 is an enlarged view of the circuit board mounting portion in Fig. 4.

Figure 7 is an illustrative diagram depicting the ink cartridge installed in the printer.

Figure 8 is an illustrative diagram of an exemplary circuit board terminal arrangement pertaining to a second embodiment.

Figure 9 is an illustrative diagram of an exemplary circuit board terminal arrangement pertaining to an example.

Figure 10 is an illustrative diagram of an exemplary circuit board terminal arrangement pertaining to a third embodiment.

Figure 11 is a perspective view of the general scheme of an on carriage type ink cartridge comprising the circuit board of the first embodiment.

Figure 12 is an illustrative diagram of a conventional board terminal arrangement.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0027] A more complete understanding of the present invention will be gained through the following description of the embodiments in the order indicated below, making reference to the accompanying drawings. The description that follows is arranged in the following 6 sections, A-D

A. Exemplary circuit board terminal arrangement pertaining to a first embodiment

B. Exemplary circuit board circuit arrangement pertaining to a first embodiment

C. Exemplary ink cartridge arrangement comprising circuit board pertaining to a first embodiment

D. Other embodiments

A. Exemplary circuit board terminal arrangement pertaining to a first embodiment

[0028] The following description of a circuit board terminal arrangement pertaining to a first embodiment makes reference to Figs. 1 -3. Fig. 1 is an illustrative diagram of an exemplary circuit board terminal arrangement pertaining to a first embodiment. Fig. 2 is a side view of the circuit board shown in Fig. 1. Fig. 3 is an illustrative diagram depicting contact of the terminals on a circuit board pertaining to a first embodiment with the contact pins of a printer.

[0029] Circuit board 10 is of substantially rectangular shape, and comprises a through-hole 11 for positioning it during ink cartridge installation, and a notch 12, formed on the peripheral edge, for positioning it during ink cartridge installation. Circuit board 10 is provided, on the face 13 thereof juxtaposed to the printer, with a plurality of terminals 20 -27 located, and on the face 14 thereof joined to the ink cartridge, with a storage device 30 connected to terminals 20 -27 (see Fig. 2).

[0030] In the upper half of the printer-juxtaposed face 13 of circuit board 10 is provided a substantially circular test terminal 20 used to test the storage device 30 when shipped from the factory. In the lower half are provided a plurality of substantially rectangular terminals 21 -27, arrayed in two rows, i.e., an upper and lower row. The terminals in the upper row are, proceeding from the left in Fig. 1, an I/O terminal 21 for data input/output, a power supply terminal 22 for supplying power, and a chip select terminal 23 for input of a chip select signal (select signal) CS for selectively activating storage device 30. The terminals in the lower row are, proceeding from the left in Fig. 1, a ground terminal 24, a read/write terminal 25 for inputting read/write control signals W/R for storage device 30, a dock terminal 26 for inputting a clock signal CLK (sync signal) for storage device 30, and a ground terminal 27.

[0031] As will be apparent from Fig. 1, in the circuit board 10 pertaining to the present embodiment, the ground terminals 24, 27 are located at the two ends of the lowermost of the two rows of terminals, with the other terminals 21 -23, 25, 26 being located further inward from the ground terminals 24, 27. The clock terminal 26 is located between the ground terminals 24, 27. The ground terminals 24, 27 are placed in a different row from the power supply terminal 22, in a spatial relation-

ship such that the gaps between the vicinities of these terminals and the vicinity of the power supply terminal 22 are not the shortest distance. That is, the ground terminals 24, 27 are not the terminals in closest proximity to the power supply terminal 22. In the present embodiment, "ground" refers to a signal reference voltage, and may be either positive or negative voltage.

[0032] In the present embodiment, circuit board 10 has dimensions of 11.9 mm height, 7.5 mm width, and 0.71 mm thickness. Terminal 21-27 dimensions are 1.8 mm height and 1.0 mm width. However, these dimensions are merely exemplary, and may differ by as much as about ± 0.5 mm, for example. Gaps between adjacent terminals, for example, the distance between I/O terminal 21 and power supply terminal 22, is about 1 mm, for example. Gaps between terminals may also differ by as much as about ± 0.5 mm, for example.

[0033] The following description of contact of the terminals on circuit board 10 herein with the contact pins of a printer makes reference to Fig. 3. The printer comprises contact pins P1 -P7 corresponding to terminals 21 -27; when the ink cartridge is installed in the mounting portion of the printer, the terminals come into contact with contact pins P1 -P7, thus supplying power from the printer to storage device 30 of circuit board 10, and enabling data exchange between the printer and circuit board 30.

[0034] Ground terminals 24, 27 are used to allow the printer to determine if the ink cartridge has been installed correctly. When two ground terminal contact pins P4, P7 located on the printer contact the ground terminals 24, 27, the printer senses electrical continuity to detect installation of the ink cartridge.

[0035] The circuit board 10 in the present embodiment is provided with ground terminals 24, 27 located at two edges thereof, the areas that are most susceptible to the effects of vertical or horizontal tilt or misalignment with respect to the direction of the terminal arrays, and to faulty terminal-pin contact. Accordingly, if the printer detects contact between the ground terminals 24, 27 and the ground terminal contact pins P4, P7, it is extremely unlikely that the other terminals 21 - 23, 25, 26 are not in respective contact with contact pins P1 -P3, P5, P6, thus avoiding the problem of an inability to access the storage device 30 despite detecting installation of the ink cartridge.

[0036] Further, since the ground terminals 24, 27 are not the terminals in closest proximity to the power supply terminal 22, short-circuiting between the power supply terminal 22 and the ground terminals 24, 27 may be prevented.

B. Exemplary circuit board circuit arrangement pertaining to a first embodiment

[0037] The following description of an exemplary circuit board circuit arrangement pertaining to a first embodiment makes reference to Fig. 4. Fig. 4 is a block

diagram depicting schematically the circuit arrangement of the circuit board pertaining to the first embodiment.

[0038] As noted, circuit board 10 comprises a storage device 30 located on the face 13 thereof joined to the ink cartridge. The storage device 30 in the present embodiment is an EEPROM comprising a serially accessed memory cell 31, a read/write controller 32 for controlling data read/write operations to and from memory cell 31, and an address counter 33 for counting up, in sync with the clock signal CLK, during data read/write operations between printer 100 and memory cell 31 via read/write controller 32.

[0039] Contact pin P1 of printer 100 is used for input/output of data to and from printer 10; contact pin P2 is used to supply power supply voltage VDD from printer 100 to the power supply terminal 22 of circuit board 10. Contact pin P3 is used to send a chip select signal CS; contact pins P4 and P7 are used to supply pullup voltage VCC; contact pin P5 is used to send read/write control signals; and contact pin P6 is used to send the clock signal CLK.

[0040] Contact pin P4 is coupled with a cartridge out sensing terminal of the central processing unit (CPU) 60 in printer 100, and is also coupled, via pullup resistance, to the cartridge out sensing power supply VCC, so as to have potential equal to the cartridge out voltage VCC. Contact pin P7 is grounded in printer 100 and has potential equal to ground voltage VSS.

C. Exemplary ink cartridge arrangement comprising circuit board pertaining to a first embodiment

[0041] The following description of an exemplary ink cartridge arrangement having installed thereon the circuit board 10 herein makes reference to Figs. 5 -7. Fig. 5 is a perspective view of the general scheme of an ink cartridge comprising the circuit board 10 of the first embodiment. Fig. 6 is an enlarged view of the circuit board 10 mounting portion in Fig. 4. Fig. 7 is an illustrative diagram depicting the ink cartridge installed in the printer.

[0042] Ink cartridge 40 is configured for installation in an "off carriage" type printer, i.e., one in which the ink cartridge is not installed on a carriage. Off carriage type printers are typically large-scale printers; the ink cartridges employed in such large-scale printers are typically larger in size than the ink cartridges employed in on-carriage type printers.

[0043] Ink cartridge 40 comprises a circuit board mounting portion 41 for mounting circuit board 10; an ink feed orifice 42 for supplying ink from ink cartridge 40 to the printer; an air feed orifice 43 allowing intake of air into ink cartridge 40 to allow smooth flow of ink; and guide portions 44 for installation in the printer. The exterior dimensions of ink cartridge 40 are such that the side thereof (i.e. the depth direction) extending perpendicular to the side on which the guide portions 44, etc. are formed (i.e. the width direction) is longer than the width direction. The relationship of the depthwise di-

mension to the widthwise direction of circuit board 10, expressed as a ratio of the two, is 15 : 1 or greater, for example.

[0044] As shown in Fig. 5, circuit board 10 is positioned by means of through-hole 11 and notch 12, and secured on the circuit board mounting portion 41 of ink cartridge 40.

[0045] When installing the ink cartridge 40 in the printer, the guide portions of ink cartridge 40 guide the guide pins 101 on the printer so that the circuit board mounting portion 41, ink feed orifice 42, and air feed orifice 43 are appropriately contacted/coupled with a contact pin 102, ink feed orifice 103, and air feed orifice 104 on the printer.

[0046] The following description of the process executed by printer 100 on circuit board 10 when the ink cartridge 40 is installed in printer 100 makes reference to Fig. 4.

[0047] Contact pin P7 of the printer 100 is grounded within the printer, and contact pin P4 is coupled to the cartridge out sensing terminal of CPU 60. When ink cartridge 40 is inserted into printer 100, the contact pins P1 -P7 of the printer 100 come into contact with the terminals 21 -27 of circuit board 10. At this point, if contact pins P4, P7 of the printer 100 are both in contact with the respective ground terminals 24, 27 of circuit board 10, CPU 60 detects ground voltage VSS. That is, when ink cartridge 40 is installed, printer 100 (CPU 60) detects electrical continuity of contact pins P4, P7 and circuit board 10 ground terminals 24, 27, and determines that the ink cartridge 40 is installed correctly.

[0048] If, on other hand, either of the contact pins P4, P7 is not in contact with the corresponding circuit board 10 ground terminal 24, 27, CPU 60 detects the cartridge out voltage VCC. That is, printer 100 cannot detect electrical continuity of contact pins P4, P7 and circuit board 10 ground terminals 24, 27, and determines that the ink cartridge 40 is not installed correctly.

[0049] If printer 100 detects electrical continuity of contact pins P4, P7 and circuit board 10 ground terminals 24, 27, and determines that the ink cartridge 40 is installed correctly, power VDD is supplied to power supply terminal 22, and a chip select signal CS is sent to the storage device 30 to be accessed.

[0050] Upon receiving chip select signal CS, storage device 30 becomes active and awaits a control signal from the printer 100. Where, for example, the printer 100 is to read data stored in storage device 30, a clock signal CLK corresponding to the desired address in memory cell 31 is input to clock terminal 26, and a read/write control signal W/R instructing a read operation is input to read/write terminal 25.

[0051] The clock signal CLK input to clock terminal 26 is input to address counter 33, whereupon the address counter 33 counts up the address, in sync with the input clock signal CLK, to indicate an address in memory cell 31. The read/write control signal W/R input via read/write terminal 25 is input and analyzed by the read/write

controller 32. In response to the analyzed read command, the read/write controller 32 reads data from the address in memory cell 31 indicated by the address counter 33, and outputs the data to the printer 100 via I/O terminal 21.

[0052] As noted, the circuit board 10 of the first embodiment 10 is provided with ground terminals 24, 27 located at two sides that are susceptible to faulty terminal-contact pin contact, thereby reducing or eliminating the likelihood that the printer [will detect that?] ground terminals 24, 27 and the ground terminal contact pins P4, P7 are in respective contact while the other terminals 21 -23, 25, 26 are not in respective contact with contact pins P1 -P3, P5, P6. As a result, ink cartridge installation may be detected accurately. It is additionally possible to avoid the problem of an inability to access the storage device 30 despite detecting installation of the ink cartridge.

[0053] Typically, where the ratio of circuit board 10 widthwise dimension to ink cartridge 40 depthwise dimension is large, as in the present embodiment, slight misalignment of the ink cartridge during installation can easily result in significant misalignment of circuit board terminals with the corresponding contact pins, so that terminals do not contact the corresponding contact pins. With the circuit board 10 of the present embodiment, however, installation of the ink cartridge can be detected accurately despite the large external dimensions of the circuit board and ink cartridge.

[0054] Further, as the ground terminals 24, 27 are not the terminals in closest proximity to the power supply terminal 22, short-circuiting between the power supply terminal 22 and the ground terminals 24, 27 may be prevented. Since the clock terminal 26 is located between the ground terminals 24, 27, the clock signal CLK may be stabilized.

D. Other embodiments

[0055] The following description of other embodiments makes reference to Figs. 8 -10. Fig. 8 is an illustrative diagram of an exemplary circuit board terminal arrangement pertaining to a second embodiment. Fig. 9 is an illustrative diagram of an exemplary circuit board terminal arrangement pertaining to a third embodiment. Fig. 10 is an illustrative diagram of an exemplary circuit board terminal arrangement pertaining to a fourth embodiment. As the circuit boards pertaining to the second to fourth embodiments have arrangements similar to those of circuit board 10 pertaining to the first embodiment, elements having the same function are assigned the same symbols used in the first embodiment and are not described in unnecessary detail.

[0056] The following description of a second embodiment makes reference to Fig. 8. The circuit board 200 pertaining to the second embodiment is provided in the upper half of its juxtaposed face 13 with a substantially circular test terminal 20 used to test the storage device

30 when shipped from the factory, and in the lower half of its juxtaposed face 13 with a plurality of substantially rectangular terminals 201 -207, arrayed in two rows, i. e., an upper and lower row. Of the plurality of rectangular terminals, those in the upper row are, proceeding from the left in the drawing, an I/O terminal 201 for data input/output, a power supply terminal 202 for supplying power, and a chip select terminal 203 for input of a chip select signal for selectively activating storage device 30. The terminals in the lower row are, proceeding from the left in the drawing, a ground terminal 204, a read/write terminal 205 for inputting read/write control signals for storage device 30, a clock terminal 206 for inputting a clock signal (sync signal) for storage device 30, and a ground terminal 207.

[0057] As will be apparent from Fig. 8, in the circuit board 200 pertaining to the present embodiment, the ground terminals 204, 207 are located at two edges thereof. I/O terminal 201 and chip select terminal 203 are wider than the I/O terminal 21 and chip select terminal 23 in the first embodiment, but notwithstanding, ground terminals 204, 207 are located at two edges of circuit board 100, with the other terminals 202, 205, 206 being located further inward from the ground terminals 204, 207. The clock terminal 206 is located between the ground terminals 204, 207. The ground terminals 204, 207 are placed in a different row from the power supply terminal 222, and are not the terminals in closest proximity to the power supply terminal 202.

[0058] Accordingly, by using the circuit board 200 pertaining to the second embodiment, it is possible to accurately detect installation of the ink cartridge. It is also possible to avoid situations of an inability to access the storage device 30 despite detecting installation of the ink cartridge. Further, the clock signal CLK can be stabilized, and short-circuiting between the power supply terminal 202 and the ground terminals 204, 207 may be prevented.

[0059] The following description of an example makes reference to Fig. 9. The circuit board 300 pertaining to this example is provided in the upper half of its juxtaposed face 13 with a substantially circular test terminal 20 used to test the storage device 30 when shipped from the factory, and in the lower half of its juxtaposed face 13 with a plurality of circular terminals 301 -306, arrayed randomly. The circular terminals are, proceeding from the left in the drawing, a ground terminal 301, a read/write terminal 302 for inputting read/write control signals for storage device 30, a clock terminal 303 for inputting a clock signal (sync signal) for storage device 30, a power supply terminal 304 for supplying power, an I/O terminal 305 for data input/output, and a ground terminal 306.

[0060] As will be apparent from Fig. 9, in the circuit board 300 pertaining to this example, the ground terminals 301, 306 are located at two edges thereof, with the other terminals 302 -305 being located further inward from the ground terminals 301, 306. The ground termi-

nals 301, 306 are not the terminals in closest proximity to the power supply terminal 304.

[0061] Accordingly, by using the circuit board 300 pertaining to this example, it is possible to accurately detect installation of the ink cartridge. It is also possible to avoid situations of an inability to access the storage device 30 despite detecting installation of the ink cartridge. Further, short-circuiting between the power supply terminal 304 and the ground terminals 301, 306 may be prevented.

[0062] The following description of a third embodiment makes reference to Fig. 10. The circuit board 400 pertaining to the third embodiment is provided in the upper half of its juxtaposed face 13 with a substantially circular test terminal 20 used to test the storage device 30 when shipped from the factory, and in the lower half of its juxtaposed face 13 with a plurality of substantially rectangular terminals 401 to 407, arrayed in a single row. The substantially rectangular terminals are, proceeding from the left in the drawing, a ground terminal 401, a read/write terminal 402 for inputting read/write control signals for storage device 30, a clock terminal 403 for inputting a clock signal (sync signal) for storage device 30, a power supply terminal 404 for supplying power, an I/O terminal 405 for data input/output, a chip select terminal 406 for inputting a chip select signal, and a ground terminal 407.

[0063] As will be apparent from Fig. 10, in the circuit board 400 pertaining to the present embodiment, the ground terminals 401, 407 are located at two outermost edges thereof, with the other terminals 402 to 406 being located further inward from the ground terminals 401, 407. The ground terminals 401, 407 are not the terminals in closest proximity to the power supply terminal 404.

[0064] Accordingly, by using the circuit board 400 pertaining to the third embodiment, it is possible to accurately detect installation of the ink cartridge. It is also possible to avoid situations of an inability to access the storage device 30 despite detecting installation of the ink cartridge. Further, short-circuiting between the power supply terminal 404 and the ground terminals 401, 407 may be prevented.

[0065] While the circuit board herein has been described hereinabove with reference to certain preferred embodiments, these merely facilitate understanding of the invention, and are not limiting thereof. It is of course to be understood that a variety of modifications and improvements are possible within the scope of the invention as set forth in the claims, and that the invention includes these equivalents.

[0066] For example, in the first embodiment, circuit board 10 is described in terms of being mounted on an ink cartridge 40 for use in an off cartridge type printer, but it could also be mounted on an ink cartridge 48 for use in an on cartridge type printer, as shown in Fig. 11. In this case as well, by using the circuit board 10, 200, 300 or 400 pertaining to the first to third embodiments,

it is possible to accurately detect installation of the ink cartridge, and to avoid situations of an inability to access the storage device 30 despite detecting installation of the ink cartridge.

[0067] The terminal arrangements described in the preceding embodiments are merely exemplary; it is sufficient to locate the ground terminals at two edges of the circuit board. Or, where terminals are arranged in a plurality of rows, they may be located at the outermost ends of one of the rows. The working effects of the invention may be achieved with the proviso that such an arrangement is provided.

[0068] Whereas in the first embodiment the circuit board 10 is mounted on an ink cartridge 40 the circuit board 10 could be mounted on a toner cartridge. Mounting on a toner cartridge experiences problems similar to those encountered with mounting on an ink cartridge, and these problems may be solved through the use of circuit board 10.

[0069] Whereas in the first embodiment the relationship of the depthwise dimension of ink cartridge 40 to the widthwise direction of circuit board 10 is 15:1, the ratio of dimensions of the two is not limited thereto. Exemplary values of 10:1 to 30:1 could be used, for example. In any case, the working effects of the invention are achieved wherever the ratio of depthwise dimension of ink cartridge 40 to the widthwise direction of circuit board 10 is sufficiently great that improper installation of ink cartridge 40 prevents proper contact with circuit board 10.

[0070] Obviously, numerous modifications and variations of the present invention are possible in light of the above teachings. For example, features described for certain embodiments may be combined with other embodiments described herein. It is therefore to be understood that within the scope of the appended claims, the invention may be practiced otherwise than as specifically described herein.

Claims

1. A circuit board (400) for a marking material receptacle (40, 48), comprising:

a storage device (30) for storing data relating to a marking material for print; and

a plurality of terminals (401-407) arranged on said circuit board (400) and used for read/write operations on said data relating to said marking material for print, said plurality of terminals including a power supply terminal (404) and a control signal terminal (402),

characterized by further comprising:

at least two ground terminals (401, 407) ar-

ranged on said circuit board (400) at two edges of said circuit board that are located on one axis of said circuit board,

wherein said plurality of terminals (401-407) and said ground terminals (401, 407) are arranged in a single row, and

wherein said at least two ground terminals (401, 407) are not the terminals in closest proximity to said power supply terminal (404) and are located at the outermost ends of said row, and are used for detecting the contact of terminals (P1-P7) on a printing device (100) with said terminals (401-407) on said circuit board (400).

2. A circuit board (10, 200) for a marking material receptacle (40, 48), comprising:

a storage device (30) for storing data relating to a marking material for print; and

a plurality of terminals (21-27, 201-207) arranged on said circuit board (10, 200) and used for read/write operations on said data relating to a marking material for print, said plurality of terminals including a power supply terminal (22, 202) and a control signal terminal (25, 205),

characterized by further comprising:

at least two ground terminals (24, 27; 204, 207) arranged on said circuit board (10, 200) at two edges of said circuit board that are located on one axis of said circuit board; and

wherein said plurality of terminals (21-27, 201-207) are arranged to form a plurality of rows, and wherein said at least two ground terminals (24, 27; 204, 207) are not the terminals in closest proximity to said power supply terminal (22, 202) and two of said at least two ground terminals are located at the outermost ends of one of said plurality of rows, and are used for detecting the contact of terminals (P1-P7) on a printing device (100) with said terminals (21-27, 201-207) on said circuit board (10, 200).

3. A circuit board (10, 200) according to claim 2, further comprising a clock signal terminal (26, 206) wherein said clock signal terminal is located between two of said at least two ground terminals (24, 27; 204, 207).

4. A circuit board (10, 200) according to claim 3, wherein two of said at least two ground terminals (24, 27; 204, 207) are located at the outermost ends of a row that is different from the row that includes

said power supply terminal (22, 202) and is the same row that includes said clock signal terminal (26, 206).

5. A circuit board (10, 200) according to claim 4, wherein said power supply terminal (22, 202) is located at the center of a row that is different from the row in which said ground terminals (24, 27; 204, 207) are located. 5
6. A circuit board (10, 200) according to claim 3 or 4 or 5, further comprising a data I/O terminal (21, 201) and a select signal terminal (23, 203). 10
7. A circuit board (10, 200) according to claim 6, wherein said plurality of rows have: 15
 - a first row wherein said data I/O terminal (21, 201) and said select signal terminal (23, 203) are located to either side of said power supply terminal (22, 202), and 20
 - a second row wherein said read/write control signal terminal (25, 205) and said clock signal terminal (26, 206) are located between said two ground terminals (24, 27; 204, 207), 25
8. A circuit board (10, 200) according to claim 7, wherein said first row is located closer to the center of said circuit board than is said second row 30
9. A circuit board (10, 200) according to claim 7 or 8, wherein said terminals (21-23, 201-203) in said first row and said terminals (24-27, 204-207) in said second row are arranged in alternating fashion. 35
10. A circuit board (10, 200) according to any of claims 2 to 9, wherein said plurality of terminals (21-27, 201-207) are arranged at intervals of 1 mm in the direction of formation of said rows. 40
11. A circuit board (10, 200) according to any of claims 2 to 10, wherein said marking material receptacle (40, 48) is an ink cartridge. 45
12. A circuit board (10, 200) according to any of claims 2 to 10, wherein said marking material receptacle (40, 48) is a toner cartridge. 50
13. A circuit board (10, 200) according to any of claims 2 to 12, wherein said marking material receptacle (40, 48) comprises a connecting end face having two edges and connected to said printing device (100), said connecting end face having a marking material feed orifice (42, 103) at the center thereof, wherein said circuit board (10, 200) is located at one of said edges of said connecting end face. 55

Patentansprüche

1. Schaltplatte (400) für eine Markierungsmaterialaufnahme (40, 48), umfassend:

eine Speichereinrichtung (30) zum Speichern von Daten in Bezug auf ein Markierungsmaterial für einen Druck; und

eine Vielzahl von Anschlüssen (401-407), angeordnet auf der Schaltplatte (400) und verwendet für Lese-/Schreiboperationen in den Daten in Bezug auf das Markierungsmaterial für einen Druck, wobei die Vielzahl von Anschlüssen einen Energieversorgungsanschluss (404) und einen Steuersignalanschluss (402) inkludiert,

gekennzeichnet durch fernerer Umfassen:

mindestens zweier Erdungsanschlüsse (401, 407), angeordnet an der Schaltplatte (400) in zwei Kanten der Schaltplatte, die sich auf einer Achse der Schaltplatte befinden,

wobei die Vielzahl von Anschlüssen (401-407) und die Erdungsanschlüsse (401, 407) in einer einzelnen Zeile angeordnet sind, und wobei die mindestens zwei Erdungsanschlüsse (401, 407) nicht die Anschlüsse in nächster Nähe zu dem Energieversorgungsanschluss (404) sind und sich in den äußersten Enden der Zeile befinden, und zum Erfassen des Kontaktes von Anschlüssen (P1-P7) in einer Druckeinrichtung (100) mit den Anschlüssen (401-407) auf der Schaltplatte (400) verwendet werden.

2. Schaltplatte (10, 200) für eine Markierungsmaterialaufnahme (40, 48), umfassend:

eine Speichereinrichtung (30) zum Speichern von Daten in Bezug auf ein Markierungsmaterial für einen Druck; und

eine Vielzahl von Anschlüssen (21-27, 201-207), angeordnet auf der Schaltplatte (10, 200) und verwendet für Lese-/Schreiboperationen in den Daten in Bezug auf ein Markierungsmaterial für einen Druck, wobei die Vielzahl von Anschlüssen einen Energieversorgungsanschluss (22, 202) und einen Steuersignalanschluss (25, 205) inkludiert,

gekennzeichnet durch fernerer Umfassen:

mindestens zweier Erdungsanschlüsse (24, 27; 204, 207), angeordnet auf der Schaltplatte

(10, 200) in zwei Kanten der Schaltplatte, die sich auf einer Achse der Schaltplatte befinden, und

wobei die Vielzahl von Anschlüssen (21-27, 201-207) angeordnet sind, eine Vielzahl von Zeilen zu bilden, und

wobei die mindestens zwei Erdungsanschlüsse (24, 27; 204, 207) nicht die Anschlüsse in nächster Nähe zu dem Energieversorgungsanschluss (22, 202) sind und sich zwei der mindestens zwei Erdungsanschlüsse in den äußersten Enden von einer aus der Vielzahl von Zeilen befinden, und zum Erfassen des Kontaktes von Anschlüssen (P1-P7) in einer Druckeinrichtung (100) mit den Anschlüssen (21-27, 201-207) auf der Schaltplatte (10, 200) verwendet werden.

3. Schaltplatte (10, 200) nach Anspruch 2, ferner umfassend einen Taktsignalanschluss (26, 206), wobei sich der Taktsignalanschluss zwischen zwei der mindestens zwei Erdungsanschlüsse (24, 27; 204, 207) befindet.

4. Schaltplatte (10, 200) nach Anspruch 3, wobei sich zwei der mindestens zwei Erdungsanschlüsse (24, 27; 204, 207) in den äußersten Enden einer Zeile befinden, die sich von der Zeile unterscheidet, die den Energieversorgungsanschluss (22, 202) inkludiert und die gleiche Zeile ist, die den Taktsignalanschluss (26, 206) inkludiert.

5. Schaltplatte (10, 200) nach Anspruch 4, wobei sich der Energieversorgungsanschluss (22, 202) in der Mitte einer Zeile befindet, die sich von der Zeile unterscheidet, in der sich die Erdungsanschlüsse (24, 27; 204, 207) befinden.

6. Schaltplatte (10, 200) nach Anspruch 3 oder 4 oder 5, ferner umfassend einen Daten-E/A-Anschluss (21, 201) und einen Auswahlsignalanschluss (23, 203).

7. Schaltplatte (10, 200) nach Anspruch 6, wobei die Vielzahl von Zeilen haben:

eine erste Zeile, wobei sich der Daten-E/A-Anschluss (21, 201) und der Auswahlsignalanschluss (23, 203) auf beiden Seiten von dem Energieversorgungsanschluss (22, 202) befinden, und

eine zweite Zeile, wobei sich der Lese-/Schreibsteuersignalanschluss (25, 205) und der Taktsignalanschluss (26, 206) zwischen den zwei Erdungsanschlüssen (24, 27; 204, 207) befinden.

8. Schaltplatte (10, 200) nach Anspruch 7, wobei sich die erste Zeile näher zu der Mitte der Schaltplatte befindet als die zweite Zeile.

9. Schaltplatte (10, 200) nach Anspruch 7 oder 8, wobei die Anschlüsse (21-23, 201-203) in der ersten Zeile und die Anschlüsse (24-27, 204-207) in der zweiten Zeile auf eine abwechselnde Weise angeordnet sind.

10. Schaltplatte (10, 200) nach beliebigen von Ansprüchen 2 bis 9, wobei die Vielzahl von Anschlüssen (21-27, 201-207) in Intervallen von 1mm in der Richtung einer Bildung der Zeilen angeordnet sind.

11. Schaltplatte (10, 200) nach beliebigen von Ansprüchen 2 bis 10, wobei die Markierungsmaterialaufnahme (40, 48) eine Tintenpatrone ist.

12. Schaltplatte (10, 200) nach beliebigen von Ansprüchen 2 bis 10, wobei die Markierungsmaterialaufnahme (40, 48) eine Tonerpatrone ist.

13. Schaltplatte (10, 200) nach beliebigen von Ansprüchen 2 bis 12, wobei die Markierungsmaterialaufnahme (40, 48) eine verbindende Endfläche mit zwei Kanten und verbunden mit der Druckeinrichtung (100) umfasst, wobei die verbindende Endfläche eine Markierungsmaterialspeiseöffnung (42, 103) in der Mitte davon hat, wobei sich die Schaltplatte (10, 200) in einer der Kanten der verbindenden Endfläche befindet.

Revendications

1. Carte de circuit imprimé (400) pour un réservoir de produit de marquage (40, 48), comprenant :

un dispositif de mémorisation (30) permettant de mémoriser des données relatives à un produit de marquage pour impression ; et une pluralité de bornes (401-407) agencées sur ladite carte de circuit imprimé (400) et utilisées pour des opérations de lecture/écriture sur lesdites données relatives au dit produit de marquage pour impression, ladite pluralité de bornes comprenant une borne d'alimentation en courant (404) et une borne de signaux de commande (402),

caractérisée en ce qu'elle comprend en outre :

au moins deux bornes de masse (401, 407) agencées sur ladite carte de circuit imprimé (400) au niveau de deux bords de ladite carte de circuit imprimé qui sont situés sur un axe de

ladite carte de circuit imprimé,

dans laquelle ladite pluralité de bornes (401-407) et lesdites bornes de masse (401, 407) sont agencées en une seule rangée, et

dans laquelle lesdites au moins deux bornes de masse (401, 407) ne sont pas les bornes au plus près de ladite borne d'alimentation en courant (404), sont situées au niveau des extrémités les plus extérieures de ladite rangée et sont utilisées pour détecter le contact de bornes (P1-P7) sur un dispositif d'impression (100) avec lesdites bornes (401-407) sur ladite carte de circuit imprimé (400).

2. Carte de circuit imprimé (10, 200) pour un réservoir de produit de marquage (40, 48), comprenant :

un dispositif de mémorisation (30) permettant de mémoriser des données relatives à un produit de marquage pour impression ; et une pluralité de bornes (21-27, 201-207) agencées sur ladite carte de circuit imprimé (10, 200) et utilisées pour des opérations de lecture/écriture sur lesdites données relatives à un produit de marquage pour impression, ladite pluralité de bornes comprenant une borne d'alimentation en courant (22, 202) et une borne de signaux de commande (25, 205),

caractérisée en ce qu'elle comprend en outre :

au moins deux bornes de masse (24, 27 ; 204, 207) agencées sur ladite carte de circuit imprimé (10, 200) au niveau de deux bords de ladite carte de circuit imprimé qui sont situés sur un axe de ladite carte de circuit imprimé ; et

dans laquelle ladite pluralité de bornes (21-27, 201-207) est agencée de façon à former une pluralité de rangées, et

dans laquelle lesdites au moins deux bornes de masse (24, 27 ; 204, 207) ne sont pas les bornes au plus près de ladite borne d'alimentation en courant (22, 202) et deux desdites au moins deux bornes de masse sont situées au niveau des extrémités les plus extérieures d'une de ladite pluralité de rangées et sont utilisées pour détecter le contact de bornes (P1-P7) sur un dispositif d'impression (100) avec lesdites bornes (21-27, 201-207) sur ladite carte de circuit imprimé (10, 200).

3. Carte de circuit imprimé (10, 200) selon la revendication 2, comprenant en outre une borne de signaux d'horloge (26, 206) dans laquelle ladite borne de signaux d'horloge est située entre deux desdites au moins deux bornes de masse (24, 27 ; 204, 207).

4. Carte de circuit imprimé (10, 200) selon la revendication 3, dans laquelle deux desdites au moins deux bornes de masse (24, 27 ; 204, 207) sont situées au niveau des extrémités les plus extérieures d'une rangée qui est différente de la rangée qui comprend ladite borne d'alimentation en courant (22, 202) et est la rangée qui comprend ladite borne de signaux d'horloge (26, 206).

5. Carte de circuit imprimé (10, 200) selon la revendication 4, dans laquelle ladite borne d'alimentation en courant (22, 202) est située au centre d'une rangée qui est différente de la rangée dans laquelle sont placées lesdites bornes de masse (24, 27 ; 204, 207).

6. Carte de circuit imprimé (10, 200) selon la revendication 3 ou 4 ou 5, comprenant en outre une borne d'E/S de données (21, 201) et une borne de signaux de sélection (23, 203).

7. Carte de circuit imprimé (10, 200) selon la revendication 6, dans laquelle ladite pluralité de rangées comporte :

une première rangée dans laquelle ladite borne d'E/S de données (21, 201) et ladite borne de signaux de sélection (23, 203) sont situées de l'un ou l'autre côté de ladite borne d'alimentation en courant (22, 202), et une deuxième rangée dans laquelle ladite borne de signaux de commande de lecture/écriture (25, 205) et ladite borne de signaux d'horloge (26, 206) sont situées entre lesdites deux bornes de masse (24, 27 ; 204, 207).

8. Carte de circuit imprimé (10, 200) selon la revendication 7, dans laquelle ladite première rangée est placée plus près du centre de ladite carte de circuit imprimé que ladite deuxième rangée.

9. Carte de circuit imprimé (10, 200) selon la revendication 7 ou 8, dans laquelle lesdites bornes (21-23, 201-203) dans ladite première rangée et lesdites bornes (24-27, 204-207) dans ladite deuxième rangée sont agencées en alternance.

10. Carte de circuit imprimé (10, 200) selon l'une quelconque des revendications 2 à 9, dans laquelle ladite pluralité de bornes (21-27, 201-207) sont agencées à des intervalles de 1 mm dans la direction de formation desdites rangées.

11. Carte de circuit imprimé (10, 200) selon l'une quelconque des revendications 2 à 10, dans laquelle ledit réservoir de produit de marquage (40, 48) est une cartouche d'encre.

12. Carte de circuit imprimé (10, 200) selon l'une quelconque des revendications 2 à 10, dans laquelle ledit réservoir de produit de marquage (40, 48) est une cartouche de toner.

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13. Carte de circuit imprimé (10, 200) selon l'une quelconque des revendications 2 à 12, dans laquelle ledit réservoir de produit de marquage (40, 48) comprend une surface d'extrémité de raccordement ayant deux bords et étant raccordée au dit dispositif d'impression (100), ladite surface d'extrémité de raccordement ayant un orifice de délivrance de produit de marquage (42, 103) au centre de celle-ci, dans laquelle ladite carte de circuit imprimé (10, 200) est placée au niveau d'un desdits bords de ladite surface d'extrémité de raccordement.

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Fig.1

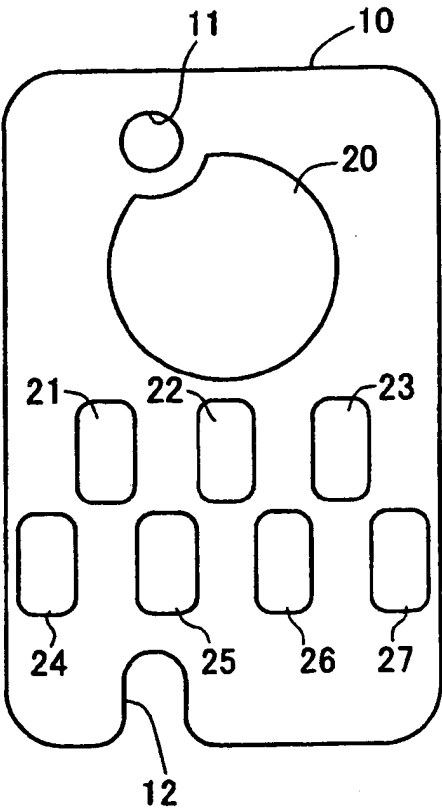


Fig.2

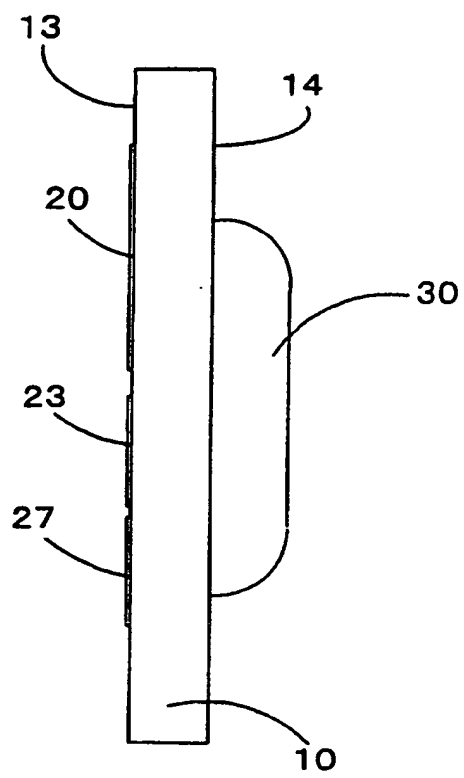


Fig.3

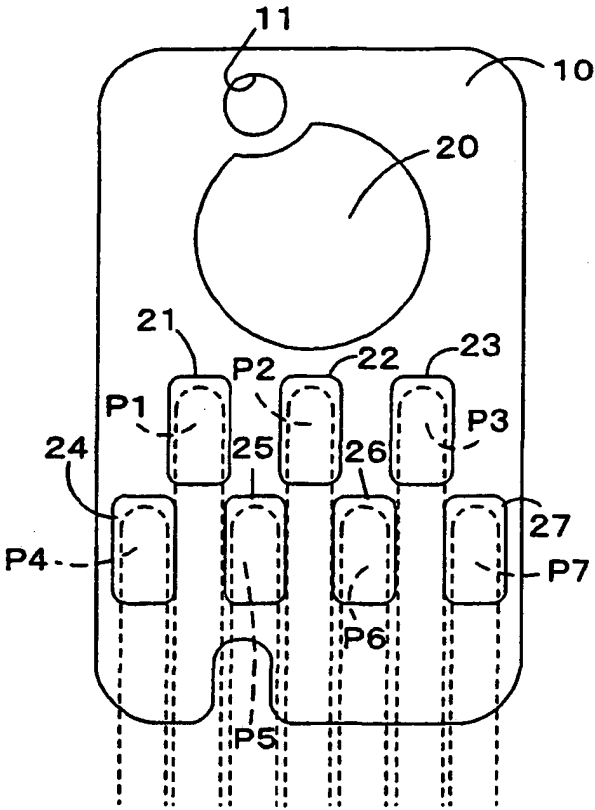


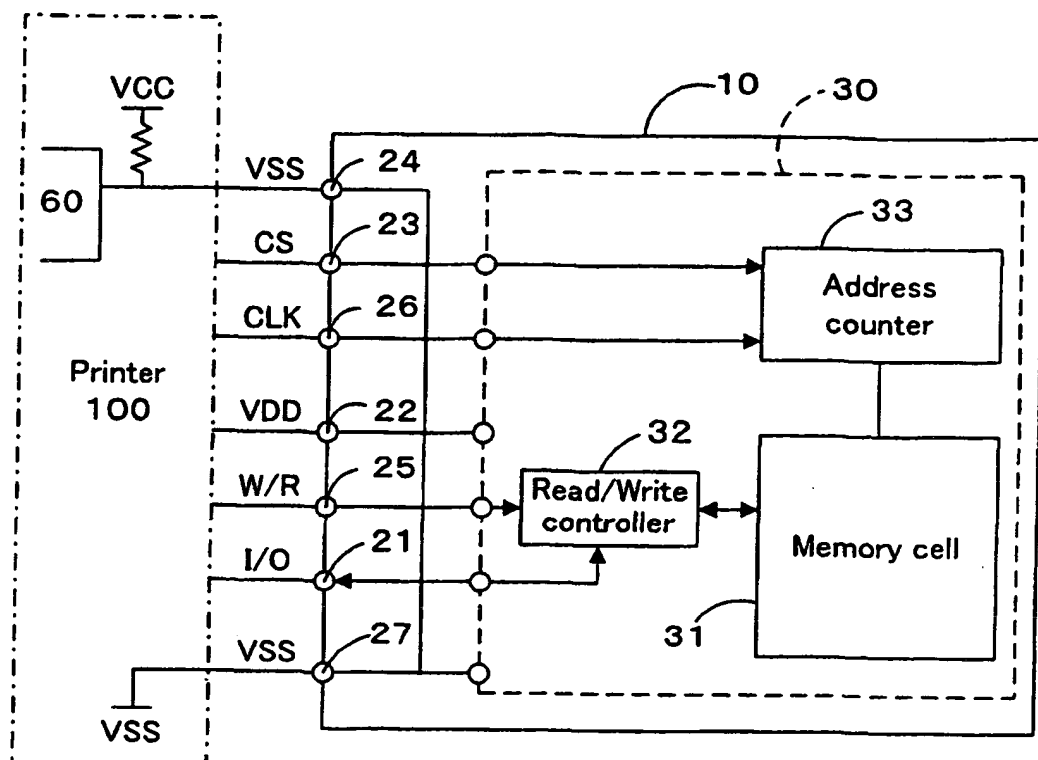
Fig.4

Fig.5

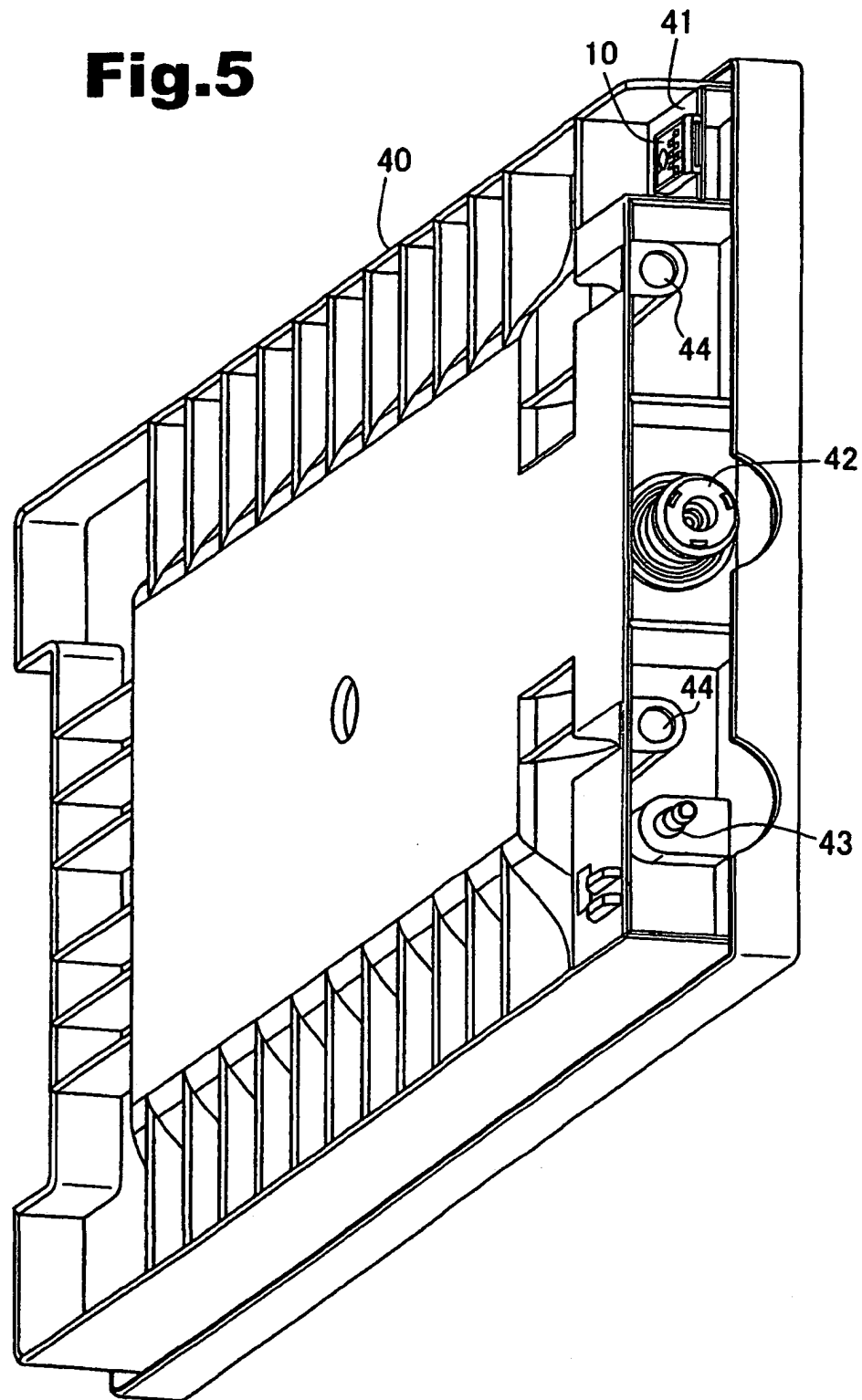


Fig.6

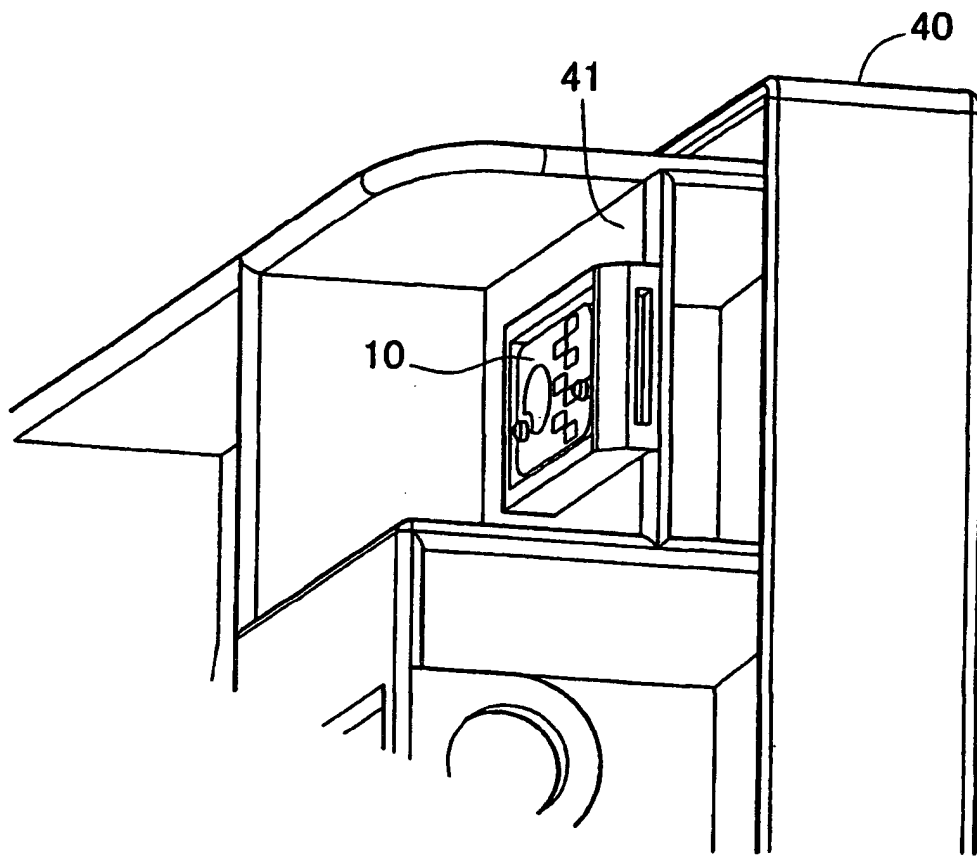


Fig.7

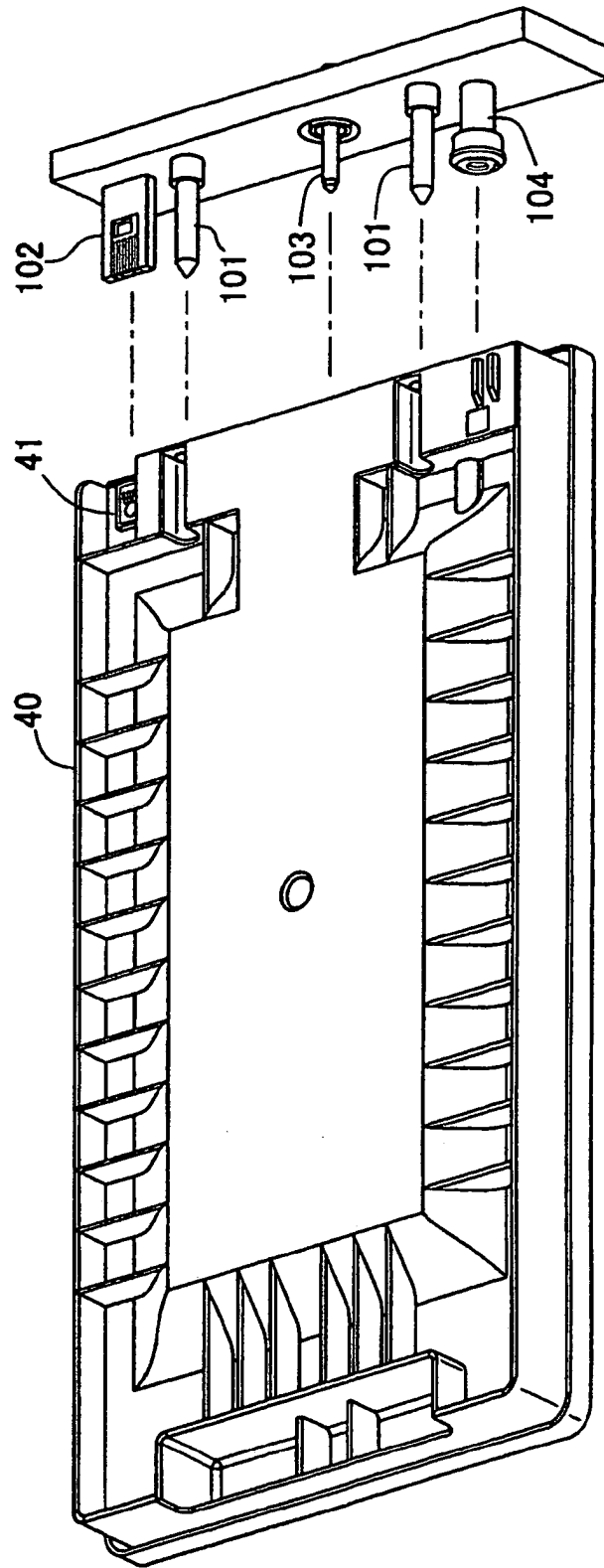


Fig.8

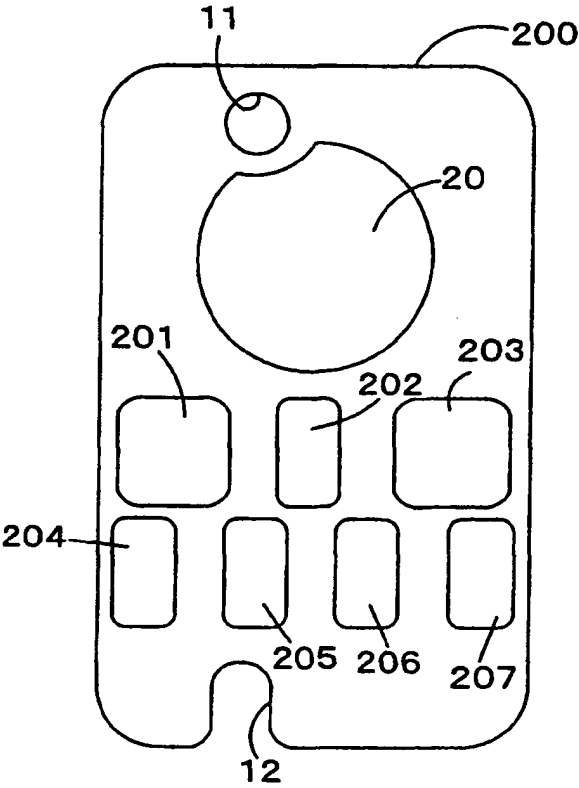


Fig.9

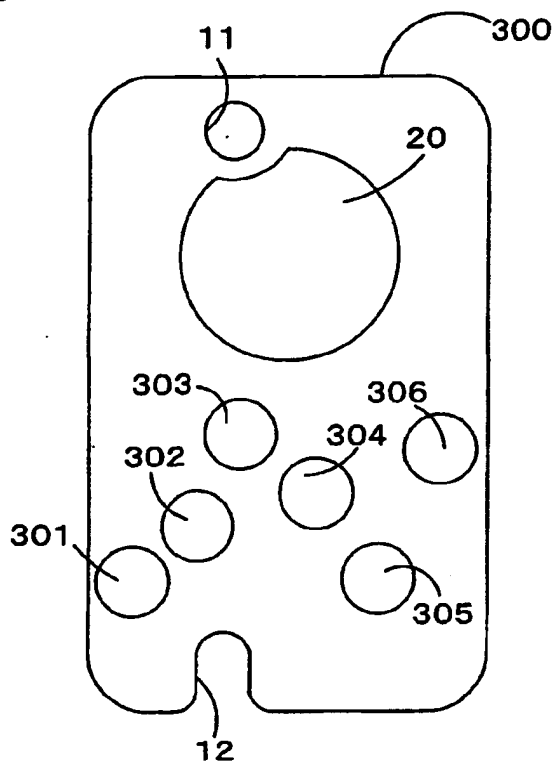


Fig.10

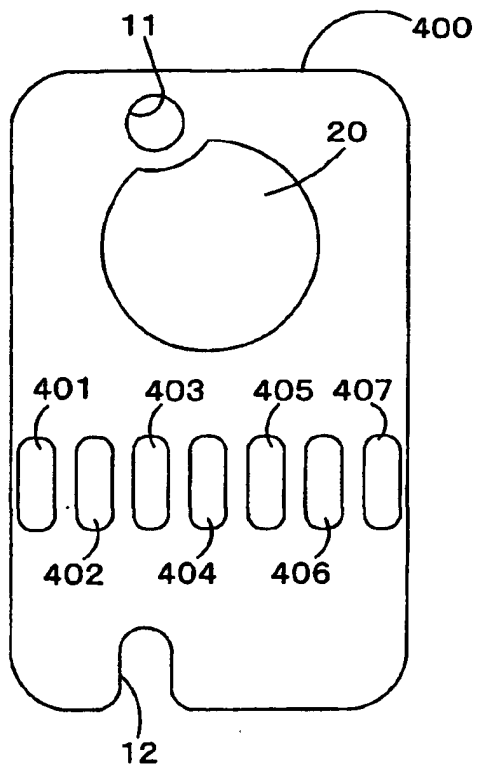


Fig.11

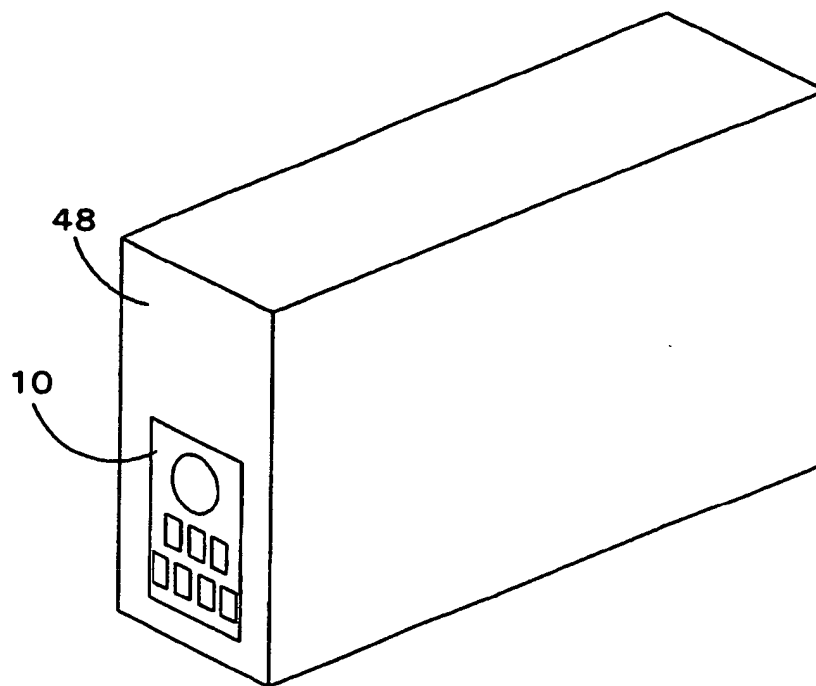
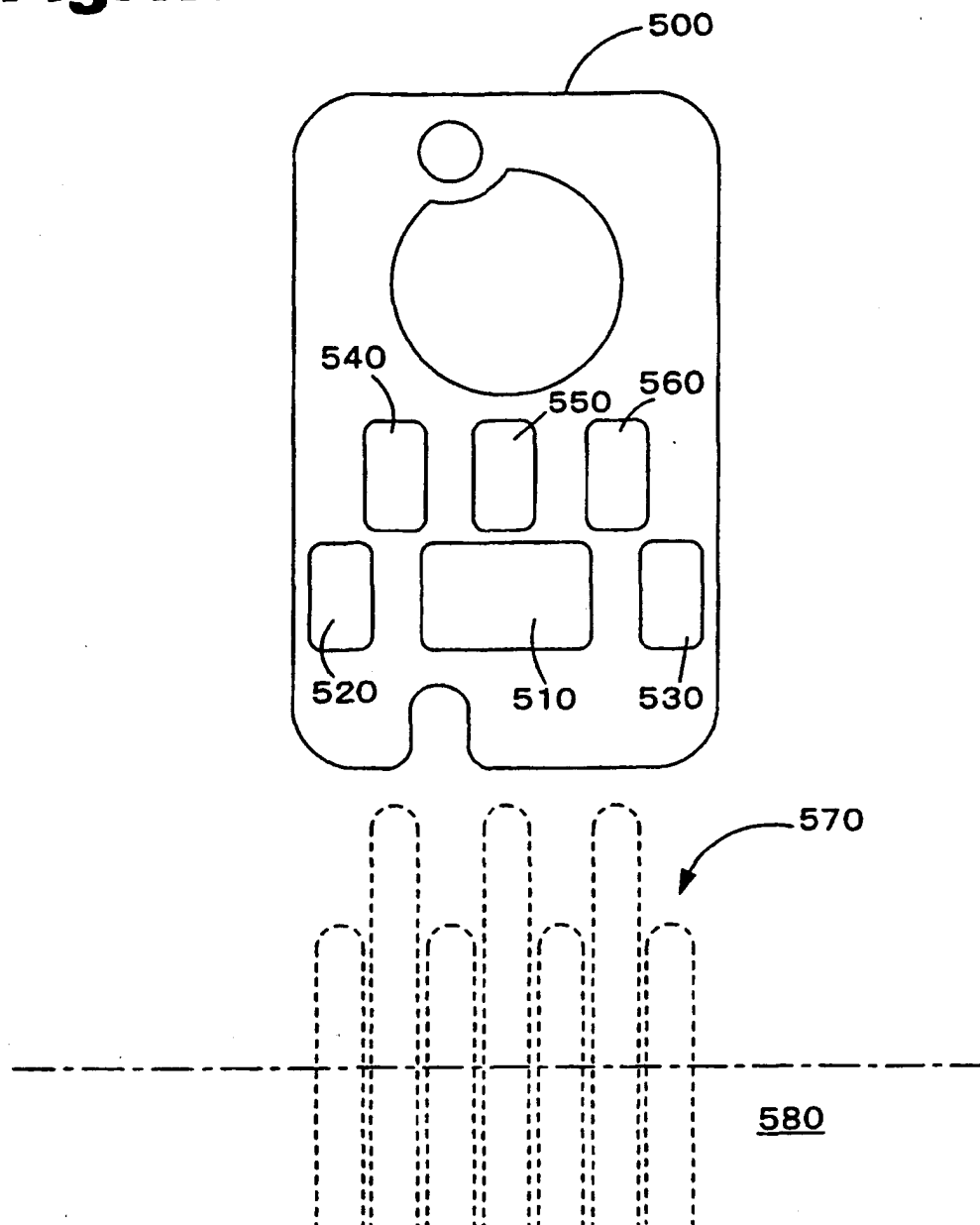


Fig.12



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