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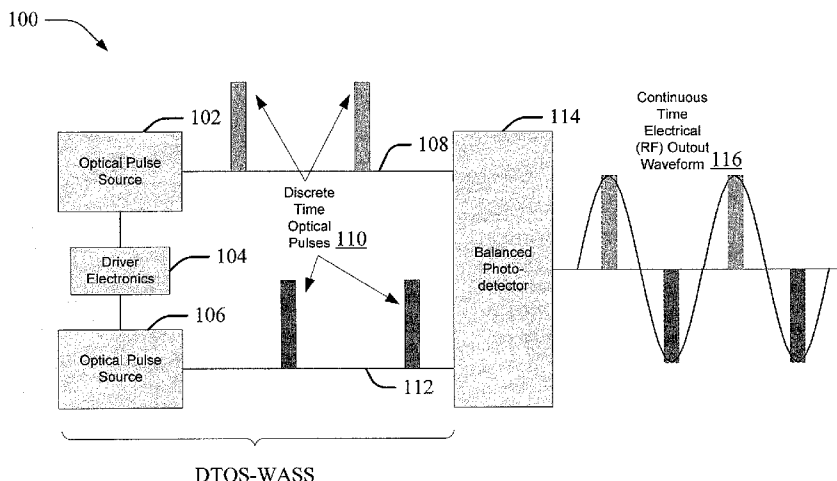


FIG. 1

(57) Abstract: An apparatus for generating an RF signal is provided includes a driver configured to generate a timing control for two optical signals. The apparatus further includes at least one optical pulse source configured to generate the two optical signals based on the timing control. In addition, the apparatus includes a photodetector configured to receive the two optical signals as input and further configured to generate an RF signal based on the two optical signals. A method for generating an RF signal is also provided.



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APPARATUS AND METHOD FOR GENERATING AN RF SIGNAL

STATEMENT AS TO RIGHTS TO INVENTIONS MADE UNDER FEDERALLY
SPONSORED RESEARCH OR DEVELOPMENT

[0001] Not Applicable.

CLAIM OF PRIORITY UNDER 35 U.S.C. §119

[0002] This application claims the benefit of priority under 35 U.S.C. §119 from U.S. Provisional Application Serial No. 60/929,341, entitled "DISCRETE TIME OPTICAL SIGNAL WIDEBAND ARBITRARY SIGNAL SYNTHESIZER (DTOS-WASS)," filed June 22, 2007, which is hereby incorporated by reference in its entirety for all purposes.

FIELD

[0003] The subject technology relates generally to generation of RF signals, and more specifically to an apparatus and method for generating an RF signal.

BACKGROUND

[0004] For RF signal generators and synthesis techniques, a digital-to-analog converter (DAC) device may be used as a waveform generator, up to the bandwidth limitation of a device (e.g., 1/2 the sample rate). If additional frequency coverage is needed, then a 2-stage architecture is typically used, where a baseband signal is typically generated and then mixed or modulated up to the desired carrier frequency.

[0005] However, conventional RF signal generators are not without problems. For example, it may be desired to have more extended operating frequency ranges, both in terms of carrier frequency and bandwidth of resulting synthesized signals. Greater ability to generate arbitrary chirp characteristics may also be desired. In addition, it may be desired to reduce the need for high performance RF oscillators and RF tuning elements.

SUMMARY

[0006] In accordance with the disclosure, the generation of an RF signal includes generating a timing control for two optical signals, and generating the two optical signals based on the timing control. An RF signal is generated based on the two optical signals.

[0007] In one aspect of the disclosure, an apparatus for generating an RF signal is provided. The apparatus includes a driver configured to generate a timing control for two optical signals, and at least one optical pulse source configured to generate the two optical signals based on the timing control. In addition, the apparatus includes a photodetector configured to receive the two optical signals as input and further configured to generate an RF signal based on the two optical signals.

[0008] In a further aspect of the disclosure, a method for generating an RF signal is provided. The method includes generating a timing control for two optical signals, and generating the two optical signals based on the timing control. In addition, the method includes generating an RF signal based on the two optical signals.

[0009] In yet a further aspect of the disclosure, a machine-readable medium encoded with instructions for generating an RF signal is provided. The instructions include code for generating a timing control for two optical signals, and generating the two optical signals based on the timing control. In addition, the instructions include code for generating an RF signal based on the two optical signals.

BRIEF DESCRIPTION OF THE DRAWINGS

[0001] FIG. 1 is a block diagram illustrating an example of a device for generating an RF signal.

[0002] FIG. 2 is a block diagram illustrating an example of the optical pulse source illustrated in FIG. 1.

[0003] FIG. 3 is a diagram illustrating an example of generating a continuous waveform (CW).

[0004] FIG. 4 is a diagram illustrating a further example of generating a continuous waveform (CW).

[0005] FIG. 5 is a diagram illustrating an example of generating a linear frequency chirp waveform.

[0006] FIG. 6 is a diagram illustrating an example of generating a binary phase shift keyed (BPSK) modulated signal.

[0007] FIG. 7 is a diagram illustrating an example of generating a linear phase modulated waveform.

[0010] FIG. 8 is a block diagram illustrating a further example of a device for generating an RF signal.

[0011] FIG. 9 is a block diagram illustrating a further example of a device for generating an RF signal.

[0012] FIG. 10 is a block diagram illustrating a further example of a device for generating an RF signal.

[0013] FIG. 11 is a flowchart illustrating an exemplary operation of generating an RF signal.

[0014] FIG. 12 is a block diagram illustrating an example of a computer system upon which may be used to generate an RF signal.

[0015] DETAILED DESCRIPTION

[0016] FIG. 1 is a block diagram illustrating an example of a device for generating an RF signal. In this regard, it should be noted that reference to an RF signal within this disclosure can also refer to a microwave signal. In FIG. 1, two sets of optical pulses 110 may be input into two input ports of a balanced photodetector 114, to generate a desired RF output signal 116.

[0017] Optical pulses 110 may be used create the equivalent of a time sampled version of the desired output signal. Balanced photodetector 114 may be used to perform the optical to electrical conversions of the signal back to the electric (RF) domain. In addition, balanced photodetector 114 may act as a reconstruction filter to provide a continuous time (RF) output signal.

[0018] As can be seen in FIG. 1, optical pulses 110 may be provided to balanced photodetector 114, on an upper arm 108 and a lower arm 112. The use of lower arm 112 may allow for the

generation of negative values with an optical signal, since the amplitude of an optical pulse is typically a positive value. Furthermore, no significant DC component is seen to be generated.

[0019] A driver electronics block 104 may be used to generate the timing control for the optical pulse generation, controlling the synchronization between two optical pulse sources 102 and 106. Driver electronics block 104 may be a high stability clock that is used to trigger the optical pulse generation in each of the optical pulse sources 102 and 106 at the correct relative timing between the upper and lower arms 108 and 112.

[0020] Driver electronics block 104, optical pulse sources 102 and 106, and upper and lower arms 108 and 112 may be seen to correspond with the generation of discrete time optical signals (DTOS) as a wideband arbitrary signal synthesizer (WASS). In this regard, as illustrated in FIG. 1, these components may be grouped together as DTOS-WASS. However, it should be noted that these components should not be limited to DTOS-WASS. In addition, certain components may be added or omitted and still correspond with DTOS-WASS.

[0021] Optical pulses 110 should typically be generated at a rate that satisfies the Nyquist criteria for the desired output signal. For example, if the desired output signal is a CW tone at 10 GHz, the composite frequency rate for the optical pulse used to generate the signal should typically be 20 GHz. However, the implementation may be simplified, since signals on each input port of balanced photodetector 114 would be at 10 GHz, which is typically considered to be an acceptable value for optical technologies.

[0022] Balanced photodetector 114 may be in a direct detection mode of operation. As such, there may be no constraints on the specific wavelength used for the optical pulse generation. This may allow multiple laser sources to be used in combination to generate optical pulse trains 108, allowing lower speed and lower cost optical devices. For example, lasers may be used in parallel to generate much higher composite pulse trains, as illustrated in FIG. 2.

[0023] FIG. 2 is a block diagram illustrating an example of the optical pulse source illustrated in FIG. 1. FIG. 2 illustrates one example of implementing optical pulse source 200 using multiple lasers. The configuration illustrated in FIG. 2 may allow for lower performance and

cost lasers to be used to enable the DTOS-WASS of FIG. 1 to generate signals in excess of the bandwidth capabilities of the individual lasers.

[0024] Driver electronics 104 may generate the trigger for each laser driver 202₁ to 202_n in optical pulse source 200 at the appropriate timing offset. Each laser source 204₁ to 204_n may generate an optical pulse λ_1 to λ_n when its respective laser driver 202₁ to 202_n is triggered. Optical pulses λ_1 to λ_n may be combined into a single pulse stream using a multi-port optical coupler 206. Multi-port optical coupler 206 may be a passive coupler, such as a fiber optic star coupler. The combined single pulse stream may be further processed by an optical pulse compressor 208, which is optional. Optical pulse compressor 208 may be implemented as a passive dispersive delay line device, such as a length of non-linear optical fiber. If used, optical pulse compressor 208 may output compressed pulses λ_1 to λ_n .

[0025] As can be seen in FIG. 1, each of upper arm 108 and lower arm 112 of the DTOS-WASS may be associated with a separate optical pulse source 102 and 106, respectively. However, it should be noted that one optical pulse source may be used to generate the optical pulses 110 for both upper and lower arms 108 and 112 in a single arrangement. For example, this may be done by placing a 2-port wavelength selective device at the output of optical pulse source 200 to separate out the pulse streams for each arm by appropriate triggering of laser sources 204₁ to 204_n. However, this may require that an approximately equal number of laser sources fall within each out pass-band of the 2-port device.

[0026] Various examples of generating different types of waveforms using the device of FIG. 1 (and FIGS. 8 to 10, described later) will now be described, with particular references to FIGS. 3 to 7. For example, simple modifications of the optical pulse train may allow for direct modulation or synthesis of a number of different signal types, such as frequency modulation, chirp and phase modulation (PM).

[0027] FIGS. 3 and 4 are diagrams illustrating different examples of generating a continuous waveform (CW). FIG. 3 shows a CW signal at frequency f , determined by the sample rate τ . FIG. 4 shows another CW signal, at frequency f_2 , determined by the sample rate τ_2 . By varying the sample rate, the output signal frequency may be tuned to any desired value (subject to the Nyquist constraint). The effective tuning speed may only then be limited by the actual sample

rate, allowing near instantaneous tuning without necessarily having to evaluate the intermediary frequencies. In addition, the need for a settling time allowance typically required in tunable RF oscillators may be reduced or eliminated.

[0028] FIG. 5 is a diagram illustrating an example of generating a linear frequency chirp waveform. In the case of a chirped signal, the equivalent of a frequency sweep may be accomplished by varying the effective sample period between optical sampling pulses. Reducing the time interval between pulses may result in a chirp with increasing frequency, and increasing the time interval between pulses may result in a chirp decreasing frequency. By varying the length and direction of the frequency sweeps, frequency modulated signals may be generated. Also, by alternating between two fixed sample periods, a binary frequency shift keyed (FSK) signal may be generated. In addition, by varying the sample periods in a more complicated sequence, a frequency hopping spread spectrum signal may be generated. Further, by modifying the sequence of optical pulses, phase modulation may be accomplished.

[0029] FIG. 6 is a diagram illustrating an example of generating a binary phase shift keyed (BPSK) modulated signal. More particularly, FIG. 6 illustrates a case of transposing the optical pulse sequences to obtain a BPSK modulated signal centered about the carrier frequency f_3 . Though it may be relatively more complex, linear and non-linear phase modulation may be accomplished by varying the timing offset of the pulse train between upper and lower arms 108 and 122 for balanced photodetector 114 of FIG. 1, while maintaining the fixed equivalent sample rate or period for consecutive pulses on each arm.

[0030] FIG. 7 is a diagram illustrating an example of generating a linear phase modulated waveform. More particularly, FIG. 7 illustrates an approach to phase modulation, which may be accomplished by an amplitude transition between upper and lower arms 108 and 112 of balanced photodetector 114. In addition, phase modulation may be accomplished by sliding the alternate phases of the signal with respect to each other, while maintaining a fixed average frequency of the signals.

[0031] FIG. 8 is a block diagram illustrating a further example of a device for generating an RF signal. As noted above, reference to an RF signal within this disclosure can also refer to a microwave signal. In addition to direct manipulation or modulation of the equivalent carrier

signal, the device of FIG. 8 may have the additional capability to generate arbitrary signals by modulating or mixing a baseband waveform of arbitrary content up to the desired output RF frequency.

[0032] The device of FIG. 8 includes driver electronics 804 and optical pulse sources 802 and 806, all of which may be similar to those discussed above with reference to FIGS. 1 and 2. In addition, a baseband waveform generator 808 may be used to generate the desired signal structure or information content of the desired output signal, as an electrical signal. For example, a DAC may be associated with the implementation of baseband waveform generator 808. The output of baseband waveform generator 808 may be provided to the input ports of a pair of EO modulators 810 and 812 in each arm of the signal generator. EO modulators 810 and 812 may modulate the amplitude of the optical pulses to capture the envelope of the baseband signal, which may effectively modulate or mix the baseband signal onto the optical time sampled version of the RF carrier signal. In this regard, the output from EO modulators 810 and 812 may be provided to amplitude and phase matching network modules 814 and 816, for further processing. The resulting electrical waveform 820 at the output of balanced photodetector 818 may be the baseband signal modulated onto the RF carrier.

[0033] While the upconversion processing in the DTOS WASS using baseband waveform generator 808 may be limited to signals equal to or less than half of the output carrier frequency (due to Nyquist criteria constraints), the ability to generate arbitrary amplitude components along with arbitrary frequency/phase components (in the form of time varying optical pulses) may allow the combination of approaches to achieve an arbitrary vector modulation capability that allows for generation of complex signals across the entire operating bandwidth of the WASS.

[0034] The operating range of the DTOS-WASS may be limited by the operating bandwidth of balanced photodetector 818. In this regard, operating bandwidths of upwards of 60 GHz can be achieved using commercial off the shelf photodetectors. However, this is not the ultimate limiting frequency on performance, since photodetectors with bandwidths in excess of 120 GHz have been demonstrated in the laboratory.

[0035] FIG. 9 is a block diagram illustrating a further example of a device for generating an RF signal (including a microwave signal). As noted above with reference to FIGS. 1 and 8, two

optical pulse sources may be used to generate two sets of optical pulses. As can be seen in FIG. 9, a single optical pulse source 902 may also be used to generate the two sets of optical pulses. Aspects of this implementation, for example, may be suitable for use in an integrated photonics integrated circuit.

[0036] Device 900 includes an optical pulse source 902, an optical pulse amplitude adjuster (e.g., EO Modulator) 904, an optical switch 906, a controller 910 and a balanced photodetector 908. Optical pulse source 902 may generate a series of optical pulses. This can be achieved in a number of ways, such as by gating a CW laser source with an electro-optic modulator (EOM), by modulating a diode laser source with a series of electronic pulses with the desired pulse profile, or by use of a mode-locked laser oscillator. Through the control of the timing for the pulse generation (e.g., the inter pulse period or pulse repetition frequency), the frequency and phase components of the generated RF signal can also be controlled.

[0037] Optical pulse amplitude adjuster 904 may allow for control of the amplitude for the pulse generation, which can be used to determine and control the resulting amplitude and phase components of the generated RF signal. Optical switch 906 may route alternating pulses to each optical input port of balanced photodetector 908, to generate the positive and negative amplitude values that emerge from the electrical output port as an RF signal. In this regard, the amplitude of each optical pulse from optical pulse source 902 can be individually adjusted using an EOM, such as a Mach-Zender (MZ) modulator (e.g., with a single output port) or an Electro-Absorption Modulator (EAM). It is also possible to integrate the pulse gating function and the amplitude adjustment function using a single implementation of an EOM.

[0038] Balanced photodetector 908 may convert the received optical signals to electrical signals. Balanced photodetector 908 may include a pair of connected photodetectors arranged with a common output, but configured with inverted polarities with respect to each other. By staggering the timing that the optical pulses are received at each input port of balanced photodetector 908, the resulting electrical signal may be forced through a zero crossing with each subsequent pulse, resulting in the generation of the RF carrier with a frequency determined by the inter-pulse period.

[0039] Controller 910 may provide a mechanism for controlling the pulse timing and amplitude, allowing for generation of arbitrary waveforms, subject to the limitations imposed by the Nyquist theorem (e.g., that the sample rate must be at least twice the frequency of the highest frequency component of the desired arbitrary waveform). Regarding optical pulse source 902, control of the timing by controller 910 for the pulse generation may determine the frequency and phase components of the generated RF signal. For optical pulse amplitude adjuster 904, control of the amplitude by controller 910 for the pulse generation may determine the amplitude and phase components of the generated RF signal.

[0040] FIG. 9 depicts each functional component that makes up device 900 (e.g., DTOS-WASS) as a separate block. However, it is possible to integrate functionality by implanting more than one functional component using a single optical device. For example, it is possible to combine elements of the pulse generation and pulse amplitude adjustment using a single EO modulator.

[0041] FIG. 10 is a block diagram illustrating a further example of a device for generating an RF signal (including a microwave signal). As can be seen in FIG. 10, a single optical pulse source 1002 can be used to generate the two sets of optical pulses. For example, device 1000 of FIG. 10 may be compatible with implementation in a photonic integrated circuit.

[0042] In FIG. 10, a single optical pulse source 1002 is split into two paths using optical splitter 1004. Each path is input into a separate optical pulse amplitude adjuster 1006 and 1008 (e.g., EO modulator), which may also be used as a pseudo-switch to suppress the alternate pulses on each arm of optical splitter 1004. For example, the arm connected to port 1 of the detector may be used to amplitude modulate the odd pulses and suppress the even pulses. Conversely, the arm connected to port 2 of the detector may be used to amplitude modulate the even pulses and suppress the odd pulses. Pulse suppression may be achieved by driving the optical pulse amplitude adjuster 1006 or 1008 (e.g., an EO modulator such as an Electro-Absorption or Mach-Zender modulator) to its extreme to minimize the optical amplitude at the modulator output. This approach may require that the modulation depth achievable by the modulator be sufficient to prevent leak-thru. Device 1000 also includes a balanced photodetector 1010 and a controller 1012, which may be similar to those described above.

[0043] FIG. 11 is a flowchart illustrating an exemplary operation of generating an RF signal. As noted above, reference to an RF signal within this disclosure can also refer to a microwave signal. As can be seen in FIG. 11, a timing control is generated for two optical signals (block 1102). The two optical signals are generated based on the timing control (block 1104). An RF signal is generated, based on the two optical signals (block 1106).

[0044] As such, the above-described examples with regard to the various figures may be seen to correspond with using a DTOS as a WASS to generate RF signals with arbitrary content. Such RF signals may include wideband signals, such as linear and non-linear chirped signals, relatively fast frequency hopping signals, and more conventional-type RF signals.

[0045] Also, the above-described examples may be applicable in a wide range of signal generation applications, including test signal generation, conventional communication transmitters, covert (LPI/LPD) communication transmitters, conventional radar transmitters, synthetic aperture radar, LPI/LPD radar transmitters and imaging radar transmitters. Of course, the above-described examples with regard to the various figures are not limited to the these applications, and may be used for other signal generation applications.

[0046] Additionally, the above-described examples are seen to correspond with extended operating frequency ranges, both in terms of carrier frequency and bandwidth of resulting synthesized signals, improved ability to generate arbitrary chirp characteristics, and reducing or eliminating the need for high performance RF oscillators and RF tuning elements.

[0047] Furthermore, the above-described examples are seen to correspond with reducing or eliminating the need for an RF synthesizer. The use of optical pulses with direct detection is seen to reduce or eliminate the need for high performance, high stability optical laser sources, thus allowing use of simpler and lower cost laser devices. The use of a balanced photodetector arrangement is seen to reduce or eliminate effective carrier (or oscillator) leak-through.

[0048] In addition, the above-described examples are seen to be compatible with a discrete time optical signal and frequency translation concept, potentially allowing for sharing of common components between the transmit and receive sides of a systems, potentially further reducing size, weight, power and cost.

[0049] FIG. 12 is a block diagram that illustrates a computer system 1200 upon which an embodiment of the present disclosure may be implemented in accordance with one aspect of the present disclosure. Computer system 1200 includes a bus 1208 or other communication mechanism for communicating information, and a processor 1202 coupled with bus 1208 for processing information. Computer system 1200 also includes a memory 1210, such as a random access memory (“RAM”) or other dynamic storage device, coupled to bus 1208 for storing information and instructions to be executed by processor 1202. Memory 1210 may also be used for storing temporary variable or other intermediate information during execution of instructions to be executed by processor 1202. Computer system 1200 further includes a data storage device 1206, such as a magnetic disk or optical disk, coupled to bus 1208 for storing information and instructions.

[0050] Computer system 1200 may be coupled via I/O module 1204 to a display device, such as a cathode ray tube (“CRT”) or liquid crystal display (“LCD”) for displaying information to a computer user. An input device, such as, for example, a keyboard or a mouse may also be coupled to computer system 1200 via I/O module 1204 for communicating information and command selections to processor 1202.

[0051] According to one aspect of the present disclosure, the generation of RF signals may be implemented using a computer system 1200 in response to processor 1202 executing one or more sequences of one or more instructions contained in memory 1210. Such instructions may be read into memory 1210 from another machine-readable medium, such as data storage device 1206. Execution of the sequences of instructions contained in main memory 1210 causes processor 1202 to perform the process steps described herein. One or more processors in a multi-processing arrangement may also be employed to execute the sequences of instructions contained in memory 1210. In alternative embodiments, hard-wired circuitry may be used in place of or in combination with software instructions to implement various embodiments of the present disclosure. Thus, embodiments of the present disclosure are not limited to any specific combination of hardware circuitry and software.

[0052] The term “machine-readable medium” as used herein refers to any medium that participates in providing instructions to processor 1202 for execution. Such a medium may take

many forms, including, but not limited to, non-volatile media, volatile media, and transmission media. Non-volatile media include, for example, optical or magnetic disks, such as data storage device 1206. Volatile media include dynamic memory, such as memory 1206. Transmission media include coaxial cables, copper wire, and fiber optics, including the wires that comprise bus 1208. Transmission media can also take the form of acoustic or light waves, such as those generated during radio frequency and infrared data communications. Common forms of machine-readable media include, for example, floppy disk, a flexible disk, hard disk, magnetic tape, any other magnetic medium, a CD-ROM, DVD, any other optical medium, punch cards, paper tape, any other physical medium with patterns of holes, a RAM, a PROM, an EPROM, a FLASH EPROM, any other memory chip or cartridge, a carrier wave, or any other medium from which a computer can read.

[0053] Those of skill in the art would appreciate that the various illustrative blocks, modules, elements, components, methods, and algorithms described herein may be implemented as electronic hardware, computer software, or combinations of both. Furthermore, these may be partitioned differently than what is described. To illustrate this interchangeability of hardware and software, various illustrative blocks, modules, elements, components, methods, and algorithms have been described above generally in terms of their functionality. Whether such functionality is implemented as hardware or software depends upon the particular application and design constraints imposed on the overall system. Skilled artisans may implement the described functionality in varying ways for each particular application.

[0054] It is understood that the specific order or hierarchy of steps or blocks in the processes disclosed is an illustration of exemplary approaches. Based upon design preferences, it is understood that the specific order or hierarchy of steps or blocks in the processes may be rearranged. The accompanying method claims present elements of the various steps in a sample order, and are not meant to be limited to the specific order or hierarchy presented.

[0055] The previous description is provided to enable any person skilled in the art to practice the various aspects described herein. Various modifications to these aspects will be readily apparent to those skilled in the art, and the generic principles defined herein may be applied to other aspects. Thus, the claims are not intended to be limited to the aspects shown herein, but is

to be accorded the full scope consistent with the language claims, wherein reference to an element in the singular is not intended to mean "one and only one" unless specifically so stated, but rather "one or more." Unless specifically stated otherwise, the term "some" refers to one or more. Pronouns in the masculine (e.g., his) include the feminine and neuter gender (e.g., her and its) and vice versa. All structural and functional equivalents to the elements of the various aspects described throughout this disclosure that are known or later come to be known to those of ordinary skill in the art are expressly incorporated herein by reference and are intended to be encompassed by the claims. Moreover, nothing disclosed herein is intended to be dedicated to the public regardless of whether such disclosure is explicitly recited in the claims. No claim element is to be construed under the provisions of 35 U.S.C. §112, sixth paragraph, unless the element is expressly recited using the phrase "means for" or, in the case of a method claim, the element is recited using the phrase "step for."

CLAIMS

WHAT IS CLAIMED IS:

1. An apparatus for generating an RF signal, comprising:
a driver configured to generate a timing control for two optical signals;
at least one optical pulse source configured to generate the two optical signals based on the timing control; and
a photodetector configured to receive the two optical signals as input and further configured to generate an RF signal based on the two optical signals.
2. The apparatus of Claim 1, wherein the photodetector comprises two input ports configured as a balanced photodetector, to receive the two optical signals, respectively.
3. The apparatus of Claim 1, wherein the photodetector comprises a reconstruction filter configured to generate a continuous time RF signal.
4. The apparatus of Claim 1, wherein the two optical signals correspond with an upper and lower arm, respectively, the upper arm corresponding with positive optical pulse values and the lower arm corresponding with negative optical pulse values.
5. The apparatus of Claim 1, wherein the at least one optical pulse source comprises two optical pulse sources, each of which generate one of the two optical signals.
6. The apparatus of Claim 1, wherein the at least one optical pulse source comprises one optical pulse source which generates both of the two optical signals.

7. The apparatus of Claim 6, further comprising a 2-port wavelength selective device for generating the two optical signals from the one optical pulse source.

8. The apparatus of Claim 1, wherein the at least one optical pulse source is configured to use a plurality of lasers to generate each of the two optical signals.

9. The apparatus of Claim 1, wherein the RF signal corresponds with at least one of a direct modulation signal, a frequency modulation signal, chirp signal and a phase modulation signal, based on the two optical signals.

10. The apparatus of Claim 1, further comprising a baseband waveform generator configured to modulate a baseband waveform of arbitrary content up to a desired output RF frequency for the RF signal.

11. A method for generating an RF signal, the method comprising:
generating a timing control for two optical signals;
generating the two optical signals based on the timing control; and
generating an RF signal based on the two optical signals.

12. The method of Claim 11, wherein the generating the RF signal comprises generating a continuous time RF signal.

13. The method of Claim 11, wherein the two optical signals correspond with an upper and lower arm, respectively, the upper arm corresponding with positive optical pulse values and the lower arm corresponding with negative optical pulse values.

14. The method of Claim 11, wherein each of the two optical sources are generated using a plurality of lasers.

15. The method of Claim 11, wherein the RF signal corresponds with at least one of a direct modulation signal, a frequency modulation signal, chirp signal and a phase modulation signal, based on the two optical signals.

16. The method of Claim 11, further comprising:
modulating a baseband waveform of arbitrary content up to a desired output RF frequency for the RF signal.

17. A machine-readable medium encoded with instructions for generating an RF signal, the instructions comprising code for:
generating a timing control for two optical signals;
generating the two optical signals based on the timing control; and
generating an RF signal based on the two optical signals.

18. The machine-readable medium of Claim 17, wherein the generating the RF signal comprises generating a continuous time RF signal.

19. The machine-readable medium of Claim 17, wherein the two optical signals correspond with an upper and lower arm, respectively, the upper arm corresponding with positive optical pulse values and the lower arm corresponding with negative optical pulse values.

20. The machine-readable medium of Claim 17, wherein the RF signal corresponds with at least one of a direct modulation signal, a frequency modulation signal, chirp signal and a phase modulation signal, based on the two optical signals.

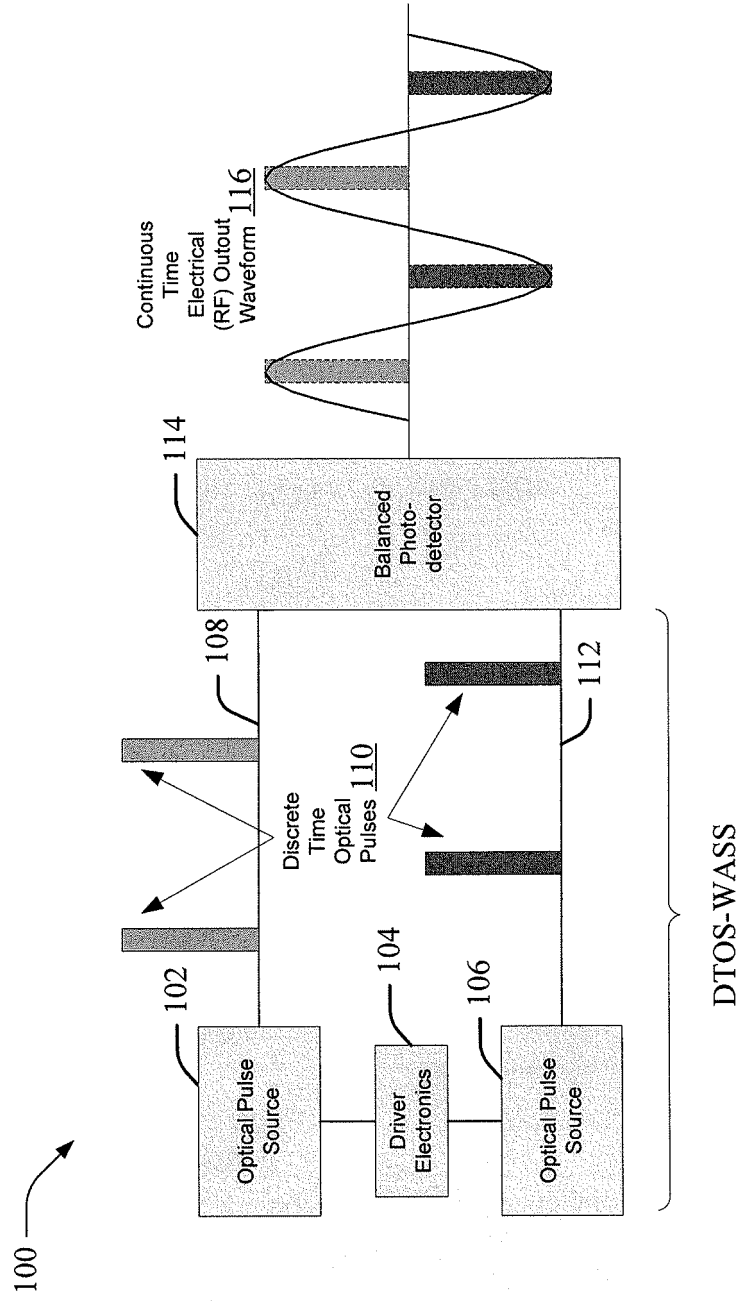


FIG. 1

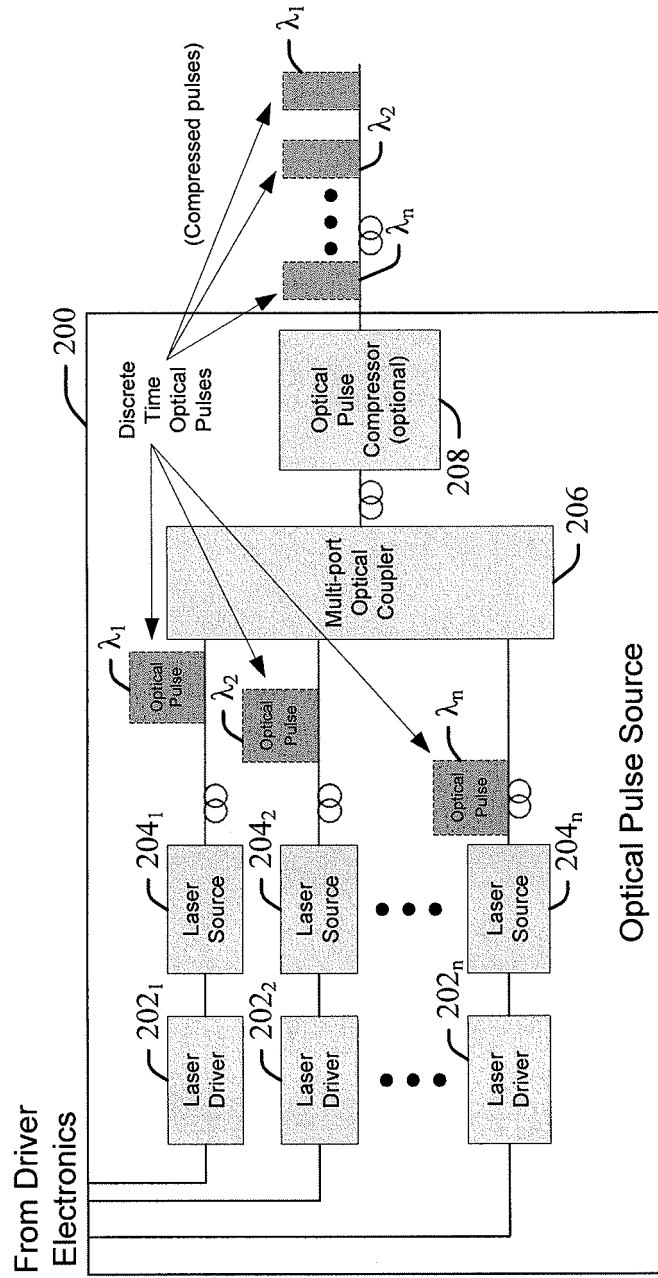


FIG. 2

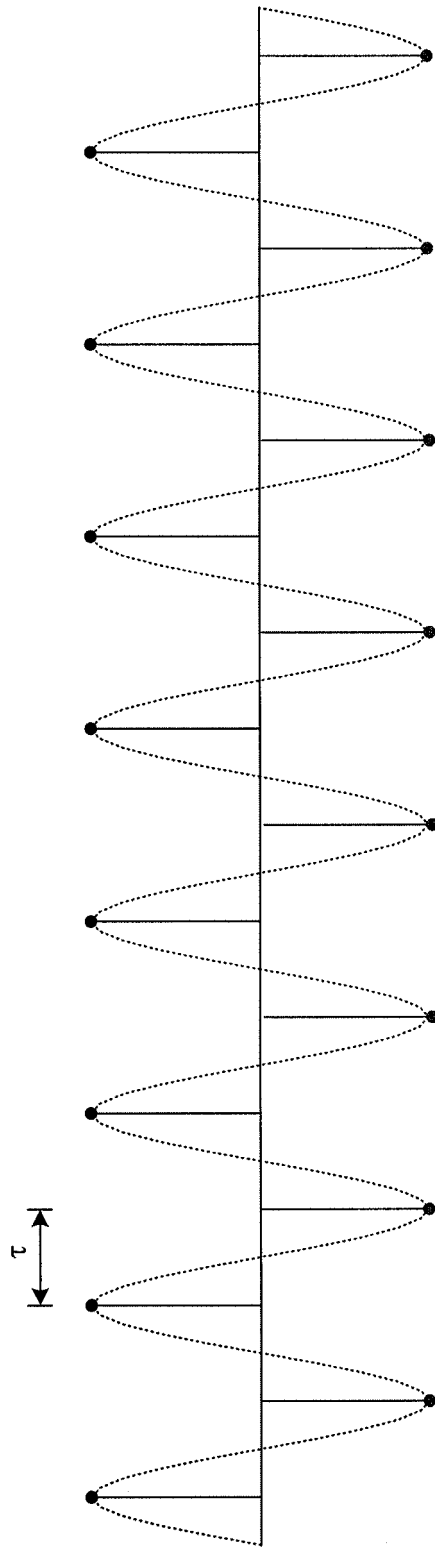


FIG. 3

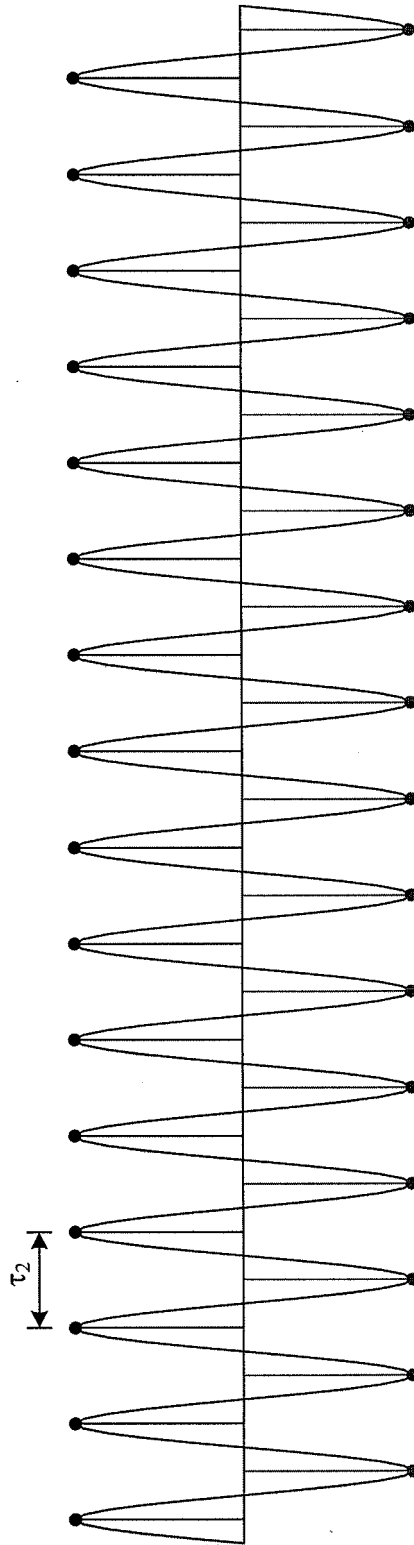


FIG. 4

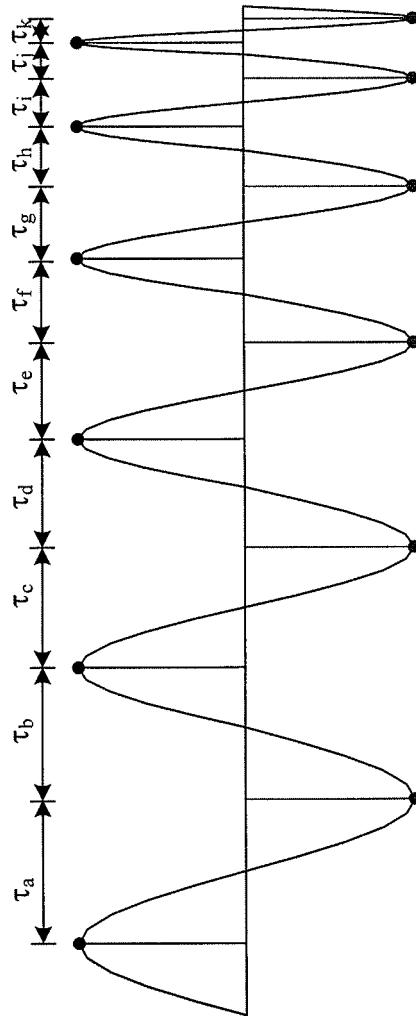


FIG. 5

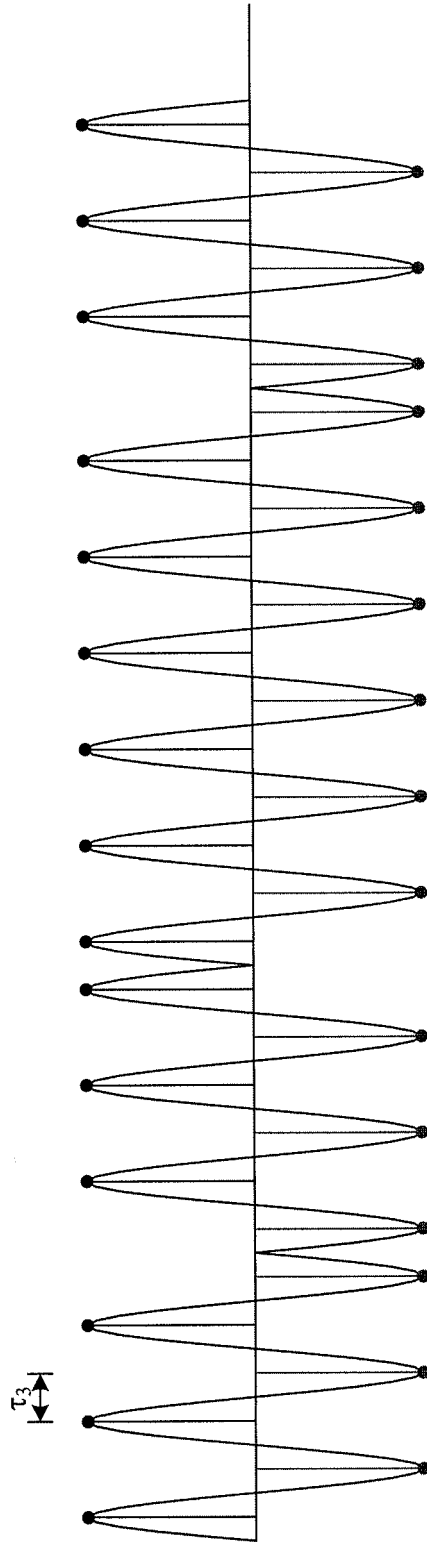


FIG. 6

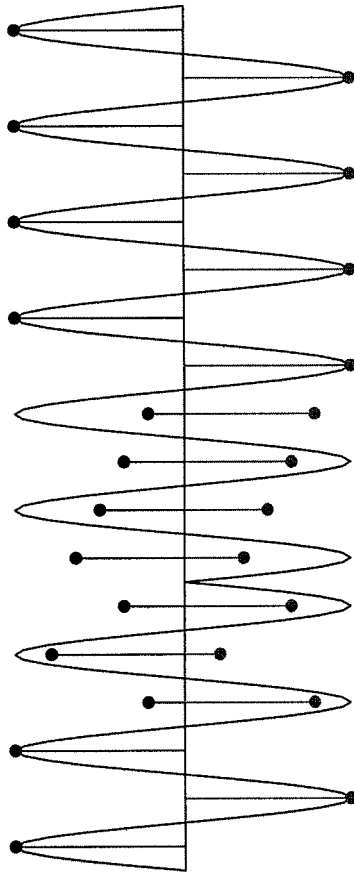


FIG. 7

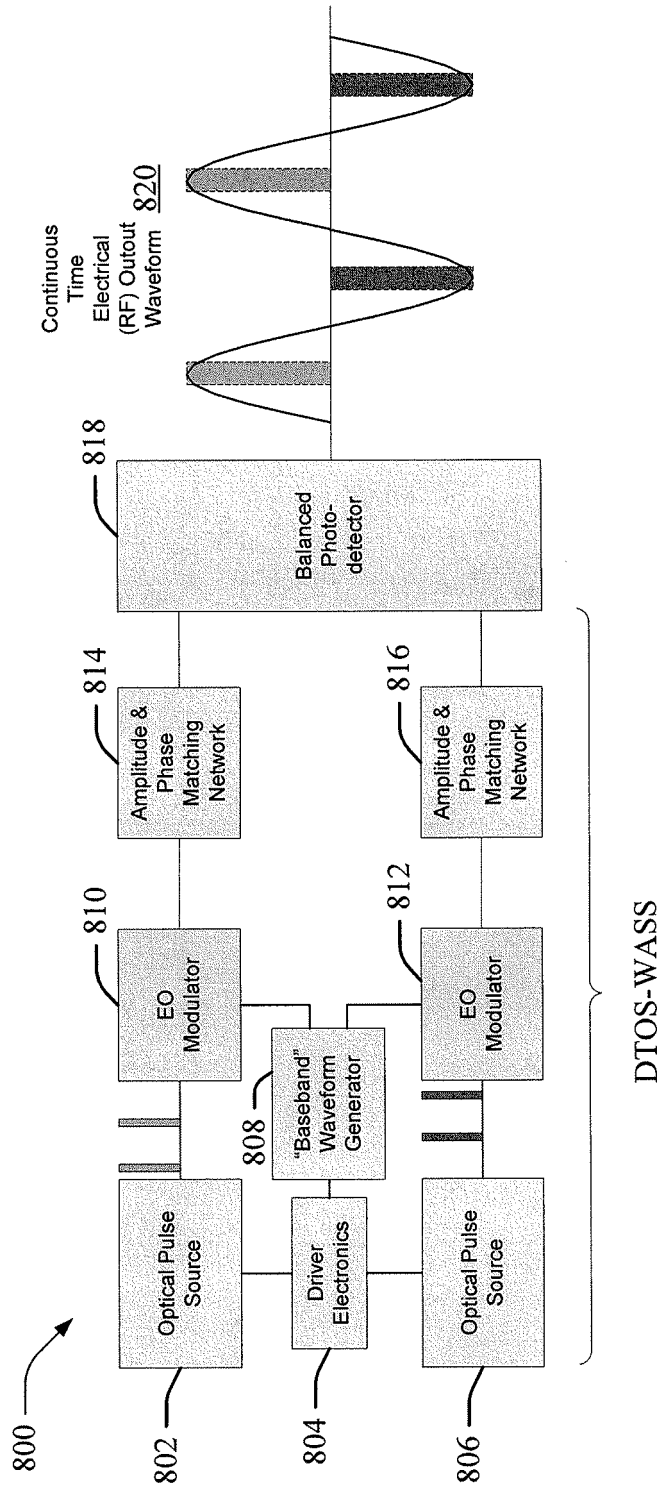


FIG. 8

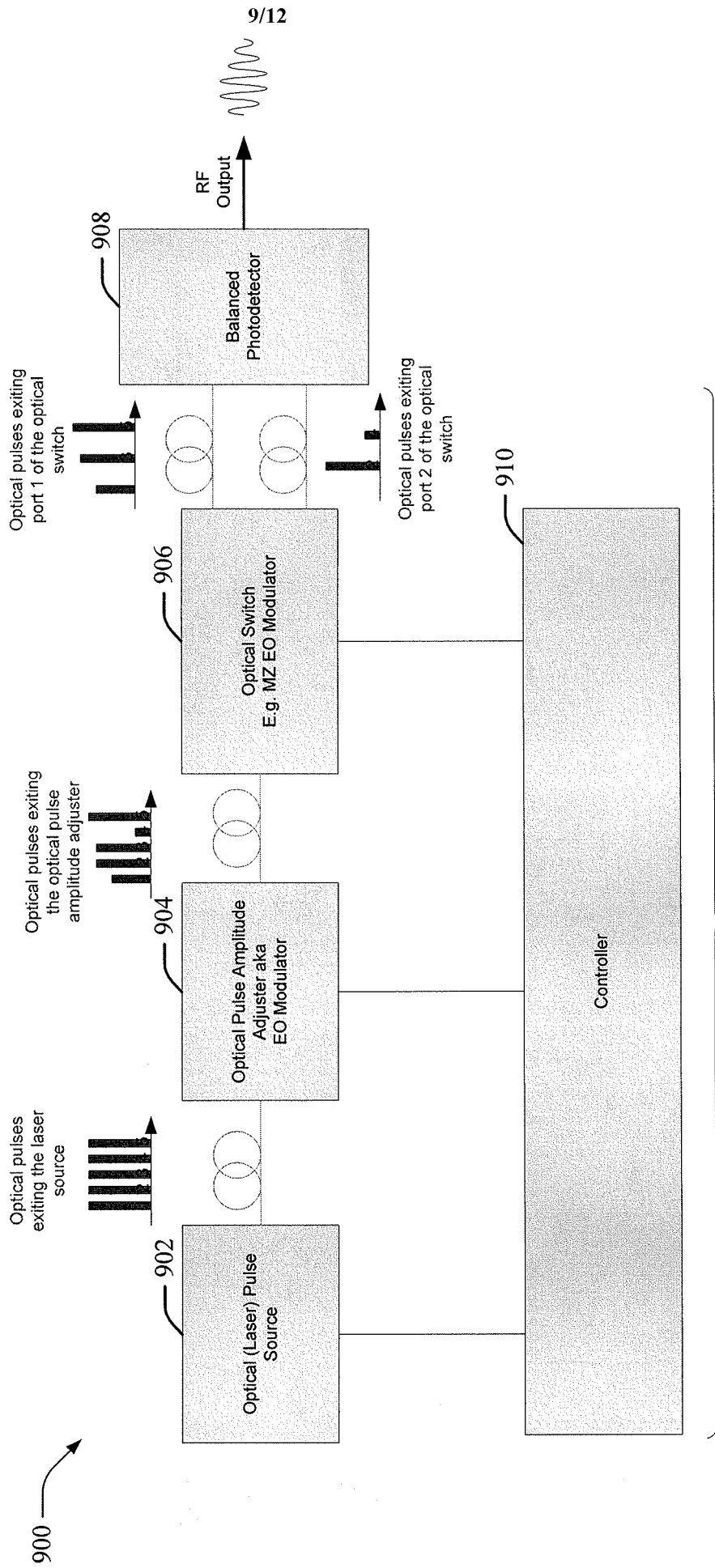


FIG. 9

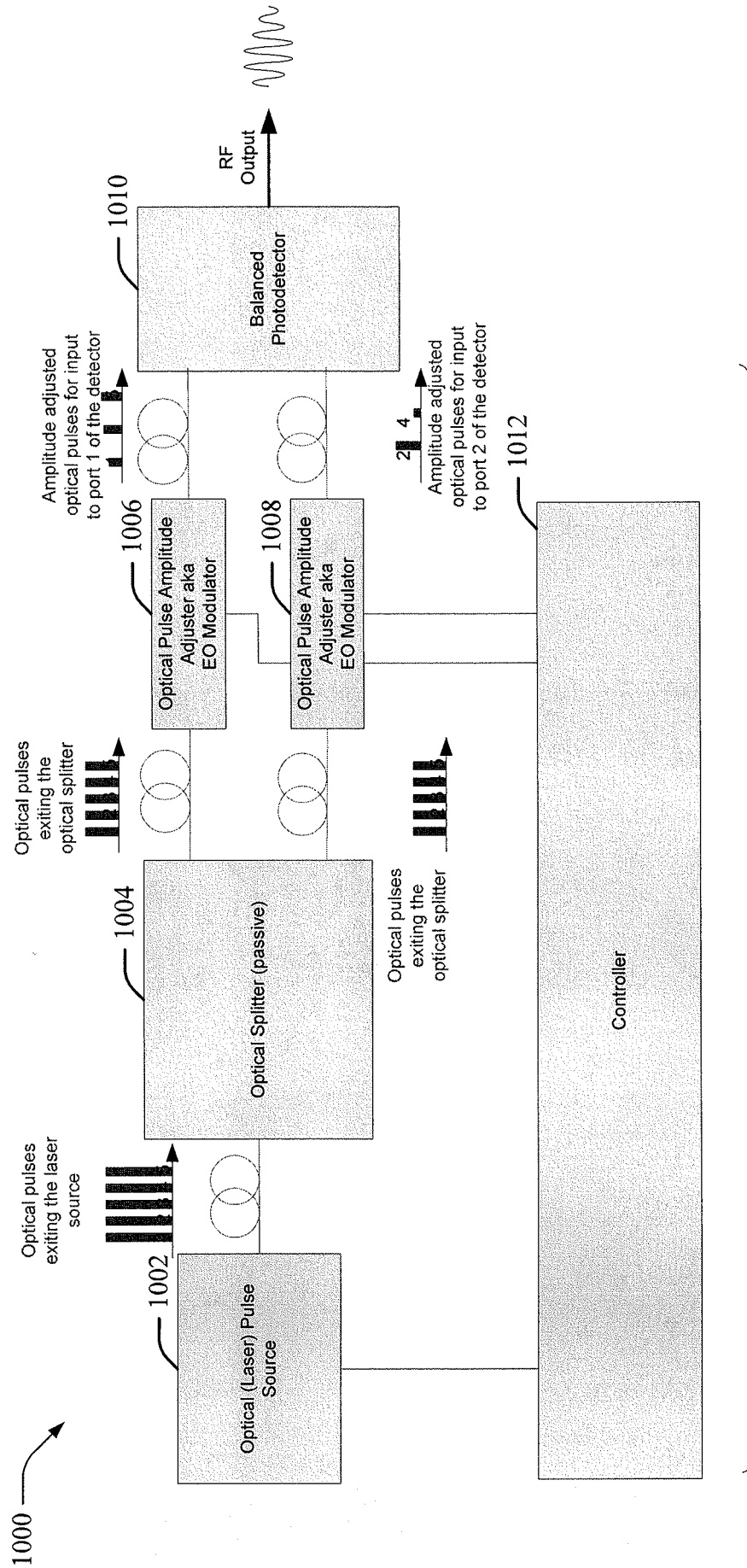


FIG. 10

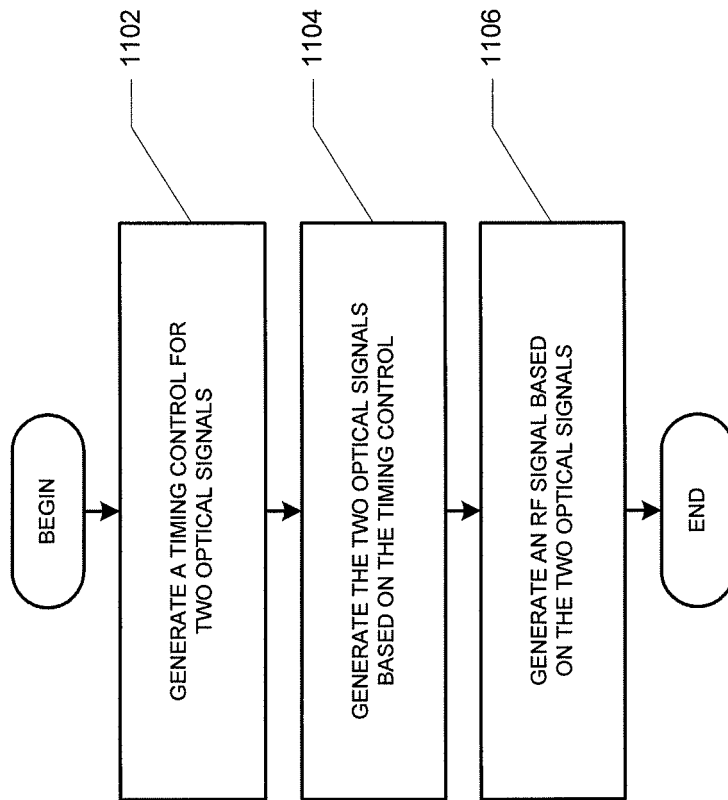


FIG. 11

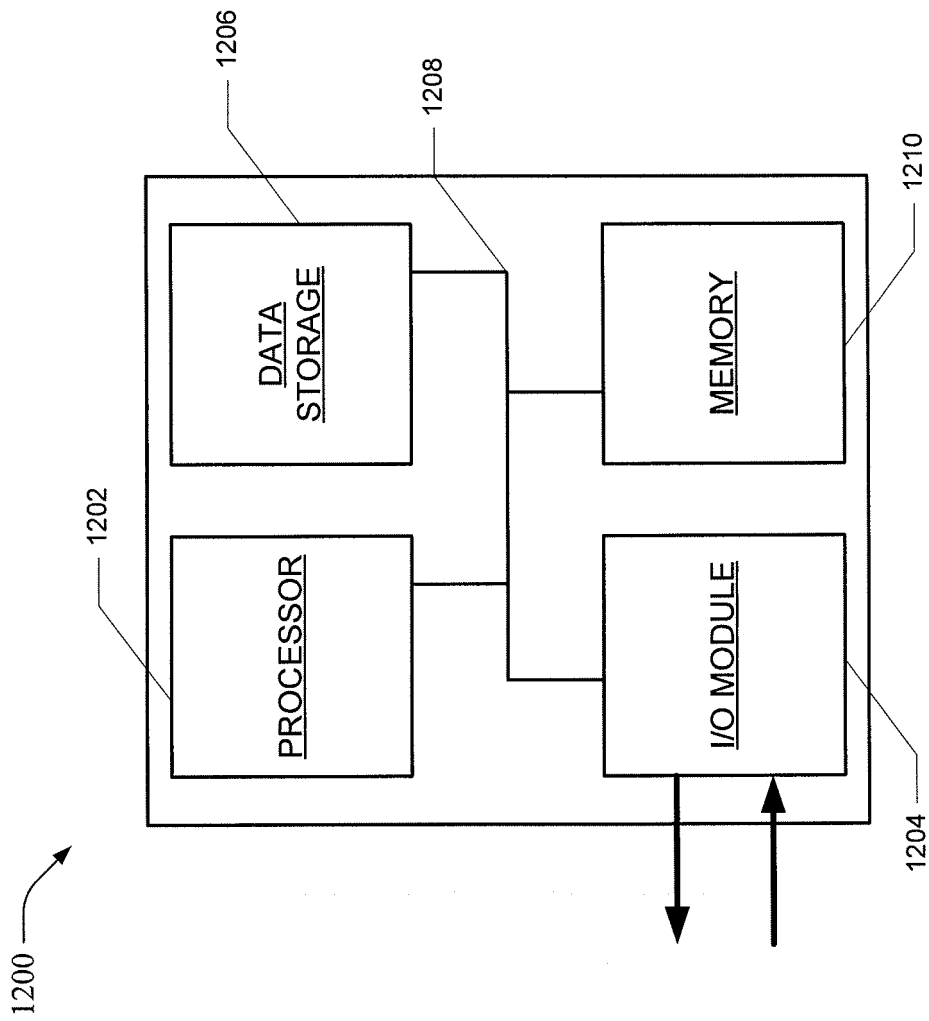


FIG. 12

INTERNATIONAL SEARCH REPORT

International application No.

PCT/US 08/67771

A. CLASSIFICATION OF SUBJECT MATTER

IPC(8) - H04B 10/00 (2008.04)

USPC - 398/115

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

USPC: 398/115

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

USPC: 398/115, 116

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

PubWest(USPB, USPT, USOC, EPAB, JPAB), DialogPro, Google Scholar; generating RF signal driver timing control two optical pulse source photodetector ports balanced reconstruction continuous upper lower arm positive negative 2-port wavelength select lasers direct modulation frequency chirp phase baseband waveform generator code

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	US 2005/0201759 A1 (Wang et al.) 15 September 2005 (15.09.2005), Figs. 1,6; para [0018]-[0027]; [0031]	1-20
Y	US 2005/0129350 A1 (Welch et al.) 16 June 2005 (16.06.2005), para [0203]-[0205]	1-20
Y	US 2004/0190907 A1 (Litvin) 30 September 2004 (30.09.2004), para [0032]-[0035]	3,12
Y	US 2006/0067709 A1 (Newberg et al.) 30 March 2006 (30.03.2006), para [0031]; [0048]; [0067]; [0078]; [0086]-[0087]; [0091]-[0092]; [0097]	6-7,17-20
A	US 7,024,120 B2 (Reynolds et al.) 04 April 2006 (04.04.2006), entire document	1-20

 Further documents are listed in the continuation of Box C.


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"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

04 August 2008 (04.08.2008)

Date of mailing of the international search report

22 AUG 2008

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