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FREQUENCY SELECTIVE VIDEO SIGNAL INTRAFRAME PROCESSOR
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- (57) Claim

1. Video signal processing apparatus comprising:
 signal delay means having an input for receiving a video signal, an output, and a point intermediate said input and output;
 means for selectively coupling (a) signals from said input and said intermediate point to a signal path during a first interval, and (b) signals from said intermediate point and said output to said signal path during a second interval; and
 first means for combining signals from said intermediate point and said signal path; wherein
 said delay means receives a television-type signal representative of a widescreen image having side portion image information and dominant nonside portion image information, and an image aspect ratio greater than that of a standard television image;
 said television-type signal contains a first component having side portion image information time compressed into an overscan region of said first component, and having dominant nonside portion information, said first component having been intraframe processed over a high frequency portion of said nonside portion information exclusive of said compressed side portion information; and

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said signal path includes a high pass filter and a gate for enabling said signal path to conduct a signal from said selective coupling means substantially only during intervals associated with said dominant nonside portion information.

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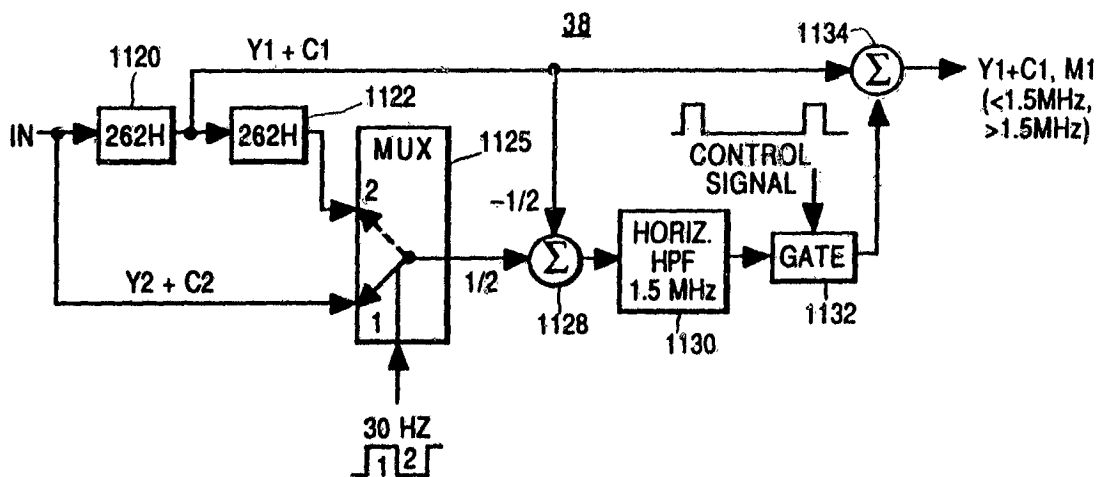
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(54) Title: FREQUENCY SELECTIVE VIDEO SIGNAL INTRAFRAME PROCESSOR



(57) Abstract

A widescreen television signal containing center panel information and time compressed side panel information in an overscan region is intraframe processed above a given frequency only with respect to the center panel information. The intraframe processing apparatus (38) includes a signal delay network (1120, 1122) with first and second 262H delay elements coupled between an input and an output of the delay network. During a first field interval, signals from the delay input and from an intermediate point between the delay elements are subtractively combined (1125, 1128) to produce a difference term which is coupled to a signal path. During a second field interval, signals from the delay output and from the intermediate point are subtractively combined to produce a difference term which is coupled to the signal path. An intraframe processed output signal is produced by combining (1134) the difference terms from the signal path with a signal from the intermediate delay point. A filter (1130) and an electronic gate (1132) in the signal path determine frequency selective intraframe processing on the center panel information alone.

FREQUENCY SELECTIVE VIDEO SIGNAL INTRAFRAME PROCESSOR~~Background of the invention~~

This invention concerns apparatus to facilitate the frequency selective intraframe processing of a video signal. In particular, this invention concerns such apparatus in a widescreen television system for processing a video signal containing center panel and side panel image information.

A conventional television receiver, such as a receiver in accordance with NTSC broadcast standards adopted in the United States and elsewhere, has a 4:3 aspect ratio (the ratio of the width to the height of a displayed image). Recently, there has been interest in using higher aspect ratios for television receiver systems, such as 2:1, 16:9 or 5:3, since such higher aspect ratios more nearly approximate or equal the aspect ratio of the human eye than does the 4:3 aspect ratio of a conventional television receiver. Video information signals with a 5:3 aspect ratio have received particular attention since this ratio approximates that of motion picture film, and thus such signals can be transmitted and received without cropping the image information. However, widescreen television systems which simply transmit signals having an increased aspect ratio as compared to conventional systems are incompatible with conventional aspect ratio receivers. This makes widespread adoption of widescreen systems difficult.

~~It is therefore desirable to have a widescreen system which is compatible with conventional television receivers. One such system is disclosed in a copending U. S. patent application of C. H. Strolle et al., Serial No. 078,150 titled "Compatible Widescreen Television System", filed July 27, 1987. It is even more desirable to have such a compatible widescreen system with provisions for enhancing or extending the definition of the displayed image so as to provide extra image detail. For example, such widescreen EDTV (extended definition television) system may include apparatus for providing a progressively~~



It is therefore desirable to have a widescreen system which is compatible with conventional television receivers. One such system is disclosed in Australian Patent No. 608650. It is even more desirable to have such a compatible widescreen system with provisions for enhancing or extending the definition of the displayed image so as to provide extra image detail. For example, such widescreen EDTV (extended definition television) system may include apparatus for providing a progressively scanned image. A system of this type is disclosed in Australian Patent No. 620352.

In the system disclosed in 620352 a main component of a widescreen television signal contains center panel information as well as side panel information time compressed into a horizontal overscan region. Both the center panel information and the side panel information are intraframe averaged above a given frequency. An improved version of the 620352 system is described in copending Australian Patent Application No. 33479/89 titled "Widescreen Television Signal Processing System with Center and Side Panel Image Resolution Uniformity". This latter application recognizes that improved side panel image resolution can be achieved by not subjecting the time compressed side panel information to intraframe processing.

In accordance with the principles of the present invention there is disclosed herein apparatus arranged to facilitate intraframe video signal processing. The disclosed apparatus requires only two image field storage devices which exhibit uncomplicated filtering requirements to perform frequency selective intraframe processing.

According to the present invention there is provided a video signal processing apparatus comprising:

signal delay means having an input for receiving a video signal, an output, and a point intermediate said input and output;

means for selectively coupling (a) signals from said input and said intermediate point to a signal path during a first interval, and (b) signals from said intermediate

point and said output to said signal path during a second interval; and

first means for combining signals from said intermediate point and said signal path; wherein

5 said delay means receives a television-type signal representative of a widescreen image having side portion image information and dominant nonside portion image information, and an image aspect ratio greater than that of a standard television image;

10 said television-type signal contains a first component having side portion image information time compressed into an overscan region of said first component, and having dominant nonside portion information, said first component having been intraframe processed over a high frequency portion of said nonside portion information exclusive of said compressed side portion information; and

15 said signal path includes a high pass filter and a gate for enabling said signal path to conduct a signal from said selective coupling means substantially only during intervals associated with said dominant nonside portion information.

20 Apparatus in accordance with a disclosed preferred embodiment of the present invention includes a signal delay network with first and second 262H delay elements coupled between an input and an output of the delay network. During a first field interval, signals from the delay input and from an intermediate point between the delay elements may be subtractively combined to produce a difference term which may be coupled to a signal path. During a second field interval, signals from the delay output and from the intermediate point may be subtractively combined to produce a difference term which may be coupled to the signal path. An intraframe processed output signal may be produced by combining the difference terms from the signal path with a signal from the intermediate delay point. Frequency selective intraframe processing may be achieved by placing an appropriate filter in the signal path. Intraframe

processing over a selected spatial portion of the input signal may be achieved by placing an electronic gate in the signal path.

5 Apparatus in accordance with the principles of the present invention is disclosed in the context of a compatible widescreen EDTV television system employing time compression and intraframe signal processing, e.g., averaging, techniques. The widescreen EDTV signal may contain plural components, including a main first
10 component containing center panel and time compressed side panel information, and an auxiliary second component containing side panel information. In the main component, preferably only center panel information above a given frequency is subjected to intraframe processing by means
15 of the disclosed apparatus. The time compressed side panel information portion of the main component is preferably not subjected to intraframe processing.

The first component may be a main 2:1 interlaced signal with a standard 4:3 aspect ratio. This component
20 may comprise a central portion of the widescreen signal that has been time expanded to occupy nearly the entire 4:3 aspect ratio active line time, and side panel horizontal low frequency information that has been time compressed into the left and right horizontal image
25 overscan regions where such information is hidden from view in a standard television receiver display. Preferably only the central portion of this component is subjected to intraframe averaging above a given frequency.

The second component may be an auxiliary 2:1
30 interlaced signal which may comprise left and right side panel high frequency information that have each been time expanded to half the active line time. Thus expanded side panel information may occupy substantially the entire active line time. This component may be "mapped" so as to
35 occupy the same time period as the center portion of the first component, and may be intraframe averaged.

A third component may be an auxiliary 2:1 interlaced signal, derived from the widescreen signal source, comprising high frequency horizontal luminance detail

information between approximately 5.0 MHz and 6.0 MHz. This component may also be "mapped" so as to occupy the same time period as the center portion of the first component, and may also be intraframe averaged. The
5 intraframe averaged second and third components may quadrature modulate a phase controlled alternate subcarrier which may be combined with the intraframe averaged first component.

An optional fourth component is an auxiliary 2:1
10 interlaced "helper" signal comprising temporal field difference luminance detail information to help reconstruct missing image information at a widescreen EDTV receiver.

At a widescreen EDTV receiver, a composite signal
15 containing the described four components may be decoded into the constituent four components by means including the disclosed apparatus. The decoded components may be processed separately and used to develop an image representative widescreen signal with enhanced resolution.

Preferred embodiments of the present invention will
20 now be described with reference to the accompanying drawings wherein:

Figure 1 illustrates a general overview of a
compatible widescreen EDTV encoder system including
25 intraframe processor apparatus in accordance with the present invention;

Figure 1a shows a detailed block diagram of the
encoder for the disclosed system;

Figure 1b-1e contain diagrams helpful in
30 understanding the operation of the disclosed system;

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FIGURES 2-5 depict signal waveforms and diagrams helpful in understanding the operation of the disclosed system;

5 FIGURE 11b illustrates intraframe processor apparatus according to the present invention in a signal encoder;

 FIGURES 15 and 16 illustrate intraframe processor apparatus according to the present invention in a signal decoder;

10 FIGURE 13 shows a block diagram of a portion of widescreen EDTV receiver decoder apparatus; and

 FIGURES 6-11a, 12-14 and 17-24 illustrate other aspects of the disclosed system in greater detail.

 A system intended to transmit wide aspect ratio
15 pictures, e.g., 5:3, through a standard, e.g., NTSC, broadcast channel should achieve a high quality picture display by a widescreen receiver, while greatly reducing or eliminating observable degradations in a standard 4:3 aspect ratio display. The use of signal compression
20 techniques on the side panels of a picture takes advantage of the horizontal overscan region of a standard NTSC television receiver display, but may sacrifice image resolution in the side panel regions of a reconstructed widescreen picture. Since compression in time results in
25 an expansion in the frequency domain, only low frequency components would survive processing in a standard television channel, which exhibits a smaller bandwidth compared with that required for a widescreen signal. Thus, when the compressed side panels of a compatible widescreen
30 signal are expanded in a widescreen receiver, there results a noticeable difference between the resolution or high frequency content of the center portion of a displayed widescreen picture and the side panels, unless steps are taken to avoid this effect. This noticeable difference is
35 due to the fact that low frequency side panel information would be recovered, but high frequency information would be lost due to video channel bandlimiting effects.

In the system of FIGURE 1, elements which are common to the more detailed system of FIGURE 1a are identified by the same reference number. As shown in Figure 1, an original widescreen progressive scan signal with left, right and center panel information is processed so as to develop four separate encoding components. These four components were described above, and are illustrated pictorially in Figure 1. Processing of the first component (containing time expanded center portion information and time compressed side portion low frequency information) is such that the resulting luminance bandwidth does not exceed the NTSC luminance bandwidth of 4.2 MHz in this example. This signal is color encoded in standard NTSC format, and the luminance and chrominance components of this signal are suitably pre-filtered (e.g., using field comb filters) to provide improved luminance-chrominance separation at both standard NTSC and widescreen receivers.

The time expansion of the second component (side panel high frequency information) reduces its horizontal bandwidth to about 1.16 MHz. This component is spatially uncorrelated with the main signal (the first component), and special precautions are taken to mask its visibility on standard NTSC receivers, as will be discussed.

The 5.0 to 6.0 MHz extended high-frequency luminance information content of the third component is first shifted downward in frequency to a frequency range of 0 to 1.0 MHz before further processing.

The fourth component (temporal field difference helper) is mapped into standard 4:3 format to correlate it with the main signal component to thereby mask its visibility on standard NTSC receivers, and is horizontally bandwidth limited to 750 KHz .

As will be discussed in greater detail subsequently, the first, second, and third components are processed by respective intraframe averagers 38, 64, and 76 (a type of vertical-temporal (V-T) filter) to eliminate V-T crosstalk between the main and auxiliary signal components at a widescreen receiver. The center panel information of

the first component is intra-frame averaged above approximately 1.5 MHz. The second and third intraframe averaged components, identified as X and Z, are non-linearly amplitude compressed prior to quadrature modulating a 3.108 MHz alternate subcarrier ASC, having a field alternating (inverting) phase, in a block 80. A modulated signal (M) from block 80 is added to the intraframe averaged first component (N) in an adder 40. A resulting output signal is a 4.2 MHz bandwidth baseband signal (NTSCF) that, together with a 750 KHz low pass filtered fourth component (YTN) from a filter 79, quadrature modulates an RF picture carrier in a block 57 to produce an NTSC compatible RF signal which can be transmitted to a standard NTSC receiver or a widescreen progressive scan receiver via a single, standard bandwidth, broadcast channel.

The use of time compression on the first component allows low frequency side panel information to be squeezed entirely into the horizontal overscan region of a standard NTSC signal. The high frequency side panel information of the second component and the high frequency luminance detail information of the third component are spectrally shared with the standard NTSC signal through the video transmission channel, in a manner transparent to a standard receiver, through the use of an alternate subcarrier quadrature modulation technique involving block 80 as will be discussed. When received by a standard NTSC receiver, only the center panel portion of the main signal (the first component) is seen. The second and third components may create a low amplitude interference pattern that is not perceived at normal viewing distances and at normal picture control settings. The fourth component is removed completely in receivers with synchronous video detectors. In receivers with envelope detectors, the fourth component is processed but not perceived because it is correlated with the main signal.

The main signal (component 1) exhibits a standard NTSC active horizontal line interval of approximately 52

microseconds (μ s). Only high frequency information of this component above about 1.5 MHz is intraframe averaged. The time compressed side panel low frequency information of this component is not subjected to the process of
5 intraframe averaging. It has been found that such selective intraframe processing of the main component improves the resolution of diagonal side panel image information by eliminating undesirable jagged diagonal artifacts, sometimes referred to as "jaggies", which would
10 otherwise be produced in a reconstructed image if the compressed side panel information of the main signal were intraframe averaged.

In this regard it is noted that the side panel low frequency information of the main signal component has
15 been time compressed with a side compression factor (SCF) of approximately six. If such time compressed information is intraframe averaged before being time expanded at the receiver for reconstructing the image, the reconstructed side panel image information would exhibit jagged diagonals
20 because the horizontal frequency at which intraframe averaging began would then be approximately SCF times lower than for the center panel. Diagonal image information becomes increasingly distorted ("jagged") as the frequencies over which intraframe averaging is performed
25 decrease. For example, if the main signal is intraframe averaged for frequencies above 1.5 MHz and the side panel low frequency information of component 1 is time compressed with an SCF of six, intraframe averaging of the side panel information effectively begins at a much lower frequency of
30 250 KHz (1.5 MHz/SCF), whereby jagged diagonals result. Thus jagged diagonals would be more noticeable in the reconstructed side panel regions. Since component 1 is not intraframe averaged in the time compressed side panel regions, the entire range of original frequencies in these
35 regions (0-700 KHz) retains full vertical resolution without distortion due to jagged diagonal artifacts.

Component 2, containing the left and right side panel high frequency information, is mapped so that it

occupies the same time period as the center panel portion of component 1. Thus the left and right side panel highs are time expanded to fill the entire center panel region whereby component 2 exhibits an active horizontal scanning interval of approximately 50 μ s, which corresponds to the horizontal scanning interval of the center panel portion of component 1. For this purpose the side expansion factor (SEF) is about 4.32, compared to an SEF of about 4.49 which would be required to expand the left and right side panel information of component 2 to the full active line time of 52 μ s.

Both components 2 and 3 are mapped into the center panel region because of the intraframe processing performed on main component 1 and auxiliary components 2 and 3. As will be explained subsequently, intraframe averaging is a process which facilitates the separation of two previously combined signal components, such as main signal N and auxiliary modulated signal M in this example. Since the region of intraframe processing in component 1 has been reduced to encompass only the 50 μ s center panel region, the mapping of modulating components 2 and 3 is similarly modified to encompass only the center panel region.

As mentioned above, component 3 is mapped so as to coincide with the center panel interval by linearly time compressing the extended horizontal luminance information to 50 μ s. Time compressing component 3 from 52 μ s to 50 μ s sacrifices some spatial correlation with main component 1, but more importantly assures that the center and side panel regions of a reconstructed image will exhibit similar horizontal resolution. Although spatial correlation between components 1 and 3 is desirable to mask the effects of crosstalk between the alternate subcarrier and the main signal, the importance of maintaining perfect spatial correlation of component 3 is reduced because the alternate subcarrier already contains noncorrelated information in the form of component 2. The amount of spatial correlation given up in component 3 is negligible and is outweighed by

the resulting similar center and side panel horizontal resolution. Component 4 is not intraframe averaged and remains unchanged, exhibiting a full 52 μ s active line time consistent with the main signal.

5 In the decoder, as will be discussed in connection with Figure 13, intraframe processing is performed only with respect to the center panel region for separating signals M and N. After demodulating component M into constituent components 2 and 3, components 2 and 3 are
10 mapped into their original time slots, i.e., to occupy a full active line interval of 52 μ s.

Figure 1b illustrates the RF spectrum of the disclosed EDTV widescreen system, including the auxiliary information, compared to the RF spectrum of a standard NTSC
15 system. In the spectrum of the disclosed system the side panel highs and the extra high frequency horizontal luminance detail information extend approximately 1.16 MHz on either side of the 3.108 MHz alternate subcarrier (ASC) frequency. The V-T helper signal information (component 4)
20 extends 750 KHz on either side of the main signal picture carrier frequency.

A widescreen progressive scan receiver includes apparatus for reconstructing the original widescreen progressive scan signal. Compared to a standard NTSC
25 signal, the reconstructed widescreen signal has left and right side panels with standard NTSC resolution, and a 4:3 aspect ratio center panel with superior horizontal and vertical luminance detail particularly in stationary portions of an image.

30 Two basic considerations govern the signal processing technique associated with the development and processing of the first, second, third, and fourth signal components. These considerations are compatibility with existing receivers, and recoverability at the receiver.

35 Full compatibility implies receiver and transmitter compatibility such that existing standard receivers can receive widescreen EDTV signals and produce a standard display without special adaptors. Compatibility

in this sense requires, for example, that the transmitter image scanning format is substantially the same as, or within the tolerance of, the receiver image scanning format. Compatibility also means that extra non-standard components must be physically or perceptually hidden in the main signal when displayed on standard receivers. To achieve compatibility in the latter sense, the disclosed system uses the following techniques to hide the auxiliary components.

As discussed above, the side panel lows are physically hidden in the normal horizontal overscan region of a standard receiver. Component 2, which is a low-energy signal compared to the side panel lows component, and component 3, which is a normally low energy high frequency detail signal, are amplitude compressed and quadrature modulated onto an alternate subcarrier at 3.108 MHz, which is an interlaced frequency (an odd multiple of one-half the horizontal line rate). The frequency, phase, and amplitude of the alternate subcarrier are chosen so that the visibility of the modulated alternate subcarrier signal is reduced as much as possible, e.g., by controlling the phase of the alternate subcarrier from field to field so that it alternates 180° from one field to the next, unlike the phase of the chrominance subcarrier from one field to the next. Although the modulated alternate subcarrier components reside entirely within the chrominance passband (2.0-4.2 MHz), the modulated alternate subcarrier components are perceptually hidden because they are displayed as field rate complementary color flicker, which is not perceived by the human eye at normal levels of chrominance saturation. Also, nonlinear amplitude compression of the modulation components prior to amplitude modulation advantageously reduces instantaneous amplitude overshoots to an acceptable lower level. Component 3 is spatially correlated with respect to the central information portion of component 1 and is slightly less spatially correlated with respect to the left and right

information portions of component 1. This is accomplished by means of a format encoder as will be discussed.

Component 4, the "helper" signal, also is hidden by time expanding the center panel information to match the standard 4:3 format, thereby spatially correlating component 4 with the main signal. Component 4 is removed at standard receivers with synchronous detectors, and is perceptually hidden at standard receivers with envelope detectors because it is spatially correlated with the main signal.

Recovery of components 1, 2, and 3 at a widescreen progressive scan receiver is accomplished by utilizing a process of intraframe processing at the transmitter and receiver. This process is associated with elements 38, 64, and 76 in the transmitter system of FIGURES 1 and 1a, and with associated elements at the receiver as will be discussed. Intraframe averaging is one type of signal conditioning technique which prepares two visually correlated signals for mutual combining so that they can be recovered efficiently and accurately afterwards, such as by means of a field storage device, free from V-T (vertical-temporal) crosstalk even in the presence of motion in the case of image representative signals. The type of signal conditioning employed for this purpose essentially involves making two signals identical on a field basis, i.e., by obtaining two samples with identical values a field apart. Intraframe averaging is a convenient technique for achieving this objective, but other techniques can also be used. Intraframe averaging is basically a linear, time varying digital pre-filtering and post-filtering process to ensure the accurate recovering of two visually correlated combined signals. Horizontal crosstalk is eliminated by guardbands between horizontal pre-filters at the transmitter encoder and post-filters at the receiver decoder.

Intraframe averaging is a form of paired pixel processing. The process of intraframe averaging in the time domain is illustrated generally by FIGURE 1c, wherein

pairs of fields are made identical by averaging pixels (A, B and C,D) that are 262H apart. The average value replaces the original values in each pair. FIGURE 1d illustrates the process of intraframe averaging in the context of the system of Figure 1. Starting with components 2 and 3, pairs of pixels (picture elements) 262H apart within a frame are averaged, and the average value (e.g., X1, X3 and Z1, Z3) replaces the original pixel values. This V-T averaging occurs within a frame and does not cross frame boundaries. In the case of component 1, intraframe averaging is performed only on center panel information above approximately 1.5 MHz so as not to affect lower frequency vertical detail information. In the case of components 1 and 2, intraframe averaging is performed on a composite signal including luminance (y) and chrominance (c) components throughout the chrominance band. The chrominance component of the composite signal survives intraframe averaging because pixels 262H apart are "in-phase" with respect to the color subcarrier. The phase of the new alternate subcarrier is controlled so that it is exactly out of phase for pixels 262H apart, and is unlike the phase of the chrominance subcarrier. Thus when components 2 and 3 (after quadrature modulation) are added to component 1 in unit 40, pixels 262H apart have the form (M + A) and (M - A), where M is a sample of the main composite signal above 1.5 MHz, and A is a sample of the auxiliary modulated signal.

With intraframe averaging V-T crosstalk is virtually eliminated, even in the presence of motion. In this regard, the process of intraframe averaging produces identical samples 262H apart. At the receiver it is a simple matter to recover the information content of these samples exactly, i.e., free from crosstalk, by processing pixel samples 262H apart within a frame as will be discussed, thereby recovering main and auxiliary signal information. At a decoder in the receiver, the intraframe averaged original information can be recovered substantially intact via intraframe processing since the

original highly visually correlated information has been made substantially identical field-to-field.

Also at the receiver, the RF channel is quadrature demodulated using a synchronous RF detector.

5 Component 4 is thereby separated from the other three components. Intraframe processing is used to separate component 1 from modulated components 2 and 3, and quadrature demodulation is used to separate components 2 and 3, as will be discussed with regard to FIGURE 13.

10 After the four components have been recovered, the composite signals are NTSC decoded and separated into luminance and chrominance components. Inverse mapping is performed on all components to recover the widescreen aspect ratio, and the side panel highs are combined with
15 the lows to recover full side panel resolution. The extended high frequency luminance detail information is shifted to its original frequency range and added to the luminance signal, which is converted to the progressive scan format using temporal interpolation and the helper
20 signal. The chrominance signal is converted to progressive scan format using unassisted temporal interpolation. Finally, the luminance and chrominance progressive scan signals are converted to analog form and matrixed to produce RGB color image signals for display by a widescreen
25 progressive scan display device.

Before discussing the compatible widescreen encoding system of Figure 1a, reference is made to signal waveforms A and B of Figure 2. Signal A is a 5:3 aspect ratio widescreen signal that is to be converted to a
30 standard NTSC compatible signal with a 4:3 aspect ratio as depicted by signal B. Widescreen signal A includes a center panel portion associated with primary image information occupying an interval TC, and left and right side panel portions associated with secondary image
35 information and occupying intervals TS. In this example the left and right side panels exhibit substantially equal aspect ratios, less than that of the dominant center panel which is centered therebetween.

Widescreen signal A is converted to NTSC signal B by compressing certain side panel information completely into the horizontal overscan regions associated with time intervals T_0 . The standard NTSC signal has an active line interval T_A (52.6 microseconds duration) which encompasses
5 overscan intervals T_0 , a display time interval T_D which contains the video information to be displayed, and a total horizontal line time interval T_H of 63.556 microseconds duration. Intervals T_A and T_H are the same for both the
10 widescreen and the standard NTSC signals. It has been found that almost all consumer television receivers have an overscan interval which occupies at least 4% of the total active line time T_A , i.e., 2% overscan on the left and right sides. At an interlace sampling rate of $4 \times f_{sc}$
15 (where f_{sc} is the frequency of the color subcarrier), each horizontal line interval contains 910 pixels (picture elements) of which 754 constitute the active horizontal line image information to be displayed.

The widescreen EDTV system is shown in greater
20 detail in Figure 1a. Referring to Figure 1a, a 525 line, 60 field/sec. widescreen progressive scan camera 10 provides a widescreen color signal with R, G, B components and a wide aspect ratio of 5:3 in this example. An interlaced signal source could also be used, but a
25 progressive scan signal source produces superior results. A widescreen camera has a greater aspect ratio and a greater video bandwidth compared to a standard NTSC camera, the video bandwidth of a widescreen camera being
30 proportional to the product of its aspect ratio and the total number of lines per frame, among other factors. Assuming constant velocity scanning by the widescreen camera, an increase in its aspect ratio causes a
35 corresponding increase in its video bandwidth as well as horizontal compression of picture information when the signal is displayed by a standard television receiver with a 4:3 aspect ratio. For these reasons, it is necessary to modify the widescreen signal for full NTSC compatibility.

The color video signal processed by the encoder system of Figure 1 contains both luminance and chrominance signal components. The luminance and chrominance signals contain both low and high frequency information, which in the following discussion will be referred to as "lows" and "highs", respectively.

The wide bandwidth widescreen progressive scan color video signals from camera 10 are matrixed in a unit 12 to derive luminance component Y and color difference signal components I and Q from the R, G, B color signals. Wideband progressive scan signals Y, I, Q are sampled at an eight-times chrominance subcarrier rate ($8 \times f_{sc}$), and are converted from analog to digital (binary) form individually by separate analog-to-digital converters (ADC) in an ADC unit 14 before being filtered individually by separate vertical-temporal (V-T) low pass filters in a filter unit 16 to produce filtered signals YF, IF and QF. These signals are each of the form indicated by waveform A in Figure 2. The separate filters are 3X3 linear time invariant filters of the type shown in Figure 10d as will be discussed. These filters reduce vertical-temporal resolution slightly, particularly diagonal V-T resolution, to prevent unwanted interlace artifacts (such as flicker, jagged edges, and other aliasing related effects) in the main signal (component 1 in Figure 1) after progressive scan to interlace conversion. The filters maintain nearly full vertical resolution in stationary portions of the image.

The center panel expansion factor (CEF) is a function of the difference between the width of an image displayed by a widescreen receiver and the width of an image displayed by a standard receiver. The image width of a widescreen display with a 5:3 aspect ratio is 1.25 times greater than the image width of a standard display with a 4:3 aspect ratio. This factor of 1.25 is a preliminary center panel expansion factor which must be adjusted to account for the overscan region of a standard receiver, and to account for an intentional slight overlap of the

boundary regions between the center and side panels as will be explained. These considerations dictate a CEF of 1.19.

The progressive scan signals from filter network 16 exhibit a bandwidth of 0-14.32 MHz and are respectively
5 converted into 2:1 interlaced signals by means of progressive scan (P) to interlace (I) converters 17a, 17b and 17c, details of which will be discussed in connection with FIGURES 22 and 23. The bandwidth of output signals
10 IF', QF' and YF' from converters 17a-17c exhibit a bandwidth of 0-7.16 MHz since the horizontal scanning rate for interlaced signals is half that of progressive scan signals. In the conversion process, the progressive scan signal is subsampled, taking half the available pixel
15 samples to produce the 2:1 interlaced main signal. Specifically, each progressive scan signal is converted to 2:1 interlaced format by retaining either the odd or even lines in each field and reading out the retained pixels at a 4 x fsc rate (14.32 MHz). All subsequent digital
20 processing of the interlaced signals occurs at the 4 x fsc rate.

Network 17c also includes an error prediction network. One output of network 17c, YF', is the interlaced subsampled luminance version of the prefiltered progressive scan component. Another output (luminance) signal of
25 network 17c, YT, comprises temporal information derived from image field difference information and represents a temporal prediction, or temporal interpolation, error between actual and predicted values of luminance samples "missing" at the receiver, as will be explained. The
30 prediction is based on a temporal average of the amplitudes of "before" and "after" pixels, which are available at the receiver. Signal YT, a luminance "helper" signal that assists to reconstruct the progressive scan signal at the receiver, essentially accounts for an error that the
35 receiver is expected to make with respect to non-stationary image signals and facilitates cancellation of such error at the receiver. In stationary portions of an image the error is zero, and perfect reconstruction is performed at the

receiver. It has been found that a chrominance helper signal is not needed as a practical matter, and that a luminance helper signal is sufficient to produce good results since the human eye is less sensitive to a lack of chrominance vertical or temporal detail. FIGURE 2a illustrates the algorithm used to develop helper signal YT.

Referring to FIGURE 2a, pixels A, X, and B in the progressive scan signal occupy the same spatial position in an image. Black pixels such as A and B are transmitted as the main signal and are available at the receiver. A white pixel, such as X, is not transmitted and is predicted by a temporal frame average $(A + B)/2$. That is, at the encoder a prediction is made for "missing" pixel X by averaging the amplitudes of "before" and "after" pixels A and B. The prediction value, $(A + B)/2$, is subtracted from the actual value, X, to produce a prediction error signal, corresponding to the helper signal, with an amplitude in accordance with the expression $X - (A + B)/2$. This expression defines temporal field difference information in addition to temporal frame average information. The helper signal is lowpass filtered horizontally by means of a 750 KHz low pass filter and conveyed as helper signal YT. Bandlimiting of the helper signal to 750 KHz is necessary to prevent this signal from interfering with the next lower RF channel after this signal is modulated onto the RF picture carrier. At the receiver, a similar prediction of missing pixel X is made by using an average of samples A and B, and the prediction error is added to the prediction. That is, X is recovered by adding the prediction error $X - (A + B)/2$ to the temporal average $(A + B)/2$. Thus the helper signal facilitates the conversion from interlaced to progressive scan format.

The helper signal produced by the disclosed temporal prediction algorithm advantageously is a low energy signal compared to a prediction signal produced by some other algorithms, such as that used to produce a line differential signal as described by M. Tsingberg in an article "ENTSC Two-Channel Compatible HDTV System", IEEE

Transactions on Consumer Electronics, Vol. CE-33, No. 3, August 1987, pp. 146-153. In still areas of an image, the error energy is zero because the prediction is perfect. A low energy condition is manifested by still and

5 substantially still images (such as a news broadcast featuring a reporter against a still background). The disclosed algorithm has been found to produce the least objectionable artifacts after image reconstruction at the receiver, and the helper signal produced by the disclosed

10 algorithm retains its usefulness after being bandlimited (filtered) to about 750 KHz. The helper signal produced by the disclosed algorithm advantageously exhibits zero energy in the presence of still image information, and consequently a helper signal associated with a still image

15 is unaffected by filtering. A highly improved reconstructed widescreen image results even if the helper signal is not transmitted. In such case still portions of the image will be much sharper than a standard NTSC image, but moving portions will be somewhat "softer" and may

20 exhibit a "beat" artifact. Thus a broadcaster need not transmit the helper signal initially, but can choose to upgrade the RF transmission at a later time.

The disclosed temporal prediction system is useful for both progressive scan and interlaced systems

25 with higher than standard line rates, but works best with a progressive scan source having pixels A, X and B occupying the same spatial position in an image, which results in a perfect prediction for still images. The temporal prediction will be imperfect even in still portions of an

30 image if the original widescreen image comes from an interlaced signal source. In such case the helper signal will have more energy and will introduce slight artifacts in still portions of a reconstructed image. Experiments have shown that the use of an interlaced signal source

35 yields acceptable results with artifacts being noticeable only upon close inspection, but that a progressive scan signal source introduces fewer artifacts and produces preferred results.

Returning to FIGURE 1a, interlaced widescreen signals IF', QF' and YF' from converters 17a-17c are respectively filtered by horizontal lowpass filters 19a, 19b and 19c to produce a signal IF" with a bandwidth of 0-600 KHz, a signal QF" with a bandwidth of 0-600 KHz, and a signal YF" with a bandwidth of 0-5 MHz. These signals are next subjected to a format encoding process which encodes each of these signals into a 4:3 format by means of format encoding apparatus associated with a side-center signal separator and processor unit 18. Briefly, the center portion of each widescreen line is time-expanded and mapped into the displayed portion of the active line time with a 4:3 aspect ratio. Time expansion causes a decrease in bandwidth so that the original widescreen interlaced frequencies are made compatible with the standard NTSC bandwidth. The side panels are split into horizontal frequency bands so that the I and Q color highs component exhibit a bandwidth of 83KHz-600KHz (as shown for signal IH in Figure 7) and the Y luminance highs component exhibits a bandwidth of 700KHz-5.0MHz (as shown for signal YH in Figure 6). The side panel lows, i.e., signals YO, IO and QO developed as shown in Figures 6 and 7, include a DC component and are time-compressed and mapped into the left and right horizontal image overscan regions on each line. The side panel highs are processed separately. Details of this format encoding process follow immediately below.

In the course of considering the following encoding details, it will be helpful to also consider Figure 1e, which depicts the process of encoding components 1, 2, 3 and 4 in the context of displayed center and side panel information. Filtered interlaced signals IF", QF" and YF" are processed by side-center panel signal separator and processor 18 to produce three groups of output signals: YE, IE and QE; YO, IO and QO; and YH, IH and QH. The first two groups of signals (YE, IE, QE and YO, IO, QO) are processed to develop a signal containing a full bandwidth center panel component, and side panel luminance lows compressed into horizontal overscan regions. The third

group of signals (YH, IH, QH) is processed to develop a signal containing side panel highs. When these signals are combined, an NTSC compatible widescreen signal with a 4:3 display aspect ratio is produced. Details of circuits comprising unit 18 will be shown and discussed in connection with Figures 6, 7 and 8.

Signals YE, IE and QE contain complete center panel information and exhibit the same format, as indicated by signal YE in Figure 3. Briefly, signal YE is derived from signal YF" as follows. Widescreen signal YF" contains pixels 1-754 occurring during the active line interval of the widescreen signal, containing side and center panel information. The wideband center panel information (pixels 75-680) is extracted as a center panel luminance signal YC via a time de-multiplexing process. Signal YC is time expanded by the center panel expansion factor of 1.19 (i.e., 5.0 MHz ÷ 4.2 MHz) to produce NTSC compatible center panel signal YE. Signal YE exhibits an NTSC compatible bandwidth (0-4.2 MHz) due to the time expansion by factor 1.19. Signal YE occupies picture display interval TD (Figure 2) between overscan regions TO. Signals IE and QE are developed from signals IF" and QF", respectively, and are similarly processed in the manner of signal YE.

Signals YO, IO and QO provide the low frequency side panel information ("lows") which is inserted into the left and right horizontal overscan regions. Signals YO, IO and QO exhibit the same format, as indicated by signal YO in Figure 3. Briefly, signal YO is derived from signal YF" as follows. Widescreen signal YF contains left panel information associated with pixels 1-84 and right panel information associated with pixels 671-754. As will be discussed, signal YF" is low pass filtered to produce a luminance lows signal with a 0-700 KHz bandwidth, from which signal a left and right side panel lows signal is extracted (signal YL' in Figure 3) via a time de-multiplexing process. Luminance lows signal YL' is time compressed to produce side panel lows signal YO with compressed low frequency information in the overscan

regions associated with pixels 1-14 and 741-754. The compressed side lows signal exhibits an increased BW proportional to the amount of time compression. Signals IO and QO are developed from signals IF" and QF" respectively, and are similarly processed in the manner of signal YO.

Signals YE, IE, QE and YO, IO, QO are combined by a side-center signal combiner 28, e.g. a time multiplexer, to produce signals YN, IN and QN with an NTSC compatible bandwidth and a 4:3 aspect ratio. These signals are of the form of signal YN shown in Figure 3. Combiner 28 also includes appropriate signal delays for equalizing the transit times of the signals being combined. Such equalizing signal delays are also included elsewhere in the system as required to equalize signal transit times.

A modulator 30, bandpass filter 32, H-V-T bandstop filter 34 and combiner 36 constitute an improved NTSC signal encoder 31. Chrominance signals IN and QN are quadrature modulated on a subcarrier SC at the NTSC chrominance subcarrier frequency, nominally 3.58 MHz, by modulator 30 to produce a modulated signal CN. Modulator 30 is of conventional design and will be described in connection with Figure 9. Modulated signal CN is bandpass filtered in the vertical (V) and temporal (T) dimensions by means of two-dimensional (V-T) filter 32, which removes crosstalk artifacts in the interlaced chrominance signal before it is applied to a chrominance signal input of combiner 36 as a signal CP. Luminance signal YN is bandstop filtered in the horizontal (H), vertical (V) and temporal (T) dimensions by means of three-dimensional H-V-T bandstop filter 34 before being applied, as a signal YP, to a luminance input of combiner 36. Filtering luminance signal YN and chrominance color difference signals IN and QN serves to assure that luminance-chrominance crosstalk will be significantly reduced after subsequent NTSC encoding. Multi-dimensional spatial-temporal filters such as H-V-T filter 34 and V-T filter 32 in Figure 1 comprise structure as illustrated by Figure 10 which will be discussed subsequently.

H-V-T bandstop filter 34 in Figure 1a exhibits the configuration of Figure 10b, and removes upwardly moving diagonal frequency components from luminance signal YN. These frequency components are similar in appearance to chrominance subcarrier components and are removed to make a hole in the frequency spectrum into which modulated chrominance will be inserted. The removal of the upwardly moving diagonal frequency components from luminance signal YN does not visibly degrade a displayed picture because it has been determined that the human eye is substantially insensitive to these frequency components. Filter 34 exhibits a cut-off frequency of approximately 1.5 MHz so as not to impair luminance vertical detail information.

V-T bandpass filter 32 reduces the chrominance bandwidth so that modulated chrominance side panel information can be inserted into the hole created in the luminance spectrum by filter 34. Filter 32 reduces the vertical and temporal resolution of chrominance information such that static and moving edges are slightly blurred, but this effect is of little or no consequence due to the insensitivity of the human eye to such effect.

An output center/side lows signal C/SL from combiner 36 contains NTSC compatible information to be displayed, as derived from the center panel of the widescreen signal, as well as compressed side panel lows (both luminance and chrominance) derived from the side panels of the widescreen signal and situated in the left and right horizontal overscan regions not seen by a viewer of an NTSC receiver display. The compressed side panel lows in the overscan region represent one constituent part of the side panel information for a widescreen display. The other constituent part, the side panel highs, is developed by processor 18 as will be discussed below. Side panel high signals YH (luminance highs), IH (I highs) and QH (Q highs) are illustrated by Figure 4. Figures 6, 7 and 8 illustrate apparatus for developing these signals, as will be discussed. In Figure 4, signals YH, IH and QH contain left panel high frequency information associated

with left panel pixels 1-84, and right panel high frequency information associated with right panel pixels 671-754.

The center panel portion of signal C/SL is processed by an intraframe averager 38 to produce a signal
5 N, which is applied to an input of an adder 40. Intraframe averaged signal N is essentially identical to signal C/SL because of the high visual correlation of intraframe image information of signal C/SL. Averager 38 averages signal
10 C/SL above approximately 1.5 MHz and assists to reduce or eliminate vertical-temporal crosstalk between the main and auxiliary signals. The highpass frequency range of 1.5 MHz and above over which intraframe averager 38 operates was chosen to assure that full intraframe averaging is
15 accomplished for information at 2 MHz and above, to prevent luminance vertical detail information from being degraded by the process of intraframe averaging. Horizontal crosstalk is eliminated by means of a 200 KHz guardband between a filter associated with intraframe averager 38 in
20 encoder 31 and a filter associated with an intraframe processor unit in the decoder of FIGURE 13. Figure 11b shows details of high intraframe averager 38. Figures 11b and 13 will be discussed subsequently.

Signals IH, QH, and YH are placed in NTSC format by means of an NTSC encoder 60 which is similar to encoder
25 31. Specifically, encoder 60 includes apparatus of the type shown in Figure 9, as well as apparatus for quadrature modulating side panel chrominance highs information onto the side panel luminance highs information at 3.58 MHz, to produce signal NTSCH, the side panel highs information in
30 NTSC format. This signal is illustrated by Figure 5.

The use of multi-dimensional bandpass filtering in NTSC encoders 31 and 60 advantageously permits the luminance and chrominance components to be separated
35 virtually free of crosstalk at the receiver when the receiver includes complementary multi-dimensional filtering for separating the luminance and chrominance information. The use of complementary filters for luminance/chrominance encoding and decoding is called cooperative processing and

is discussed in detail in an article by C. H. Strolle titled "Cooperative Processing for Improved Chrominance/Luminance Separation", published in the SMPTE Journal, Vol. 95, No. 8, August 1986, pp. 782-789. Even
5 standard receivers using conventional notch and line-comb filters will benefit from the use of such multi-dimensional pre-filtering at the encoder by exhibiting reduced chrominance/luminance crosstalk.

Signal NTSCH is time expanded by a unit 62 to
10 produce an expanded side highs signal ESH with a 50 μ s active horizontal line interval, i.e., less than the standard NTSC active line interval of approximately 52 μ s. Specifically, as shown in Figure 5, the expansion is accomplished by a "mapping" process which maps left side
15 panel pixels 1-84 of signal NTSCH into pixel positions 15-377 of signal ESH, i.e., the left side highs of signal NTSCH are expanded to occupy approximately one half the line time of signal ESH. The right side panel portion (pixels 671-754) of signal NTSCH is similarly processed.
20 The time expansion process reduces the horizontal bandwidth of the information comprising signal ESH (compared to that of signal NTSCH) by a factor of 363/84. The mapping process by which time expansion is accomplished can be realized by apparatus of the type shown and to be discussed
25 in connection with Figures 12-12d. Signal ESH is intra-frame averaged by a network 64, of the type shown in Figure 11a, to produce a signal X as illustrated in Figure 5. Intraframe averaged signal X is essentially identical to signal ESH because of the high visual correlation of
30 intraframe image information of signal ESH. Signal X is applied to a signal input of a quadrature modulator 80.

Signal YF' is also filtered by a horizontal bandpass filter 70 with a passband of 5 MHz - 6.0 MHz. The output signal from filter 70, horizontal luminance highs,
35 is applied to an amplitude modulator 72 where it amplitude modulates a 5 MHz carrier signal f_c . Modulator 72 includes an output low pass filter with a cut-off frequency of approximately 1.0 MHz to obtain a signal with a 0-1.0 MHz

passband at the output of modulator 72. The upper (aliased) sideband (5.0 - 6.0 MHz) produced by the modulation process is removed by the 1.0 MHz lowpass filter. Effectively, horizontal luminance highs

5 frequencies in the range 5.0 MHz - 6.0 MHz have been shifted to the range 0-1.0 MHz as a result of the amplitude modulation process and subsequent low pass filtering. The carrier amplitude should be large enough so that the original signal amplitudes are retained after filtering by

10 the 1.0 MHz low pass filter. That is, a frequency shift without affecting amplitude is produced.

The frequency-shifted horizontal luminance highs signal from unit 72 is encoded (time compressed) by means of a format encoder 74. That is, encoder 74 encodes the

15 frequency shifted horizontal luminance highs so that this signal exhibits an active line interval of 50 μ s, less than the standard NTSC active line interval of 52.6 μ s, by using techniques that will be discussed in connection with Figures 6-8. When the input signal to encoder 74 is time

20 compressed by encoder 74, its bandwidth increases from approximately 1.0 MHz to 1.1 MHz at the output of encoder 74. The signal from encoder 74 is intraframe averaged by means of apparatus 76 similar to that illustrated in Figure 11a, before being applied to unit 80 as signal Z.

25 Intraframe averaged signal Z is essentially identical to the signal from encoder 74 because of the high visual correlation of intraframe image information of the signal from encoder 74. Modulating signal X, a composite signal containing luminance and chrominance information, and

30 modulating signal Z exhibit substantially the same bandwidth, approximately 0-1.1 MHz.

As will be discussed in connection with Figure 24, unit 80 performs nonlinear gamma function amplitude compression on large amplitude excursions of the two

35 auxiliary signals, X and Z, before these signals quadrature modulate an alternate subcarrier signal ASC. A gamma of 0.7 is used, whereby the absolute value of each sample is raised to the 0.7 power and multiplied by the sign of the

original sample value. Gamma compression reduces the visibility of potentially interfering large amplitude excursions of the modulated signals on existing receivers, and allows predictable recovery at the widescreen receiver since the inverse of the gamma function employed at the encoder is predictable and can be readily implemented at the receiver decoder.

The amplitude compressed signals are then quadrature modulated on a 3.1075 MHz phase-controlled alternate subcarrier ASC, which is an odd multiple of one half the horizontal line frequency ($395 \times H/2$). The phase of the alternate subcarrier is caused to alternate 180° from one field to the next, unlike the phase of the chrominance subcarrier. The field alternating phase of the alternate subcarrier permits the auxiliary modulating information of signals X and Z to overlap chrominance information and produces complementary phased auxiliary information components A1, -A1 and A3, -A3 of the modulated auxiliary signal, which facilitates the separation of the auxiliary information using a relatively uncomplicated field storage device at the receiver. The quadrature modulated signal, M, is added to signal N in adder 40. The resulting signal, NTSCF, is a 4.2 MHz NTSC compatible signal.

The described non-linear gamma function employed in the encoder for the purpose of large amplitude compression is a constituent part of a non-linear companding (compression-expansion) system which also includes a complementary gamma function in the decoder of a widescreen receiver for the purpose of amplitude expansion, as will be discussed subsequently. The disclosed non-linear companding system has been found to significantly reduce the impact of auxiliary non-standard information upon the standard information, without causing visible degradation of an image due to noise effects. The companding system uses a non-linear gamma function to instantaneously compress large amplitude excursions of auxiliary, non-standard widescreen high frequency

information at the encoder, with a complementary non-linear gamma function being used to correspondingly expand such high frequency information at the decoder. The result is a reduction in the amount of interference with existing standard video information caused by large amplitude auxiliary high frequency information, in the disclosed compatible widescreen system wherein non-standard auxiliary widescreen information is split into low and high frequency portions subjected to companding. At the decoder, non-linear amplitude expansion of the compressed high frequency information does not result in excessive perceived noise since large amplitude high frequency information is typically associated with high contrast image edges, and the human eye is insensitive to noise at such edges. The described companding process also advantageously reduces cross-modulation products between the alternate and chrominance subcarriers, with associated reduction in visible beat products.

Luminance detail signal YT exhibits a bandwidth of 7.16 MHz and is encoded into the 4:3 format by means of a format encoder 78 (e.g., in the manner shown in Figure 6), and is horizontally lowpass filtered to 750 KHz by a filter 79 to produce a signal YTN. The side portions are lowpass filtered to 125 KHz before time compression by means of an input lowpass filter of format encoder 78, corresponding to input filter 610 of the apparatus shown in Figure 6 but with a cut-off frequency of 125 KHz. The side portion highs are discarded. Thus signal YTN is spatially correlated with main signal C/SL.

Signals YTN and NTSCF are converted from digital (binary) to analog form by means of DAC units 53 and 54 respectively, before these signals are applied to an RF quadrature modulator 57 for modulating a TV RF carrier signal. The RF modulated signal is afterwards applied to a transmitter 55 for broadcast via an antenna 56.

Alternate subcarrier ASC associated with modulator 80 is horizontally synchronized and has a frequency chosen to insure adequate separation (e.g., 20-30

db.) of side and center information, and to have insignificant impact upon an image displayed by a standard NTSC receiver. The ASC frequency preferably should be an interlace frequency at an odd multiple of one half the horizontal line rate so as not to produce interference which would compromise the quality of a displayed picture.

Quadrature modulation such as provided by unit 80 advantageously permits two narrowband signals to be transmitted simultaneously. Time expanding the modulating highs signals results in a bandwidth reduction, consistent with the narrowband requirements of quadrature modulation. The more the bandwidth is reduced, the less likely it is that interference between the carrier and modulating signals will result. Furthermore, the typically high energy DC component of the side panel information is compressed into the overscan region rather than being used as a modulating signal. Thus the energy of the modulating signal, and therefore the potential interference of the modulating signal, are greatly reduced.

The encoded NTSC compatible widescreen signal broadcast by antenna 56 is intended to be received by both NTSC receivers and widescreen receivers, as illustrated by Figure 13.

In Figure 13, a broadcast compatible widescreen EDTV interlaced television signal is received by an antenna 1310 and applied to an antenna input of an NTSC receiver 1312. Receiver 1312 processes the compatible widescreen signal in normal fashion to produce an image display with a 4:3 aspect ratio, with the widescreen side panel information being in part compressed (i.e., "lows") into the horizontal overscan regions out of sight of the viewer, and being in part (i.e., "highs") contained in the modulated alternate subcarrier signal which does not disrupt the standard receiver operation.

The compatible widescreen EDTV signal received by antenna 1310 is also applied to a widescreen progressive scan receiver 1320 capable of displaying a video image with a wide aspect ratio of, e.g., 5:3. The received widescreen

signal is processed by an input unit 1322 including radio frequency (RF) tuner and amplifier circuits, a synchronous video demodulator (a quadrature demodulator) which produces a baseband video signal, and analog-to-digital (ADC)

5 converter circuits for producing a baseband video signal (NTSCF) in binary form. The ADC circuits operate at a sampling rate of four times the chrominance subcarrier frequency ($4 \times f_{sc}$).

Signal NTSCF is applied to an intraframe
10 processor 1324 which processes image lines 262H apart within frames, above 1.7 MHz, to recover main signal N and quadrature modulated auxiliary signal M substantially free from V-T crosstalk. A 200 KHz horizontal crosstalk guardband is provided between the 1.7 MHz lower limit
15 operating frequency of unit 1324 and the 1.5 MHz lower limit operating frequency of unit 38 in the encoder of FIGURE 1a. Recovered signal N contains information which is essentially visually identical to image information of main signal C/SL, due to the high visual intraframe image
20 correlation of original main signal C/SL as intraframe averaged in the encoder of Figure 1a.

Signal M is coupled to a quadrature demodulator and amplitude expander unit 1326 for demodulating auxiliary
25 signals X and Z in response to an alternate subcarrier ASC with a field alternating phase, similar to signal ASC discussed in connection with FIGURE 1a. Demodulated signals X and Z contain information which is essentially visually identical to image information of signal ESH and
30 of the output signal from unit 74 in Figure 1a, due to the high visual intraframe image correlation of these signals as intraframe averaged by the encoder of Figure 1a. Unit 1326 also includes a 1.5 MHz lowpass filter to remove unwanted high frequency demodulation products at twice the alternate subcarrier frequency, and an amplitude expander
35 for expanding the (previously compressed) demodulated signals using an inverse-gamma function ($\gamma = 1/0.7 = 1.429$), i.e., the inverse of the non-linear compression function performed by unit 80 in FIGURE 1a.

A unit 1328 time compresses the color encoded side panel highs so that they occupy their original time slots, thereby recovering signal NTSCH. Unit 1328 time compresses signal NTSCH by the same amount that unit 62 of
5 FIGURE 1a time expanded signal NTSCH.

A luminance (Y) highs decoder 1330 decodes luminance horizontal highs signal Z into widescreen format by time expanding this signal by the same amount as time compression of the corresponding component in the encoder
10 of FIGURE 1a, as indicated by Figure 17, using mapping techniques described herein.

Modulator 1332 amplitude modulates the signal from decoder 1330 on a 5.0 MHz carrier f_c . The amplitude modulated signal is afterwards high pass filtered by a
15 filter 1334 with a 5.0 Mhz cut-off frequency to remove the lower sideband. In the output signal from filter 1334, center panel frequencies of 5.0 to 6.0 MHz are recovered, and side panel frequencies of 5.0 to 6.0 MHz are recovered. The signal from filter 1334 is applied to an adder 1336.

20 Signal NTSCH from compressor 1328 is applied to a unit 1340 for separating the luminance highs from the chrominance highs to produce signals YH, IH and QH. This can be accomplished by the arrangement of FIGURE 18.

Signal N from unit 1324 is separated into its
25 constituent luminance and chrominance components YN, IN and QN by means of a luminance-chrominance separator 1342 which can be similar to separator 1340 and which can employ apparatus of the type shown in FIGURE 18.

Signals YH, IH, QH and YN, IN, QN are provided as
30 inputs to a Y-I-Q format decoder 1344, which decodes the luminance and chrominance components into widescreen format. The side panel lows are time expanded, the center panel is time compressed, the side panel highs are added to the side panel lows, and the side panels are spliced to the
35 center panel in the 10-pixel overlap region using the principles of FIGURE 14. Details of decoder 1344 are shown in FIGURE 19.

Signal YF' is coupled to adder 1336 where it is summed with the signal from filter 1334. By this process recovered extended high frequency horizontal luminance detail information is added to decoded luminance signal YF'.

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Signals YF', IF' and QF' are converted from interlaced to progressive scan format by means of converters 1350, 1352 and 1354, respectively. Luminance progressive scan converter 1350 also responds to "helper" luminance signal YT from a format decoder 1360, which decodes encoded "helper" signal YTN. Decoder 1360 decodes signal YTN into widescreen format, and exhibits a configuration similar to that of FIGURE 17.

I and Q converters 1352 and 1354 convert interlace to progressive scan signals by temporally averaging lines one frame apart to produce the missing progressive scan line information. This can be accomplished by apparatus of the type shown in FIGURE 20.

Luminance progressive scan converter unit 1350 is similar to that shown in FIGURE 20, except that signal YT is added as shown by the arrangement of FIGURE 21. In this unit a "helper" signal sample, YT, is added to a temporal average to assist reconstructing a missing progressive scan pixel sample. Full temporal detail is recovered within the band of horizontal frequencies contained in the encoded line difference signal (750 KHz, after encoding). Above this band of horizontal frequencies signal YT is zero, so the missing sample is reconstructed by temporal averaging.

Widescreen progressive scan signals YF, IF and QF are converted to analog form by means of a digital-to-analog converter 1362 before being applied to a video signal processor and matrix amplifier unit 1364. The video signal processor component of unit 1364 includes signal amplifying, DC level shifting, peaking, brightness control, contrast control and other conventional video signal processing circuits. Matrix amplifier 1364 combines luminance signal YF with color difference signals IF and QF to produce color image representative video signals R, G

and B. These color signals are amplified by display driver amplifiers in unit 1364 to a level suitable for directly driving a widescreen color image display device 1370, e.g. a widescreen kinescope.

5 Figure 6 illustrates apparatus included in
processor 18 of Figure 1a for developing signals YE, YO,
and YH from wideband widescreen signal YF. Signal YF" is
horizontally low pass filtered by an input filter 610 with
10 a cutoff frequency of 700 KHz to produce low frequency
luminance signal YL, which is applied to one input of a
subtractive combiner 612. Signal YF" is applied to another
input of combiner 612 and to time de-multiplexing apparatus
616 after being delayed by a unit 614 to compensate for the
15 signal processing delay of filter 610. Combining delayed
signal YF" and filtered signal YL produces high frequency
luminance signal YH at the output of combiner 612.

 Delayed signal YF" and signals YH and YL are
applied to separate inputs of de-multiplexing apparatus
616, which includes de-multiplexing (DEMUX) units 618, 620
20 and 621 for respectively processing signals YF", YH and YL.
The details of de-multiplexing apparatus 616 will be
discussed in connection with Figure 8. De-multiplexing
units 618, 620 and 621 respectively derive full bandwidth
center panel signal YC, side panel highs signal YH and side
25 panel lows signal YL' as illustrated in Figures 3 and 4.

 Signal YC is time expanded by a time expander 622
to produce signal YE. Signal YC is time expanded with a
center expansion factor sufficient to leave room for the
left and right horizontal overscan regions. The center
30 expansion factor (1.19) is the ratio of the intended width
of signal YE (pixels 15-740) to the width of signal YC
(pixels 75-680) as shown in Figure 3.

 Signal YL' is compressed with a side compression
factor by a time compressor 628 to produce signal YO. The
35 side compression factor (6.0) is the ratio of the width of
the corresponding portion of signal YL' (e.g. left pixels
1-84) to the intended width of signal YO (e.g. left pixels
1-14) as shown in Figure 3. Time expanders 622, 624 and

626 and time compressor 628 can be of the type shown in Figure 12, as will be discussed.

5 Signals IE, IH, IO and QE, QH, QO are respectively developed from signals IF" and QF" in a manner similar to that by which signals YE, YH and YO are developed by the apparatus of Figure 6. In this regard reference is made to Figure 7, which illustrates apparatus for developing signals IE, IH and IO from signal IF". Signals QE, QH and QO are developed from signal QF" in a
10 similar manner.

In Figure 7, wideband widescreen signal IF", after being delayed by a unit 714, is coupled to de-multiplexing apparatus 716 and is also subtractively combined with low frequency signal IL from a low pass
15 filter 710 in a subtractive combiner 712 to produce high frequency signal IH. Delayed signal IF" and signals IH and IL are respectively de-multiplexed by de-multiplexers 718, 720 and 721 associated with de-multiplexing apparatus 716 to produce signals IC, IH and IL'. Signal IC is time
20 expanded by an expander 722 to produce signal IE, and signal IL' is time compressed by a compressor 728 to produce signal IO. Signal IC is expanded with a center expansion factor similar to that employed for signal YC as discussed, and signal IL' is compressed with a side
25 compression factor similar to that employed for signal YL', also as discussed.

Figure 8 illustrates a de-multiplexing apparatus 816 such as can be used for apparatus 616 of Figure 6 and 716 of Figure 7. The apparatus of Figure 8 is illustrated
30 in the context of de-multiplexer 616 of Figure 6. Input signal YF" contains 754 pixels defining the image information. Pixels 1-84 define the left panel, pixels 671-754 define the right panel, and pixels 75-680 define the center panel which overlaps the left and right panels
35 slightly. Signals IF" and QF" exhibit similar overlap. As will be discussed, such panel overlap has been found to facilitate combining (splicing) the center and side panels

at the receiver to substantially eliminate boundary artifacts.

De-multiplexing apparatus 816 includes first, second and third de-multiplexer (DEMUX) units 810, 812 and 814 respectively associated with left, center and right panel information. Each de-multiplexer unit has an input "A" to which signals YH, YF" and YL are respectively applied, and an input "B" to which a blanking signal (BLK) is applied. The blanking signal may be a logic 0 level or ground, for example. Unit 810 extracts output signal YH, containing the left and right highs, from input signal YH as long as a signal select input (SEL) of unit 810 receives a first control signal from a count comparator 817 indicating the presence of left panel pixel elements 1-84 and right panel pixel elements 671-754. At other times, a second control signal from count comparator 817 causes the BLK signal at input B rather than signal YH at input A to be coupled to the output of unit 810. Unit 814 and a count comparator 820 operate in a similar fashion for deriving side panel lows signal YL' from signal YL. Unit 812 couples signal YF" from its input A to its output to produce center panel signal YC only when a control signal from a count comparator 818 indicates the presence of center panel pixels 75-680.

Count comparators 817, 818 and 820 are synchronized to video signal YF" by means of a pulse output signal from a counter 822 which responds to a clock signal at four times the chrominance subcarrier frequency ($4 \times f_{cc}$), and to a horizontal line synchronizing signal H derived from video signal YF". Each output pulse from counter 822 corresponds to a pixel position along a horizontal line. Counter 822 exhibits an initial offset of a -100 count corresponding to the 100 pixels from the beginning of the negative going horizontal sync pulse at time T_{HS} to the end of the horizontal blanking interval, at which time pixel 1 appears at the onset of the horizontal line display interval. Thus counter 822 exhibits a count of "1" at the onset of the line display interval. Other

counter arrangements can also be developed. The principles employed by de-multiplexing apparatus 816 can also be applied to multiplexing apparatus for performing a converse signal combining operation, such as is performed by
5 side-center panel combiner 28 in Figure 1a.

Figure 9 shows details of modulator 30 in encoders 31 and 60 of Figure 1a. In Figure 9, signals IN and QN appear at a four times chrominance subcarrier rate ($4 \times \text{fsc}$) and are applied to signal inputs of latches 910 and 912, respectively. Latches 910 and 912 also receive
10 $4 \times \text{fsc}$ clock signals to transfer in signals IN and QN, and a $2 \times \text{fsc}$ switching signal which is applied to an inverting switching signal input of latch 910 and to a noninverting switching signal input of latch 912. Signal outputs of
15 latches 910 and 912 are combined into a single output line at which signals I and Q appear alternately and are applied to signal inputs of a noninverting latch 914 and an inverting latch 916. These latches are clocked at a
20 $4 \times \text{fsc}$ rate and receive a switching signal, at the chrominance subcarrier frequency fsc , at inverting and noninverting inputs respectively. Noninverting latch 914 produces an output alternating sequence of positive polarity signals I and Q, and inverting latch 916 produces
25 an output alternating sequence of negative polarity I and Q signals, i.e. $-I$, $-Q$. The outputs of latches 914 and 916 are combined in a single output line on which appears an alternating sequence of paired I and Q signals of mutually opposite polarity pairs, i.e., I, Q, $-I$, $-Q$... etc., constituting signal CN. This signal is filtered by filter
30 32 before being combined in unit 36 with a filtered version of luminance signal YN to produce NTSC encoded signal C/SL of the form $Y+I$, $Y+Q$, $Y-I$, $Y-Q$, $Y+I$, $Y+Q$... and so on.

Figure 10 illustrates a vertical-temporal (V-T) filter which can exhibit V-T bandpass, V-T bandstop or V-T
35 low pass configurations by adjusting weighting coefficients a_1 - a_9 . The table of Figure 10a illustrates the weighting coefficients associated with V-T bandpass and bandstop filter configurations which are employed in the disclosed

system. An H-V-T bandstop filter such as filter 34 of Figure 1a, and H-V-T bandpass filters such as are included in the decoder system of Figure 13, respectively comprise the combination of a horizontal lowpass filter 1020 and a V-T bandstop filter 1021 as shown in FIGURE 10b, and the combination of a horizontal bandpass filter 1030 and a V-T bandpass filter 1031 as shown in Figure 10c.

In the H-V-T bandstop filter of Figure 10b, horizontal lowpass filter 1020 exhibits a given cut-off frequency and provides a filtered low frequency signal component. This signal is subtractively combined in a combiner 1023 with a delayed version of the input signal from a delay unit 1022 to produce a high frequency signal component. The low frequency component is subjected to a one frame delay by means of a network 1024 before being applied to a n additive combiner 1025 for providing an H-V-T bandstop filtered output signal. V-T filter 1021 exhibits the V-T bandstop filter coefficients shown in Figure 10a. An H-V-T bandpass filter such as included in the decoder of Figure 13 is shown in Figure 10c as comprising a horizontal bandpass filter 1030 having a given cut-off frequency, cascaded with a V-T bandpass filter 1031 having V-T bandpass filter coefficients as indicated by the table of Figure 10a.

The filter of Figure 10 includes a plurality of cascaded memory units (M) 1010a - 1010h for providing successive signal delays at respective taps t1-t9, and for providing an overall filter delay. Signals conveyed by the taps are respectively applied to one input of multipliers 1012a - 1012i. Another input of each of the multipliers respectively receives a prescribed weighting a1 - a9 depending on the nature of the filtering process to be performed. The nature of the filtering process also dictates the delays imparted by memory units 1010a - 1010h. Horizontal dimension filters employ pixel storage memory elements such that the overall filter delay is less than the time interval of one horizontal image line (1H). Vertical dimension filters employ line storage memory

elements exclusively, and temporal dimension filters employ frame storage memory elements exclusively. Thus an H-V-T 3-D filter comprises a combination of pixel, (<1H), line (1H) and frame (>1H) storage elements, while a V-T filter
5 comprises only the latter two types of memory elements. Weighted tapped (mutually delayed) signals from elements 1012a - 1012i are combined in an adder 1015 to produce a filtered output signal.

Such filters are non-recursive, finite impulse
10 response (FIR) filters. The nature of the delay provided by the memory elements depends on the type of signal being filtered and the amount of crosstalk that can be tolerated between the luminance, chrominance and side panel highs signals in this example. The sharpness of the filter
15 cutoff characteristics is enhanced by increasing the number of cascaded memory elements.

Figure 10d illustrates one of the separate filters of network 16 in Figure 1a, including cascaded memory (delay) units 1040a-1040d, associated multipliers
20 1042a-1042e with designated respective weighting factors a1-a5 for receiving signals from signal taps t1-t5, and a signal combiner 1045 which sums the weighted output signals from multipliers a1-a5 to produce an output signal.

Figure 11a depicts an intraframe averager
25 suitable for use as intraframe averagers 64 and 76 of Figure 1a. An input composite video signal is applied to a delay network including 262H delay elements 1110 and 1112, and also to one input of a multiplexer (MUX) 1115 which is switched at the field rate in response to a 30Hz switching
30 signal. The 30Hz MUX switching signal is vertically synchronized in response to vertical interval sync pulses associated with the input composite video signal. Another input of MUX 1115 receives a signal from an output of delay element 1112. A combiner 1118 additively combines output
35 signals from MUX 1115 and from a center tap point between delay elements 1110 and 1112, after these signals have been weighted by an averaging factor of $\frac{1}{2}$. This weighting factor can be provided by means of suitable matrixing

networks within combiner 1118, or by means of signal multipliers situated in the input signal paths of combiner 1118, respectively.

5 Signals "Y1+C1" and "Y2+C2" are composite color video signals 262H apart in successive first and second image fields, and signal "M1" is an intraframe averaged output signal, as shown in Figure 1d for example. During a first field, multiplexer 1115 is in input position "1" and conveys signal Y2+C2 to combiner 1118, where it is summed
10 with center tap signal Y1+C1 to produce average output signal M1. In the next field the center tap between delay elements 1110 and 1112 contains signal value Y2+C2 and MUX 1115 is in position "2" for selecting the signal path from the output of delay element 1112, which contains signal
15 value Y1+C1, thereby yielding the same average value of signal M1 from the output of combiner 1118. The described apparatus produces identical pixel pairs 262H apart, and is not limited to using an averaging process. Any weighting values may be used to produce a desired weighted
20 combination of pixel pairs, and delays other than 262H may be used (along with an associated change in the MUX switching rate), depending on the requirements of a particular system.

Figure 11b illustrates a frequency selective
25 intraframe averager suitable for use as intraframe averager 38 of Figure 1a. Figure 11b includes the arrangement of Figure 11a, except that subtractive rather than additive signal combination is associated with a combiner 1128, and in that a filter 1130, gate 1132 and combiner 1134 are
30 included. Briefly, the output of combiner 1128 is representative of a difference between image fields, rather than an average as in the arrangement of Figure 11a. This difference is essentially a cancellation term which is added back to signal Y1+C1 via combiner 1134 for cancelling
35 differences between successive image fields to insure that the contents of successive image fields are identically averaged. Filter 1130 filters the cancellation term from the output of combiner 1128 to restrict the averaging

process to a desired frequency range. Gate 1132 is controlled to determine when the averaging process occurs over an image interval, in this case over the center panel region exclusive of the time compressed side panel regions.

5 More specifically, the input signal paths to combiner 1128 exhibit signal weighting coefficients of $+\frac{1}{2}$ and $-\frac{1}{2}$ as shown so that the output signal from combiner 1128 corresponds to the difference in the information content of the input signals to combiner 1128, which are
10 262H apart in time in adjacent fields. The complementary weighting coefficients can be provided by employing signal multipliers in the respective input paths of combiner 1128, or by arranging combiner 1128 as a differencing amplifier. The output signal from combiner 1128 is filtered by 1.5 MHz
15 horizontal high pass filter 1130 before being applied to an electronic transmission gate 1132. Gate 1132 responds to a switching control signal for passing the high frequency signal from the output of filter 1130 only during the center portion of the main signal (component 1). At this
20 time gate 1132 is open (conductive). Gate 1132 is closed (nonconductive) during the time compressed side panel portions of the main signal, e.g., during the illustrated positive pulse intervals of the control signal. The output signal from gate 1132 is summed in a combiner 1134 with the
25 composite video signal appearing at the center tap point between delay elements 1120 and 1122. The gate control signal is vertically synchronized in response to vertical interval sync pulses associated with the input composite video signal. The gate control signal is also horizontally
30 synchronized. Horizontal sync can be achieved by means responsive to the horizontal line synchronizing pulse component of the input composite video signal and including a pixel counter to determine the timing of the positive pulse components of the gate control signal following each
35 horizontal line synchronizing pulse. A predetermined time interval can readily be established between a horizontal line sync pulse and the first image pixel.

Again referring to Figure 1d together with Figure 11b, when MUX 1125 is in position 1 as shown and gate 1132 is closed, only composite video signal $Y1+C1$ from the center tap between elements 1120 and 1122 appears at the output of combiner 1134. Thus at this time, the output signal of combiner 1134 is unaltered compressed side panel information of composite video signal $Y1+C1$ associated with field 1. The output signal of combiner 1134 is unaltered compressed side panel information of composite video signal $Y2+C2$ associated with a successive image field 2 when MUX 1125 occupies position 2.

When MUX 1125 is in position 1 for field 1 and gate 1132 is closed during the center panel interval between side panel intervals, the output signal from combiner 1134 contains signal components $Y1+C1$ and $M1$. Component $Y1+C1$ contains unaltered, i.e., not intraframe averaged, center panel information at and below approximately 1.5 MHz. Component $M1$ contains intraframe averaged center panel information above approximately 1.5 MHz. When MUX 1125 is in position 2 during succeeding field 2 and gate 1132 is closed during the center panel interval, the output signal from combiner 1134 contains intraframe averaged component $M1$ as discussed above, and component $Y2+C2$. The latter component contains unaltered (nonintraframe averaged) center panel information at and below approximately 1.5 MHz.

Figure 12 illustrates raster mapping apparatus which can be used for the time expanders and compressors of Figures 6 and 7. In this regard, reference is made to the waveforms of Figure 12a which illustrates the mapping process. Figure 12a shows an input signal waveform S with a center portion between pixels 84 and 670 which is intended to be mapped into pixel locations 1-754 of an output waveform W by means of a time expansion process. End point pixels 1 and 670 of waveform S map directly into end point pixels 1 and 754 of waveform W . Intermediate pixels do not map directly on a 1:1 basis due to the time expansion, and in many cases do not map on an integer

basis. The latter case is illustrated when, for example, pixel location 85.33 of input waveform S corresponds to integer pixel location 3 of output waveform W. Thus pixel location 85.33 of signal S contains an integer part (85) and a fractional part DX (.33), and pixel location 3 of waveform W contains an integer part (3) and a fractional part (0).

In Figure 12, a pixel counter operating at a $4 \times$ fsc rate provides an output WRITE ADDRESS signal M representative of pixel locations (1...754) on an output raster. Signal M is applied to PROM (Programmable Read Only Memory) 1212 which includes a look-up table containing programmed values depending upon the nature of raster mapping to be performed, eg., compression or expansion. In response to signal M, PROM 1212 provides an output READ ADDRESS signal N representing an integer number, and an output signal DX representing a fractional number equal to or greater than zero but less than unity. In the case of a 6-bit signal DX ($2^6=64$), signal DX exhibits fractional parts 0, 1/64, 2/64, 3/64...63/64.

PROM 1212 permits expansion or compression of a video input signal S as a function of stored values of signal N. Thus a programmed value of READ ADDRESS signal N and a programmed value of fractional part signal DX are provided in response to integer values of pixel location signal M. To achieve signal expansion, for example, PROM 1212 is arranged to produce signal N at a rate slower than that of signal M. Conversely, to achieve signal compression, PROM 1212 provides signal N at a rate greater than that of signal M.

Video input signal S is delayed by cascaded pixel delay elements 1214a, 1214b and 1214c to produce video signals S(N+2), S(N+1) and S(N) which are mutually delayed versions of the video input signal. These signals are applied to video signal inputs of respective dual port memories 1216a - 1216d, as are known. Signal M is applied to a write address input of each of memories 1216a - 1216d, and signal N is applied to a read address input of each of

memories 1216a - 1216d. Signal M determines where incoming video signal information will be written into the memories, and signal N determines which values will be read out of the memories. The memories can write into one address
5 while simultaneously reading out of another address. Output signals S(N-1), S(N), S(N+1) and S(N+2) from memories 1216a-1216d exhibit a time expanded or time compressed format depending upon the read/write operation of memories 1216a - 1216d, which is a function of how PROM
10 1212 is programmed.

Signals S(N-1), S(N), S(N+1) and S(N+2) from memories 1216a - 1216d are processed by a four-point linear interpolator including peaking filters 1220 and 1222, a PROM 1225 and a two point linear interpolator 1230, details
15 of which are shown in Figures 12b and 12c. Peaking filters 1220 and 1222 receive three signals from the group of signals including signals S(N-1), S(N), S(N+1) and S(N+2), as shown, as well as receiving a peaking signal PX. The value of peaking signal PX varies from zero to unity as a
20 function of the value of signal DX, as shown in Figure 12d, and is provided by PROM 1225 in response to signal DX. PROM 1225 includes a look-up table and is programmed to produce a given value of PX in response to a give value of DX.

25 Peaking filters 1220 and 1222 respectively provide peaked mutually delayed video signals S'(N) and S'(N+1) to two-point linear interpolator 1230 which also receives signal DX. Interpolator 1230 provides a (compressed or expanded) video output signal , where output
30 signal W is defined by the expression

$$W = S'(N) + DX [S'(N+1) - S'(N)]$$

The described four-point interpolator and peaking function advantageously approximates a $(\sin X)/X$ interpolation function with good resolution of high frequency detail.

35 Figure 12b shows details of peaking filters 1220 and 1222, and interpolator 1230. In Figure 12b, signals S(N-1), S(N) and S(N+1) are applied to a weighting circuit 1240 in peaking filter 1220 where these signals are

respectively weighted by peaking coefficients $-1/4$, $1/2$ and $-1/4$. As shown in Figure 12c, weighting circuit 1240 comprises multipliers 1241a - 1241c for respectively multiplying signals $S(N-1)$, $S(N)$ and $S(N+1)$ with peaking
5 coefficients $-1/4$, $1/2$ and $-1/4$. Output signals from multipliers 1241a - 1241c are summed in an adder 1242 to produce a peaked signal $P(N)$, which is multiplied by signal PX in multiplier 1243 to produce a peaked signal which is summed with signal $S(N)$ in adder 1244 to produce peaked
10 signal $S'(N)$. Peaking filter 1222 exhibits similar structure and operation.

In two point interpolator 1230, signal $S'(N)$ is subtracted from signal $S'(N+1)$ in a subtractor 1232 to produce a difference signal which is multiplied by signal
15 DX in a multiplier 1234. The output signal from multiplier 1234 is summed with signal $S'(N)$ in an adder 1236 to produce output signal W .

Figure 15 shows details of intraframe processor 1324 of Figure 13. The decoder apparatus of Figure 15 is
20 basically similar to the encoder apparatus of Figure 11b.

An input composite video signal to processor 1324 in Figure 15 includes, in a first field, signal components " $Y1+C1$ " and " $M1+A1$ ". In a succeeding second field the input signal includes components " $Y2+C2$ " and " $M1-A1$ ".
25 Components $Y1+C1$, $M1$ and $Y2+C2$, $M1$ are components provided by intraframe processor 38 as discussed in detail in connection with Figure 11b. Components $+A1$ and $-A1$ represent the alterate subcarrier signal modulated with component 2 and component 3 intraframe averaged information
30 from units 64 and 76, for respective succeeding fields. In this regard reference is made to Figures 1, 1a and Figure 1d in particular.

The intraframe processor of Figure 15 operates in essentially the same manner as the arrangement of Figure
35 11b discussed previously. With MUX 1525 in position 1, field difference component is obtained at the output of combiner 1528. After filtering by high pass filter 1530 and gating by unit 1532, the result is a component $-A1$

which, when combined with signal $Y1+C1$, $M1+A1$ in combiner 1534, cancels the modulated auxiliary subcarrier component ($+A1$) to produce a recovered main signal $Y1+C1$, $M1$.

Component $Y1+C1$ of the recovered main signal is unaltered
5 below the 1.7 MHz cut-off frequency of high pass filter 1530, and component $M1$ represents intraframe averaged center panel information above approximately 1.7 MHz. The field difference cancellation term ($-A1$), after inversion by a unity gain amplifier 1535, is the recovered modulated
10 auxiliary signal $A1$.

Recovered main signal $Y1+C1$, $M1$ corresponds to signal N in Figure 13, and is further processed by network 1342 as discussed. Recovered auxiliary signal $A1$ corresponds to signal M in Figure 13 and is demodulated by
15 network 1326.

Figure 16 illustrates the operation of network 1324, as shown in Figure 15, for the next succeeding image field. In this case signal $Y2+C2$, $M1-A1$ is developed between delay elements 1520 and 1522, and MUX 1528 occupies
20 position 2 for receiving signal $Y1+C1$, $M1+A1$. A recovered main signal $Y2+C2$, $M1$ is produced at the output of combiner 1534, and an oppositely phased modulated auxiliary signal $-A1$ is recovered.

In FIGURE 18 an H-V-T bandpass filter 1810, which
25 has the configuration of FIGURE 10c and a passband of 3.58 ± 0.5 MHz, passes signal NTSCH to a subtractive combiner 1814, which also receives signal NTSCH after being passed through a transit time equalizing delay 1812. Separated luminance highs signal YH appears at the output of combiner
30 1814. The filtered NTSCH signal from filter 1810 is quadrature demodulated by a demodulator 1816 in response to chrominance subcarrier signal SC for producing chrominance highs IH and QH.

In FIGURE 19, signals YN, IN and QN are separated
35 into compressed side panel lows YO, IO, QO and into expanded center panel signals YE, IE, QE by means of a side-center panel signal separator (time de-multiplexer)

1940. Demultiplexer 1940 can employ the principles of demultiplexer 816 of FIGURE 8 discussed previously.

5 Signals YO, IO and QO are time expanded by a side expansion factor (corresponding to the side compression factor in the encoder of FIGURE 1a) by means of a time expander 1942 to restore the original spatial relationship of the side panel lows in the widescreen signal, as represented by restored side panel lows signals YL, IL and QL. Similarly, to make room for the side panels, center panel signals YE, IE and QE are time compressed by a center compression factor (corresponding to the center expansion factor in the encoder of FIGURE 1a) by means of a time compressor 1944 to restore the original spatial relationship of the center panel signal in the widescreen signal, as represented by restored center panel signals YC, IC and QC. Compressor 1944 and expander 1942 can be of the type shown in FIGURE 12 discussed previously.

15 Spatially restored side panel highs YH, IH and QH are combined with spatially restored side panel lows YL, IL and QL by a combiner 1946 to produce reconstructed side panel signals YS, IS and QS. These signals are spliced to reconstructed center panel signal YC, IC and QC by means of a splicer 1960 to form a fully reconstructed widescreen luminance signal YF' and fully reconstructed widescreen color difference signals IF' and QF'. Splicing of the side and center panel signal components is accomplished in a manner which virtually eliminates a visible seam at the boundary between the center and side panels, as will be seen from the subsequent discussion of splicer 1960 shown in FIGURE 14.

30 In FIGURE 20, interlace signals IF' (or QF') are delayed 263H by an element 2010 before being applied to an input of a dual port memory 2020. This delayed signal is subjected to an additional 262H delay by an element 2012 before being added with the input signal in adder 2014. The output signal from adder 2014 is coupled to a divide-by-two network 2016 before being applied to an input of a dual port memory 2018. Memories 2020 and 2018 read

data at an 8 x fsc rate and write data at a 4 x fsc rate. Outputs from memories 2018 and 2020 are applied to a multiplexer (MUX) 2022 for producing output progressive scan signals IF (QF). Also shown are waveforms

5 illustrative of the interlace input signal (two lines, with pixel samples C and X designated) and the progressive scan output signal comprising pixel samples C and X.

Figure 21 illustrates apparatus suitable for use as converter 1350 for signal YF' in Figure 13. Interlaced
10 signal YF' is delayed by elements 2110 and 2112 before being combined in an adder 2114 as shown. The delayed signal from element 2110 is applied to a dual port memory 2120. An output signal from adder 2114 is coupled to a divide-by-two network 2116, the output of which is added to
15 signal YT in an adder 2118. The output from adder 2118 is applied to a dual port memory 2122. Memories 2120 and 2122 write at a 4 x fsc rate and read at an 8 x fsc rate, and provide output signals to a multiplexer 2124 which develops progressive scan signal YF.

20 FIGURE 14 depicts side panel-center panel splicing apparatus suitable for use as splicer 1960 in FIGURE 19, for example. In FIGURE 14, the splicer is shown as comprising a network 1410 for producing full bandwidth luminance signal YF' from side panel luminance signal
25 component YS and center panel luminance signal component YC, as well as an I signal splicer 1420 and a Q signal splicer 1430 which are similar in structure and operation to network 1410. The center panel and the side panels are purposely overlapped by several pixels, e.g. ten pixels.
30 Thus the center and side panel signals have shared several redundant pixels throughout the signal encoding and transmission process prior to splicing.

In the widescreen receiver, the center and side panels are reconstructed from their respective signals, but
35 because of the time expansion, time compression and filtering performed on the panel signals, several pixels at the side and center panel boundaries are corrupted, or distorted. The overlap regions (OL) and corrupted pixels

(CP; slightly exaggerated for clarity) are indicated by the waveforms associated with signals YS and YC in FIGURE 14. If the panels had no overlap region, the corrupted pixels would be abutted against each other, and a seam would be visible. An overlap region ten pixels wide has been found to be wide enough to compensate for three to five corrupted boundary pixels.

The redundant pixels advantageously allow blending of the side and center panels in the overlap region. A multiplier 1411 multiplies side panel signal YS by a weighting function W in the overlap regions, as illustrated by the associated waveform, before signal YS is applied to a signal combiner 1415. Similarly, a multiplier 1412 multiplies center panel signal YC by a complementary weighting function (1-W) in the overlap regions, as illustrated by the associated waveform, before signal YC is applied to combiner 1415. These weighting functions exhibit a linear ramp-type characteristic over the overlap regions and contain values between 0 and 1. After weighting, the side and center panel pixels are summed by combiner 1415 so that each reconstructed pixel is a linear combination of side and center panel pixels.

The weighting functions preferably should approach unity near the innermost boundary of the overlap region, and should approach zero at the outermost boundary. This will insure that the corrupted pixels have relatively little influence on the reconstructed panel boundary. The illustrated linear ramp type weighting function satisfies this requirement. However, the weighting functions need not be linear, and a nonlinear weighting function with curvilinear or rounded end portions, i.e. in the vicinity of 1 and 0 weight points, can also be used. Such a weighting function can readily be obtained by filtering a linear ramp weighting function of the type illustrated.

Weighting functions W and 1-W can be readily generated by a network including a look-up table responsive to an input signal representative of pixel positions, and a subtractive combiner. The side-center pixel overlap

locations are known, and the look-up table is programmed accordingly to provide output values from 0 to 1, corresponding to weighting function W , in response to the input signal. The input signal can be developed in a variety of ways, such as by a counter synchronized by each horizontal line synchronizing pulse. Complementary weighting function $1-W$ can be produced by subtracting weighting function W from unity.

Figure 22 shows apparatus suitable for use as progressive scan to interlace converter 17c for signal YF in Figure 1a. Figure 22 also shows a diagram of a portion of progressive scan input signal YF with samples A, B, C and X in a vertical (V) and temporal (T) plane indicated, as also shown in Figure 2a. Progressive scan signal YF is subjected to a 525H delay via elements 2210 and 2212 for producing relatively delayed samples X and A from sample B. Samples B and A are summed in an adder 2214 before being applied to a divide-by-two network 2216. An output signal from network 2216 is subtractively combined in a network 2218 with sample X to produce signal YT. This signal is applied to one input of a switch 2220, which operates at twice the interlaced horizontal line scanning rate. Another input of switch 2220 receives delayed signal YF from the output of delay 2210. The output of switch 2220 is applied to a dual port memory 2222 which reads at a 4 x fsc rate and writes at an 8 x 4 fsc rate, for producing signals YF' and YT in interlace form at an output.

Figure 23 shows apparatus suitable for use as converters 17a and 17b in Figure 1a. In Figure 23 progressive scan signal IF (or QF) is applied to a 525H delay element 2310 before being applied to a dual port memory 2312 which reads at a 4 x fsc rate and writes at an 8 x fsc rate, for producing interlace output signal IF' (or QF'). Also shown are waveforms illustrative of the progressive scan input signal with first and second lines associated with samples C and X, and the interlace output signal (the first line with sample C stretched at a H/2

rate). Dual port memory 2312 outputs only the first line sample (C) of the input signal, in stretched form.

Figure 24 shows details of unit 80. Signals X and Z are applied to address inputs of non-linear amplitude compressors 2410 and 2412, respectively. Compressors 2410 and 2412 are programmable read-only memory (PROM) devices each including a look-up table containing programmed values corresponding to the desired non-linear gamma compression function. This function is illustrated by the instantaneous input vs. output response adjacent to unit 2412. Compressed signals X and Z from data outputs of units 2410 and 2412 are applied to signal inputs of signal multipliers 2414 and 2416, respectively. Reference inputs of multipliers 2414 and 2416 receive respective alternate subcarrier signals ASC in mutually quadrature phase relationships, i.e., signals ASC are in sine and cosine form. Output signals from multipliers 2414 and 2416 are added in a combiner 2420 to produce quadrature modulated signal M. In the decoder arrangement of Figure 13, compressed signals X and Z are recovered via a conventional quadrature demodulation technique, and complementary non-linear amplitude expansion of these signals is performed by associated PROMs with look-up tables programmed with values complementary to the values of PROMs 2410 and 2412.

THE CLAIMS DEFINING THE INVENTION ARE AS FOLLOWS:

1. Video signal processing apparatus comprising:

signal delay means having an input for receiving a video signal, an output, and a point intermediate said input and output;

means for selectively coupling (a) signals from said input and said intermediate point to a signal path during a first interval, and (b) signals from said intermediate point and said output to said signal path during a second interval; and

first means for combining signals from said intermediate point and said signal path; wherein

said delay means receives a television-type signal representative of a widescreen image having side portion image information and dominant nonside portion image information, and an image aspect ratio greater than that of a standard television image;

said television-type signal contains a first component having side portion image information time compressed into an overscan region of said first component, and having dominant nonside portion information, said first component having been intraframe processed over a high frequency portion of said nonside portion information exclusive of said compressed side portion information; and

said signal path includes a high pass filter and a gate for enabling said signal path to conduct a signal from said selective coupling means substantially only during intervals associated with said dominant nonside portion information.

2. Apparatus according to Claim 1, wherein:

during said first interval said selective coupling means provides a difference signal to said signal path representative of a difference in information content between signals at said delay means input and at said intermediate point;

during said second interval said selective coupling means provides a difference signal to said signal path representative of a

difference in information content between signals at said delay means output and at said intermediate point;

said signal delay means exhibits an interval of $262H$ between said input and said intermediate point and between said intermediate point and said output, where H is a horizontal line interval; and

said first and second intervals are image field intervals.

3. Apparatus according to Claim 2, wherein:

said television-type signal contains a second component comprising intraframe processed auxiliary information modulating an alternate subcarrier, other than a chrominance subcarrier, having a phase which reverses from one image field to the next.

4. Apparatus according to Claim 3, and further comprising:

signal inverting means having an input coupled to said signal path subsequent to said high pass filter and gate, and an output for providing said second component.

5. Apparatus according to Claim 1, wherein:

during said first interval said selective coupling means provides a difference signal to said signal path representative of a difference in information content between signals at said delay means input and at said intermediate point; and

during said second interval said selective coupling means provides a difference signal to said signal path representative of a difference in information content between signals at said delay means output and at said intermediate point.

6. Apparatus according to Claim 1, wherein:

said signal delay means exhibits an interval of $262H$ between said input and said intermediate point and between said intermediate point and said output, where H is a horizontal line interval; and

said first and second intervals are image field intervals.



7. Apparatus according to Claim 6, wherein:
a combined signal provided by said first combining means contains intraframe processed high frequency information exclusive of low frequency information.

8. Apparatus according to Claim 1, wherein said selective coupling means comprises:

second combining means having a first input for receiving a signal from said intermediate point, an output coupled to said signal path, and a second input; and

switching means for coupling signals from said input of said delay means to said second input of said second combining means during said first interval, and for coupling signals from said output of said delay means to said second input of said second combining means during said second interval.

9. Apparatus according to Claim 8, wherein:
signals processed by said second combining means are substantially equally weighted and of opposite polarity.

10. Video signal processing apparatus substantially as herein described with reference to the accompanying drawings.

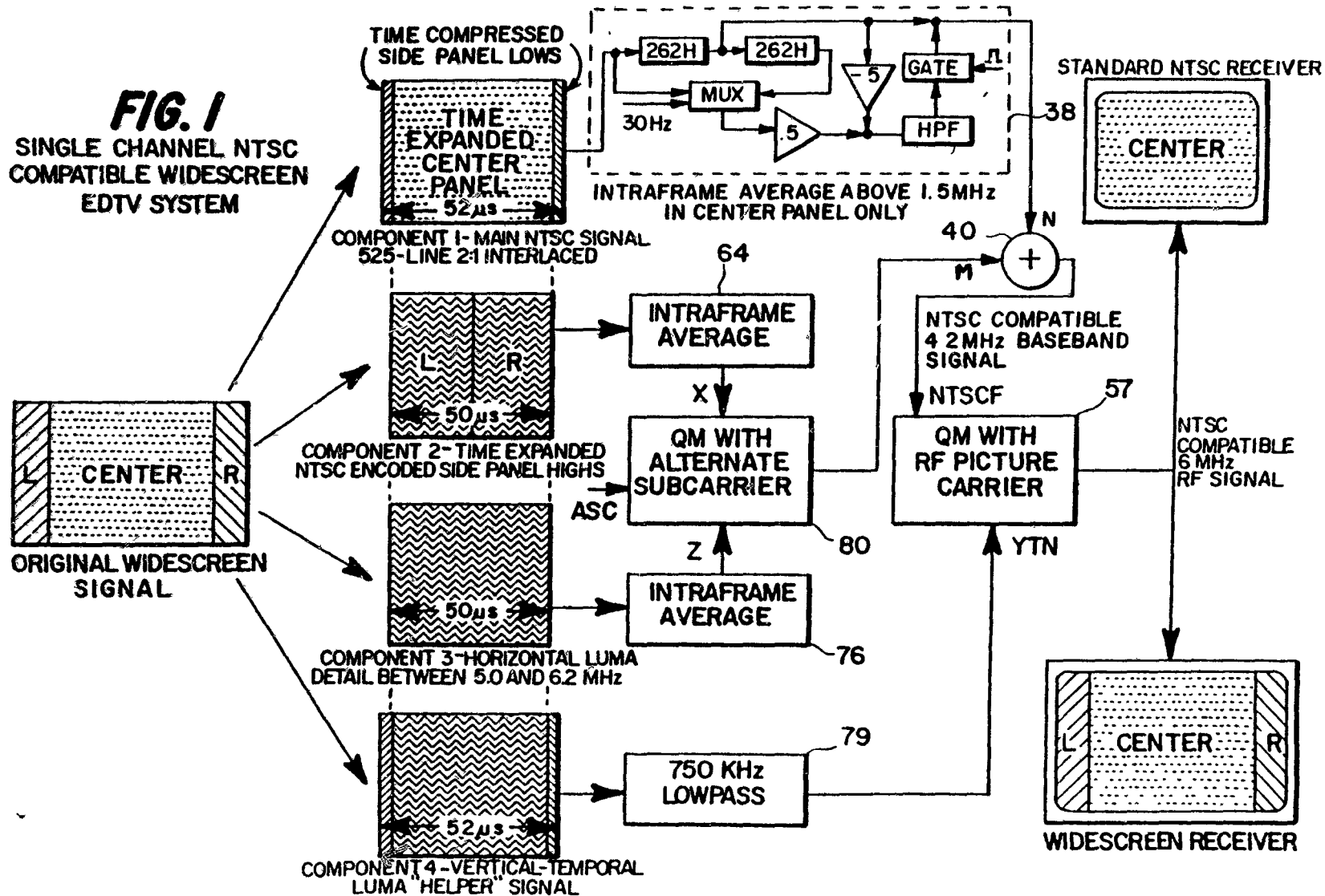
DATED: 9 NOVEMBER 1992

PHILLIPS ORMONDE & FITZPATRICK
Attorneys For:
GENERAL ELECTRIC COMPANY

David B Fitzpatrick



FIG. 1
SINGLE CHANNEL NTSC
COMPATIBLE WIDESCREEN
EDTV SYSTEM



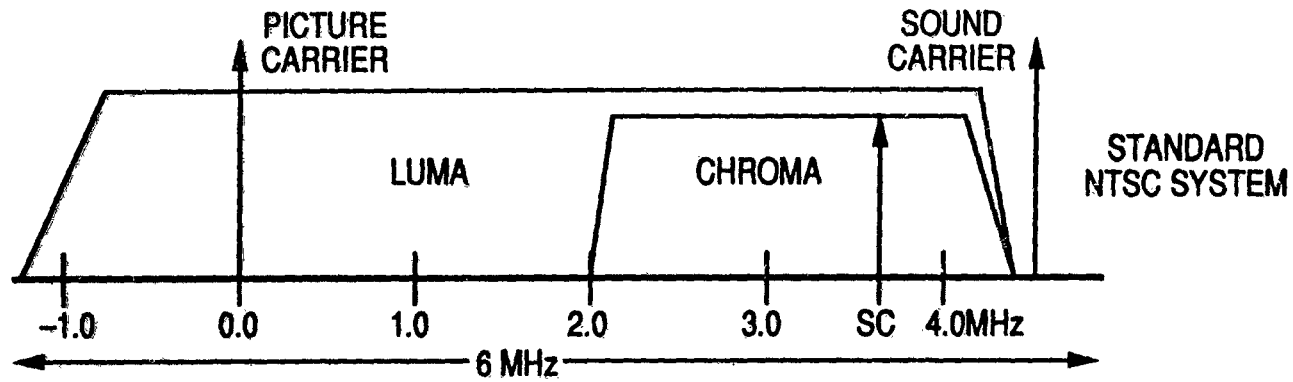


FIG. 1b

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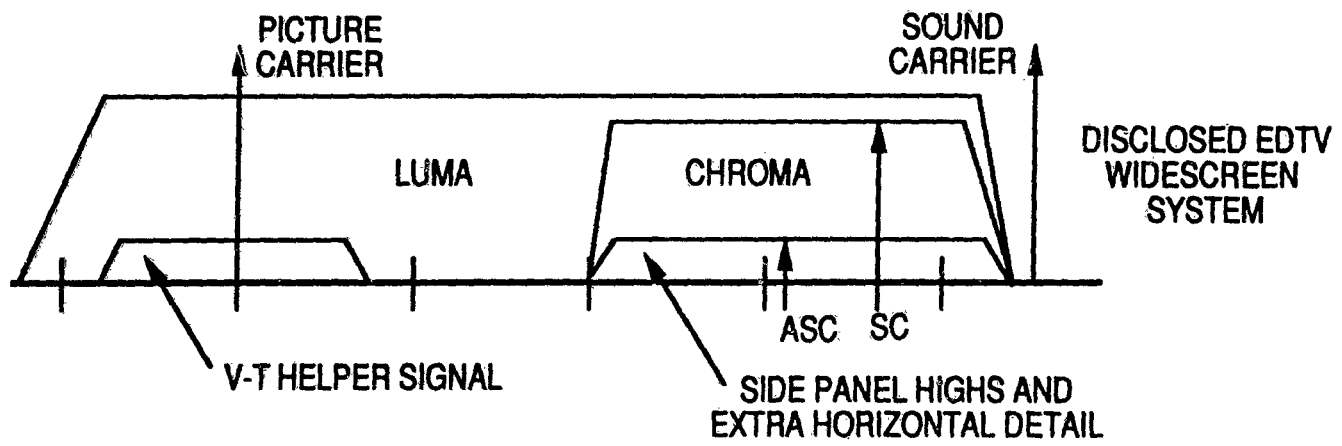
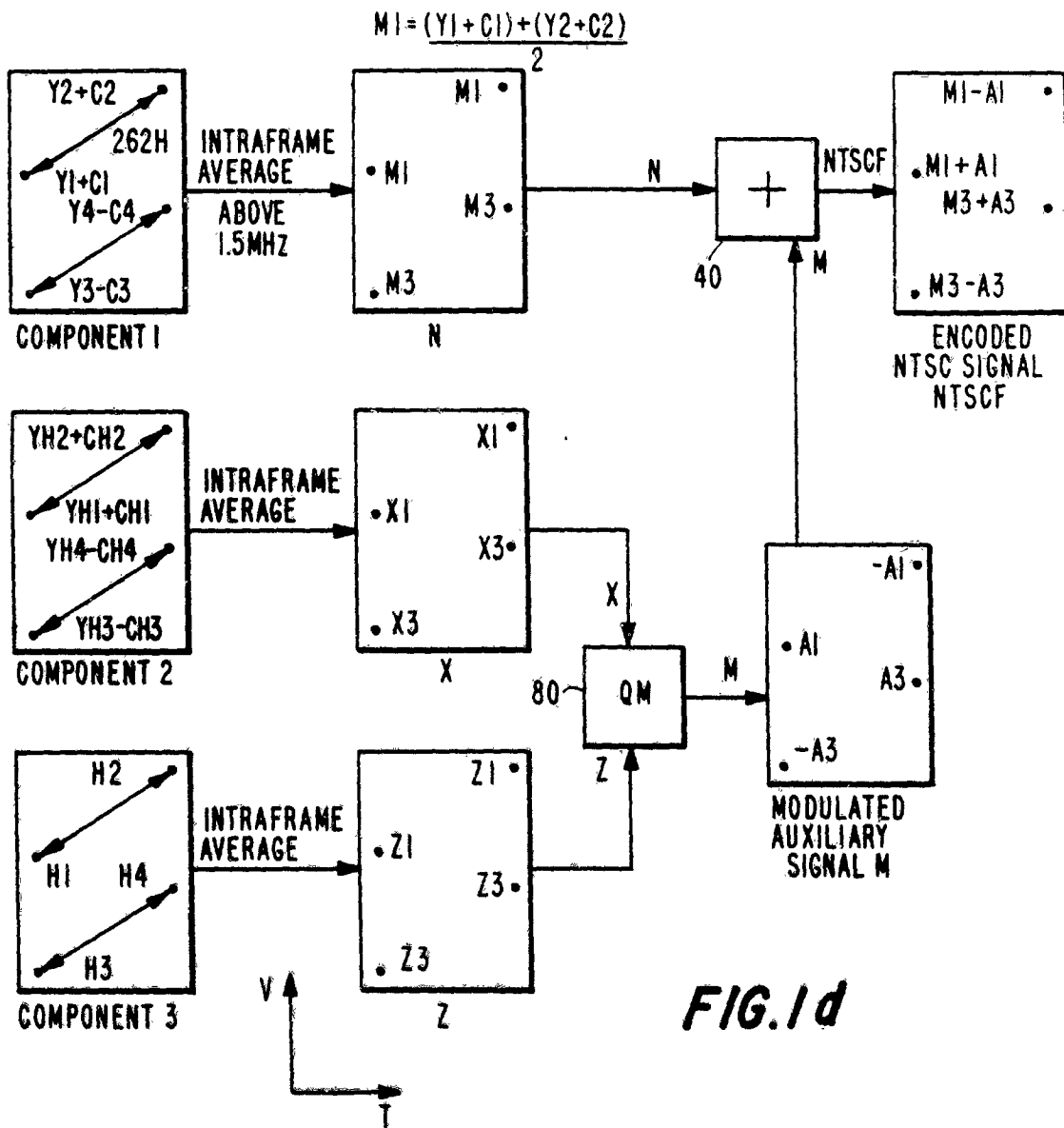
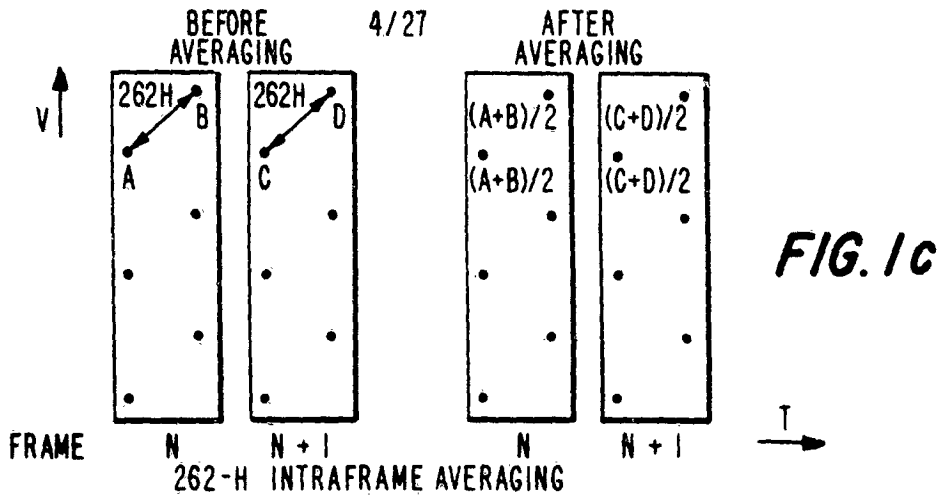
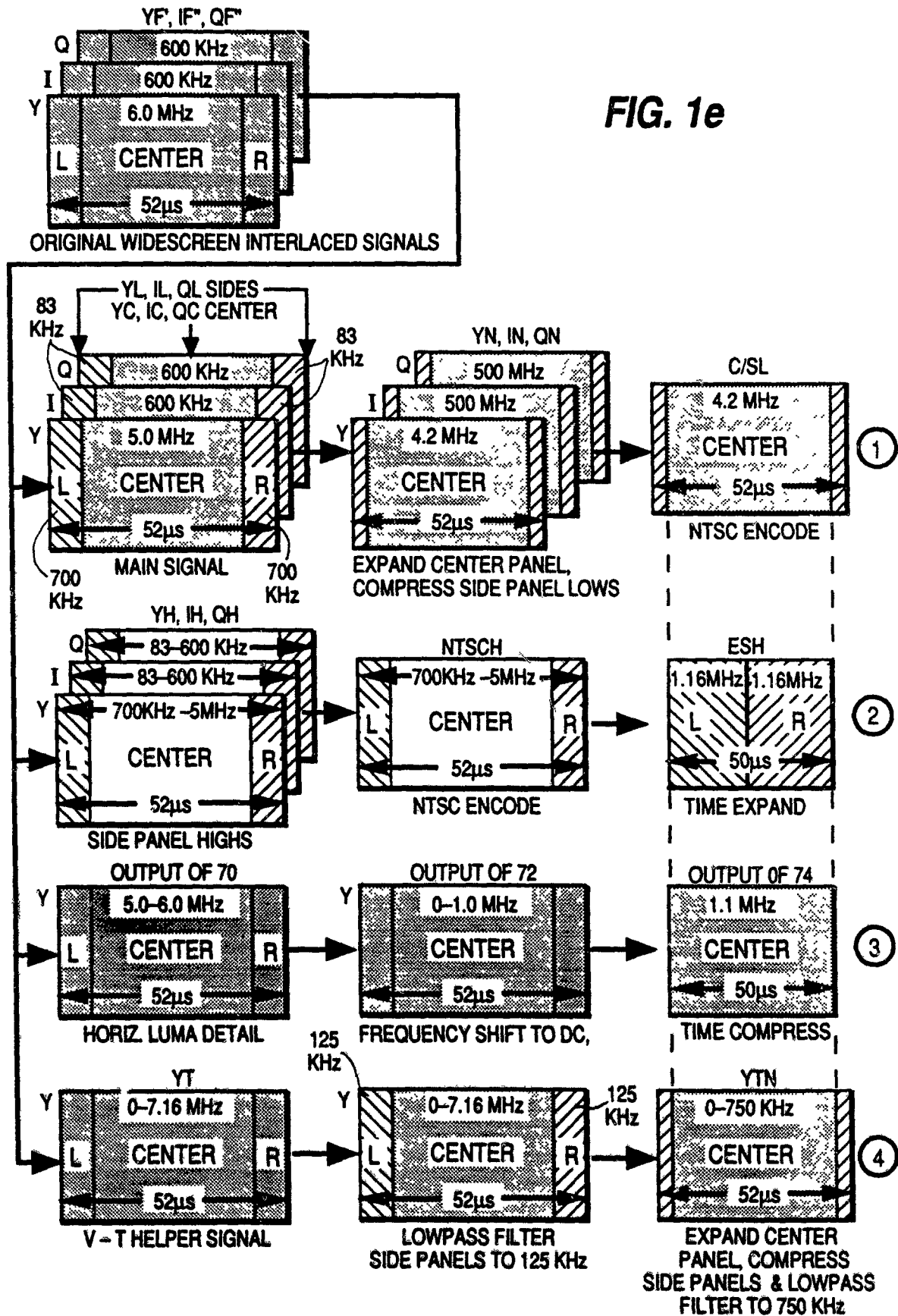


FIG. 1b'

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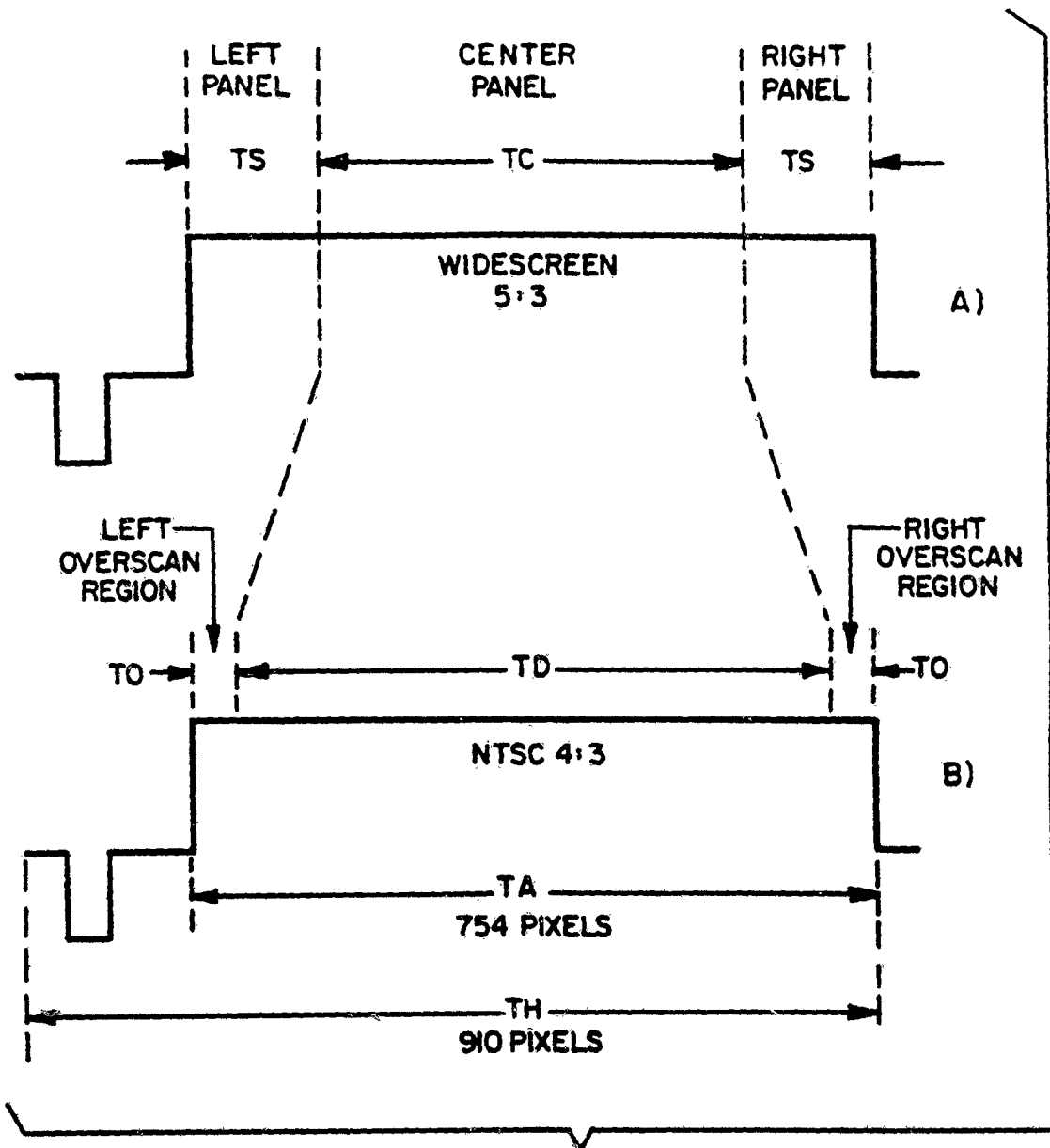
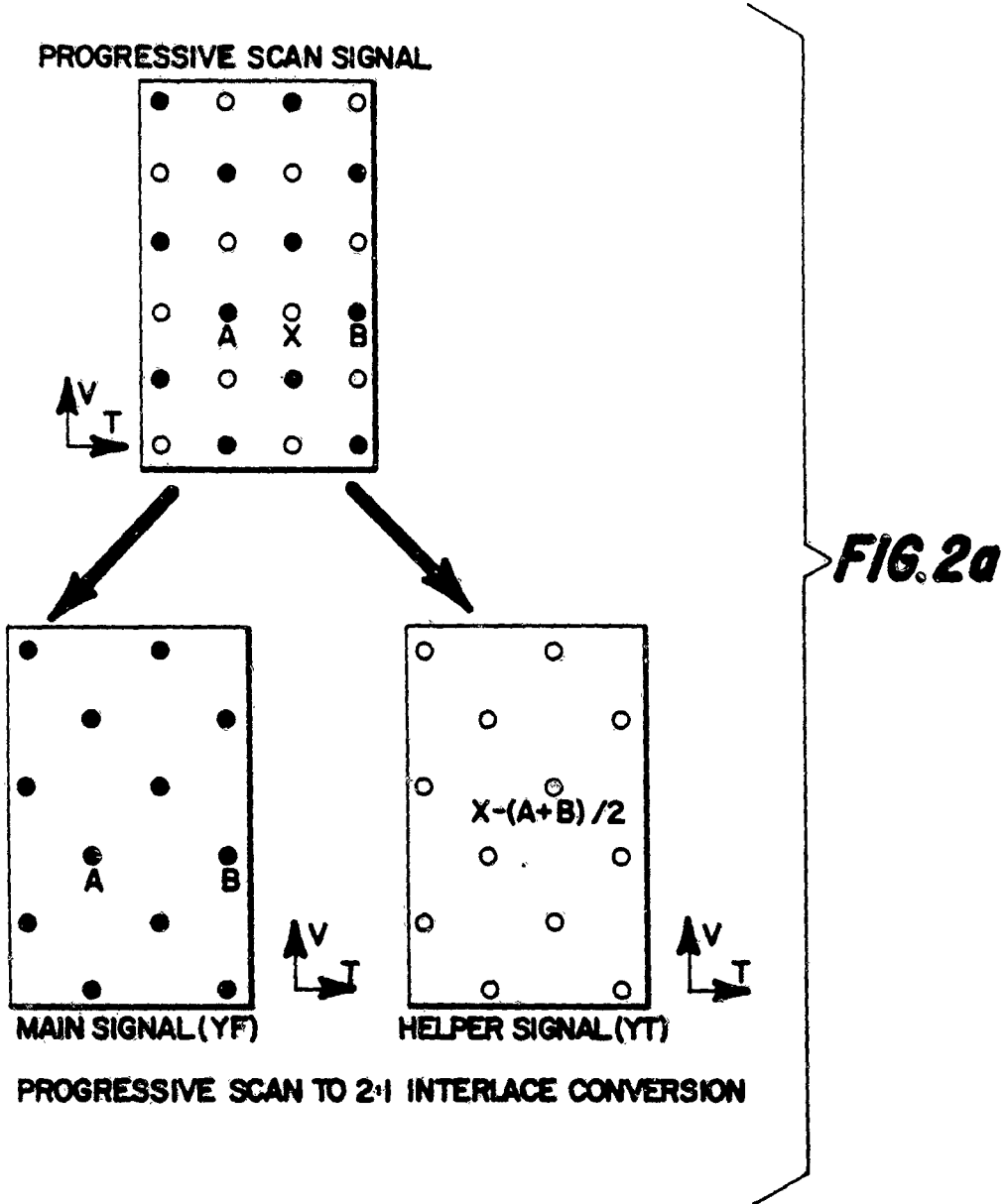
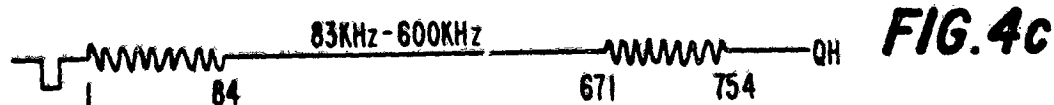
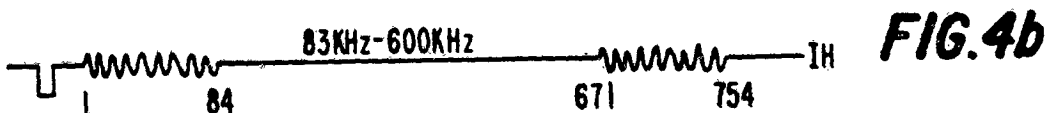
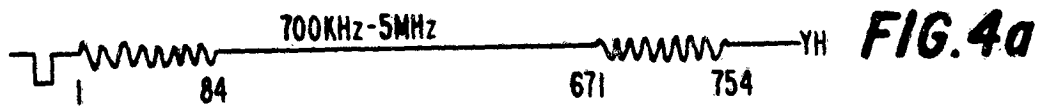
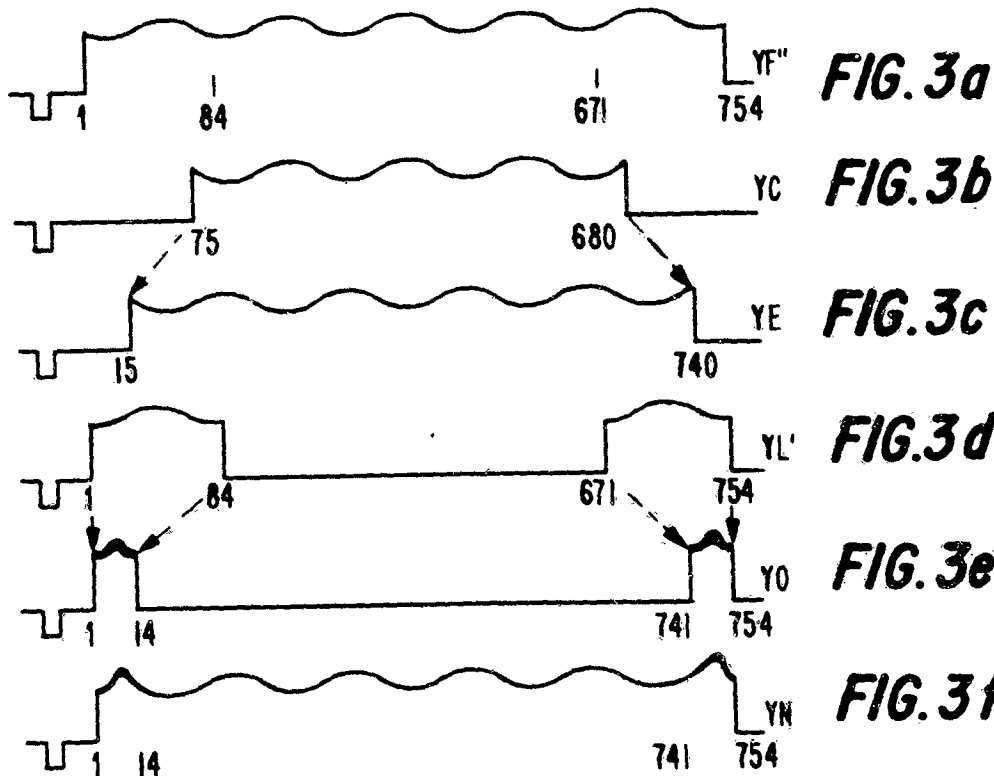


FIG. 2



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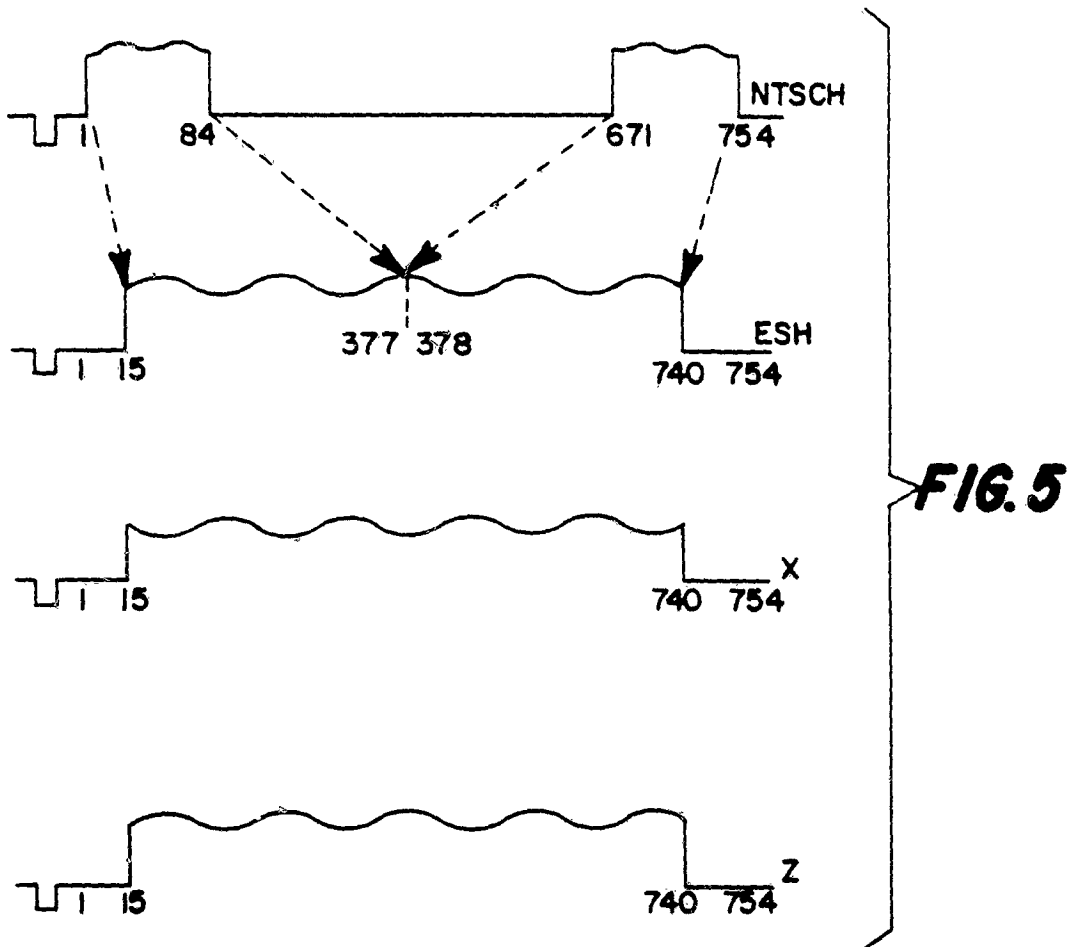
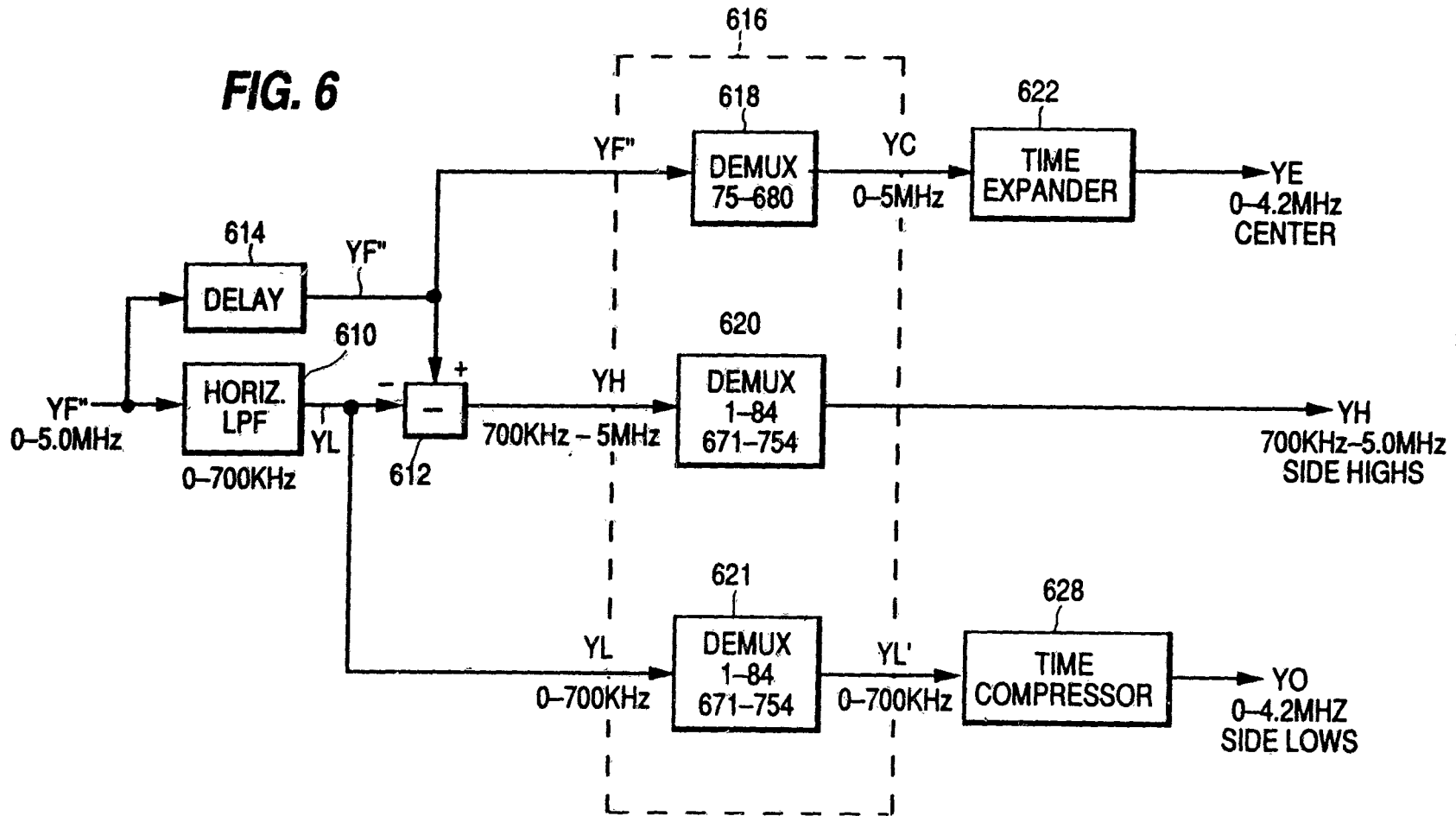
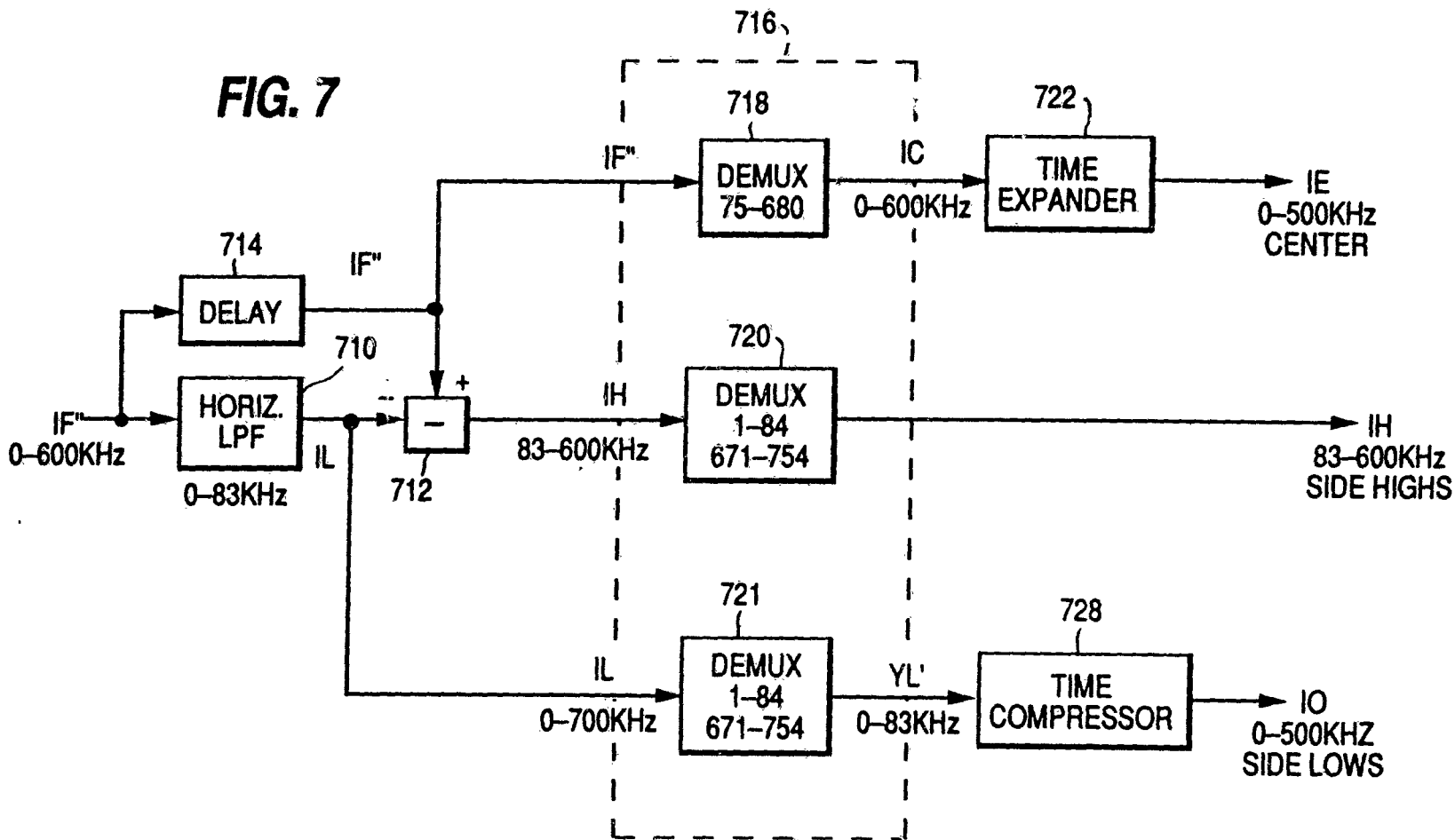


FIG. 6



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FIG. 7



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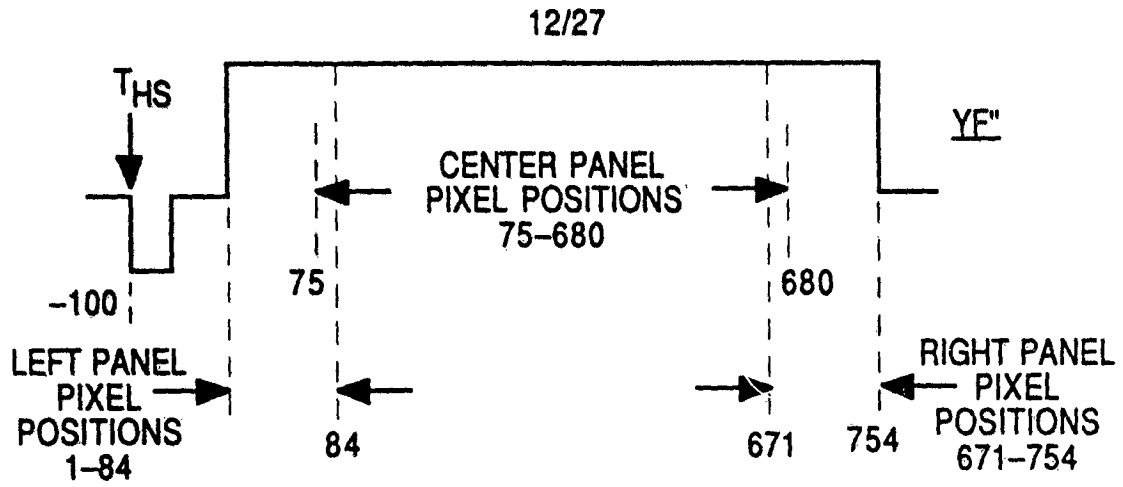


FIG. 8'

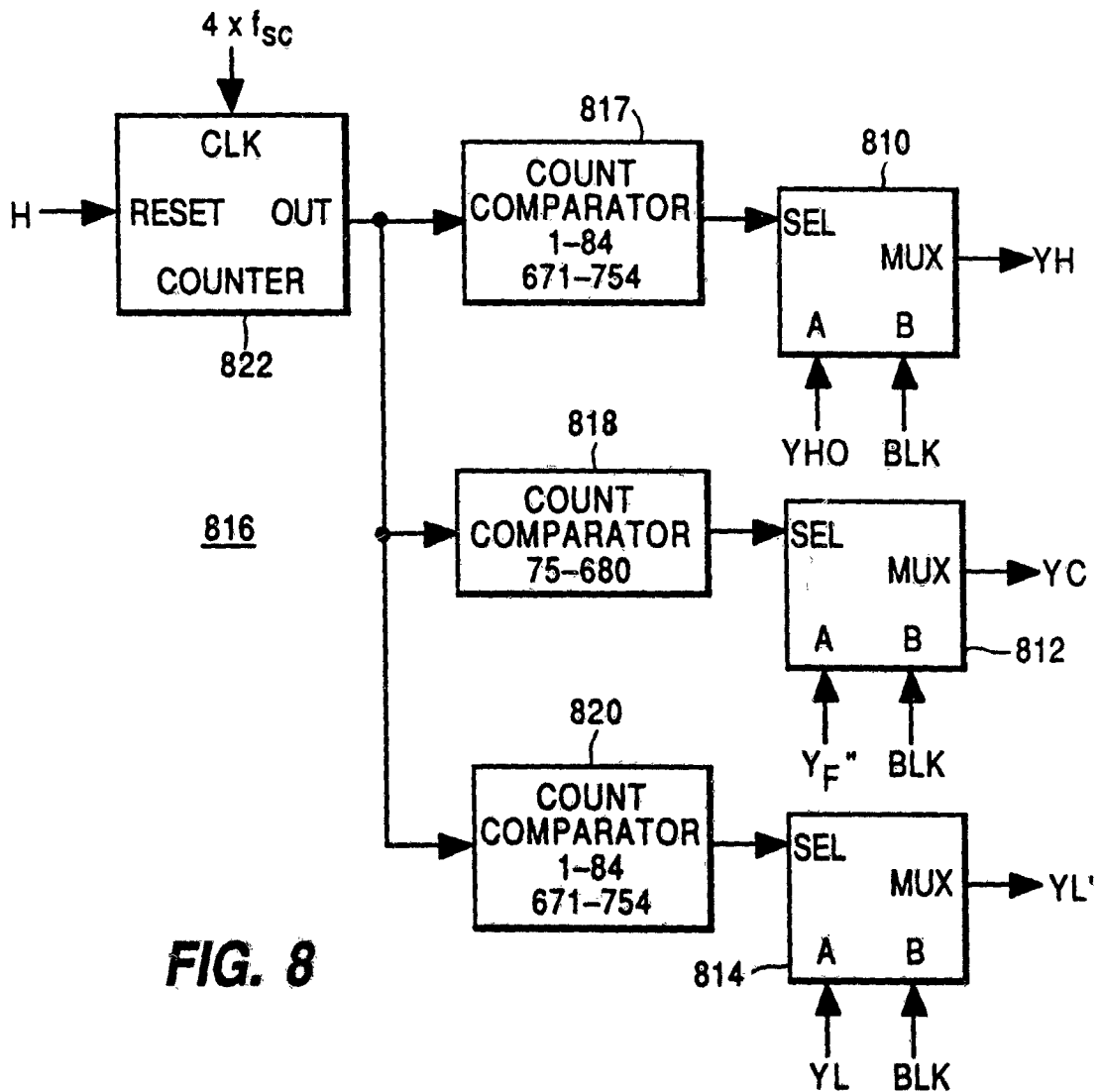


FIG. 8

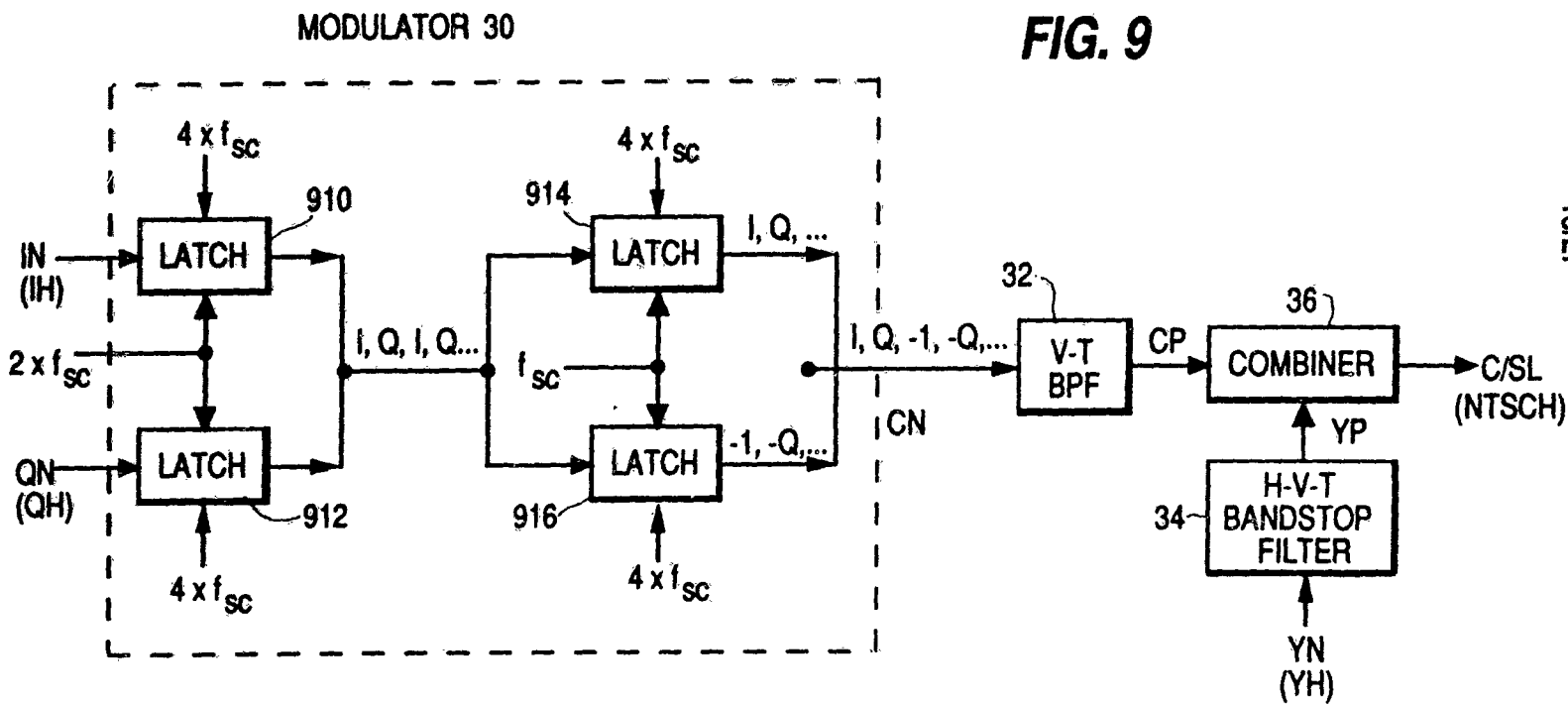
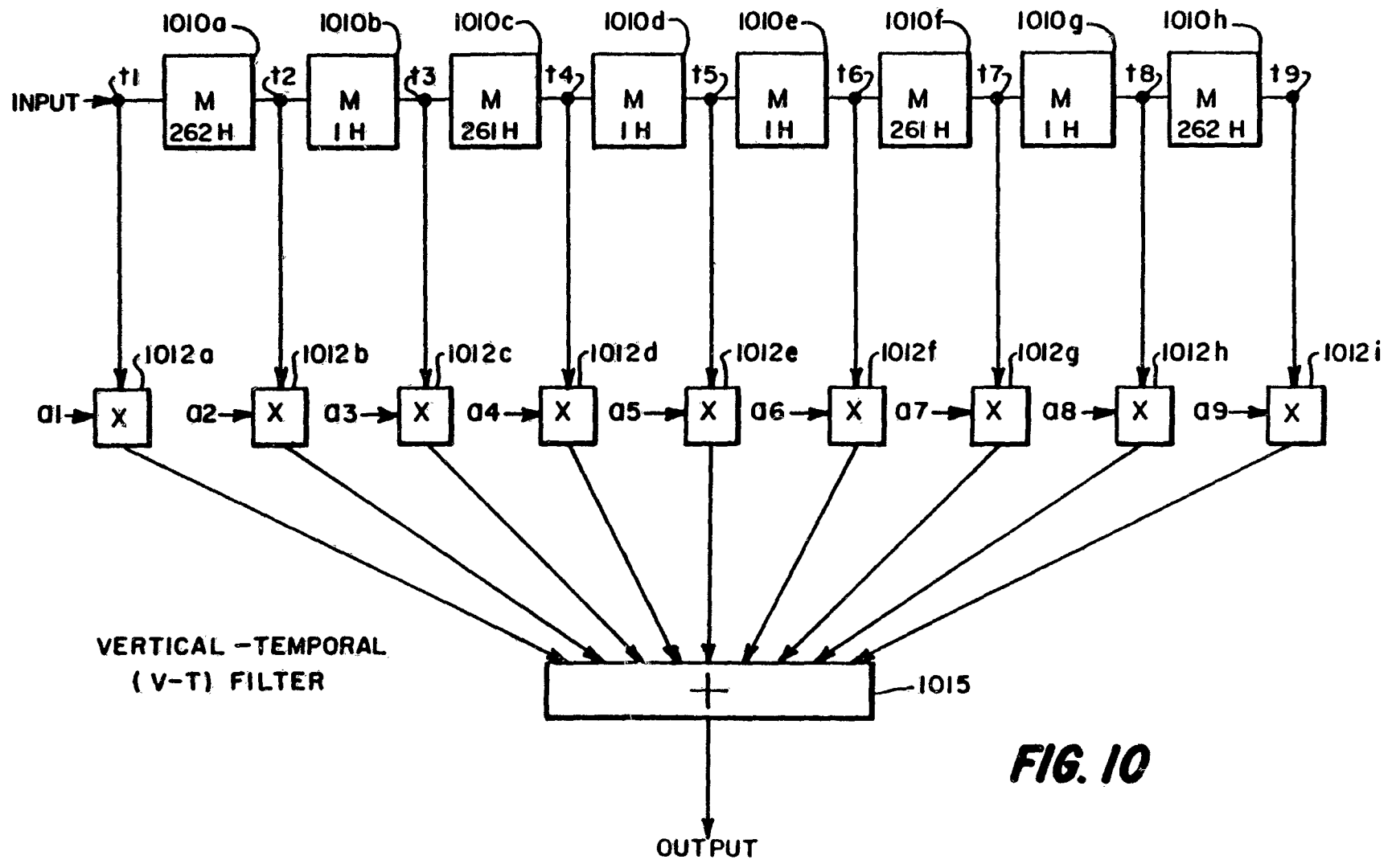


FIG. 9

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FIG. 10

	WEIGHTING COEFFICIENTS								
	a1	a2	a3	a4	a5	a6	a7	a8	a9
V-T BANDPASS FILTER	$\frac{1}{16}$	$-\frac{1}{8}$	$\frac{1}{8}$	$-\frac{1}{16}$	$\frac{1}{4}$	$-\frac{1}{16}$	$\frac{1}{8}$	$-\frac{1}{8}$	$-\frac{1}{16}$
V-T BANDSTOP FILTER	$\frac{1}{16}$	$\frac{1}{8}$	$-\frac{1}{8}$	$\frac{1}{16}$	$\frac{3}{4}$	$\frac{1}{16}$	$-\frac{1}{8}$	$\frac{1}{8}$	$\frac{1}{16}$

FIG 10a

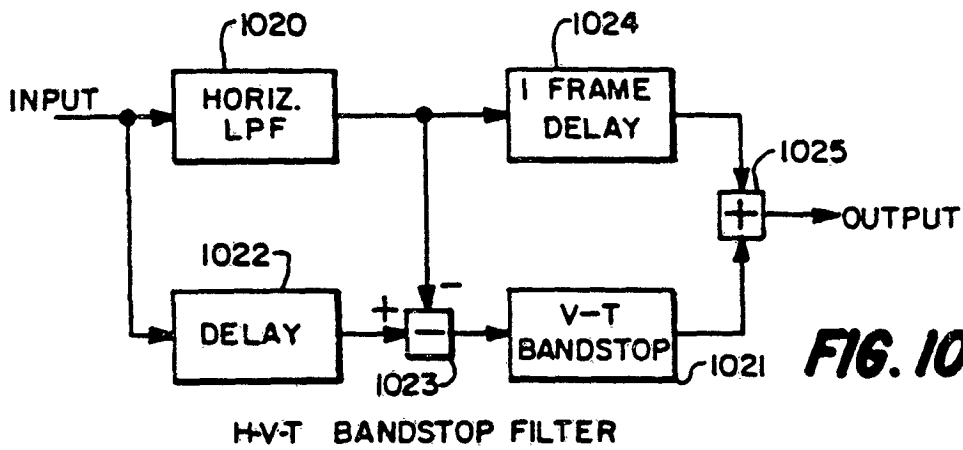


FIG. 10b

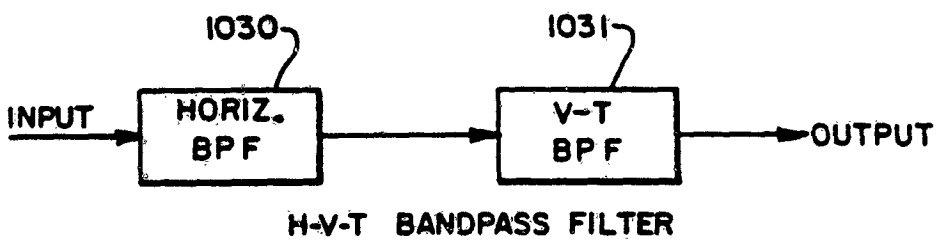
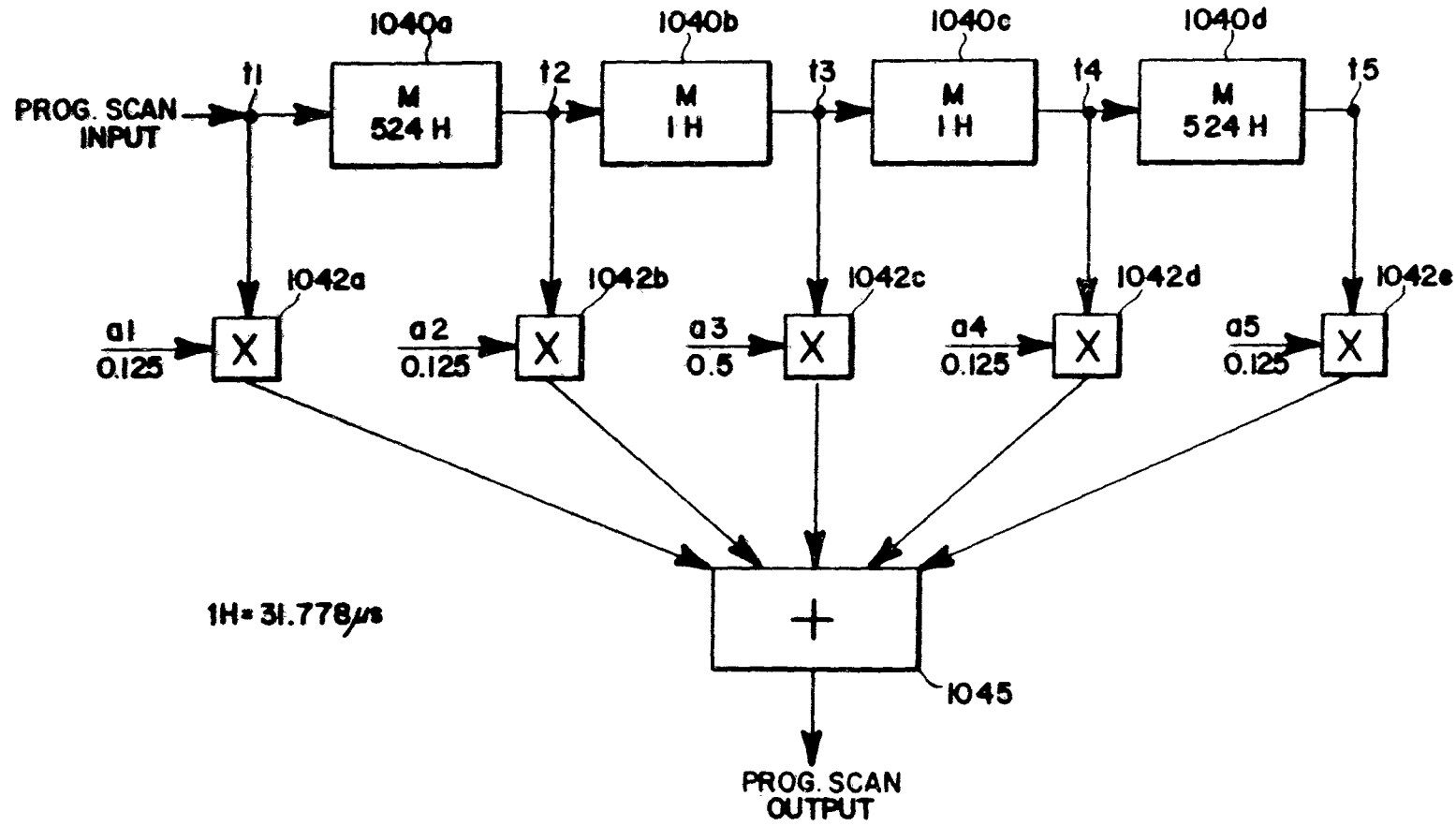
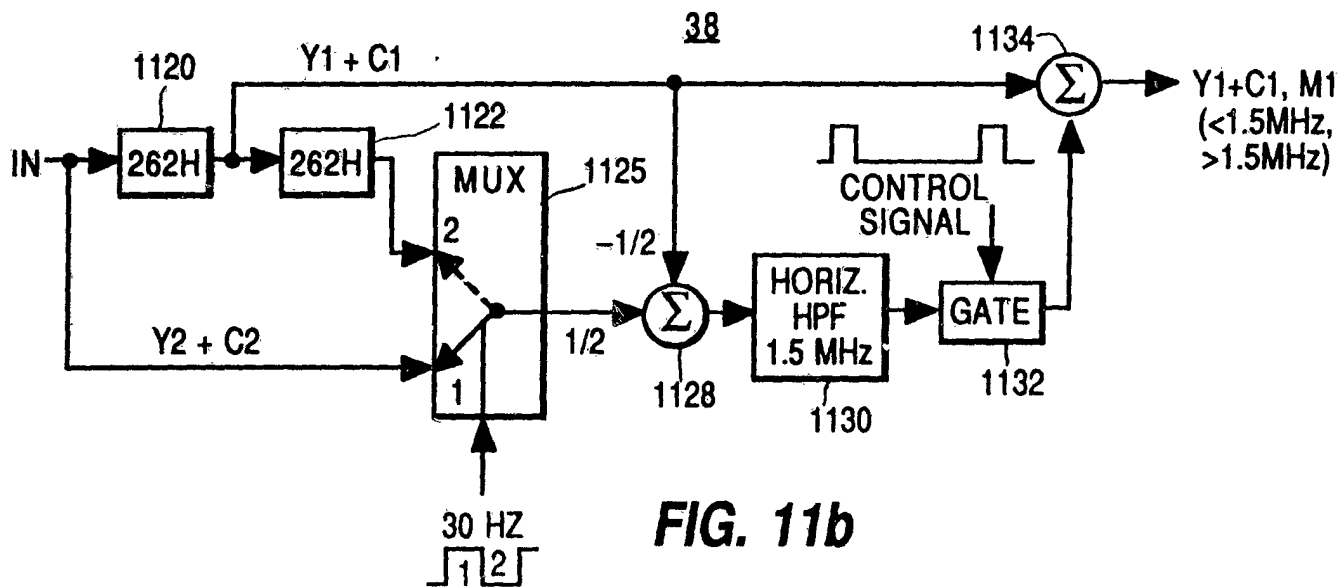
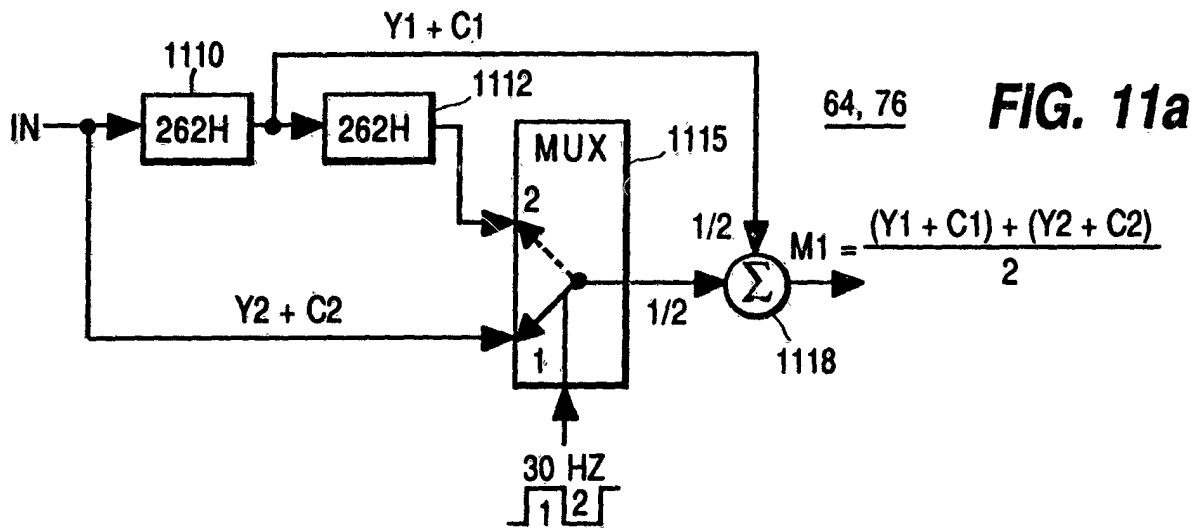


FIG. 10c

FIG. 10d





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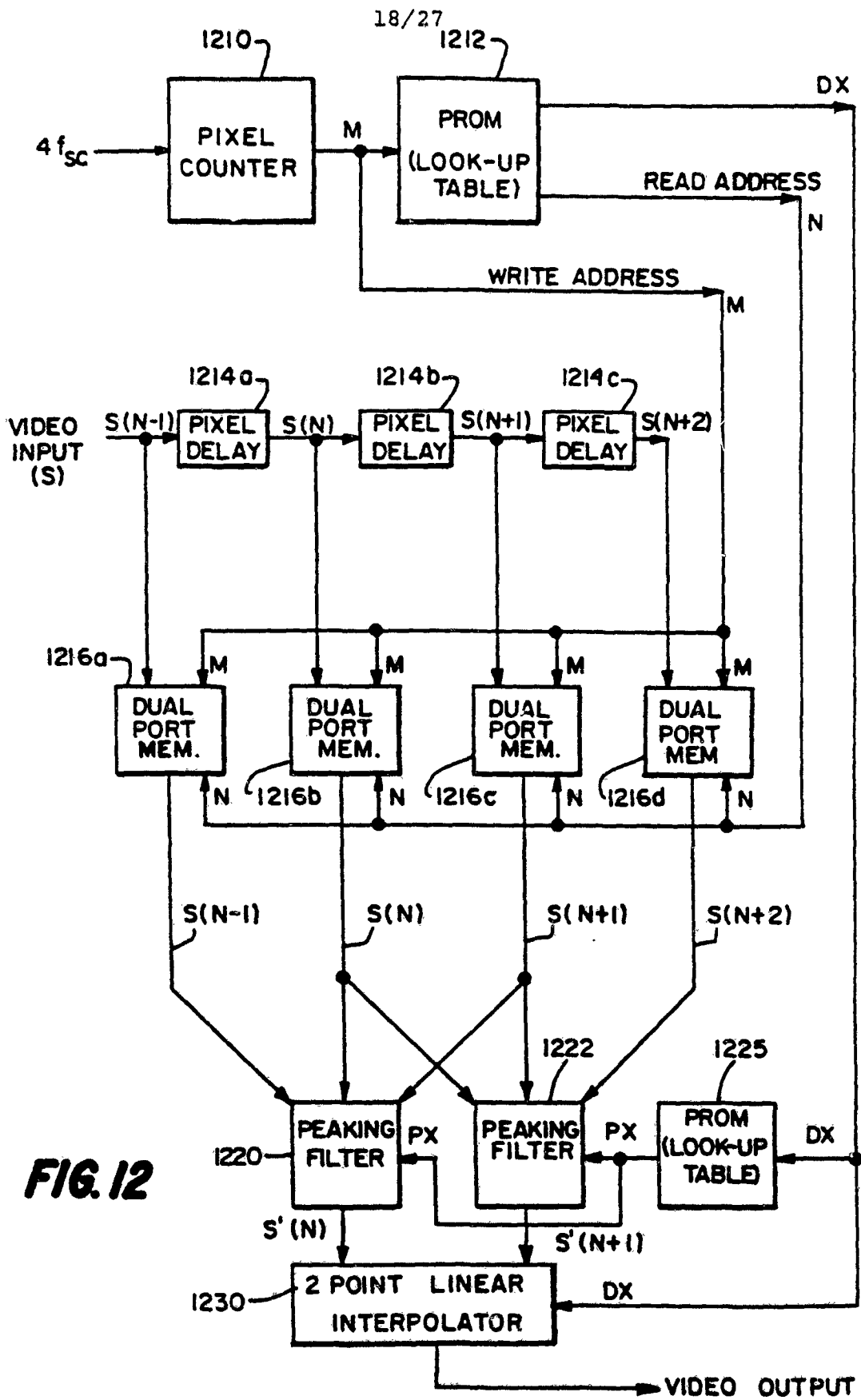


FIG. 12

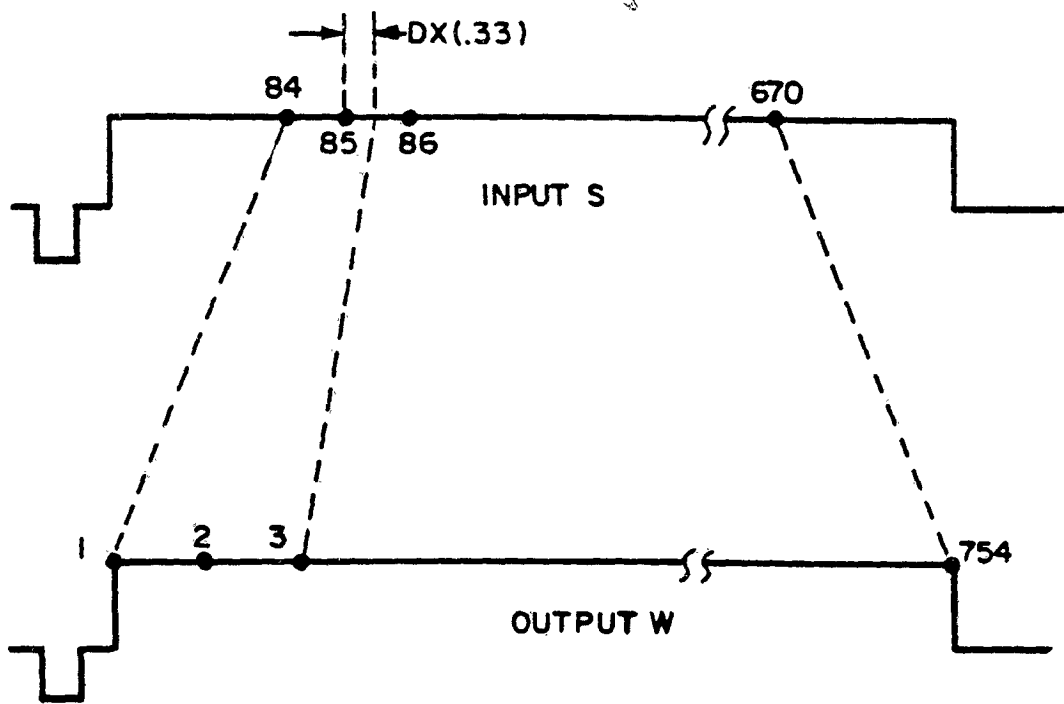


FIG. 120

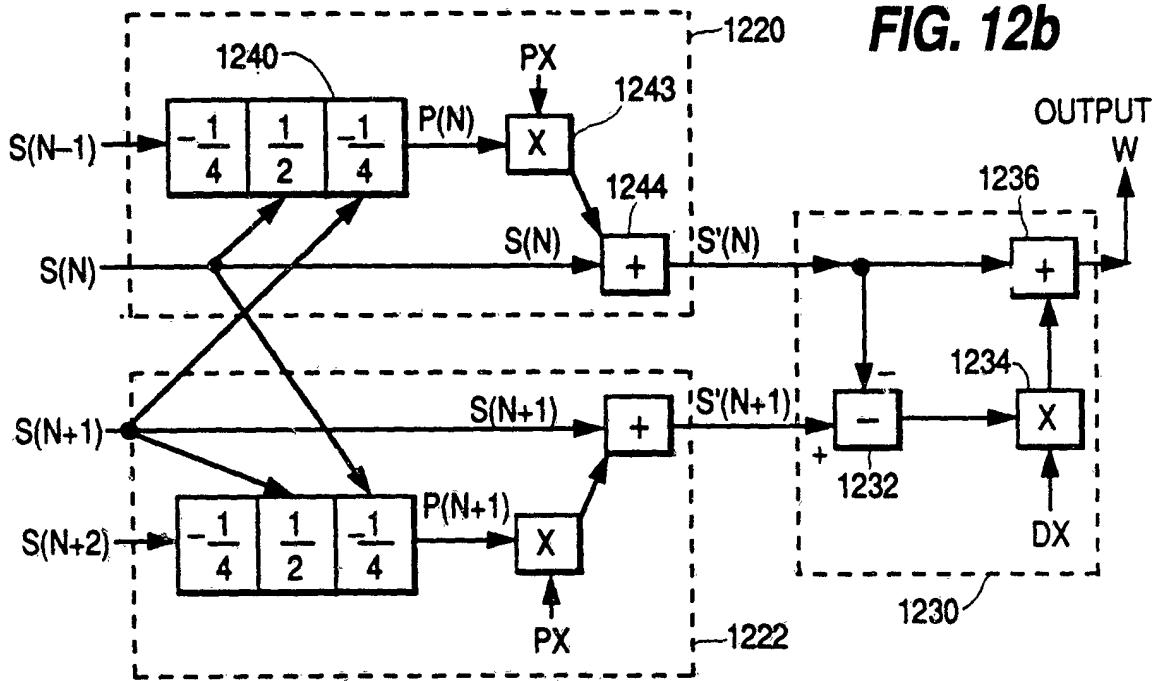


FIG. 12d

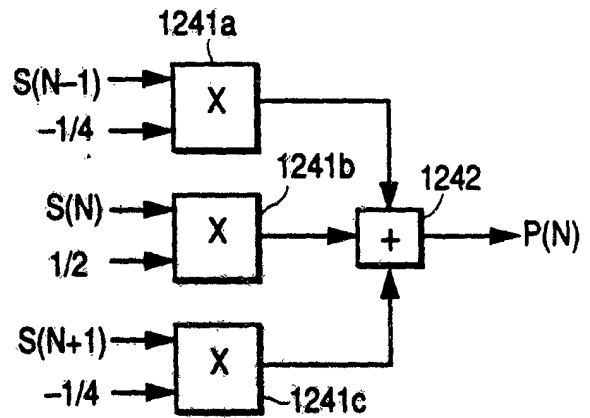
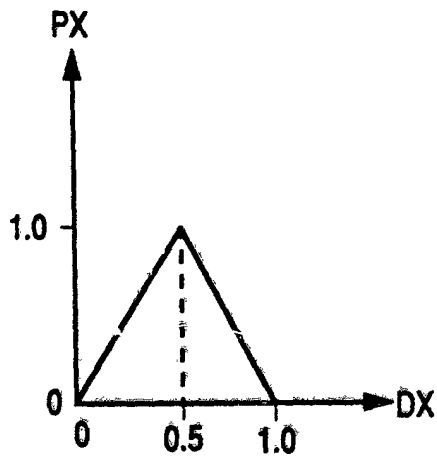
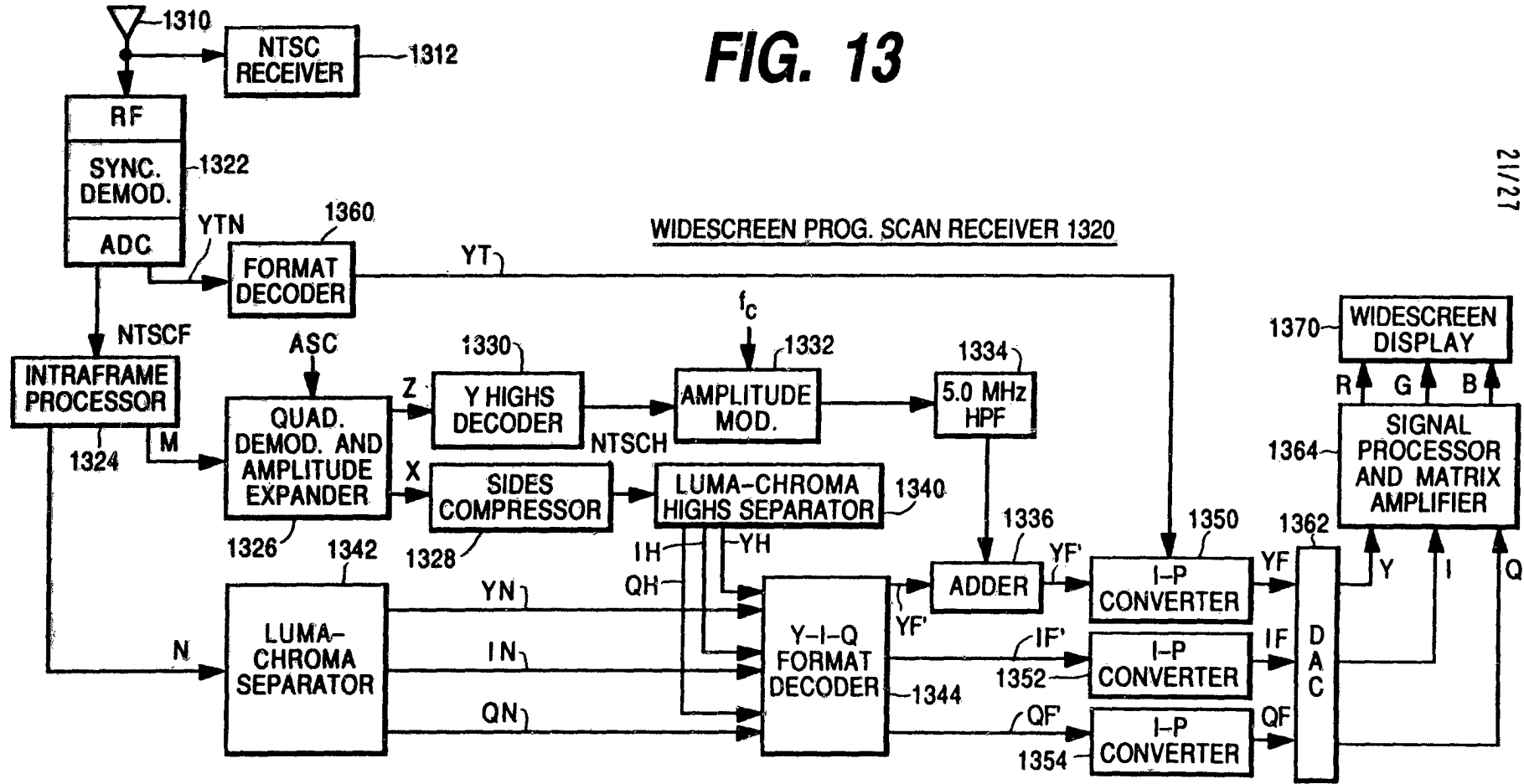


FIG. 12c

FIG. 13



SUBSTITUTE SHEET

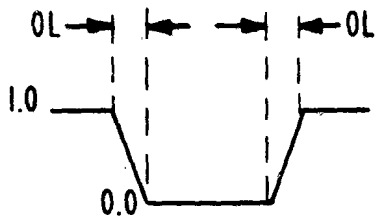


FIG. 14a

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FIG. 14b

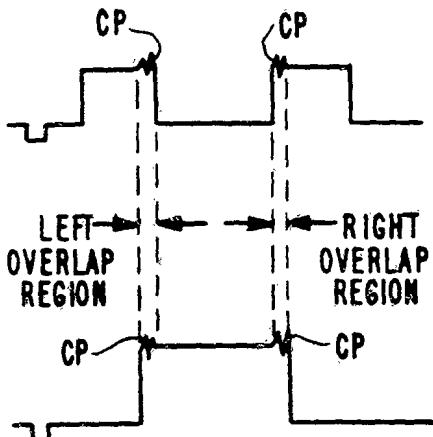


FIG. 14c

FIG. 14d

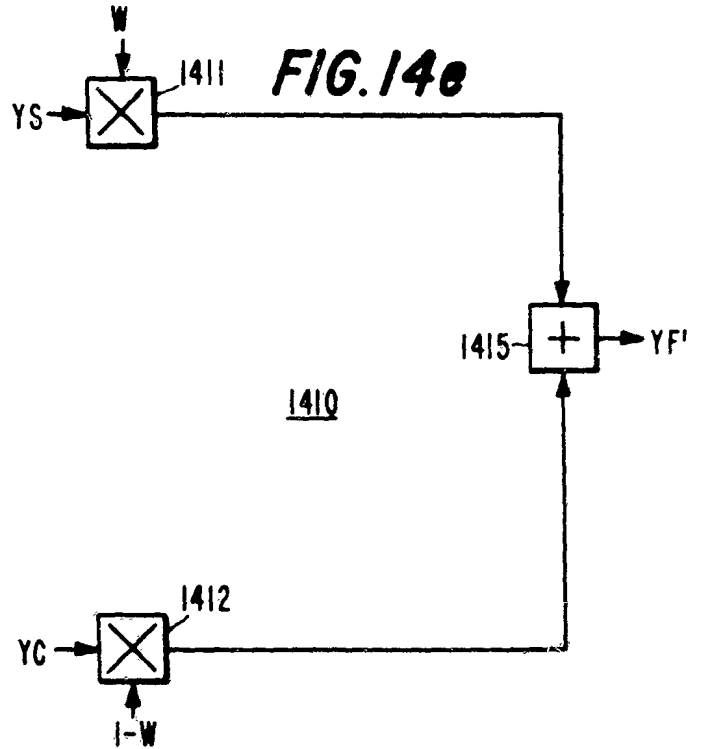


FIG. 14e



FIG. 14f

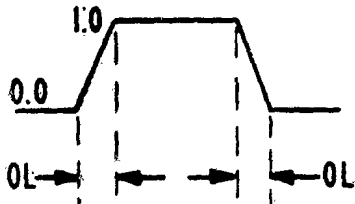


FIG. 14g

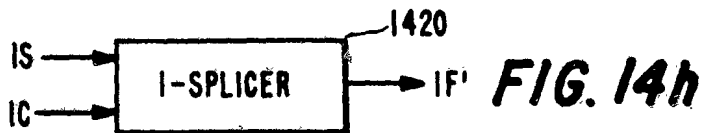


FIG. 14h



FIG. 14i

FIG. 15

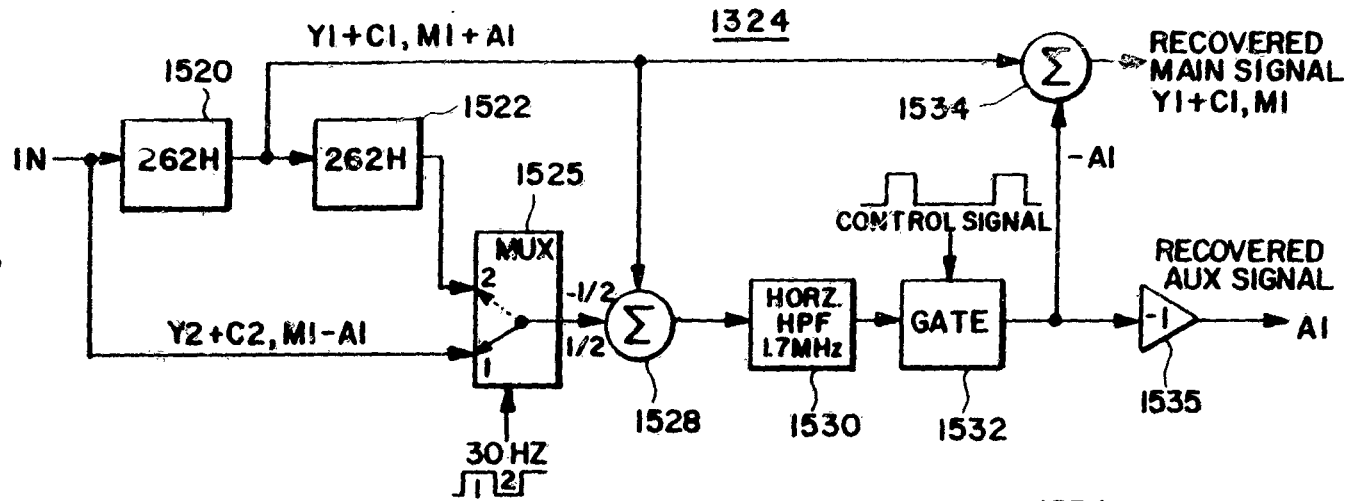


FIG. 16

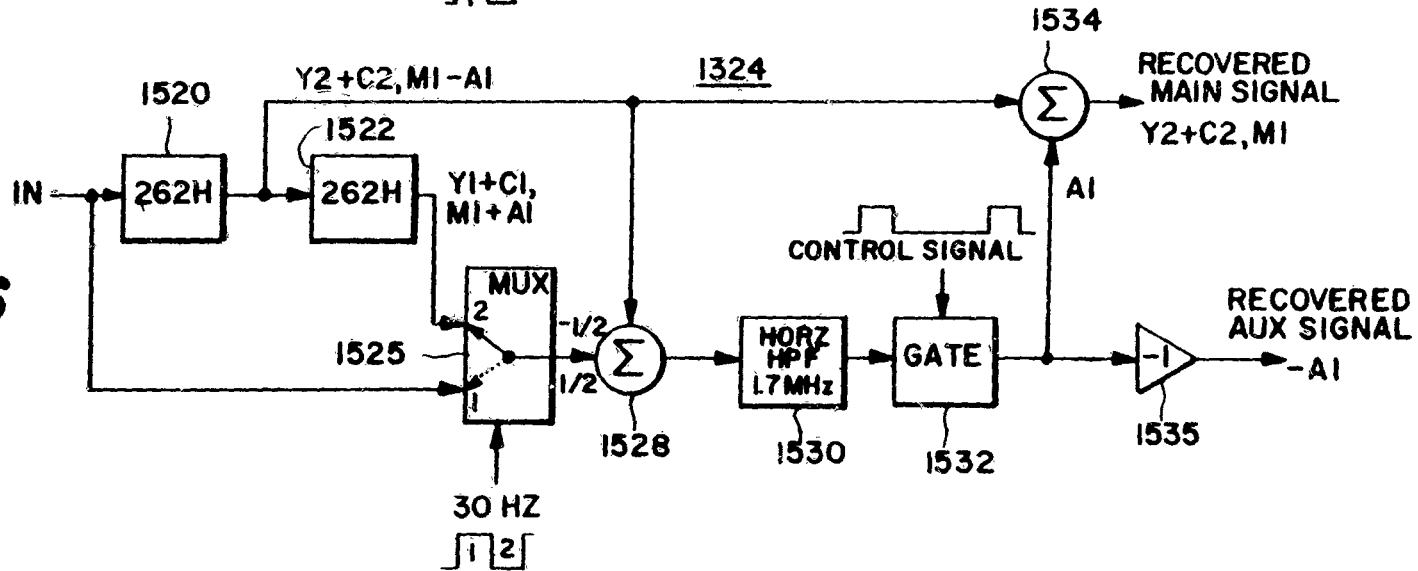


FIG. 17

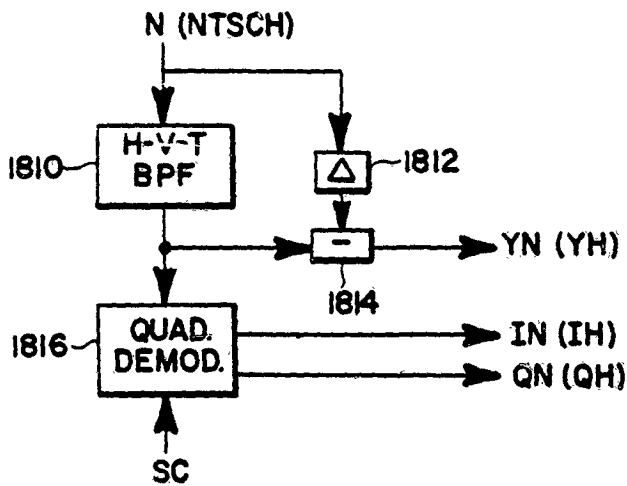
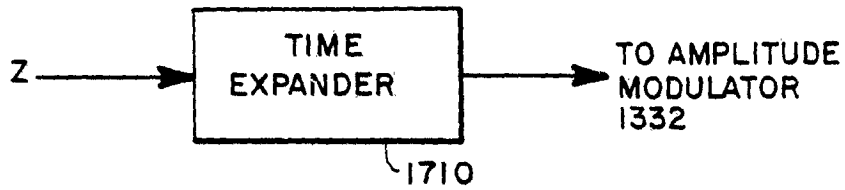


FIG. 18

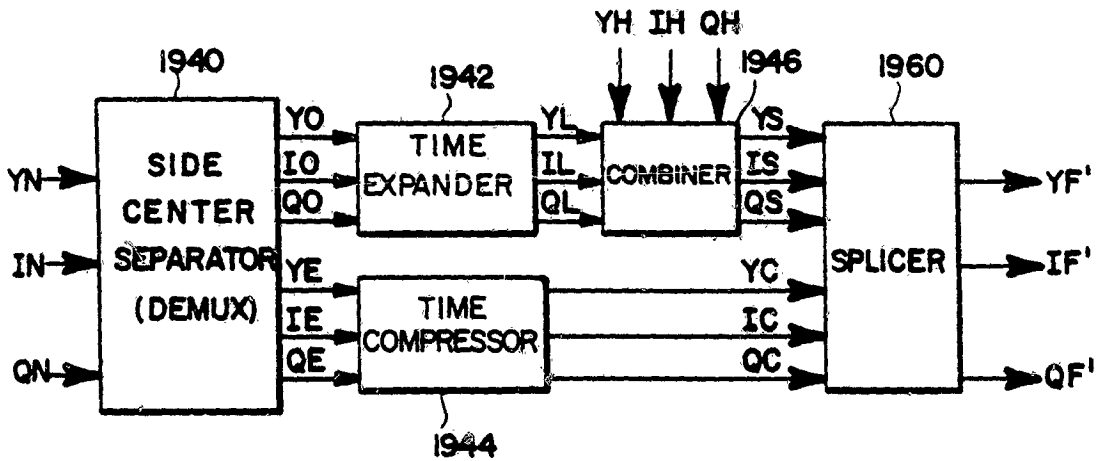


FIG. 19

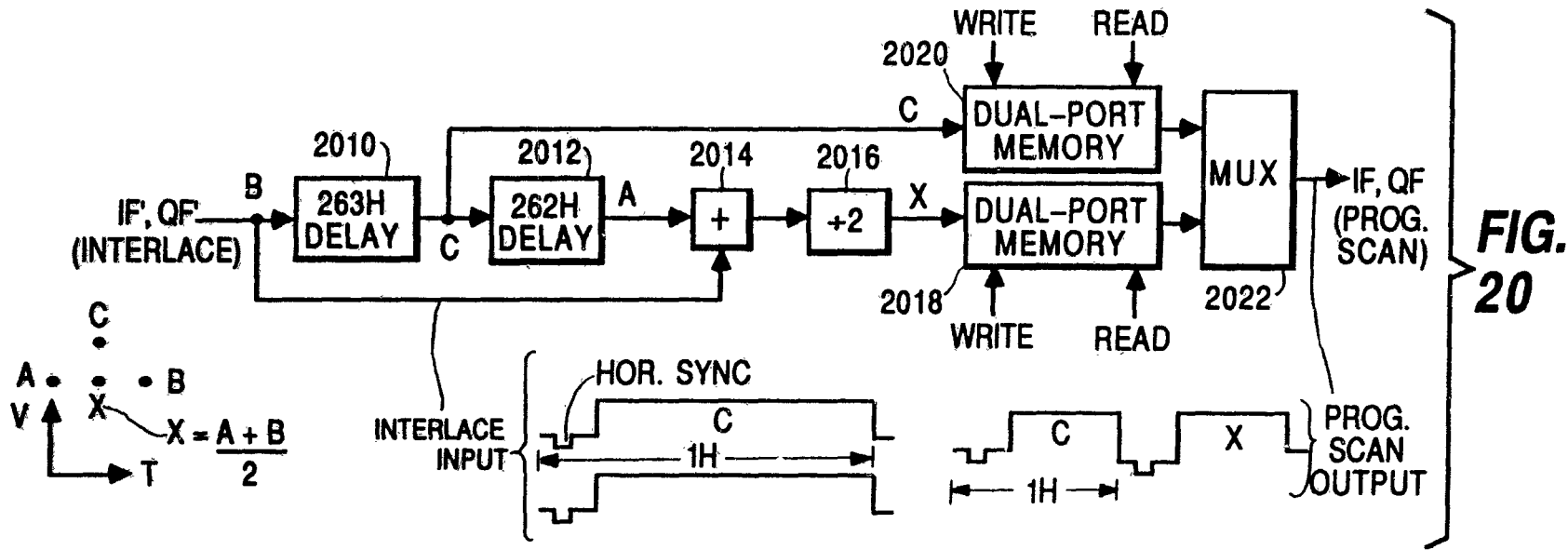


FIG. 20

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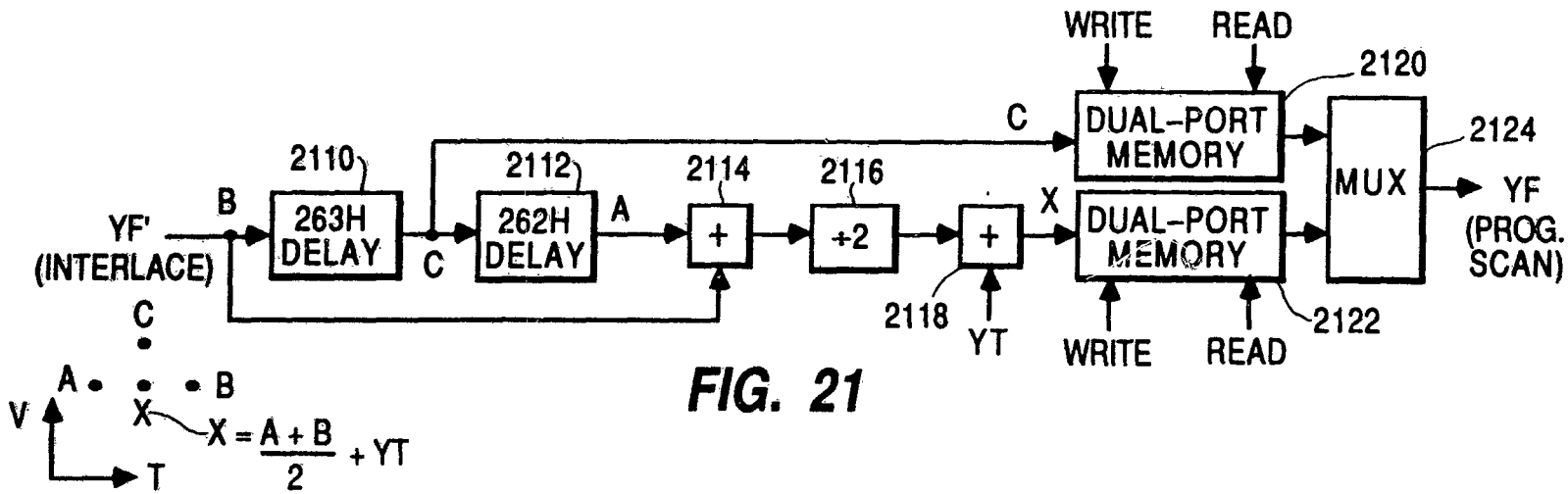


FIG. 21

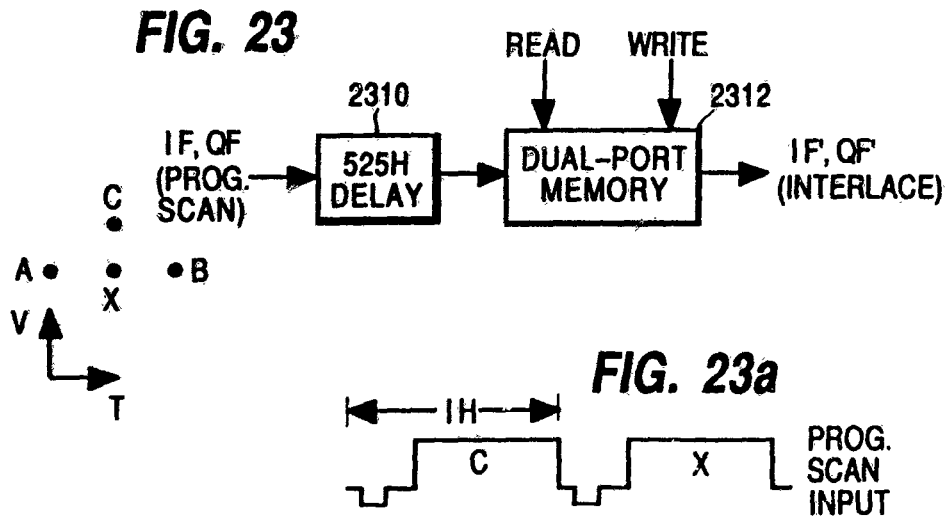
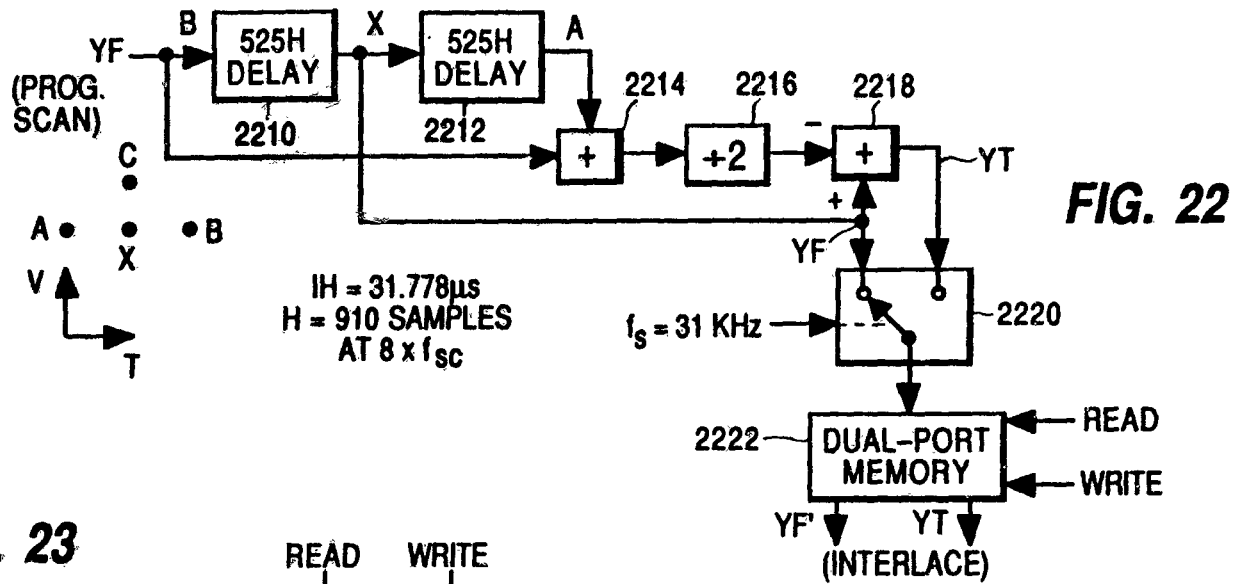


FIG. 23b

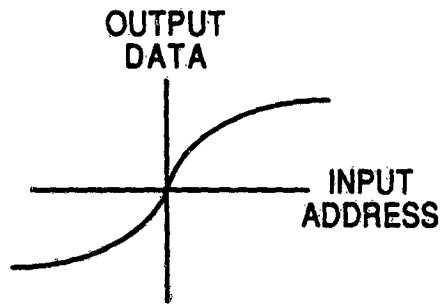
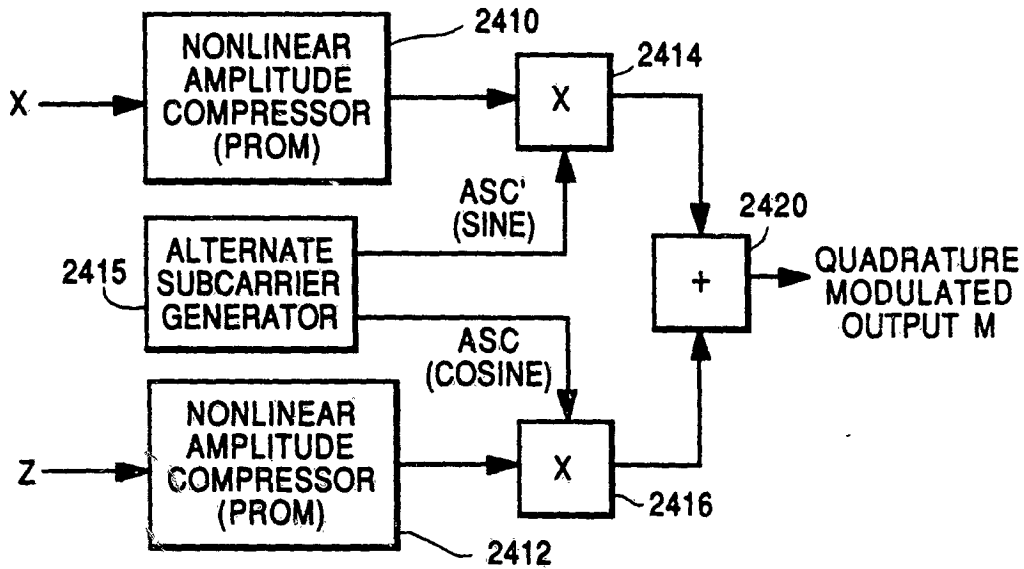
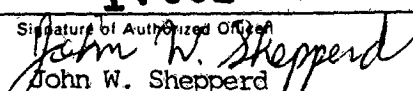


FIG. 24

INTERNATIONAL SEARCH REPORT

International Application No. PCT/US89/01788

I. CLASSIFICATION OF SUBJECT MATTER (if several classification symbols apply, indicate all) ⁶		
According to International Patent Classification (IPC) or to both National Classification and IPC		
IPC(4): H04N 11/14, 11/00, 9/64, -9/78		
US CL.: 358/12, 16		
II. FIELDS SEARCHED		
Minimum Documentation Searched ⁷		
Classification System	Classification Symbols	
US IPC(4)	358/12, 15, 16, 31 H04N 11/00-12, 9/64-78 [1986 to Date]	
Documentation Searched other than Minimum Documentation to the Extent that such Documents are Included in the Fields Searched ⁸		
III. DOCUMENTS CONSIDERED TO BE RELEVANT ⁹		
Category *	Citation of Document, ¹¹ with indication, where appropriate, of the relevant passages ¹²	Relevant to Claim No. ¹³
A	US, A, 4,266,240, LEVY 05 May 1981	
A	US, A, 4,287,528, LEVY 01 September 1981, Figures 3 and 4	
A	US, A, 4,701,783, GLENN 20 October 1987	
A	US, A, 4,456,922, BALABAN ET AL 26 June 1984	
A	GB, A, 2,054,313, BRITISH BROADCASTING CORPORATION 11 February 1981	
A	GB, A, 2,072,991, BRITISH BROADCASTING CORPORATION 07 October 1981	
A	GB, A, 2,079,091, BRITISH BROADCASTING CORPORATION 13 January 1982	
A	JP, A, 61-281696, SONY CORPORATION 12 December 1986	
<p>⁶ Special categories of cited documents: ¹⁰</p> <p>"A" document defining the general state of the art which is not considered to be of particular relevance</p> <p>"E" earlier document but published on or after the international filing date</p> <p>"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)</p> <p>"O" document referring to an oral disclosure, use, exhibition or other means</p> <p>"P" document published prior to the international filing date but later than the priority date claimed</p> <p>"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention</p> <p>"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step</p> <p>"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.</p> <p>"&" document member of the same patent family</p>		
IV. CERTIFICATION		
Date of the Actual Completion of the International Search	Date of Mailing of this International Search Report	
19 Jun 1989	17 JUL 1989	
International Searching Authority	Signature of Authorized Officer	
ISA/US	 John W. Shepperd	