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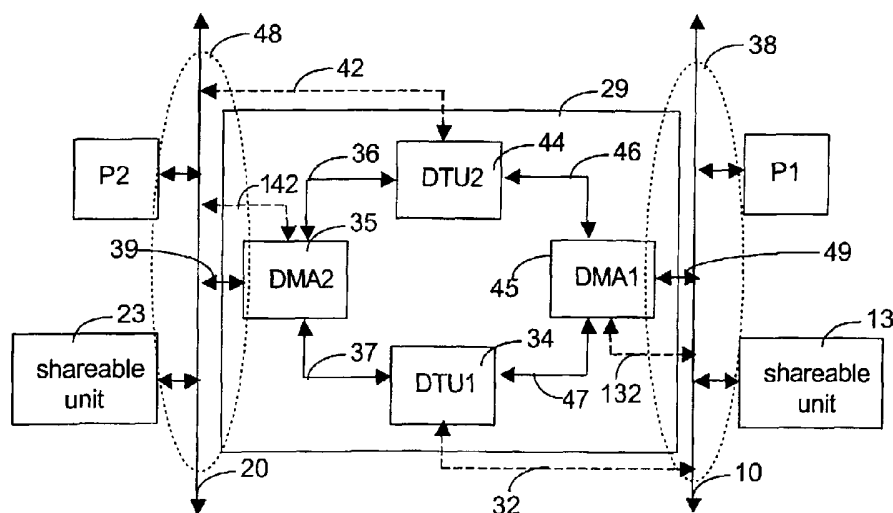
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(54) Title: IMPROVED INTER-PROCESSOR COMMUNICATION SYSTEM FOR COMMUNICATION BETWEEN PROCESSORS



(57) Abstract: System comprising at least two integrated processors (P1 and P2). These two processors (P1 and P2) are operably connected via two bi-directional communication channels for exchanging information. For establishing the bi-directional communication channels, the system comprises a first processor bus (10) to which the first processor (P1) is connected, a first direct memory access unit (45), a first programmable unit (34), and a first shareable unit (13). The programmable unit (34) can be programmed by the first processor (P1). Also comprised is a second processor bus (20), the second processor (P2) being connectable to the second processor bus (20), a second direct memory access unit (35), and a second programmable unit (44). Said second programmable unit (44) is programmable by the second processor (P2).



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INTERNATIONAL SEARCH REPORT

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B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, IBM-TDB

C. DOCUMENTS CONSIDERED TO BE RELEVANT

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Y	US 5 644 729 A (BOURY BECHARA FOUAD ET AL) 1 July 1997 (1997-07-01) the whole document -----	1-11
A	US 5 335 326 A (APOSTOL GEORGE ET AL) 2 August 1994 (1994-08-02) the whole document ----- -/--	1-11

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☒ Patent family members are listed in annex.

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT		
Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
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