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(54) **Title:** TRANSMITTER AND METHOD OF TRANSMITTING

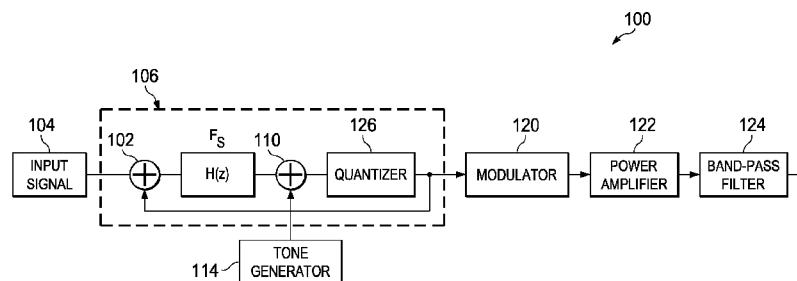


FIG. 1

(57) **Abstract:** In described examples, at least one tone is generated (114). An output signal is generated (106) in response to an input signal (104) and the at least one tone. The output signal is modulated (120). The input signal (104) and the at least one tone are represented in the modulated output signal. The at least one tone is outside a bandwidth of the input signal (104) as represented in the modulated output signal. The modulated output signal is amplified (122). The at least one tone in the amplified signal is attenuated (124) after the amplifying.

WO 2015/119668 A1

TRANSMITTER AND METHOD OF TRANSMITTING

[0001] This relates in general to radio frequency (RF) power amplifiers, and in particular to a transmitter and method of transmitting.

BACKGROUND

[0002] Some radio frequency (RF) transmitters operate inefficiently with high peak to average ratios (PAR). With high PAR, the RF output is relatively low most of the time, but occasionally jumps to peak levels. A typical RF power amplifier operates most efficiently at high power or low PAR. As the power level is reduced, the efficiency decreases exponentially, and the average efficiency is less than optimal with high PAR.

SUMMARY

[0003] In described examples, at least one tone is generated. An output signal is generated in response to an input signal and the at least one tone. The output signal is modulated. The input signal and the at least one tone are represented in the modulated output signal. The at least one tone is outside a bandwidth of the input signal as represented in the modulated output signal. The modulated output signal is amplified. The at least one tone in the amplified signal is attenuated after the amplifying.

BRIEF DESCRIPTION OF THE DRAWINGS

[0004] FIG. 1 is a block diagram of an embodiment of a transmitter.

[0005] FIG. 2 is an example graph of an output signal from a modulator of FIG. 1.

[0006] FIG. 3 is a block diagram of another embodiment of a transmitter.

[0007] FIG. 4 is a flow chart of operation of a processor of FIG. 3.

DETAILED DESCRIPTION OF EXAMPLE EMBODIMENTS

[0008] The transmitters and methods disclosed herein introduce tones (such as sidebands) outside a modulated bandwidth of an input signal, in order to reduce the PAR of power amplifiers. In some embodiments, the tones are filtered out prior to transmission. By introducing the tones, the ratio of the average power transmitted to the average power consumed increases, which increases efficiency of the transmitter.

[0009] FIG. 1 is a block diagram of an embodiment of a transmitter 100. The transmitter 100

includes an adder 102, which receives an input (such as a baseband) signal 104. The adder 102 is part of a sigma-delta modulator (“SDM”) 106. The SDM 106 operates at a sampling frequency F_s . Also, the SDM 106 includes an adder 110, which is coupled to a tone generator 114. In a first embodiment, the SDM 106 and the tone generator 114 are distinct from one another. In a second embodiment, the SDM 106 and the tone generator 114 are integral with one another. The SDM 106 generates its output signal in response to the input signal 104, and the tone generator 114 generates at least one tone.

[0010] The SDM 106 is coupled (e.g., connected) to a transmission modulator 120 (such as an RF modulator), which modulates (or otherwise converts) the output signal from the SDM 106 into a signal for transmission. In some embodiments, the modulator 120 generates a carrier for performing such modulation at a frequency F_c , which is higher than the frequency F_s .

[0011] The modulated output signal from the modulator 120 is received by a power amplifier 122. An example of the amplifier 122 includes a switching power amplifier, such as class D and class E amplifiers. Another example of the amplifier 122 includes a linear amplifier, such as class A, class B, and class A/B amplifiers. The amplifier 122 amplifies the modulated output signal from the modulator 120, including the tones from the tone generator 114. The amplified signal is output from the amplifier 122 to a filter 124. In some embodiments, the filter 124 is incorporated into a conventional antenna. The filter 124 attenuates the tones (in the amplified signal) from the tone generator 114.

[0012] In a first example, the input signal 104 is an audio signal. In a second example, the input signal 104 is a component of a complex signal. In one example, the input signal 104 is the I component of a quadrature signal. The SDM 106 includes a loop filter $H(z)$, which receives and filters an output signal from the adder 102. An output signal from the filter $H(z)$ is received by the adder 110, which adds such output signal to the tones from the tone generator 114. An output signal from the adder 110 is received and quantized by a quantizer 126. The adder 102 receives the quantized output signal from the quantizer 126 (which is the output signal from the SDM 106), and subtracts such quantized output signal from the input signal 104. In one embodiment, even though respective signals from the adder 102, the filter $H(z)$, the adder 110, and the quantizer 126 are mentioned as different signals, the SDM 106 adds the tones and generates its output signal in one step.

[0013] FIG. 2 is an example graph of the modulated output signal from the modulator 120. As

shown in FIG. 2, the modulator 120 generates a carrier 150 for modulating at the frequency F_c . The input signal 104 and the tones from the tone generator 114 are represented in the modulated output signal from the modulator 120. For example, as represented in the modulated output signal of FIG. 2, frequencies of first and second tones 152 and 154 (such as from the tone generator 114) are: (a) outside a bandwidth 156 (“modulated bandwidth”) of the input signal 104; and (b) within a frequency range of the amplifier 122, so those tones are amplified by the amplifier 122. For example, the first tone 152 is located at a frequency that is below the frequency F_c , and the second tone 154 is located at a frequency that is above the frequency F_c . In some embodiments, the tones 152 and 154 are substantially symmetric around the frequency F_c , so they are approximately the same distance from the frequency F_c .

[0014] In the embodiments described herein, the modulator 120 performs either LINC modulation or AMO operations to modulate the output signal from the SDM 106. Accordingly, examples of the modulator 120 include a LINC/AMO (linear modulation using non-linear components/asymmetrical multi-level out-phasing) out-phasing digital modulator and an RF upconverter. In other embodiments, the modulator 120 includes conventional quadrature modulators.

[0015] After the modulator 120 modulates the output signal from the SDM 106 at the carrier frequency F_c , the modulated output signal from the modulator 120 is amplified by the amplifier 122. The transmitter 100 keeps the tones 152 and 154 within the frequency range of the amplifier 122, so that: (a) the average operating power of the amplifier 122 is greater than it would otherwise be without the tones 152 and 154; and (b) the peak to average ratio (PAR) is lower. In that manner, the amplifier 122 operates at a higher average power, so it operates more efficiently.

[0016] In the embodiment of FIG. 1, the filter 124 is a band-pass filter that passes signals within the modulated bandwidth and attenuates the tones 152 and 154. In some embodiments, the filter 124 is incorporated into the load of an antenna and includes load components, such as resistors and inductors, for matching the antenna’s impedance to other circuits. In those embodiments, the loading of the antenna additionally attenuates the tones 152 and 154. The result is that the amplifier 122 operates at a higher efficiency by amplifying the tones 152 and 154, but the tones 152 and 154 are attenuated so that they are not transmitted.

[0017] In a first embodiment, the tone generator 114 generates a square wave, which produces

single tones 152 and 154. In a second embodiment, the tone generator 114 generates other waveforms, such as a triangular wave or a sine wave. In some embodiments, waveforms of the tones from the tone generator 114 are periodic and produce dithering. Such various waveforms are capable of generating different tones or harmonics, suitable to accommodate a variety of design choices.

[0018] In some embodiments, the tone generator 114 generates the tones in response to the input signal 104. In those embodiments, the input signal 104 is mixed to the sideband frequencies for addition by the adder 110. In one example, those tones have wider bandwidths than tones that would have otherwise been generated by the tone generator 114. In one version of such embodiments, the filter 124 is a narrow band-pass filter that attenuates the tones (in the amplified signal) from the tone generator 114. In some embodiments, the transmitter 100 includes additional filters for performing such attenuation.

[0019] FIG. 3 is a block diagram of another embodiment of a transmitter 200. The transmitter 200 operates in a similar manner as the transmitter 100, except that the transmitter 200 processes two components of a complex signal, such as I and Q components. The transmitter 200 has an input stage 202, which receives a complex signal. In this example, the input stage 202 receives: (a) a first signal 204 that represents the I component of the complex signal; and (b) a second signal 206 that represents the Q component of the complex signal. The first signal 204 is received by a first portion (“I processor”) 210 of the transmitter 200, which processes the I component of the complex signal. The second signal 206 is received by a second portion (“Q processor”) 212 of the transmitter 200, which processes the Q component of the complex signal.

[0020] With reference to the I processor 210, the first signal 204 is received by a sampling circuit 214, which samples the first signal 204. In some embodiments, the sampling circuit 214 generates and outputs a pulse coded or quantized signal that is representative of the I component.

[0021] The output signal from the sampling circuit 214 is received by an SDM 216. In some embodiments, the SDM 216 is referred to as an oversampling modulator that has a sampling rate greater than a Nyquist sampling rate. In some embodiments, the sampling rate is many factors greater than the Nyquist sampling rate. Accordingly, high frequency nonlinearities are typically outside the modulated bandwidth. A tone generator 220 generates the tones described hereinabove. The SDM 216 includes a loop filter 222, which receives and filters an output signal from an adder 223. An output signal from the filter 222 is received by an adder 224, which adds

such output signal to the tones from the tone generator 220. An output signal from the adder 224 is received and quantized by a quantizer 226. The adder 223 receives the quantized output signal from the quantizer 226 (which is the output signal from the SDM 216), and subtracts such quantized output signal from the signal that the SDM 216 receives from the circuit 214.

[0022] The Q processor 212 has the same types of components and performs the same types of operations as the I processor 210, except that the Q processor 212 operates in response to the second signal 206 (instead of the first signal 204). Accordingly, the Q processor 212 includes a sampling circuit 230 and an SDM 232. In the embodiment of FIG. 3, the output signal from the SDM 216 is the sampled, filtered and quantized I component of the first signal 204 with added tones from the tone generator 220 (“I signal”). Likewise, the output signal from the SDM 232 is the sampled, filtered and quantized Q component of the second signal 206 with added tones from a tone generator that is coupled to the SDM 232 (“Q signal”). In one embodiment, the SDM 232 is substantially identical to the SDM 216, and the tone generators are substantially identical to one another.

[0023] The I and Q signals are received by a transmission modulator 238. In some embodiments, the modulator 238 is substantially similar to the modulator 120 of FIG. 1. Accordingly, examples of the modulator 238 include LINC (linear using non-linear components) and AMO (asymmetrical multi-level out-phasing) modulators.

[0024] The modulator 238 includes a signal component separator 240 that is coupled to the SDMs 216 and 232. In the embodiment of FIG. 3, the separator 240 converts the I and Q signals (from the SDMs 216 and 232 respectively) into two phase signals (namely S1 and S2) representing S1 and S2 phases of the I and Q signals. Accordingly, the separator 240 generates: the S1 phase signal as a voltage representative of the S1 phase; and the S2 phase signal as a voltage representative of the S2 phase.

[0025] The S1 phase signal is received and modulated by a first phase modulator 242. Similarly, the S2 phase signal is received and modulated by a second phase modulator 244. Accordingly, the phase modulators 242 and 244 generate and output those phase modulated signals, which include the tones. In the illustrative embodiments, the signals output by the SDMs 216 and 232 are quantized, so the S1 and S2 phase signals are quantized to a predetermined number of phases.

[0026] The modulated output signal from the first phase modulator 242 is received by a first

amplifier 246, and the modulated output signal from the second phase modulator 244 is received by a second amplifier 248. In some embodiments, the amplifiers 246 and 248 are power amplifiers and amplify those signals to a power level for radio frequency (RF) transmission. As represented in the modulated output signals from the modulator 238, frequencies of the tones from the tone generators (such as from the tone generator 220) are: (a) outside modulated bandwidths of the signals 204 and 206; and (b) within frequency ranges of the amplifiers 246 and 248. Accordingly, the average operating power of the amplifiers 246 and 248 is greater than it would otherwise be without the tones being amplified, so the peak to average ratio (PAR) is lower. For example, the ratio of the average power transmitted to the average power consumed increases. In that manner, the amplifiers 246 and 248 operate at a higher average power, so they operate more efficiently.

[0027] The amplified signals from the amplifiers 246 and 248 are received by a power combiner 250, which combines those amplified signals. In some embodiments, the power combiner 250 includes a filter, such as a band-pass filter to attenuate the tones so they are not transmitted. The power combiner 250 outputs the combined signal to an antenna (not shown) that is matched with a load R_L . The load R_L provides impedance matching for the antenna or other transmitting device. In some embodiments, the load R_L (along with other impedance matching device) provides additional filtering.

[0028] FIG. 4 is a flow chart 300 of operation of the I processor 210, which is a representative one of the processors 210 and 212. At a step 302, a first component of an input signal is received. At a step 304, a first signal is generated by modulating the first component and adding at least one tone thereto. At a step 306, a second signal is generated by modulating the first signal. At a step 308, the second signal is amplified by an amplifier, wherein the at least one tone increases the average power of the amplifier. At a step 310, the at least one tone is filtered from the amplified second signal after the amplifying.

[0029] With reference to FIGS. 1 and 2, the energy wasted on the tones 152 and 154 at the power amplifier 122 has been found to be negligible compared to the energy saved on transmitting the carrier 150 at the power amplifier. In some embodiments, less than 1% of the total transmitted power is wasted on the tones 152 and 154. However the tones 152 and 154 reshape the overall signal distribution such that the power amplifier 122 mostly operates at larger amplitude values, which correspond to a more efficient operating point. In some embodiments,

the average efficiency of the power amplifier 122 is increased from 10% to 50% by adding the tones 152 and 154. For example, if the power saved is equal to 0.9 times the average power minus 0.5 times the average power, the power saved is equal to 0.4 times the average power. If the extra power used to generate the tones 152 and 154 is equal to one percent of the average power, the extra power is equal to 0.01 times the average power. Therefore, the total power saved is equal to 0.39 times the average power.

[0030] Modifications are possible in the described embodiments, and other embodiments are possible, within the scope of the claims.

CLAIMS

What is claimed is:

1. A transmitter, comprising:
 - a tone generator for generating at least one tone;
 - a sigma-delta modulator for generating an output signal in response to an input signal and the at least one tone;
 - a transmission modulator for modulating the output signal, wherein: the input signal and the at least one tone are represented in the modulated output signal, and the at least one tone is outside a bandwidth of the input signal as represented in the modulated output signal;
 - an amplifier for amplifying the modulated output signal; and
 - a filter coupled to the amplifier for attenuating the at least one tone in the amplified signal after the amplifying.
2. The transmitter of claim 1, wherein generating the output signal includes sigma-delta modulation of the input signal.
3. The transmitter of claim 1, wherein the transmission modulator includes a phase modulator for modulating the output signal.
4. The transmitter of claim 1, wherein the tone generator is a first tone generator, the at least one tone is at least one first tone, the sigma-delta modulator is a first sigma-delta modulator, the input signal is a first component of a complex signal, and the output signal is a first output signal, and comprising:
 - a second tone generator for generating at least one second tone; and
 - a second sigma-delta modulator for generating a second output signal in response to a second component of the complex signal and the at least one second tone;
 - wherein the transmission modulator is for modulating the second output signal, and wherein: the second component and the at least one second tone are represented in the modulated second output signal, and the at least one second tone is outside a bandwidth of the second component as represented in the modulated second output signal; and
 - wherein the transmission modulator includes a separator for converting the first and second output signals into first and second phase signals representing first and second phases thereof.
5. The transmitter of claim 4, wherein the transmission modulator includes:
 - a first phase modulator for modulating the first phase signal; and
 - a second phase modulator for modulating

the second phase signal.

6. The transmitter of claim 5, wherein the amplifier includes: a first amplifier for amplifying the modulated first phase signal; and a second amplifier for amplifying the modulated second phase signal; and the filter is for attenuating the tones in the amplified signals after the amplifying.

7. The transmitter of claim 1, wherein the sigma-delta modulator is for receiving the input signal at a first frequency, and wherein the transmission modulator is for modulating the output signal at a second frequency higher than the first frequency.

8. The transmitter of claim 1, wherein the at least one tone includes: a low tone below the bandwidth; and a high tone above the bandwidth.

9. The transmitter of claim 8, wherein the modulated tones are within a frequency range of the amplifier.

10. The transmitter of claim 1, wherein the at least one tone is for increasing an average operating power of the amplifier.

11. A method, comprising:

generating at least one tone;

generating an output signal in response to an input signal and the at least one tone;

modulating the output signal, wherein: the input signal and the at least one tone are represented in the modulated output signal, and the at least one tone is outside a bandwidth of the input signal as represented in the modulated output signal;

amplifying the modulated output signal; and

attenuating the at least one tone in the amplified signal after the amplifying.

12. The method of claim 11, wherein generating the output signal includes sigma-delta modulation of the input signal.

13. The method of claim 11, wherein modulating the output signal includes modulating the output signal with a phase modulator.

14. The method of claim 11, wherein the at least one tone is at least one first tone, the input signal is a first component of a complex signal, and the output signal is a first output signal, and comprising: generating at least one second tone; generating a second output signal in response to a second component of the complex signal and the at least one second tone; and modulating the second output signal;

wherein the second component and the at least one second tone are represented in the modulated second output signal, and the at least one second tone is outside a bandwidth of the second component as represented in the modulated second output signal; and wherein modulating the first and second output signals includes converting them into first and second phase signals representing first and second phases thereof.

15. The method of claim 14, wherein modulating the first and second output signals includes: modulating the first phase signal with a first phase modulator; and modulating the second phase signal with a second phase modulator.

16. The method of claim 15, wherein amplifying the modulated first output signal includes amplifying the modulated first phase signal with a first amplifier, and comprising: amplifying the modulated second phase signal with a second amplifier; and attenuating the tones in the amplified signals after the amplifying.

17. The method of claim 11, wherein generating the output signal includes receiving the input signal at a first frequency, and wherein modulating the output signal includes modulating the output signal at a second frequency higher than the first frequency.

18. The method of claim 11, wherein the at least one tone includes: a low tone below the bandwidth; and a high tone above the bandwidth.

19. The method of claim 18, wherein amplifying the modulated output signal includes amplifying the modulated output signal with an amplifier, and wherein the modulated tones are within a frequency range of the amplifier.

20. The method of claim 11, wherein amplifying the modulated output signal includes amplifying the modulated output signal with an amplifier, and wherein the at least one tone is for increasing an average operating power of the amplifier.

21. A transmitter, comprising:

a tone generator for generating: at least one first tone; and at least one second tone;

a first sigma-delta modulator for generating a first output signal in response to a first component of a complex signal and the at least one first tone;

a second sigma-delta modulator for generating a second output signal in response to a second component of the complex signal and the at least one second tone;

a phase modulator for modulating the first and second output signals, wherein: the first component and the at least one first tone are represented in the modulated first output signal, and

the at least one first tone is outside a bandwidth of the first component as represented in the modulated first output signal; and the second component and the at least one second tone are represented in the modulated second output signal, and the at least one second tone is outside a bandwidth of the second component as represented in the modulated second output signal;

an amplifier for amplifying the modulated first and second output signals; and

a filter coupled to the amplifier for attenuating the tones in the amplified signals after the amplifying.

22. The transmitter of claim 21, wherein the phase modulator includes a separator for converting the first and second output signals into first and second phase signals representing first and second phases thereof.

23. The transmitter of claim 22, wherein the phase modulator includes: a first phase modulator for modulating the first phase signal; and a second phase modulator for modulating the second phase signal.

24. The transmitter of claim 21, wherein the first and second sigma-delta modulators are for receiving the first and second components at a first frequency, and the phase modulator is for modulating the first and second output signals at a second frequency higher than the first frequency.

25. The transmitter of claim 21, wherein the at least one first tone includes: a low tone below the bandwidth of the first component; and a high tone above the bandwidth of the first component.

26. The transmitter of claim 21, wherein the modulated tones are within a frequency range of the amplifier.

27. The transmitter of claim 21, wherein the tones are for increasing an average operating power of the amplifier.

28. The transmitter of claim 21, wherein the tone generator includes: a first tone generator for generating the at least one first tone; and a second tone generator for generating the at least one second tone.

29. The transmitter of claim 21, wherein the amplifier includes: a first amplifier for amplifying the modulated first output signal; and a second amplifier for amplifying the modulated second output signal.

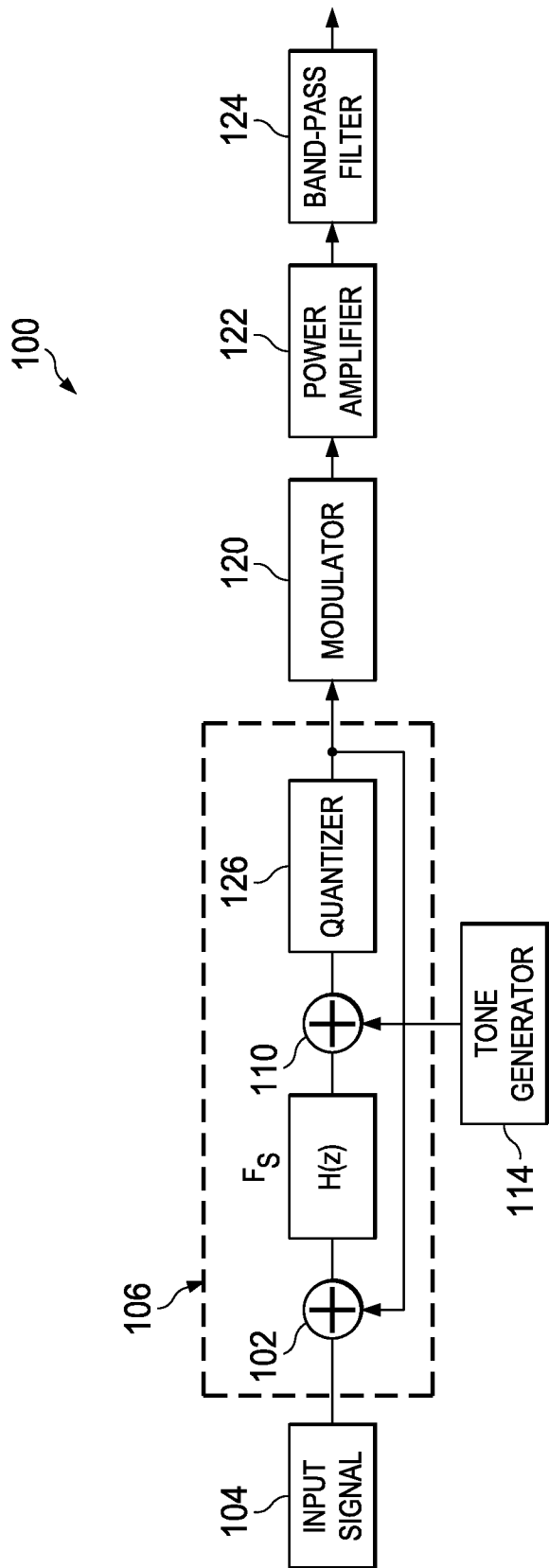


FIG. 1

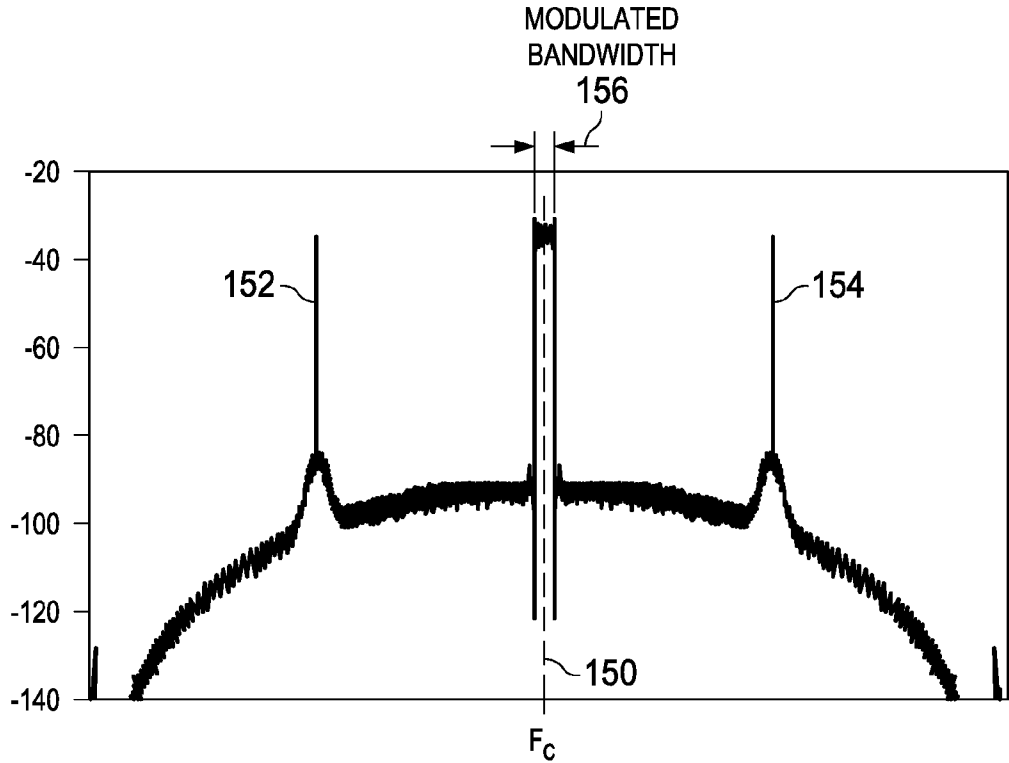


FIG. 2

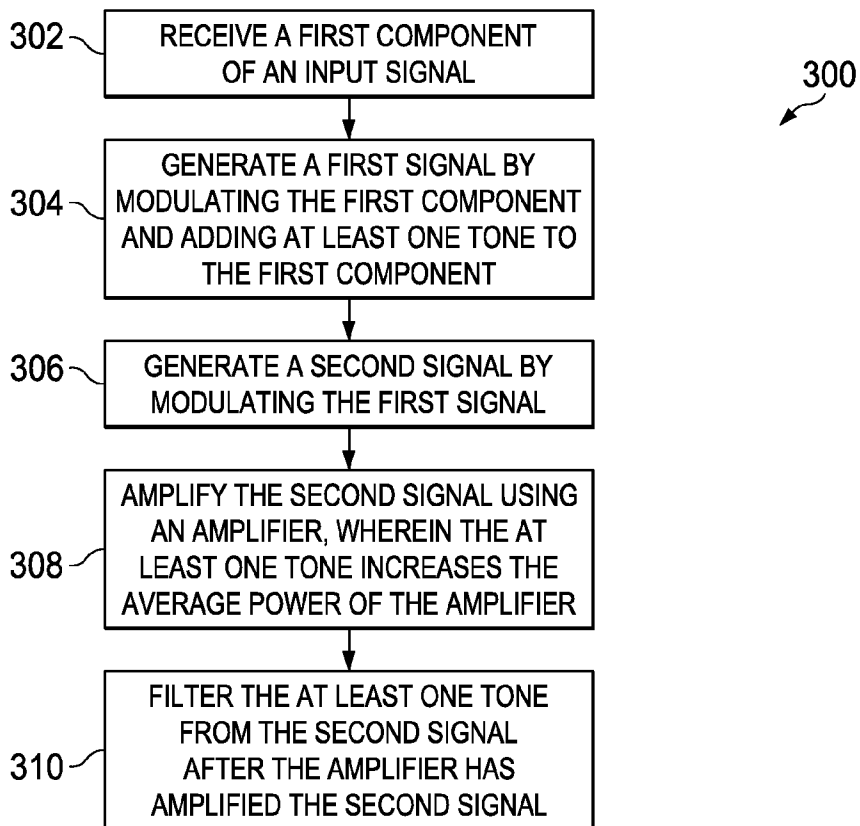


FIG. 4

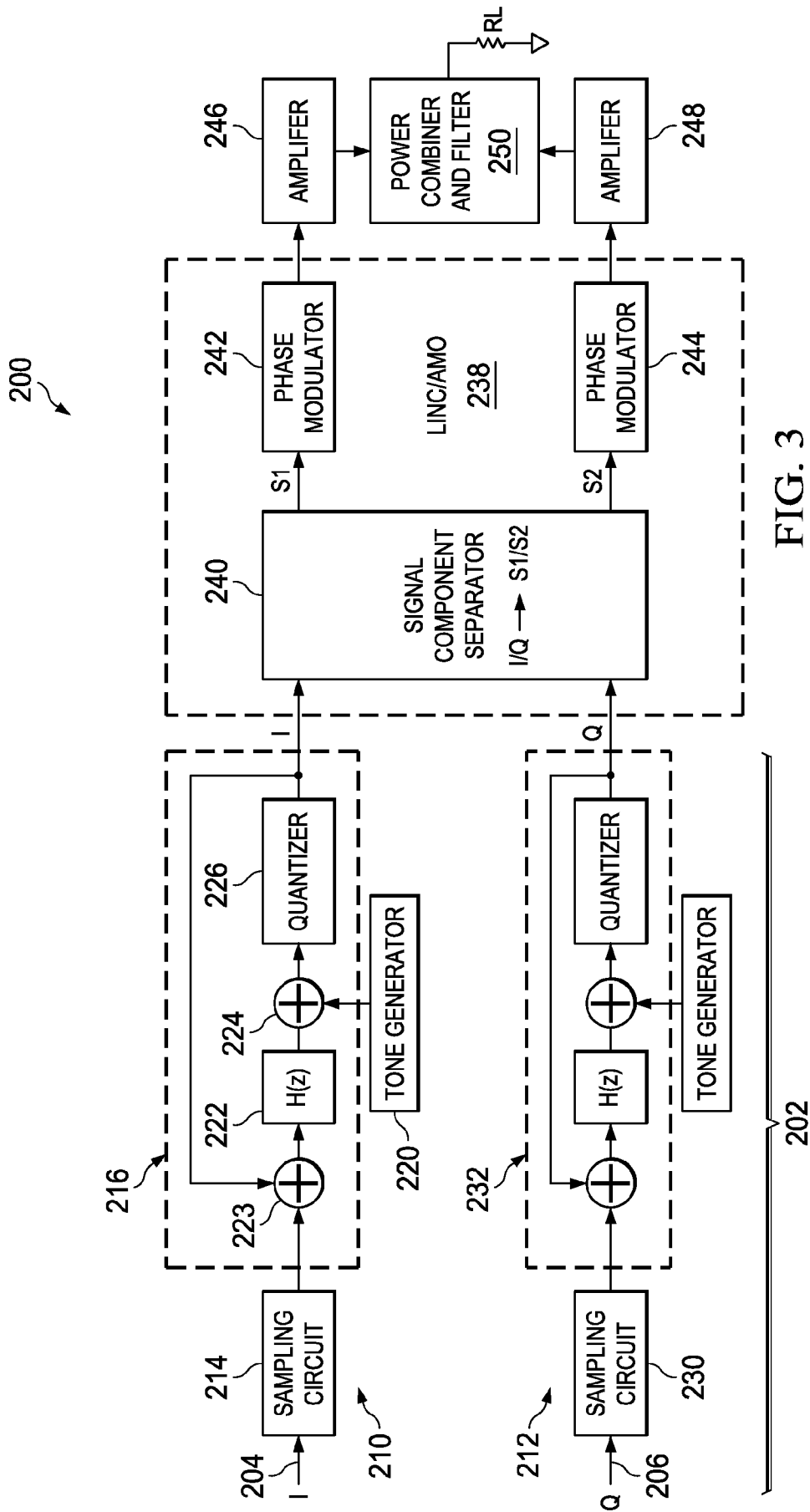


FIG. 3

INTERNATIONAL SEARCH REPORT

International application No.

PCT/US 2014/058281

A. CLASSIFICATION OF SUBJECT MATTER		
<i>H04B 1/02 (2006.01)</i> <i>H03F 3/189 (2006.01)</i>		
According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED		
Minimum documentation searched (classification system followed by classification symbols)		
H04B 1/00, 1/02, 1/04, 1/38, 1/40, H03F 3/00, 3/189, 3/60, 3/72, H03B 1/00, H03C 3/00, H03H 1/00, 1/02, H03G 1/00, H04K 1/00, 1/02, H04L 25/00, 25/38, 25/40, 25/52, 27/00, G10H 5/00, 5/02, 5/06		
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched		
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)		
DWPI, Espacenet, K-PION, PAJ, SIPO, PatSearch (RUPTO internal), RUPTO, USPTO, Patentscope, Information Retrieval System of FIPS		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5490172 A (AIRNET COMMUNICATIONS CORPORATION) 06.02.1996, abstract, col. 2, lines 47-67, col. 3, line 1-23, 60-66, col. 4, lines 19-23, col. 5, lines 65-67, col. 6, lines 6-67, col. 7, lines 1-6, col. 8, lines 42-44, claim 1	11, 20
Y		1-10, 12-19, 21-29
Y	US 2012/0064839 A1 (JONG-RYUL YANG et al.) 15.03.2012, paragraphs [0009], [0029]	1-10, 12-16, 21-29
Y	US 2010/0018383 A1 (FREESCALE SEMICONDUCTOR, INC.) 28.01.2010, paragraph [0013]	4-6, 14-16, 22, 23, 28
Y	US 2009/0225897 A1 (KENJI TOYOTA et al.) 10.09.2009, claim 1	5, 6, 15, 16, 23
<input checked="" type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.		
* Special categories of cited documents:	“T”	later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
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Date of the actual completion of the international search	Date of mailing of the international search report	
17 March 2015 (17.03.2015)	19 March 2015 (19.03.2015)	
Name and mailing address of the ISA/RU: Federal Institute of Industrial Property, Berezhkovskaya nab., 30-1, Moscow, G-59, GSP-3, Russia, 125993 Facsimile No: (8-495) 531-63-18, (8-499) 243-33-37	Authorized officer T. Kisileva Telephone No. (495)531-64-81	

INTERNATIONAL SEARCH REPORT

International application No.

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C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	WO 2000/018004 A1 (QUALCOMM INCORPORATED) 30.03.2000, p. 10, lines 8-22	6, 16, 29
Y	US 2012/0147931 A1 (HITACHI CABLE, LTD.) 14.06.2012, paragraph [0014], claim 1	7, 17, 24
Y	US 6708004 B1 (TYCO TELECOMMUNICATIONS (US) INC.) 16.03.2004, col. 4, lines 19-34	8, 9, 18, 19, 25