(19) World Intellectual Property **Organization**

International Bureau



(43) International Publication Date 10 March 2005 (10.03.2005)

PCT

(10) International Publication Number WO 2005/022951 A3

(51) International Patent Classification⁷: G10L 21/02

H04R 3/00,

(21) International Application Number:

PCT/US2004/025660

- (22) International Filing Date: 4 August 2004 (04.08.2004)
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data:

10/650,409

27 August 2003 (27.08.2003)

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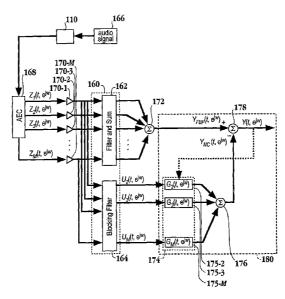
- (81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL. AM. AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.
- (84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

with international search report

[Continued on next page]

(54) Title: AUDIO INPUT SYSTEM



(57) Abstract: A method for reducing noise associated with an audio signal received through a microphone sensor array is provided. A first filter enhances a target signal component of the audio signal. A second filter is blocking the target signal component. The output of the first filter and the output of the second filter are combined in a manner to reduce noise without distorting the target signal. An acoustic set-up associated with the audio signal is periodically monitored. The first filter and the second filter are both calibrated based upon the acoustic set-up. Preferably the calibration of the filters includes a blind source separation scheme using second order statistics calculation. Preferably the first filter is an adaptive beam-forming module enhancing a target signal component and the second filter is an inverse adaptive beam-forming module blocking the target signal component. The system can be included in a video game controller.



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before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(88) Date of publication of the international search report:

28 April 2005

INTERNATIONAL SEARCH REPORT

tional Application No

PCT/US2004/025660 A. CLASSIFICATION OF SUBJECT MATTER IPC 7 H04R3/00 G10L G10L21/02 According to International Patent Classification (IPC) or to both national classification and IPC **B. FIELDS SEARCHED** Minimum documentation searched (classification system followed by classification symbols) IPC 7 HO4R G10L Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Electronic data base consulted during the international search (name of data base and, where practical, search terms used) EPO-Internal, INSPEC C. DOCUMENTS CONSIDERED TO BE RELEVANT Category 9 Citation of document, with indication, where appropriate, of the relevant passages Relevant to claim No. γ US 6 339 758 B1 (KANAZAWA HIROSHI ET AL) 1 - 3915 January 2002 (2002-01-15) figures 1,2A,2B column 2, line 33 - line 52 Υ HOSHUYAMA O ET AL: "A ROBUST GENERALIZED 1 - 39SIDELOBE CANCELLER WITH A BLOCKING MATRIX USING LEAKY ADAPTIVE FILTERS" ELECTRONICS & COMMUNICATIONS IN JAPAN, PART III - FUNDAMENTAL ELECTRONIC SCIENCE, SCRIPTA TECHNICA. NEW YORK, US, vol. 80, no. 8, August 1997 (1997-08), pages 56-65, XP000736573 ISSN: 1042-0967 figure 1 paragraph '02.1! -/--Further documents are listed in the continuation of box C. Patent family members are listed in annex. ° Special categories of cited documents: "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the "A" document defining the general state of the art which is not considered to be of particular relevance invention "E" earlier document but published on or after the international "X" document of particular relevance; the claimed invention filing date cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled *O* document referring to an oral disclosure, use, exhibition or document published prior to the international filing date but later than the priority date claimed in the art. "&" document member of the same patent family Date of the actual completion of the international search Date of mailing of the international search report 04/03/2005 16 February 2005 Name and mailing address of the ISA Authorized officer European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,

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