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— *as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii))*

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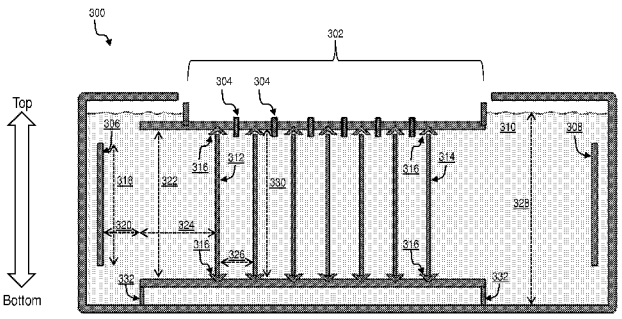


FIG. 9

(57) Abstract: This disclosure enables high-productivity controlled fabrication of uniform porous semiconductor layers (made of single layer or multi-layer porous semiconductors such as porous silicon, comprising single porosity or multi-porosity layers). Some applications include fabrication of MEMS separation and sacrificial layers for die detachment and MEMS device fabrication, membrane formation and shallow trench isolation (STI) porous silicon (using porous silicon formation with an optimal porosity and its subsequent oxidation). Further, this disclosure is applicable to the general fields of photovoltaics, MEMS, including sensors and actuators, stand-alone, or integrated with integrated semiconductor microelectronics, semiconductor microelectronics chips and optoelectronics.

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A. CLASSIFICATION OF SUBJECT MATTER

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According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
H01L 21/3063; C25B 9/00; C25C 7/00; H01L 21/306; C25D 17/10; C25D 17/06; C25D 21/00; H01L 21/76; H01L 21/02

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched
Korean utility models and applications for utility models
Japanese utility models and applications for utility models

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)
eKOMPASS(KIPO internal) & Keywords: anodizing, electrolyte, anode, cathode, wafer, seal and vent

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 6417069 B1 (KIYOFUMI SAKAGUCHI et al.) 09 July 2002 See abstract; column 5, lines 1-20, column 14, line 14 - column 17, line 16 and figures 3-5.	1-20
A	US 5458755 A (YASUTOMO FUJIYAMA et al.) 17 October 1995 See abstract; column 3, lines 40-48, column 5, line 28 - column 9, line 57 and figures 8-12.	1-20
A	US 2004-0035532 A1 (SOON-JONG JUNG) 26 February 2004 See paragraphs [0028]-[0035] and figures 5-6.	1-20
A	US 2003-0121773 A1 (SATOSHI MATSUMURA et al.) 03 July 2003 See abstract; paragraphs [0101]-[0151] and figures 2-12.	1-20
A	US 6726815 B1 (HANS ARTMANN et al.) 27 April 2004 See abstract; column 4, line 38 - column 6, line 67 and figures 1-3.	1-20

☐ Further documents are listed in the continuation of Box C.

☒ See patent family annex.

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"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents,such combination being obvious to a person skilled in the art
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Information on patent family members

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