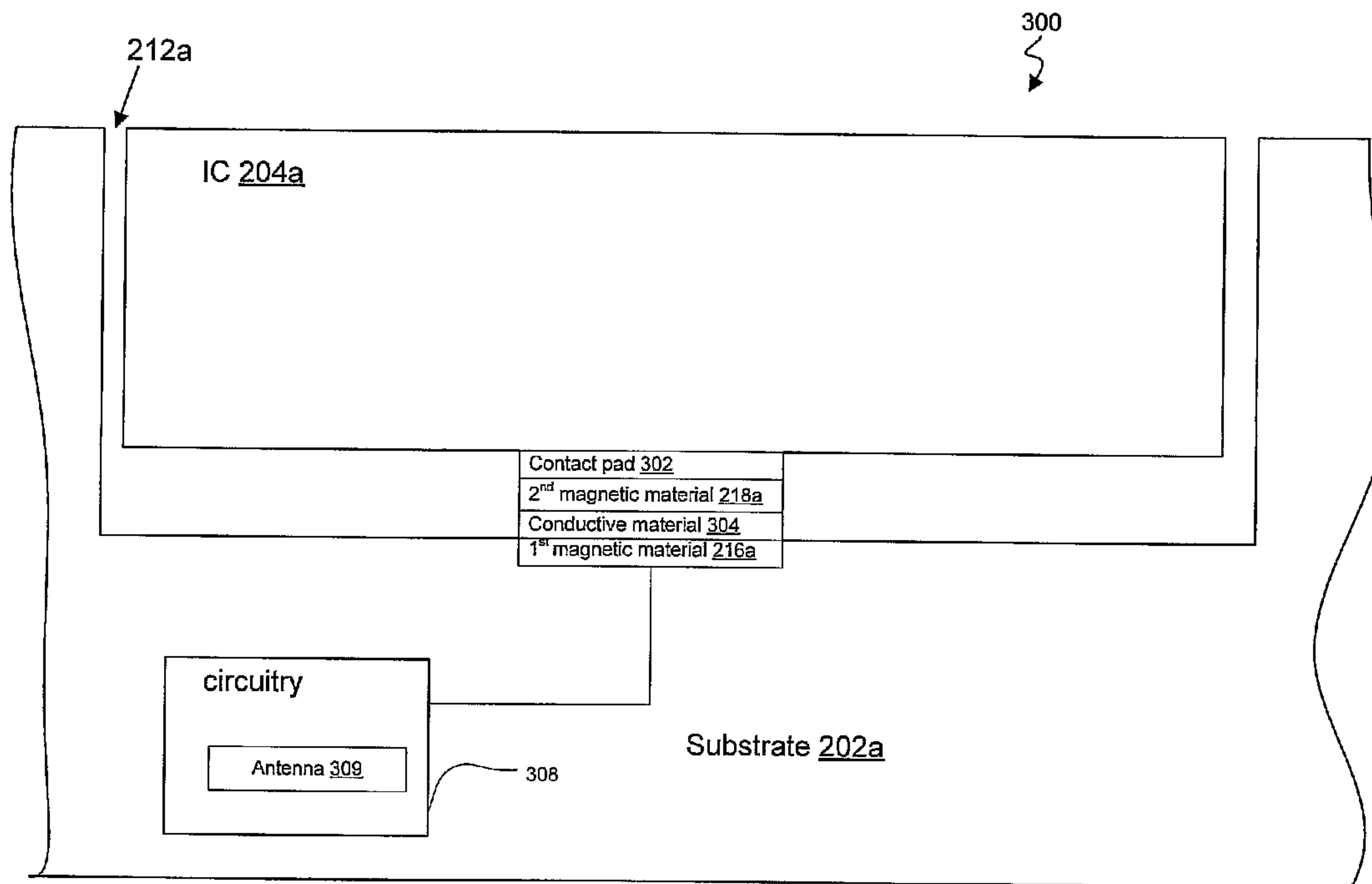




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(57) **Abrégé/Abstract:**

An integrated circuit package may include a substrate and an integrated circuit. The substrate may include at least one region, and a first magnetic material associated with the at least one region. The integrated circuit may have a second magnetic material associated therewith. The second magnetic material may be attracted to the first magnetic material to couple the integrated circuit to the at least one region of the substrate. The IC package may be utilized in an RFID tag of an RFID system. An associated method for assembling an integrated circuit to a substrate is also provided.



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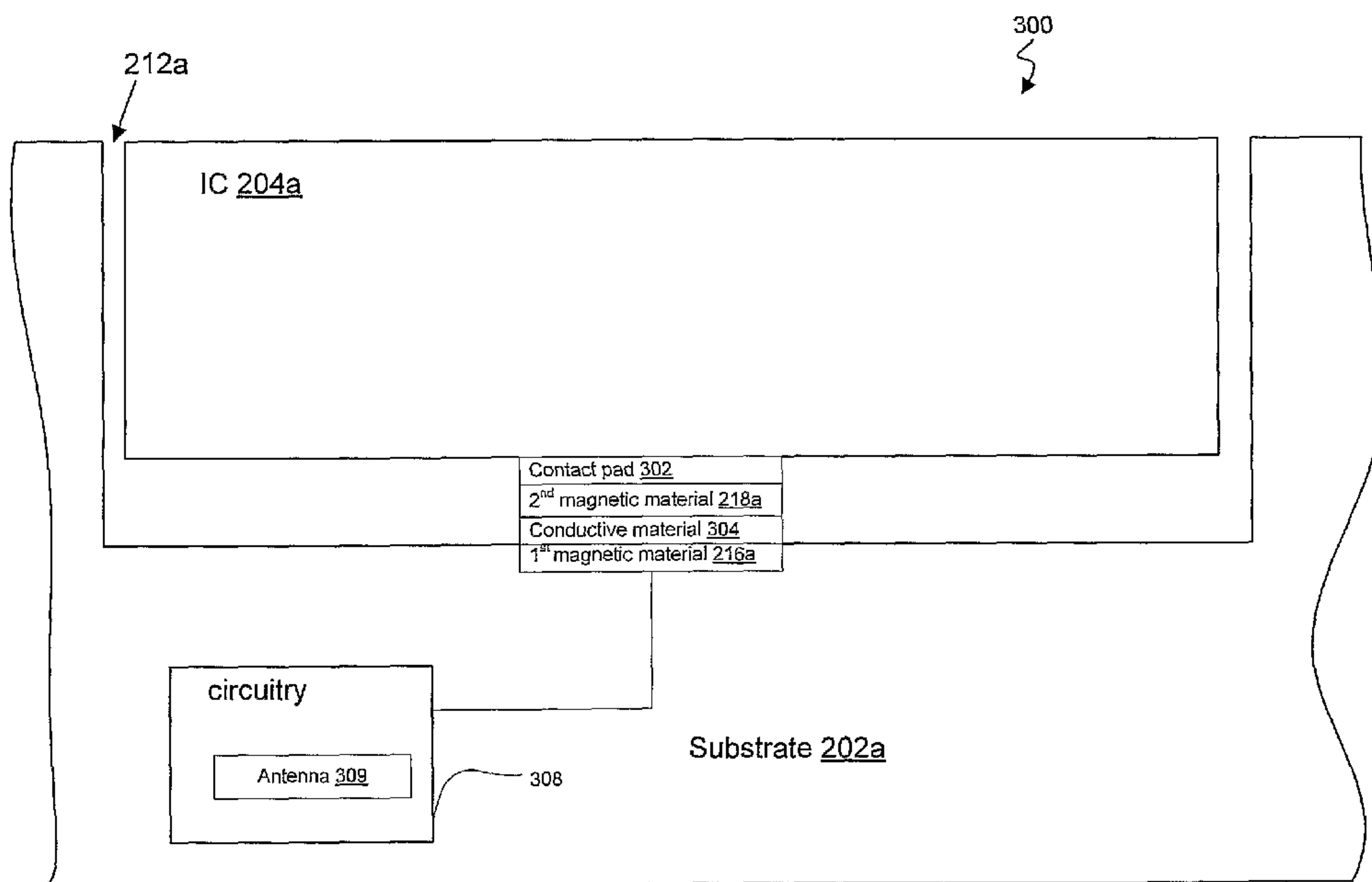
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(54) Title: MAGNETIC SELF-ASSEMBLY FOR INTEGRATED CIRCUIT PACKAGES



(57) Abstract: An integrated circuit package may include a substrate and an integrated circuit. The substrate may include at least one region, and a first magnetic material associated with the at least one region. The integrated circuit may have a second magnetic material associated therewith. The second magnetic material may be attracted to the first magnetic material to couple the integrated circuit to the at least one region of the substrate. The IC package may be utilized in an RFID tag of an RFID system. An associated method for assembling an integrated circuit to a substrate is also provided.

## MAGNETIC SELF-ASSEMBLY FOR INTEGRATED CIRCUIT PACKAGES

### FIELD OF THE INVENTION

[001] This disclosure relates to integrated circuits, and in particular, to integrated  
5 circuit packages formed using magnetic self-assembly. The integrated circuit packages  
may be utilized in a variety of systems such as in a radio frequency identification (RFID)  
system.

### BACKGROUND OF THE INVENTION

10 [002] Integrated circuits (ICs) are utilized in a variety of electronic devices to  
perform a myriad of functions. Integrated circuits may be packaged with a substrate to  
protect the IC and to provide electrical connections from the substrate to the contacts of  
the IC. In general, functionality provided by ICs has increased over the years while IC  
costs have decreased. However, the costs associated with packaging an IC with a  
15 substrate have not experienced a comparative cost reduction. In fact, packaging costs may  
even increase in the future given the trend towards reduced IC sizes and complicated  
interconnection schemes.

[003] One conventional method of connecting an IC to a substrate involves placing  
the IC on the substrate, either manually or using a robotic arm. This conventional process  
20 requires human intervention and/or complex robotic machinery and becomes increasingly  
difficult as IC sizes decrease. This conventional process also usually requires a wire-  
bonding process to electrically connect contact pads of the IC to other components or  
terminals.

[004] Another conventional method of connecting an IC to a substrate involves use  
25 of fluid flow. The fluid flow method involves creation of a plurality of shaped recesses in  
a substrate configured to mate with correspondingly shaped ICs. A slurry containing a  
plurality of the shaped ICs is directed over the substrate, and the ICs fall into the recesses  
in the substrate. The substrate may be examined for empty recesses and other devices  
such as a robotic arm may then place ICs into the empty regions. This fluid flow approach  
30 requires formation of particularly shaped recesses in the substrate, which requires  
additional precision and cost. The ICs must also be precisely manufactured with  
complimentary mating geometries to mate with the recesses. Furthermore, the fluid flow

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approach can leave an excessive number of empty recesses on the substrate, since gravity may be the only force urging the ICs into the recesses.

[005] Accordingly, there is a need for a magnetic self-assembly for integrated circuit packages to simplify IC package manufacturing and reduce manufacturing costs. Such a method may be used in a variety of applications and systems. One application may be to manufacture RFID tags at reduced costs compared to conventional methods. Such RFID tags may be used in an RFID system for tagging retail merchandise where the cost of the RFID tag is an important factor.

### SUMMARY OF THE INVENTION

10 According to one aspect of the present invention, there is provided an integrated circuit package comprising: a substrate comprising: circuitry; at least one region; and a first magnetic material associated with said at least one region; and an integrated circuit, said integrated circuit having: a second magnetic material associated therewith, said second magnetic material being attracted to said first  
15 magnetic material to electrically and magnetically couple said integrated circuit to said at least one region of said substrate; and at least one contact pad, said at least one contact pad being in direct electrical contact with said circuitry of said substrate to establish an electrical connection between said integrated circuit and circuitry of said substrate through the first magnetic material and the second magnetic material,  
20 wherein said second magnetic material is disposed on said at least one contact pad.

According to another aspect of the present invention, there is provided a radio frequency identification (RFID) tag comprising: a substrate comprising: at least one region, and a first magnetic material associated with said at least one region, said substrate further comprising an antenna for transmitting signals to and  
25 receiving signals from an associated RFID reader; and an integrated circuit having: a second magnetic material associated therewith, said second magnetic material attracted to said first magnetic material to electrically and magnetically couple said integrated circuit to said at least one region of said substrate and said antenna; and

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at least one contact pad, said at least one contact pad being in direct electrical contact with said antenna of said substrate to establish an electrical connection between said integrated circuit and antenna of said substrate through the first magnetic material and the second magnetic material, wherein said second magnetic material is disposed on said at least one contact pad.

According to still another aspect of the present invention, there is provided an integrated circuit package comprising: a substrate comprising: circuitry; at least one region; and a first magnetic material associated with said at least one region; an integrated circuit, said integrated circuit having: a second magnetic material associated therewith, said second magnetic material being attracted to said first magnetic material to electrically and magnetically couple said integrated circuit to said at least one region of said substrate; at least one contact pad, said at least one contact pad being in direct electrical contact with said circuitry of said substrate to establish an electrical connection between said integrated circuit and circuitry of said substrate through the first magnetic material and the second magnetic material, wherein said second magnetic material is disposed on said at least one contact pad, the integrated circuit being aided into a particular orientation on the substrate by a magnetic attraction between the first magnetic material and the second magnetic material.

According to yet another aspect of the present invention, there is provided a method of assembling an integrated circuit to a substrate comprising: providing a first magnetic material associated with at least one region of said substrate; providing an integrated circuit having a second magnetic material associated therewith; directing a slurry comprising said integrated circuit and a fluid over said substrate; and transporting said integrated circuit towards said at least one region of said substrate, whereby said second magnetic material is attracted to said first magnetic material to urge at least a portion of said integrated circuit towards said at least one region.

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According to a further aspect of the present invention, there is provided a method of assembling an integrated circuit package, comprising: providing a substrate having circuitry therein; providing a first magnetic material associated with at least one region of said substrate; providing an integrated circuit having a second magnetic material associated therewith wherein said integrated circuit comprises at least one contact pad and said second magnetic material is disposed on said at least one contact pad; transporting said integrated circuit over said substrate toward said at least one region, whereby said second magnetic material is attracted to said first magnetic material to urge at least a portion of said integrated circuit towards said at least one region; establishing an electrical connection between said integrated circuit and said circuitry of said substrate through said first magnetic material and said second magnetic material; and electrically and magnetically coupling said integrated circuit to said at least one region of said substrate.

According to yet a further aspect of the present invention, there is provided a method of assembling an integrated circuit to a substrate comprising: providing a first magnetic material associated with at least one region of said substrate; providing an integrated circuit having a second magnetic material associated therewith; vibrating said substrate to transport said integrated circuit over said substrate towards said at least one region, whereby said second magnetic material is attracted to said first magnetic material to urge at least a portion of said integrated circuit towards said at least one region.

#### BRIEF DESCRIPTION OF THE DRAWINGS

[006] Features and advantages of embodiments of the claimed subject matter will become apparent as the following Detailed Description proceeds, and upon reference to the Drawings, where like numerals depict like parts, and in which:

[007] FIG. 1 is block diagram of a RFID system for use in a retail environment;

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[008] FIG. 2A is an exploded cross-sectional view of an IC and substrate for accepting the IC;

[009] FIG. 2B is a cross-sectional view of the IC and substrate of FIG. 2A where the IC is coupled to the substrate;

5 [0010] FIG. 3 is a cross-sectional view of an electrical connection formed between an IC and substrate;

[0011] FIG. 4 illustrates an embodiment of a magnetic keying arrangement to particularly position the IC on the substrate;

10 [0012] FIGs. 5 and 6 illustrate magnetization arrangements of magnetic material; and

[0013] FIG. 7 illustrates operations according to an embodiment.

[0014] Although the following Detailed Description will proceed with reference being made to illustrative embodiments, many alternatives, modifications, and variations thereof will be apparent to those skilled in the art. Accordingly, it is  
15 intended that the claimed subject matter be viewed broadly.

## DETAILED DESCRIPTION

[0015] Systems and methods consistent with the present invention will be described herein in connection with various embodiments. Those skilled in the art will recognize that the features and advantages of the present invention may be implemented in a variety of configurations. It is to be understood, therefore, that the embodiments described herein are presented by way of illustration, not of limitation.

[0016] FIG. 1 illustrates an RFID system 100 utilized in a retail environment 102. The RFID system 100 may include a plurality of RFID tags 106-1, 106-2 ... 106-n and an RFID reader 112. Each of the RFID tags 106-1, 106-2 ... 106-n may be affixed to an associated article 104-1, 104-2 ... 104-n presented for purchase in the retail environment 102. The RFID reader 112 may be portable or may be fixed to a particular location, e.g., to a location proximate a point-of-sale (POS) station 108. The RFID reader 112 may scan for RFID tags 106-1, 106-2 ... 106-n by transmitting an interrogation signal at a known frequency. RFID tags may respond to the interrogation signal with a response signal containing, for example, data associated with the article to which it is affixed. The RFID reader 112 may include processing capabilities to detect the response signal and decode the data. Since the RFID system 100 utilizes radio wave signals to communicate data, no line of sight between the RFID reader 112 and RFID tags 106-1, 106-2 ... 106-n is required.

[0017] One or more of the RFID tags, as shown in connection with RFID tag 106-n, may include an IC package 115 including an IC 120 coupled to a substrate 122. As used herein, an "integrated circuit" or IC means a semiconductor device and/or microelectronic device, such as, for example, a semiconductor integrated circuit chip. The substrate 122 may include an antenna. The IC 120 may include computer processing capabilities and a certain amount of memory depending on the type of RFID tag 106-n. The IC package 115 of the RFID tag 106-n may be manufactured according to the embodiments detailed herein to enable associated cost reductions in the manufacture of RFID tags. Of course, IC packages manufactured according to the magnetic self-assembly embodiments detailed herein may also be utilized in a variety of other systems and devices.

[0018] FIG. 2A is an exploded view of an integrated circuit package 200 including a substrate 202 and an IC 204. The substrate 202 may be formed from a variety of materials including flexible materials and may include one or more regions 212 to mate with the IC 204. In some embodiments, the region 212 may be recessed relative to a top surface of the

substrate. A recessed region may be provided in any geometric shape and may be dimensioned to accept at least a portion of the IC 204. In one embodiment, the recessed region may be configured to mate with a correspondingly shaped IC.

[0019] The substrate 202 may include a first magnetic material 216 associated  
5 therewith. The first magnetic material may, for example, be disposed on or beneath a surface of the region 212. A second magnetic material 218 may be associated with the IC 204. For example, the second magnetic material may be disposed on or beneath a surface of the IC. Although the first 216 and second 218 magnetic materials are illustrated as having particular orientations relative to the substrate and IC, respectively, it is to be  
10 understood that the magnetic materials may be associated with the IC and substrate by providing the material completely or partially on a surface, completely or partially beneath a surface, or integral with the IC or substrate. Also, the magnetic materials may be provided at any location on or within the IC or substrate.

[0020] The first and second magnetic materials 216, 218 may, for example, include  
15 high energy metallic magnetic materials. Examples of such materials include, but are not limited to iron platinum, cobalt platinum, and copper. As the IC 204 is transported about the substrate 202 by any of a variety of techniques, it is aided into position, e.g. as shown in FIG. 2B, by the magnetic attraction between materials 216 and 218.

[0021] FIG. 3 illustrates another embodiment of an IC package 300 that provides for  
20 magnetic self-assembly of the IC 204a to the substrate 202a and also facilitates formation of an electrical connection between the IC 204a and the substrate 202a. In the embodiment of FIG. 3, the region 212 of FIG. 2A may be a recessed region 212a. The IC 204a may include a contact pad 302. Although only one contact pad 302 is illustrated for clarity, the IC 204a may include a plurality of contact pads. The second magnetic material  
25 218a may be disposed on the contact pad 302. In addition, a conductive material 304 may initially be attached to the second magnetic material 218a. Alternatively, the conductive material may initially be attached to the first magnetic material 216a.

[0022] The conductive material 304 may be a conductive adhesive in one  
embodiment such as a thermally activated conductive adhesive. In one example, the  
30 conductive adhesive may be a conductor-filled thermal set adhesive film such as the Z-Axis Adhesive Film 7303 by 3M Corporation. In other examples, the conductive adhesive 304 may be a thermoplastic conductive adhesive or a non-heat activated conductive adhesive such as a room temperature adhesive. The conductive material 304 may also be

solder. The solder may be applied as a solder paste or applied to form solder bumps. The first magnetic material 216a may be positioned in or on the recessed region 212a with an exposed portion that contacts the conductive material 304, as shown, for example, in FIG. 3.

5 [0023] As the IC 204a is attracted into the region of the substrate 202a by the magnetic attraction of the first and second magnetic materials 216a, 218a, the first and second magnetic materials 216a, 218a may become affixed to each other via the conductive material 304 to form an electrical connection between the IC 204a and circuitry 308 of the substrate 202a. As used herein, "circuitry" may include, for example,  
10 singly or in any combination, hardwired circuitry, programmable circuitry, state machine circuitry, and/or firmware that stores instructions executed by programmable circuitry. In one RFID tag embodiment, the circuitry 308 may comprise an antenna 309 and the first magnetic material 216a may be deposited at an input port to the antenna.

[0024] To make the electrical connection between the IC 204a and the substrate 202a  
15 permanent when using a thermally activated conductive material, heat may be applied to the material and pressure may be applied to urge the first and second magnetic material 216a, 218a towards each other. The heat may be sufficient to bond the first and second materials 216a, 218a to the conductive material 304 to form a permanent electrical connection between the IC 204a and the substrate 202a.

20 [0025] In some embodiments, a particular orientation of the integrated circuit relative to the substrate may not be critical. This may be the case for some RFID tag embodiments. In this instance, one of the first or second magnetic materials 216 or 218 associated with the substrate 202 or the IC 204 may be a soft magnetic material, such as a nickel or iron-based alloy. The other of the first and second magnetic materials may have  
25 a permanent magnetization such as from a permanent magnet. In this embodiment, as the soft magnetic material moves closer to the field of the permanent magnet, the soft magnetic layer may be attracted to the permanent magnet.

[0026] In other embodiments, a particular orientation of the IC relative to the substrate may be desired. FIG. 4 illustrates an embodiment of a magnetic keying  
30 arrangement to particularly position the IC 404 relative to the substrate 402. The IC 404 is illustrated with its mating side facing outward, and has a rectangular configuration. A plurality of contact pads 440, e.g. sixteen in the illustrated embodiment, may be provided on the IC and the second magnetic material 418 may be disposed on each of the plurality

of contact pads to establish an arrangement of North (N) and South (S) magnetization polarities keyed to an associated arrangement in a substrate.

[0027] The substrate is shown in top view and includes a top surface 403 with a rectangular region 412 formed thereon. In the illustrated exemplary embodiment, the region 412 is adapted to mate with and receive the IC 404. The first magnetic material 416 may be disposed on or within a bottom surface of the region and may be magnetized to establish a magnetization polarity arrangement keyed to arrangement on the IC.

[0028] As shown, for example, side 420 of the first magnetic material 416 may have polarities N, S, S, N to attract the side 430 of the IC 404 having polarities S, N, N, S, respectively. Side 422 may have polarities S, N, S, N to attract polarities N, S, N, S, respectively, of side 432 of the IC 404. Side 426 may have polarities N, S, N, S to attract polarities S, N, S, N, respectively, of side 436 of the IC. Finally, side 424 may have polarities N, N, S, S to attract polarities S, S, N, N, respectively, of side 434 of the IC. The arrangement of magnetic polarities on the substrate and the IC thus establishes a magnetic keying arrangement whereby the IC 402 may be attracted to a particular orientation on the substrate.

[0029] The second magnetic material 218 on the IC 204 may have a variety of magnetization arrangements. FIG. 5 illustrates one embodiment of the second magnetic material 218b having a perpendicular magnetization arrangement that may be disposed on a contact pad 500 of an IC. FIG. 6 illustrates another embodiment of the second magnetic material 218c having a longitudinal magnetization arrangement that may be disposed on a contact pad 600.

[0030] To provide a more compact IC package assembly, an array of lead-frame elements may be used to assemble the ICs. The lead-frame elements may be used to make connection to an antenna substrate through ultrasonic sealing or other methods. The lead-frame elements may be a dense array to facilitate a more compact IC package assembly.

[0031] FIG. 7 is a flow chart of operations 700 consistent with an embodiment for assembling an IC to a substrate. Operation 702 may include providing a first magnetic material associated with at least one region of the substrate. Operation 704 may include providing an integrated circuit having a second magnetic material associated therewith. Finally, operation 706 may include transporting the integrated circuit about the substrate, whereby the second magnetic material is attracted to the first magnetic material to urge at least a portion of the integrated circuit towards the at least one region. One of several

ways to transport the integrated circuit about the substrate may include providing a slurry including one or more ICs and a fluid and directing the fluid over the substrate.

[0032] According to one aspect of the invention, there is provided an integrated circuit package including a substrate and an integrated circuit. The substrate may include  
5 at least one region, and a first magnetic material may be associated with the at least one region. The integrated circuit may have a second magnetic material associated therewith. The second magnetic material may be attracted to the first magnetic material to couple the integrated circuit to the at least one region of the substrate.

[0033] According to another aspect of the invention, there is provided an RFID tag.  
10 The RFID tag may include a substrate and an integrated circuit. The substrate may include at least one region, and a first magnetic material associated with the at least one region. The substrate may further include an antenna for transmitting signals to and receiving signals from an associated RFID reader. The integrated circuit may have a second magnetic material associated therewith. The second magnetic material may be  
15 attracted to the first magnetic material to couple the integrated circuit to the at least one region of the substrate.

[0034] According to another aspect of the invention, there is provided a method of assembling an integrated circuit to a substrate including providing a first magnetic material associated with at least one region of the substrate; providing an integrated circuit  
20 having a second magnetic material associated therewith; and transporting the integrated circuit about the substrate, whereby the second magnetic material is attracted to the first magnetic material to urge at least a portion of the integrated circuit towards the at least one region.

[0035] Advantageously, in these embodiments the magnetic attraction of the first and  
25 second magnetic materials allows for magnetic self-assembly of an IC to a region of a substrate. This provides for efficient self-assembly reducing the need for higher priced conventional alternatives such as “pick and place” machines. This results in reduced IC packaging costs. In addition, the first and second magnetic materials may be utilized, e.g., together with a conductive material, to also form an electrical connection between the IC  
30 and circuitry of the substrate. As such, conventional wire-bonding processes and its associated components and costs may be avoided. The attraction of the first and second magnetic materials may also provide for better IC alignment than conventional alternatives such as a pick and place machine. The IC package manufactured according to magnetic

self-assembly embodiments described herein may be utilized in a variety of devices and systems. One example is to manufacture RFID tags at reduced costs compared to conventional methods. Such RFID tags may be used in an RFID system for tagging retail merchandise in a retail environment where the cost of the RFID tag is an important factor.

- 5     [0036]     The terms and expressions which have been employed herein are used as terms of description and not of limitation, and there is no intention, in the use of such terms and expressions, of excluding any equivalents of the features shown and described (or portions thereof), and it is recognized that various modifications are possible within the scope of the claims. Other modifications, variations, and alternatives are also possible.
- 10    Accordingly, the claims are intended to cover all such equivalents.

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CLAIMS:

1. An integrated circuit package comprising:  
a substrate comprising:  
circuitry;  
5 at least one region; and  
a first magnetic material associated with said at least one region; and  
an integrated circuit, said integrated circuit having:  
a second magnetic material associated therewith, said second magnetic  
material being attracted to said first magnetic material to electrically and magnetically  
10 couple said integrated circuit to said at least one region of said substrate; and  
at least one contact pad, said at least one contact pad being in direct  
electrical contact with said circuitry of said substrate to establish an electrical  
connection between said integrated circuit and circuitry of said substrate through the  
first magnetic material and the second magnetic material, wherein said second  
15 magnetic material is disposed on said at least one contact pad.
2. The integrated circuit package of claim 1, wherein a conductive material  
is disposed on one of said first and second magnetic materials, said conductive  
material contacting said first and second magnetic materials when said integrated  
circuit is disposed in said at least one region to establish an electrical connection  
20 between said integrated circuit and said circuitry of said substrate.
3. A radio frequency identification (RFID) tag comprising:  
a substrate comprising:  
at least one region, and

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a first magnetic material associated with said at least one region, said substrate further comprising an antenna for transmitting signals to and receiving signals from an associated RFID reader; and

an integrated circuit having:

5 a second magnetic material associated therewith, said second magnetic material attracted to said first magnetic material to electrically and magnetically couple said integrated circuit to said at least one region of said substrate and said antenna; and

10 at least one contact pad, said at least one contact pad being in direct electrical contact with said antenna of said substrate to establish an electrical connection between said integrated circuit and antenna of said substrate through the first magnetic material and the second magnetic material, wherein said second magnetic material is disposed on said at least one contact pad.

4. The RFID tag of claim 3, wherein one of said first and second magnetic materials comprises a permanent magnet and the other of said first and second magnetic materials comprises a magnetic material attracted to said permanent magnet.

5. The RFID tag of claim 3, wherein a conductive material is disposed on one of said first and second magnetic materials, said conductive material contacting said first and second magnetic materials when said integrated circuit is coupled to said at least one region to establish an electrical connection between said integrated circuit and said antenna of said substrate.

6. The integrated circuit package of claim 1, wherein the substrate further comprises a recessed region dimensioned to accept at least a portion of the integrated circuit.

7. The RFID tag of claim 3, wherein the antenna includes at least one input port, the first magnetic material being associated with the input port.

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8. An integrated circuit package comprising:
- a substrate comprising:
- circuitry;
- at least one region; and
- 5 a first magnetic material associated with said at least one region;
- an integrated circuit, said integrated circuit having:
- a second magnetic material associated therewith, said second magnetic material being attracted to said first magnetic material to electrically and magnetically couple said integrated circuit to said at least one region of said substrate;
- 10 at least one contact pad, said at least one contact pad being in direct electrical contact with said circuitry of said substrate to establish an electrical connection between said integrated circuit and circuitry of said substrate through the first magnetic material and the second magnetic material, wherein said second magnetic material is disposed on said at least one contact pad, the integrated circuit
- 15 being aided into a particular orientation on the substrate by a magnetic attraction between the first magnetic material and the second magnetic material.
9. The integrated circuit package of claim 8, further comprising:
- a conductive material disposed on one of said first and second magnetic materials, said conductive material contacting said first and second
- 20 magnetic materials when said integrated circuit is disposed in said at least one region to establish an electrical connection between said integrated circuit and circuitry of said substrate.
10. The integrated circuit package of claim 8, wherein one of said first and second magnetic materials comprises a permanent magnet and the other of said first

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and second magnetic materials comprises a magnetic material attracted to said permanent magnet.

11. The integrated circuit package of claim 8, wherein said first magnetic material comprises a plurality of polarities and said second magnetic material  
5 comprises a complementary plurality of polarities to provide for a particular orientation of said integrated circuit on said region.

12. A method of assembling an integrated circuit to a substrate comprising:  
providing a first magnetic material associated with at least one region of said substrate;  
10 providing an integrated circuit having a second magnetic material associated therewith;  
directing a slurry comprising said integrated circuit and a fluid over said substrate; and  
transporting said integrated circuit towards said at least one region of  
15 said substrate, whereby said second magnetic material is attracted to said first magnetic material to urge at least a portion of said integrated circuit towards said at least one region.

13. The method of claim 12, wherein one of said first and second magnetic materials comprises a permanent magnet and the other of said first and second  
20 magnetic materials comprises a magnetic material attracted to said permanent magnet.

14. A method of assembling an integrated circuit package, comprising:  
providing a substrate having circuitry therein;  
providing a first magnetic material associated with at least one region of  
25 said substrate;

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providing an integrated circuit having a second magnetic material associated therewith wherein said integrated circuit comprises at least one contact pad and said second magnetic material is disposed on said at least one contact pad;

5 transporting said integrated circuit over said substrate toward said at least one region, whereby said second magnetic material is attracted to said first magnetic material to urge at least a portion of said integrated circuit towards said at least one region;

10 establishing an electrical connection between said integrated circuit and said circuitry of said substrate through said first magnetic material and said second magnetic material; and

electrically and magnetically coupling said integrated circuit to said at least one region of said substrate.

15. The method of claim 14, further comprising:

15 applying a conductive material to one of said first and second magnetic materials; and

positioning said first and second magnetic materials so that said conductive material contacts said first and second magnetic materials when said integrated circuit is deposited in said at least one region to establish an electrical connection between said integrated circuit and circuitry of said substrate.

20 16. The method of claim 15, wherein said conductive material comprises a thermally activated conductive adhesive and said method further comprises:

heating said thermally activated conductive adhesive; and

applying pressure to first and second magnetic materials to form said electrical connection.

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17. The method of claim 14 wherein said circuitry comprises an antenna of an RFID tag.

18. The method of claim 14 wherein said first magnetic material comprises a plurality of polarities and said second magnetic material comprises a plurality of polarities to provide a desired orientation of said integrated circuit in said at least one region.

19. The method of claim 14, wherein one of said first and second magnetic materials comprises a permanent magnet and the other of said first and second magnetic materials comprises a magnetic material attracted to said permanent magnet.

20. The method of claim 14, wherein said step of transporting said integrated circuit over said substrate toward said at least one region comprises directing a slurry comprising said integrated circuit and a fluid over said substrate.

21. The method of claim 14, wherein said step of transporting said integrated circuit over said substrate toward said at least one region comprises vibrating said substrate.

22. The method of claim 12, wherein said first magnetic material comprises a plurality of polarities and said second magnetic material comprises a plurality of polarities to provide a desired orientation of said integrated circuit in said at least one region.

23. The method of 12, wherein the substrate contains circuitry, and said circuitry comprises an RFID antenna.

24. A method of assembling an integrated circuit to a substrate comprising:  
providing a first magnetic material associated with at least one region of said substrate;

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providing an integrated circuit having a second magnetic material associated therewith;

vibrating said substrate to transport said integrated circuit over said substrate towards said at least one region, whereby said second magnetic material is attracted to said first magnetic material to urge at least a portion of said integrated circuit towards said at least one region.

25. The method of claim 24, wherein one of said first and second magnetic materials comprises a permanent magnet and the other of said first and second magnetic materials comprises a magnetic material attracted to said permanent magnet.

26. The method of claim 24, wherein said first magnetic material comprises a plurality of polarities and said second magnetic material comprises a plurality of polarities to provide a desired orientation of said integrated circuit in said at least one region.

15 27. The method of 24, wherein the substrate contains circuitry, and said circuitry comprises an RFID antenna.

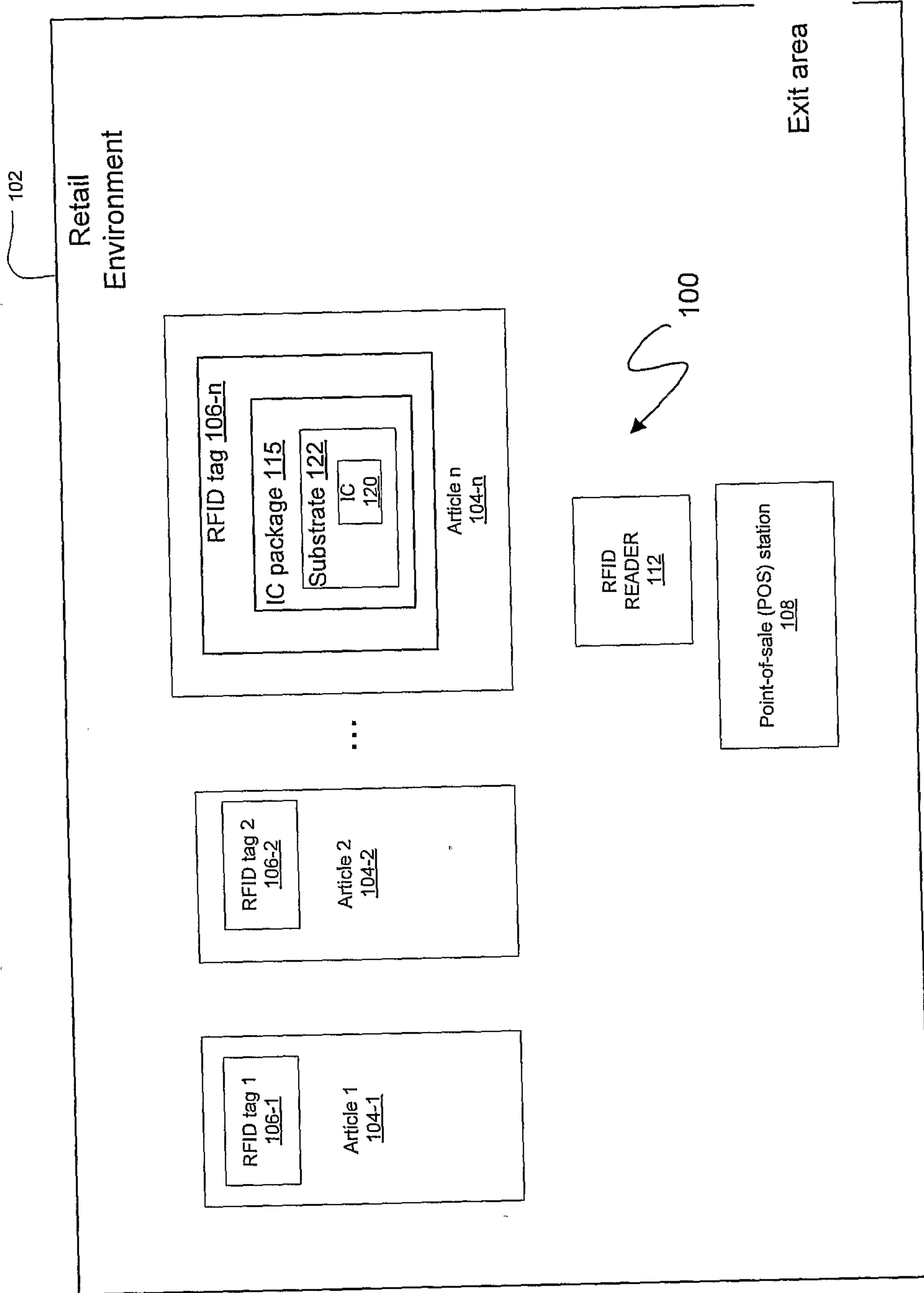


FIG. 1

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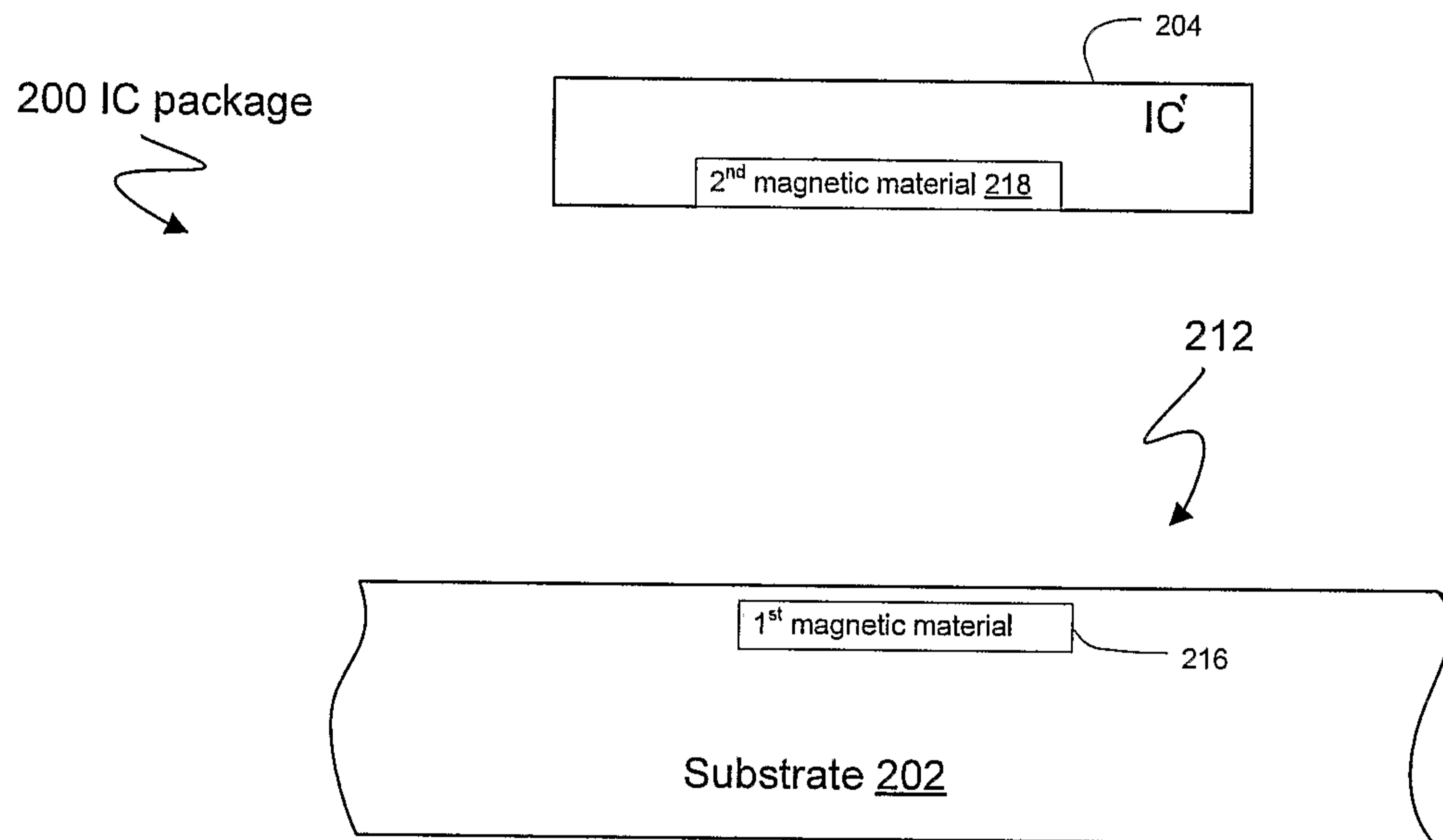


FIG. 2A

200 IC package

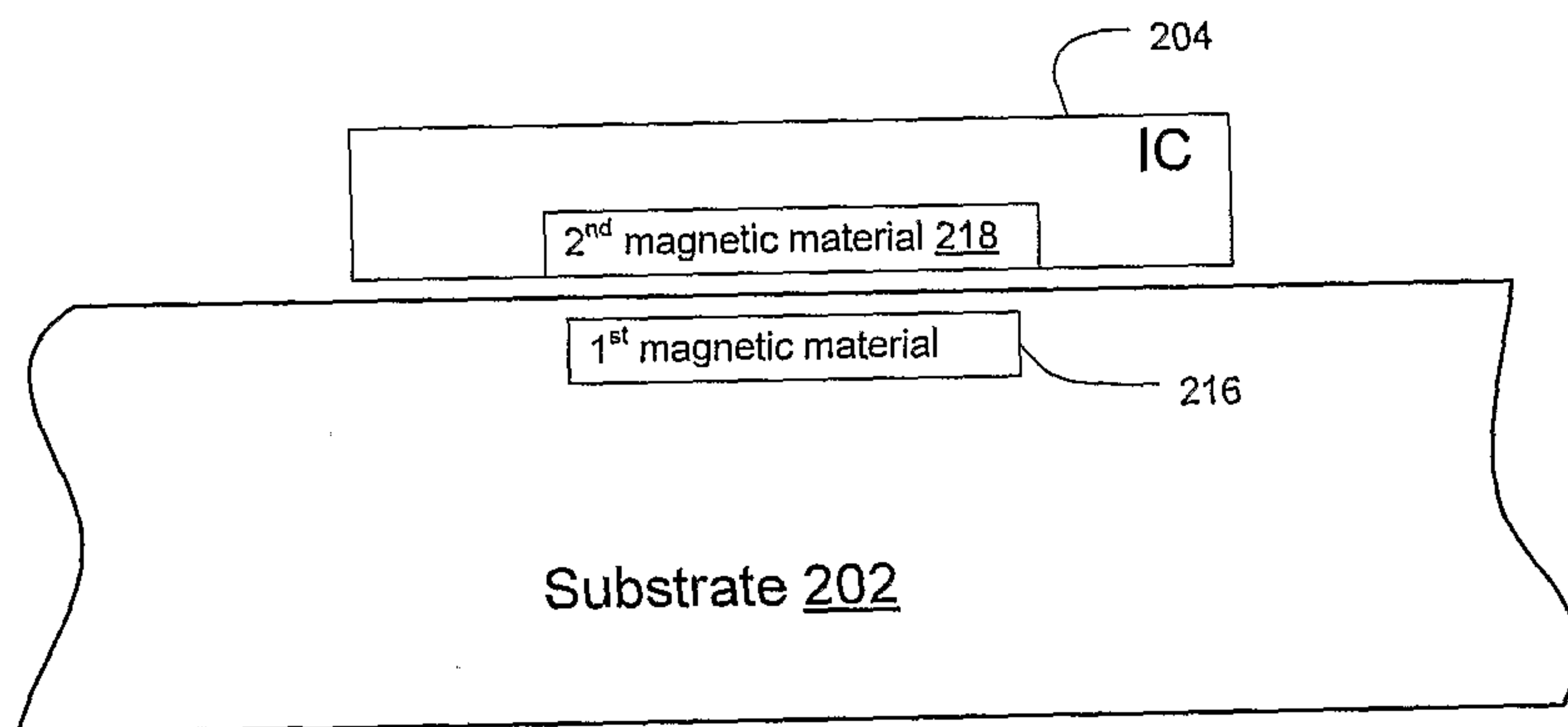


FIG. 2B

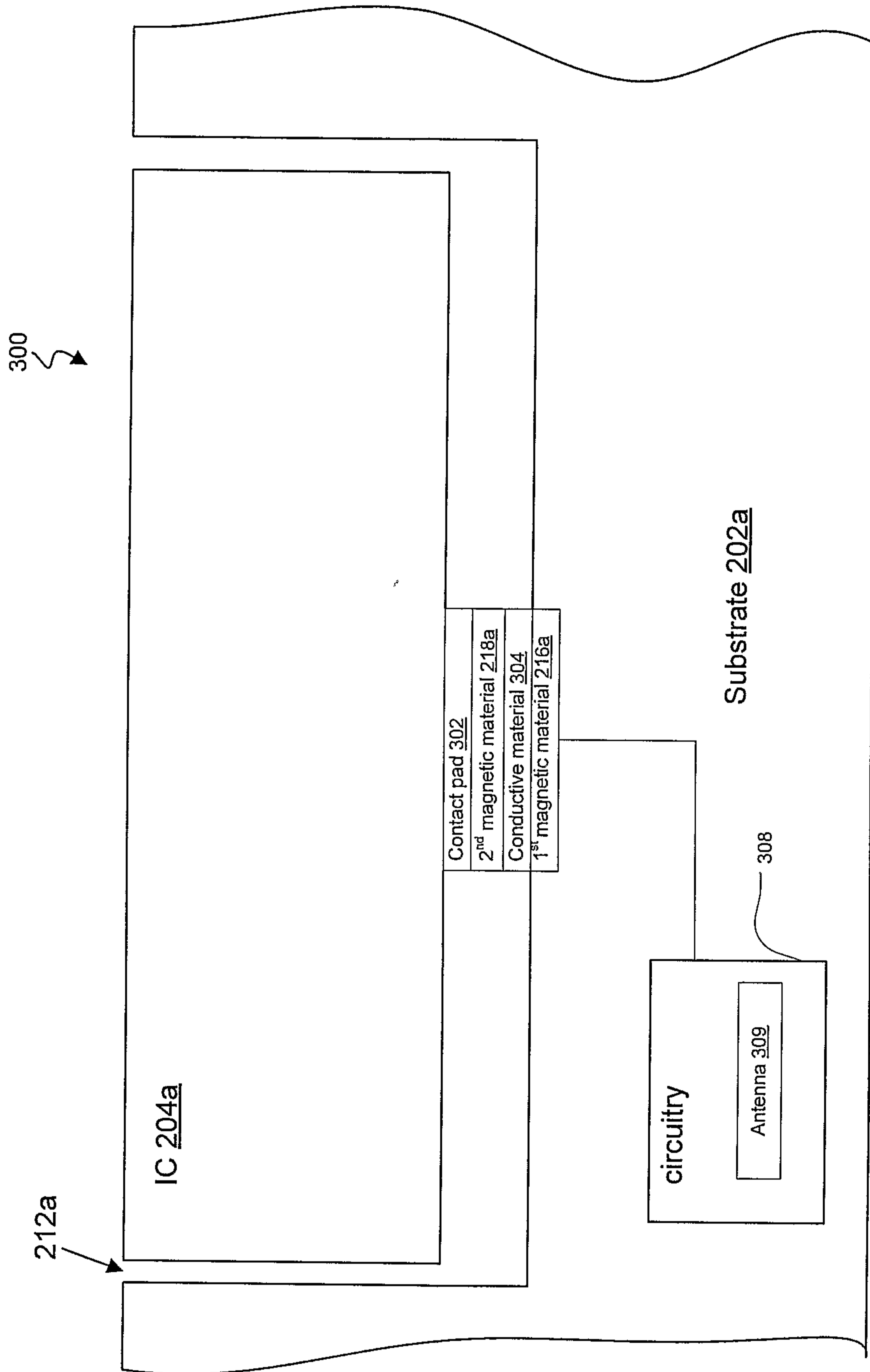


FIG. 3

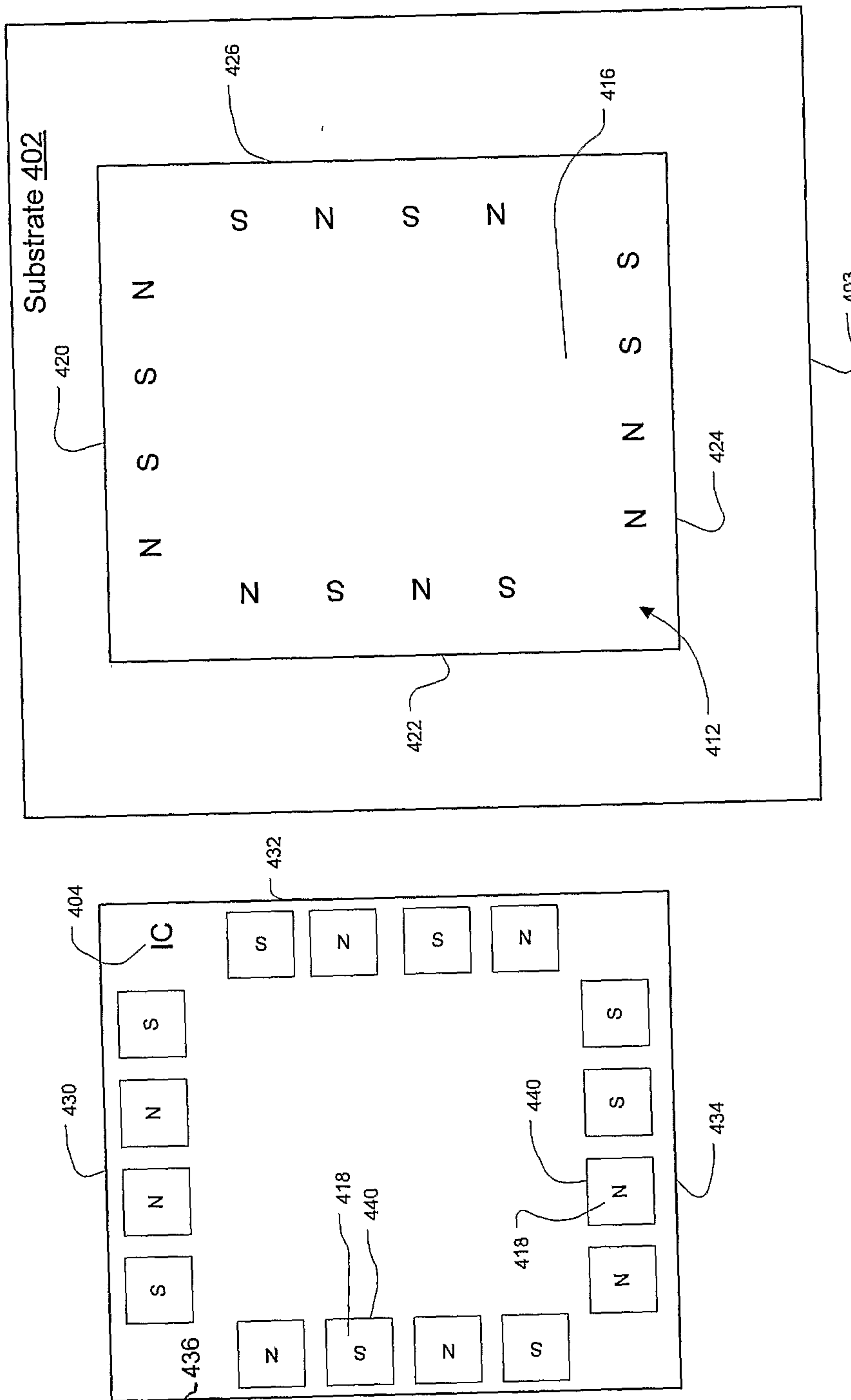


FIG. 4

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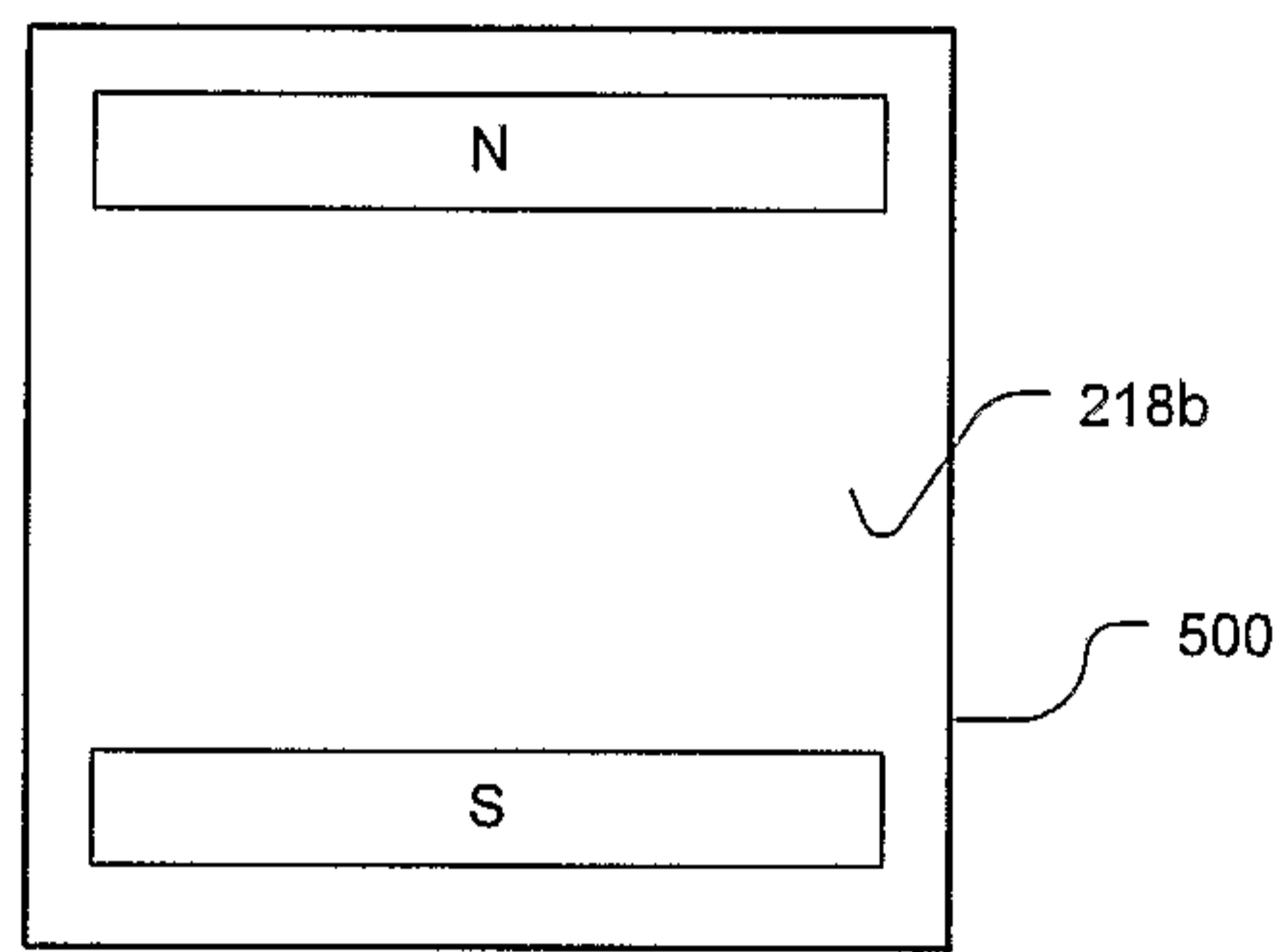


FIG. 5

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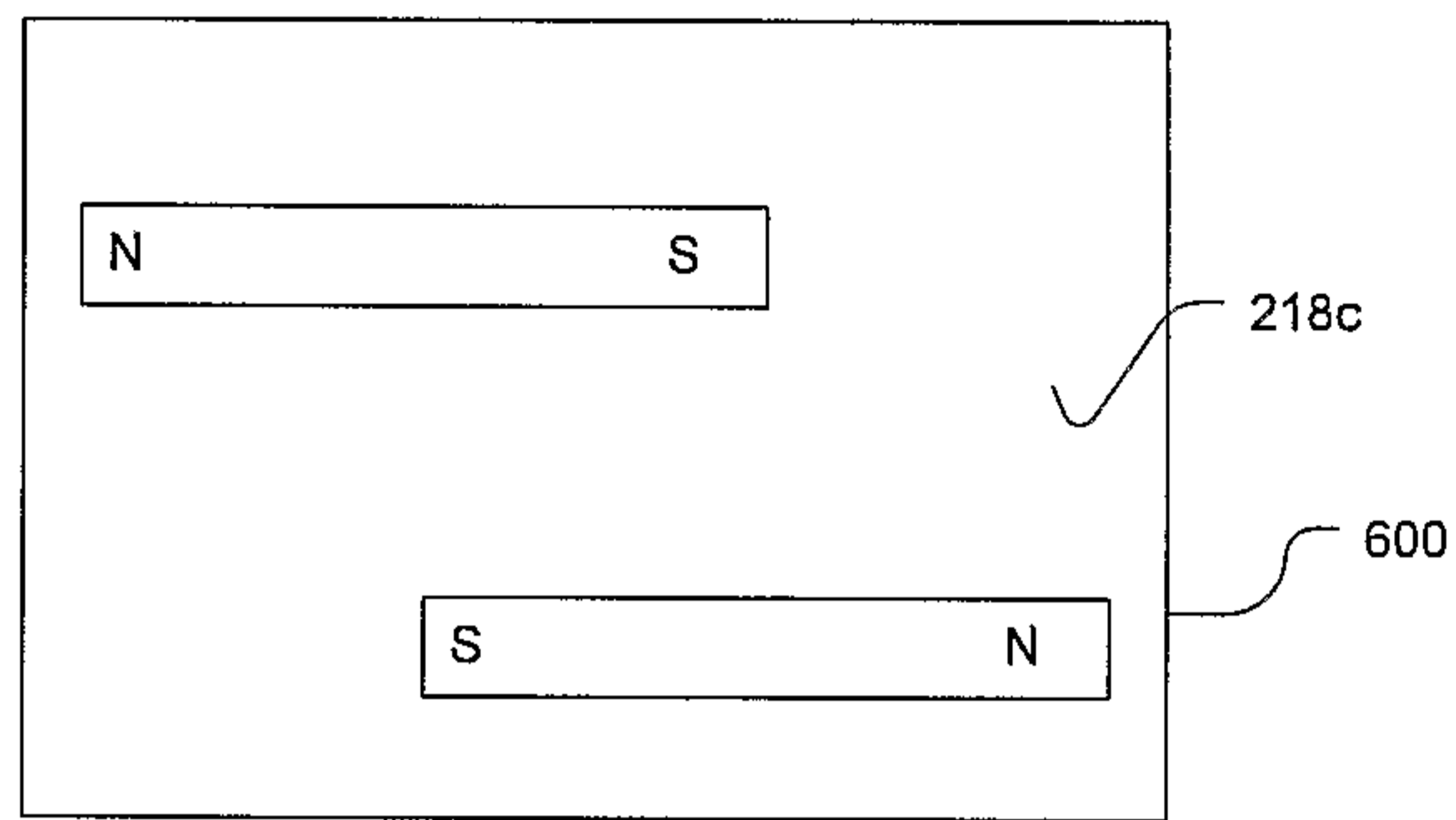


FIG. 6

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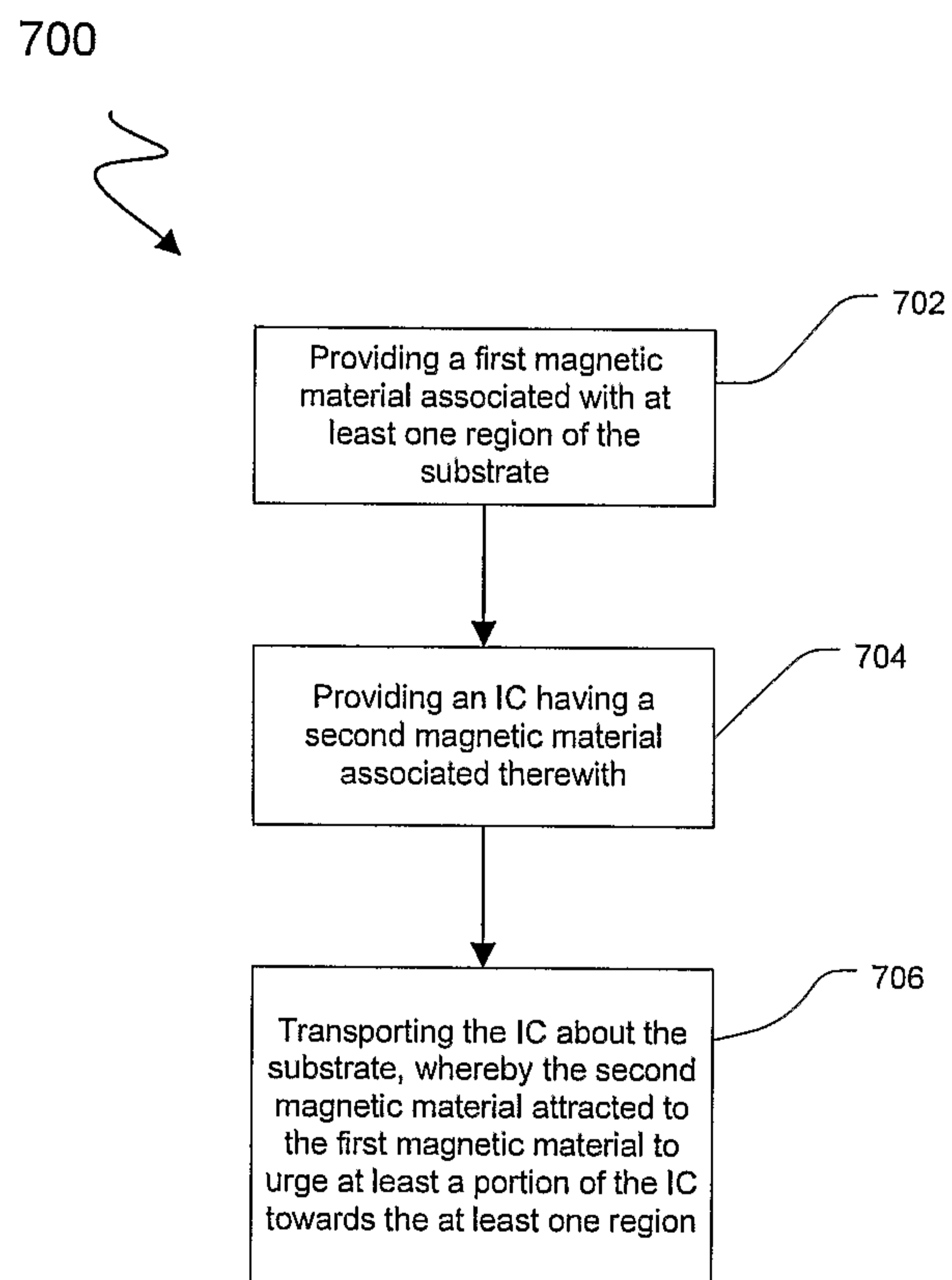


FIG. 7

