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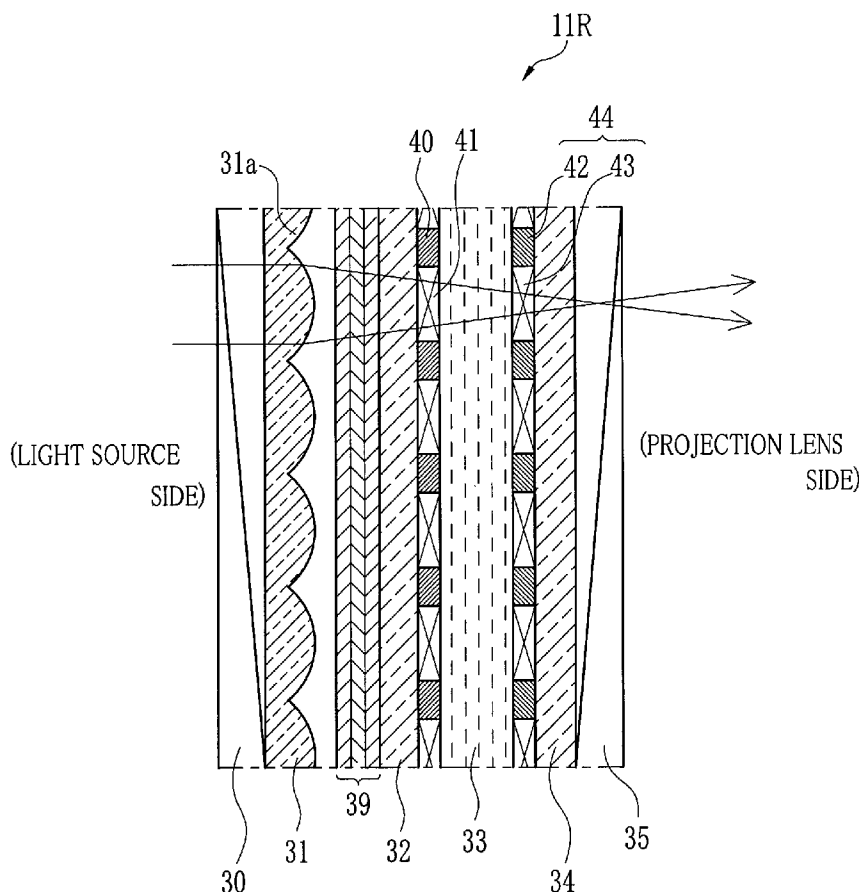
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[Continued on next page]

(54) Title: LIQUID CRYSTAL DISPLAY DEVICE AND LIQUID CRYSTAL PROJECTOR



(57) Abstract: A liquid crystal display panel (11R) is provided with, from a light source side, a MLA substrate (31) having a microlens array (37), an opposed substrate (32) having transparent common electrodes (41) formed thereon, a liquid crystal layer (33), and a TFT substrate (34) having transparent pixel electrodes associated with each pixel. The microlens array focuses the incident light onto the corresponding pixel electrodes. A light source side surface of the opposed substrate is provided with a retardation compensation layer (39). Since the light passes through the retardation compensation layer and the liquid crystal layer at the same angle even if the light diffracts and diffuses on the microlens array, leakage of light is prevented.



SD, SE, SG, SK, SL, SM, SY, TJ, TM, TN, TR, TT, TZ,
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DESCRIPTION

LIQUID CRYSTAL DISPLAY DEVICE AND LIQUID CRYSTAL PROJECTOR

5 Technical Field

The present invention relates to liquid crystal display devices and liquid crystal projectors of TFT type, and more particularly to a liquid crystal display device and a liquid crystal projector able to compensate retardation of light
10 occurred in a liquid crystal layer.

Background Art

Liquid crystal devices are classified into TN (Twisted Nematic) mode, VA (Vertical Alignment) mode, IPS (In-Plane Switching) mode, OCB (Optically Compensatory Bend) mode, ECB (Electrically Controlled Birefringence) mode and so forth, based on an operating mode of a liquid crystal layer. While these operating modes differ in alignment direction of liquid crystal molecules, they are the same in function that electrically
15 controls quantity of light passing through the liquid crystals so as to display characters and images.
20

One of the known liquid crystal devices is a TFT (thin film transistor) type. The TFT type liquid crystal device (TFT-LCD) uses the thin film transistors to control voltage applied to each pixel region of the liquid crystal layer. Able to provide a fast and exact switching of pixels, the TFT-LCD is widely used for high image quality applications. The TFT-LCD includes a TFT substrate having a TFT array with its wiring pattern and transparent pixel electrodes, and an opposed substrate having transparent common
25 electrodes that correspond to the pixel electrodes of the TFT substrate. Provided between the TFT substrate and the opposed substrate is a layer of liquid crystalline material. Generally,
30

the liquid crystal layer of the TFT-LCD is the TN mode.

The TFT substrate and the opposed substrate are both, for example, transparent glass substrates. The TFT substrate is located on a projection lens side, and the opposed substrate is located on a light source side. The opposed substrate is also provided with a matrix of light shielding layer called a black matrix, which is shaped, for the color TFT-LCDs in particular, to surround each of the pixel electrodes that constitute a mosaic pattern. While it also serves to enhance contrast of the pixels, the black matrix blocks light toward the TFTs so that the TFTs do not operate improperly upon irradiation of intense light. Further, the opposed substrate is provided with a microlens array for preventing loss of light due to the black matrix. Each microlens focuses the incident light on the corresponding pixel, and the light from the light source effectively passes through openings of the black matrix.

When used in liquid crystal projectors, the TFT-LCD needs to be small. In this case, the density of pixels has to be increased in order to project high definition images on a screen. Accordingly, the interval, or pitch, of both the electrodes and microlenses is decreased on the substrates.

For example, the microlenses and the TFTs are arranged at approximately 10 μ m pitch. This small pitch causes diffraction in a part of the light from the light source and, as shown in FIG.5, the light of a certain wavelength diffuses at an angle of approximately 10 degrees in every directions. As a result, the light passes through the liquid crystal layer at different angles than it does through a retardation compensation layer. Under this circumstance, the necessary retardation compensation effect is hardly obtained, and light leakage from an analyzer is occurred to lower the contrast ratio of the projected images. Such diffuse light due to the diffraction seriously hinders the retardation

compensation performance, but has been unrecognized previously. It is worth noted that this effect is present in the liquid crystal display devices with a minute element structure regardless of the liquid crystal modes.

5 In view of the forgoing, an object of the present invention is to provide the liquid crystal display device and the liquid crystal projector able to compensate the retardation of the light that is diffused due to the diffraction.

10 Disclosure of Invention

 In order to achieve the above and other objects, a liquid crystal display device of the present invention includes a structure of minute elements arranged at a regular pitch that possibly causes diffraction of visible spectrum light, and a
15 retardation compensation layer located between the minute element structure and a liquid crystal layer enclosed by a pair of substrates. This retardation compensation layer compensates retardation of light occurred in the liquid crystal layer.

 This invention does not specify the liquid crystal mode or
20 retardation compensating technology which should just match the liquid crystal mode.

 The minute element structure is integrated with one of the substrates, and the retardation compensation layer is provided on one or both of the substrates.

25 A liquid crystal projector of the present invention includes the structure of minute elements arranged at a regular pitch that possibly causes diffraction of visible spectrum light, the liquid crystal layer enclosed by the pair of substrates, and a retardation compensation layer located between the minute
30 element structure and the liquid crystal layer so as to compensate retardation of light occurred in the liquid crystal layer.

 According to the present invention, the retardation

compensation layer is located closer to the liquid crystal layer than the diffraction-causing minute element structure is. This arrangement allows light to pass through the retardation compensation layer and the liquid crystal layer at the same angle, and therefore the retardation of light occurred in the liquid crystal layer is precisely compensated. Light leakage in the black display state is thereby prevented, and the liquid crystal display device becomes to provide an enhanced contrast ratio, a wider viewing angle, and uniform brightness. In particular, the present invention is effective to the small sized, high pixel-density liquid crystal display devices such as those used in the liquid crystal projectors. Since the retardation compensation layer is integrated with the substrate pair that holds the liquid crystal layer, a dedicated substrate for the retardation compensation layer is not required. It is therefore possible to reduce the number of components and also the thickness of the liquid crystal display device.

Brief Description of Drawings

Figure 1 is a schematic view of a liquid crystal projector according to the present invention;

Figure 2 is a cross sectional view of a liquid crystal display device according to the present invention;

Figure 3 is a perspective view illustrating a configuration of the liquid crystal display device;

Figure 4 is a perspective view illustrating a configuration of the liquid crystal display device in another embodiment; and

Figure 5 is a perspective view illustrating a configuration of a conventional liquid crystal display device.

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Best Mode for Carrying Out the Invention

In FIG.1, a liquid crystal projector 10 has three liquid

crystal display panels 11R, 11G, and 11B of transmissive type to project full color images on a screen 3. White light irradiated by a light source 12 loses ultraviolet and infrared components when passing through a filter 13. Then, the white light passes
5 through a glass rod 14 and has uniform intensity distribution.

The white light that comes out of the glass rod 14 becomes a parallel beam when passing through a relay lens 15 and a collimate lens 16, and proceeds to a mirror 17. Reflected on the mirror 17, the white light is divided into two beams of red and cyan by a
10 dichroic mirror 18R which only transmits red light. The red light that transmits the dichroic mirror 18R is reflected on a mirror 19 and enters the liquid crystal display panel 11R.

The cyan light, which has been reflected on the dichroic mirror 18R, is further divided into two beams of green and blue
15 by a dichroic mirror 18G which reflects green light. The green light that is reflected on the dichroic mirror 18G enters the liquid crystal display panel 11G. On the other hand, the blue light that transmits the dichroic mirror 18G is reflected on mirrors 18B, 20, and enters the liquid crystal display panel 11B.

20 The liquid crystal display panels 11R, 11G, and 11B separately display gray scale images for red color, green color, and blue color images. In addition, there is a composite prism 24 placed at equal distances from the liquid crystal display panels 11R, 11G, and 11B. The red light, the green light, and the
25 blue light pass through the corresponding liquid crystal display panels 11R, 11G, and 11B, and are modulated into image light which carries image information. These image lights of three colors are combined into a single composite light beam by the composite prism having two dichroic surfaces 24a and 24b. The composite image
30 light is projected through a projection lens 25 onto the screen 3, and a full color image is displayed.

As shown in FIG.2, the liquid crystal display panel 11R has,

from the light source side, a first polarizer 30, a MLA (microlens array) substrate 31, an opposed substrate 32, a TFT substrate 34, and a second polarizer 35, which are integrated into a single component. Alternatively, the polarizers may be placed separately to avoid heat accumulation. Filled between the MLA substrate 31 and the opposed substrate 32 are liquid crystals which form a liquid crystal layer 33. The first polarizer 30 and the second polarizer 35 are in crossed nicols arrangement, where absorption axes thereof are perpendicular to each other. The first polarizer 30 converts the incident light into linearly polarized light. The second polarizer 35 regulates the passage of the linearly polarized light that has passed through the liquid crystal display panel 11R. This configuration is the same in the liquid crystal display panels 11G and 11B.

The MLA substrate 31 has a microlens array composed of plural microlenses 31a in a matrix arrangement. The microlenses 31a, each of which functions as a convex lens that condenses the incident light coming from the light source, are arranged at an approximately 10 μ m pitch so that they are separately associated with each single pixel. The opposed substrate 32 has a retardation compensation layer 39, which compensates the retardation of light occurred in the liquid crystal layer 33. Additionally, a projection lens side surface of the opposed substrate 32 is provided with a black matrix 40 made of, for example, a chromium film functioning as a light shielding layer, and transparent common electrodes 41.

Formed on a light source side surface of the TFT substrate 34 is a TFT circuit pattern 44, where thin film transistors (TFT) 42 and pixel electrodes 43 are arranged in a matrix form. When the TFT 42 is turned on, a voltage is applied across the pixel electrode 43 and the common electrode 41, and thereby an alignment direction of each liquid crystal molecule is changed in the liquid

crystal layer 33. In addition, between the common electrodes 41 and the liquid crystal layer 33 and between the pixel electrodes 43 and the liquid crystal layer 33, there are provided alignment films (not shown) which align the liquid crystal molecules in a certain direction when no voltage is applied.

An example of the liquid crystal layer 33 is a widely used TN mode liquid crystal layer, in which the liquid crystal molecules between the opposed substrate 32 and the TFT substrate 34 are aligned parallel to the substrate surfaces while twisted up to 90 degrees. When no voltage is applied, the linearly polarized light that passed through the first polarizer 30 is twisted 90 degrees in the liquid crystal layer 33 and passes through the second polarizer 35. Therefore, the liquid crystal display panel turns into a white display state. When voltage is applied, the liquid crystal molecules are aligned almost vertical to the substrate surfaces. The linearly polarized light that passed through the first polarizer 30 goes through the liquid crystal layer 33 with its polarization plane untwisted and is then blocked by the second polarizer 35. Therefore, the liquid crystal display panel turns into a black display state.

An example of the retardation compensation layer 39 is a layer of polymerized discotic liquid crystal compounds having a disk-like molecule structure. The discotic liquid crystal compounds in the retardation compensation layer 39 take a hybrid alignment whose alignment direction varies continuously with respect to the thickness direction of the layer. In this embodiment, the retardation compensation layer 39 is composed of three layers. In the liquid crystal layer 33 with voltage applied thereto (the black display state), the liquid crystal molecules are in the hybrid alignment that they exist vertical to the substrate surfaces in the middle of the layer while continuously increase their tilt angles toward the periphery of the substrates.

This means that the retardation of light occurred in such TN liquid crystal layer can be compensated by the discotic liquid crystal layer with the hybrid alignment. Other retardation compensating means can also be applied such as polymerized rod-like molecules,
5 an organic form birefringent layer, or an inorganic form birefringent layer.

Next, the operation of the present invention is described. In FIG.3, the incident light to the liquid crystal display panel 11R turns into linearly polarized light as it passes through the
10 first polarizer 30, and goes through both the MLA substrate 31 with the microlens array and the retardation compensation layer 39, and then enters the liquid crystal layer 33. A part of the linearly polarized light diffracts to diffuse as it passes through the microlens array. Each ray of such diffuse light is also
15 subjected to retardation in the retardation compensation layer 39. As a result, the light becomes to pass through the retardation compensation layer 39 and the liquid crystal layer 33 at the same angle, and the retardation of light is therefore compensated precisely.

20 The light that passed through the liquid crystal layer 33 partially diffracts to diffuse on the latticed TFT circuit pattern 44. However, this diffuse light hardly passes through the second polarizer 35 because the retardation of light in the liquid crystal layer 33 has been precisely compensated by the retardation
25 compensation layer 39. Therefore, a superior black display state can be created.

As described so far, in the liquid crystal display panel 11R, the incident angle of the light to the liquid crystal layer 33 is identical to the incident angle to the retardation
30 compensation layer 39. Therefore, excellent retardation compensation effect is achieved even if the light diffracts on either the microlens array which exhibits a refractive index

difference on the interface or on the opaque TFT circuit pattern 44. This serves to improve the extinction ratio (contrast) of the display image.

The location of the retardation compensation layer 39 is not limited to the above embodiment. For example, the retardation compensation layer 39 may be located between the liquid crystal layer 33 (more properly, the alignment film adjacent to the TFT substrate 34) and the TFT circuit pattern 44, as shown in FIG.4, and it can still provide the same effect. Instead, if the black matrix 40 causes the diffraction of light, the retardation compensation layer 39 may be located between the wiring pattern of the common electrodes 41 and the liquid crystal layer 33 (more properly, the alignment film adjacent to the opposed substrate 32). Namely, the retardation compensation layer should be located closer to the liquid crystal layer than a diffraction-causing structure is.

The liquid crystal display device of the present invention is also applicable to a color liquid crystal projector with, for example, a single liquid crystal display panel and a color mosaic filter. In addition, the liquid crystal layer 33 is not limited to the TN mode, but may have another operating mode. In this case, the retardation compensation layer is tailored for the selected operating mode.

For example, if the liquid crystal layer is a VAN mode liquid crystal layer in which nematic liquid crystal molecules are aligned vertical to the substrate surfaces when no voltage is applied thereto, the retardation compensation layer 39 is to have an optical axis vertical to the substrate surfaces and function as a C-plate for providing uniaxial birefringence. In order to compensate positive retardation induced by the VAN mode liquid crystal, this C-plate is configured to exhibit negative refractive index anisotropy. A preferable C-plate is, for example,

a form birefringence made by layering inorganic materials with high and low refractive indices alternately. Furthermore, if an A-plate having an optical axis parallel to the substrate surface and an O-plate having an optical axis oblique to the substrate surface are layered along with the C-plate, the retardation of light in the VAN mode liquid crystal layer can be compensated more effectively.

The effect of the present invention is examined in the following tests. Firstly prepared is a TFT substrate with a wiring pattern of pixels. A laser beam is irradiated to the TFT substrate, and diffracted light is observed. Secondly prepared is a VAN mode liquid crystal cell having vertical alignment films on inner surfaces of a pair of substrates. This VAN mode liquid crystal cell is disposed between the light source and the TFT substrate, and a laser beam is irradiated to it. Again, diffracted light is observed. Thirdly prepared is a retardation compensation element that functions as a negative C-plate. The liquid crystal cell, the TFT substrate, and the retardation compensation element are arranged in this order from the light source side, and a laser beam is irradiated to them. Diffracted light is also observed and, at this time, the retardation of the zeroth-order diffracted light is compensated by the retardation compensation element while the first-order or higher-order diffracted light contains the retardation component which is not compensated completely. Finally, the constituents are rearranged in the order of the liquid crystal cell, the retardation compensation element, and the TFT substrate from the light source side. A laser beam is irradiated in the same manner and, at this time, the retardation is compensated for the first-order and higher-order diffracted light as well as the zeroth-order diffracted light.

Above mentioned experiment clearly demonstrates that it is effective to place the retardation compensation layer between the

minute structure and the liquid crystal layer in order to compensate the retardation precisely.

Various changes and modifications are possible in the present invention and may be understood to be within the present
5 invention.

Industrial Applicability

The present invention is preferably applied to TFT type liquid crystal display devices and liquid crystal projectors
10 having such liquid crystal display device.

CLAIMS

1. A liquid crystal display device having a structure of minute elements arranged at a regular pitch that possibly causes diffraction of visible spectrum light, and a liquid crystal layer enclosed by a pair of substrates, said liquid crystal display device comprising:

a retardation compensation layer located between said structure and said liquid crystal layer, said retardation compensation layer compensating retardation of light occurred in said liquid crystal layer.

2. A liquid crystal display device described in claim 1, wherein said retardation compensation layer is provided on one of said substrates.

3. A liquid crystal display device described in claim 1, wherein said retardation compensation layer is provided on both of said substrates.

4. A liquid crystal display device described in claim 1, wherein said structure is integrated with one of said substrates.

5. A liquid crystal display device described in claim 4, wherein said structure has a plurality of microlenses in a matrix arrangement.

6. A liquid crystal display device described in claim 4, wherein said structure has a TFT circuit pattern.

7. A liquid crystal projector including a liquid crystal display device which has a structure of minute elements arranged

at regular intervals that possibly cause diffraction of visible spectrum light, and a liquid crystal layer enclosed by a pair of substrates, said liquid crystal display device comprising:

5 a retardation compensation layer located between said structure and said liquid crystal layer, said retardation compensation layer compensating retardation of light occurred in said liquid crystal layer.

10 8. A liquid crystal projector described in claim 7, wherein said retardation compensation layer is provided on one of said substrates.

15 9. A liquid crystal projector described in claim 7, wherein said retardation compensation layer is provided on both of said substrates.

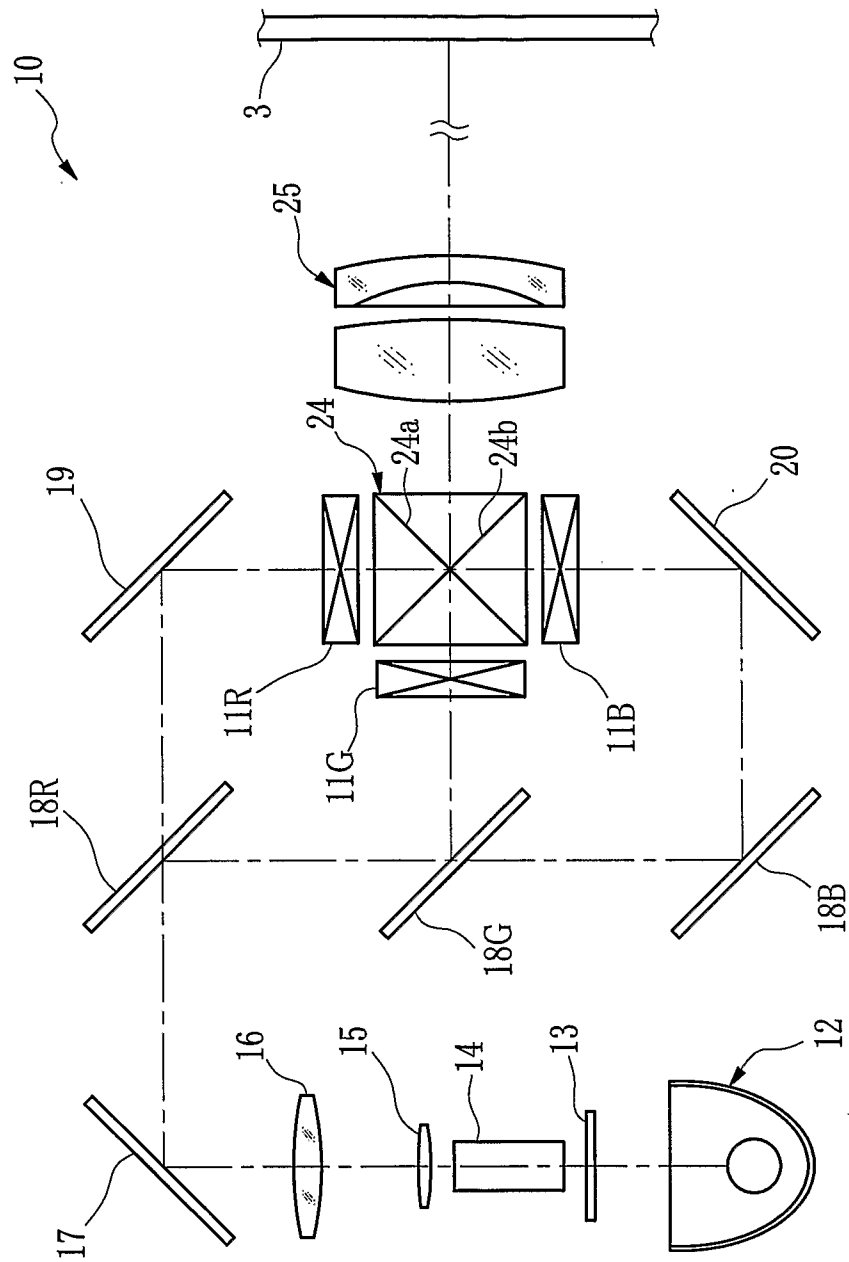
10. A liquid crystal projector described in claim 7, wherein said structure is integrated with one of said substrates.

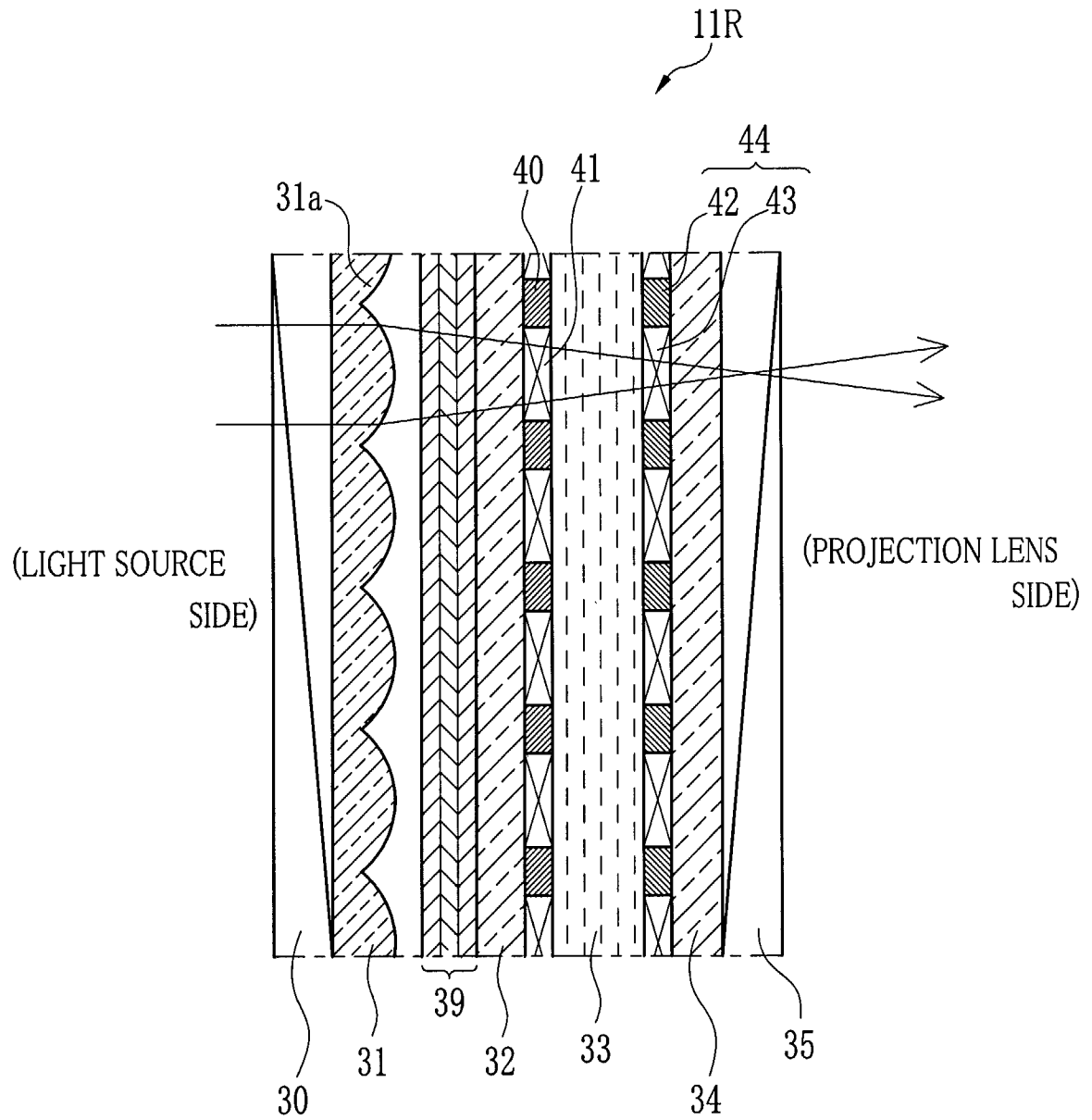
20 11. A liquid crystal projector described in claim 10, wherein said structure has a plurality of microlenses in a matrix arrangement.

25 12. A liquid crystal projector described in claim 10, wherein said structure has a TFT circuit pattern.

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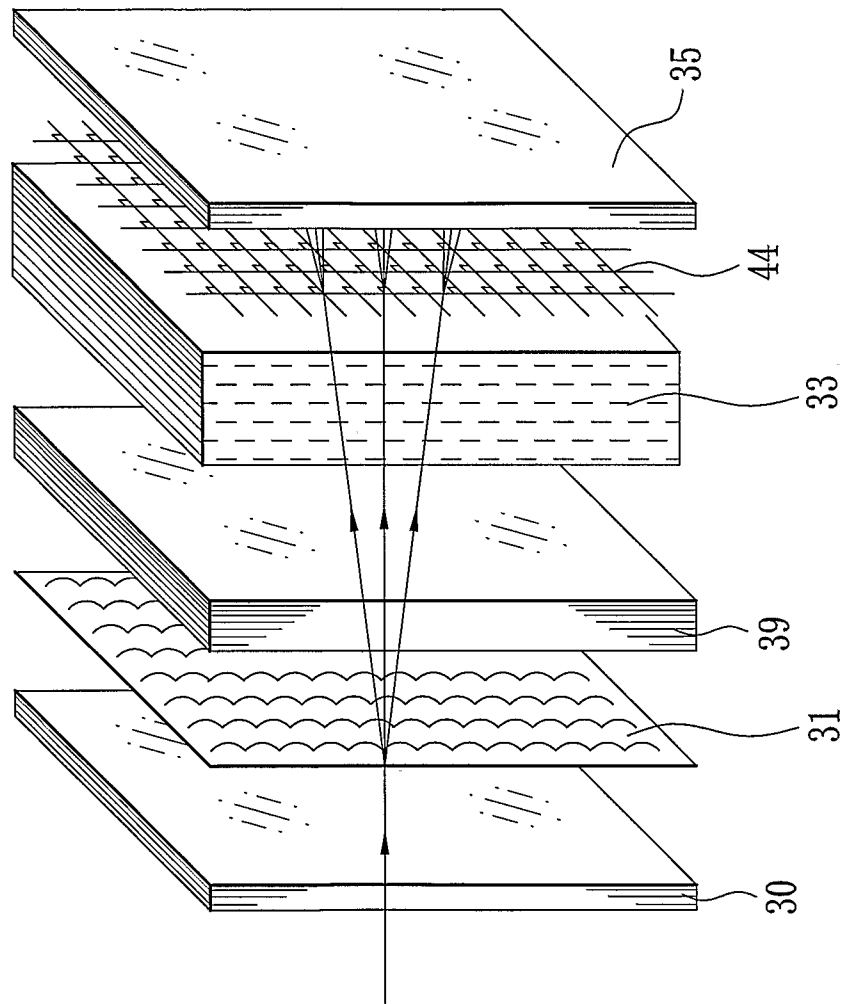
FIG. 1



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FIG. 2

3/5

FIG. 3



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FIG. 4

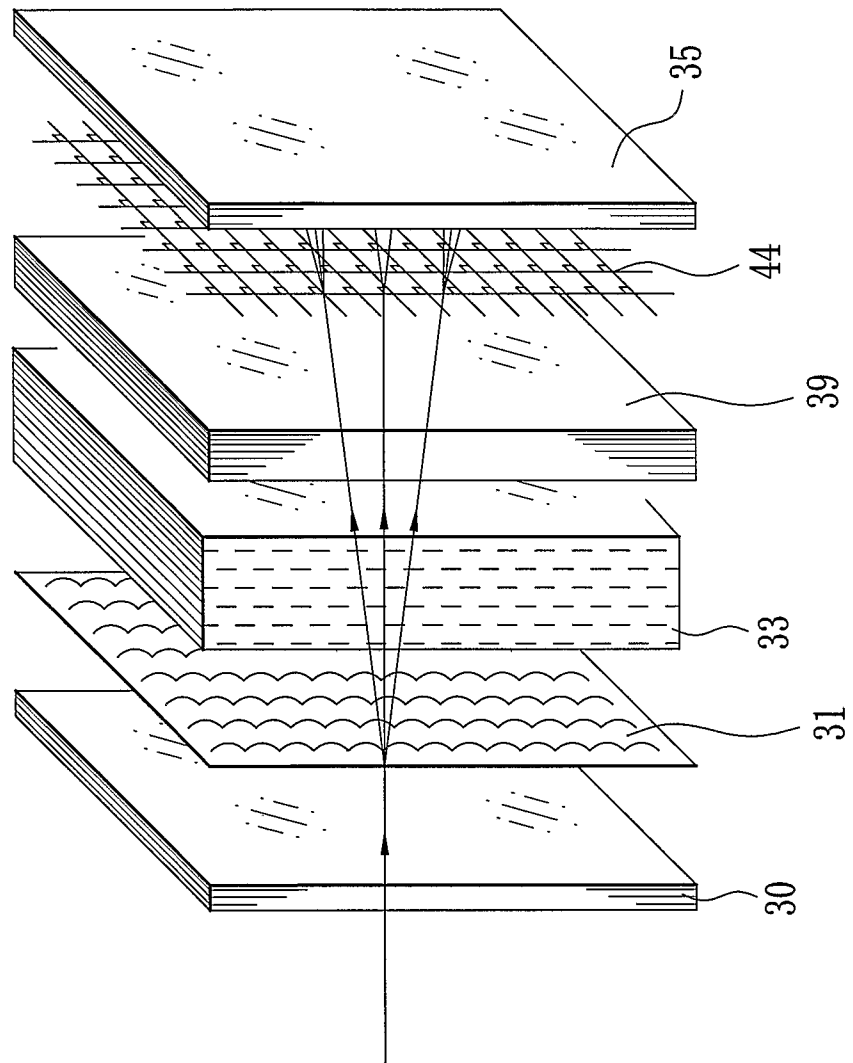
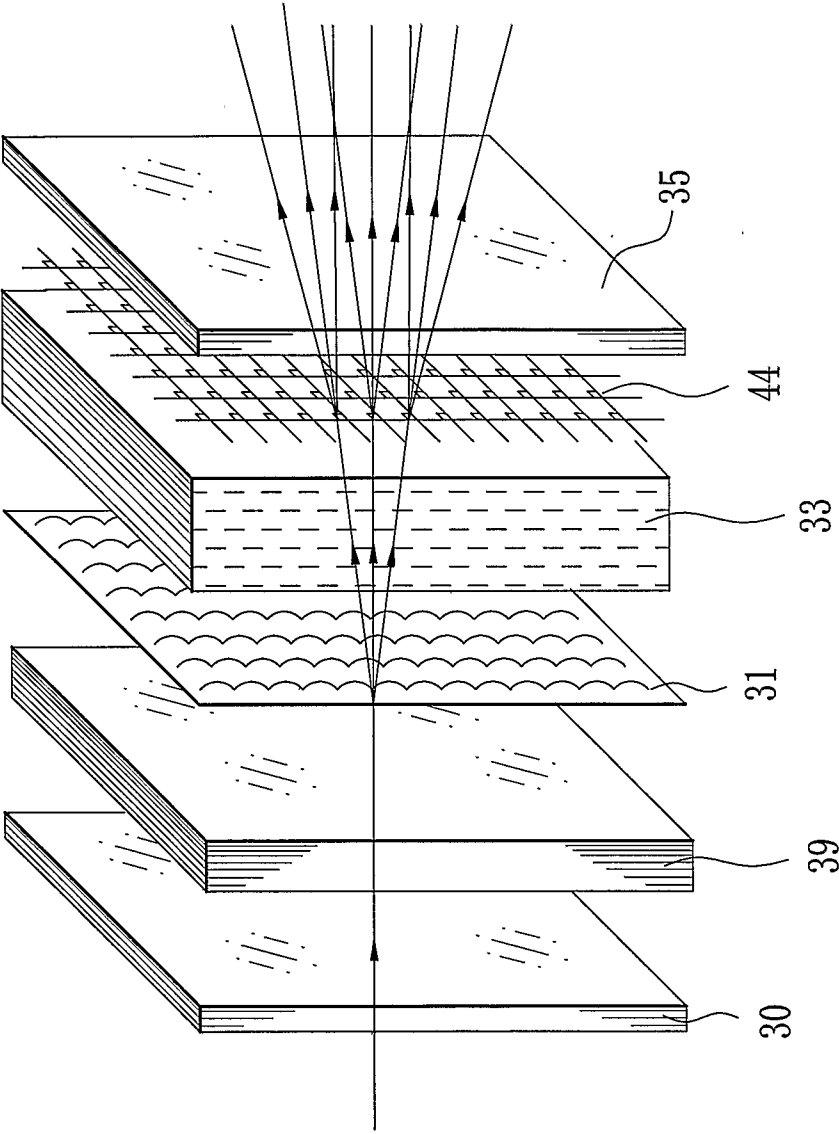


FIG. 5
PRIOR ART



INTERNATIONALSEARCHREPORT

International application No.

PCT/JP2006/313618

A. CLASSIFICATION OF SUBJECT MATTER

Int.Cl. G02F1/13363 (2006.01) i, G02F1/13 (2006.01) i

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

Int.Cl. G02F1/13363, G02F1/13

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Published examined utility model applications of Japan 1922-1996
 Published unexamined utility model applications of Japan 1971-2006
 Registered utility model specifications of Japan 1996-2006
 Published registered utility model applications of Japan 1994-2006

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	WO 2004/010712 A1 (FUJI PHOTO FILM CO., LTD.) 2004.01.29, FIG.7, 15-16 &JP 2004-163450 A &JP 2004-102200 A &US 2005/0168662 A1	1-2

☐ Further documents are listed in the continuation of Box C.
 ☐ See patent family annex.

* Special categories of cited documents:

“A” document defining the general state of the art which is not considered to be of particular relevance

“E” earlier application or patent but published on or after the international filing date

“L” document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

“O” document referring to an oral disclosure, use, exhibition or other means

“P” document published prior to the international filing date but later than the priority date claimed

“T” later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

“X” document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

“Y” document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

“&” document member of the same patent family

Date of the actual completion of the international search

21.09.2006

Date of mailing of the international search report

10.10.2006

Name and mailing address of the ISA/JP

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21 9411

Box No. II Observations where certain claims were found unsearchable (Continuation of item 2 of first sheet)

This international search report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons:

1. ☐ Claims Nos.:
because they relate to subject matter not required to be searched by this Authority, namely:

2. ☐ Claims Nos.:
because they relate to parts of the international application that do not comply with the prescribed requirements to such an extent that no meaningful international search can be carried out, specifically:

3. ☐ Claims Nos.:
because they are dependent claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a).

Box No. III Observations where unity of invention is lacking (Continuation of item 3 of first sheet)

This International Searching Authority found multiple inventions in this international application, as follows:

A liquid crystal display device having microlenses in a matrix arrangement and a liquid crystal layer enclosed by a pair of substrates, said liquid crystal display device comprising: a retardation compensation layer located between said microlenses and said liquid crystal layer, said retardation compensation layer compensating retardation of light occurred in said liquid crystal layer is described in Document 1: WO 2004/010712 A1 (FUJI PHOTO FILM CO., LTD.). Accordingly, the inventions in Claims 1 and 7 do not appear to be novel and to involve an inventive step. Since the common matter described in Claims 1-12 makes no contribution over the prior art, there is no special technical features in PCT Rule 13.2 between Claims (1-2) and Claims (3), (4-6), (7-8), (9), (10-12). As a result, these inventions do not fulfill the requirement of unity of invention.

1. ☐ As all required additional search fees were timely paid by the applicant, this international search report covers all searchable claims.
2. ☐ As all searchable claims could be searched without effort justifying additional fees, this Authority did not invite payment of additional fees.
3. ☒ As only some of the required additional search fees were timely paid by the applicant, this international search report covers only those claims for which fees were paid, specifically claims Nos.:
1-2
4. ☐ No required additional search fees were timely paid by the applicant. Consequently, this international search report is restricted to the invention first mentioned in the claims; it is covered by claims Nos.:

Remark on Protest

- ☐ The additional search fees were accompanied by the applicant's protest and, where applicable, the payment of a protest fee.
- ☐ The additional search fees were accompanied by the applicant's protest but the applicable protest fee was not paid within the time limit specified in the invitation.
- ☒ No protest accompanied the payment of additional search fees.