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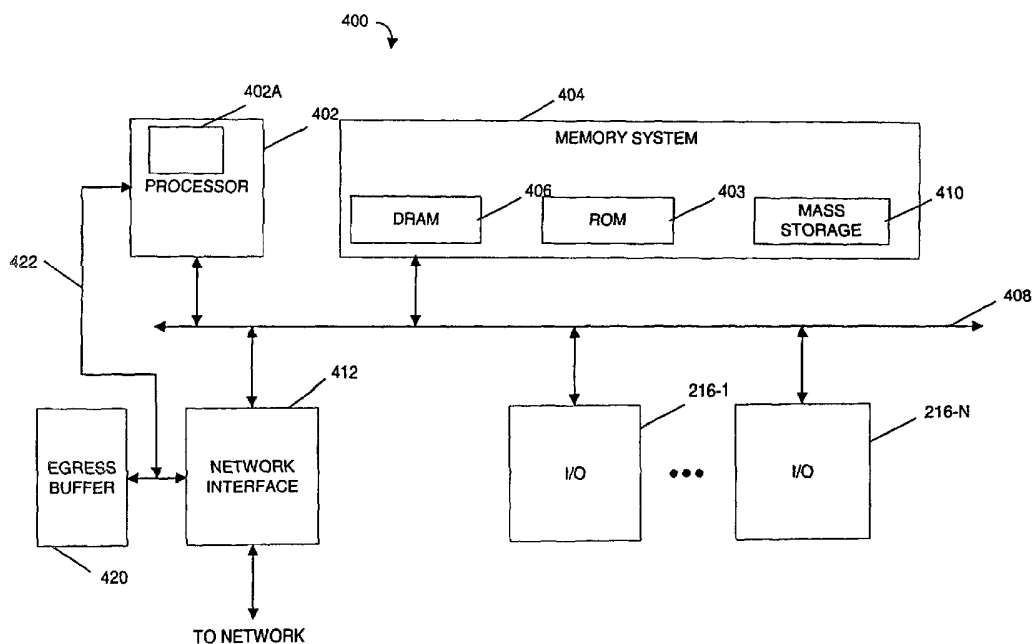


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- For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.*

(54) Title: SYSTEM AND METHOD FOR CACHING DRAM USING AN EGRESS BUFFER



(57) Abstract: A system and method includes a server that includes a processor and a memory system coupled that are coupled to a bus system. A network interface is coupled to the processor and an egress buffer is coupled to the processor and the network interface by an egress bus.

INTERNATIONAL SEARCH REPORT

Internat Application No
PCT/US 02/33441

A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 G06F13/00

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H04L G06F H04N

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ, INSPEC

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	WEEKS M ET AL: "Improved multimedia server I/O subsystems" EUROMICRO CONFERENCE, 1998. PROCEEDINGS. 24TH VASTERAS, SWEDEN 25-27 AUG. 1998, LOS ALAMITOS, CA, USA, IEEE COMPUT. SOC, US, 25 August 1998 (1998-08-25), pages 514-519, XP010297981 ISBN: 0-8186-8646-4 abstract	1-4, 7, 9-19
A	page 515, left-hand column, paragraph 6 -page 516, right-hand column, paragraph 7 page 518, right-hand column, paragraph 4. --- -/--	5, 6, 8

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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Date of the actual completion of the international search

19 June 2003

Date of mailing of the international search report

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INTERNATIONAL SEARCH REPORT

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Application No

PCT/US 02/33441

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT		
Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	<p>CHAME A: "PCI bus in high speed I/O systems applications" AEROSPACE CONFERENCE, 1998 IEEE SNOWMASS AT ASPEN, CO, USA 21-28 MARCH 1998, NEW YORK, NY, USA, IEEE, US, 21 March 1998 (1998-03-21), pages 505-514, XP010286942 ISBN: 0-7803-4311-5 abstract page 505, right-hand column, paragraph 3 -page 506, right-hand column, paragraph 1 page 513, left-hand column, paragraph 1 -page 514, left-hand column, paragraph 2; figure 5</p> <p style="text-align: center;">---</p>	1,11
X	<p>HENRY D S ; JOERG C F: "A tightly-coupled processor-network interface" FIFTH INTERNATIONAL CONFERENCE ON ARCHITECTURAL SUPPORT FOR PROGRAMMING LANGUAGES AND OPERATING SYSTEMS (ASPLOS-V), 'Online! vol. 27, no. 9, 12 - 15 October 1992, pages 111-122, XP002244857 Boston, USA Retrieved from the Internet: <URL:http://citeseer.nj.nec.com/cache/pape rs/cs/1284/ftp:zSzzSzcszSg-ftp.lcs.mit.edu/S zpubzSzpaperszSzcszSgmemozSzmemo-342.pdf/hen ry92tightlycoupled.pdf> 'retrieved on 2003-06-18! page 117, right-hand column, paragraph 3.1 -page 118, left-hand column, paragraph 3.2; figures 8,10,11</p> <p style="text-align: center;">---</p>	1,11
X	<p>US 5 974 496 A (MILLER STEVEN M) 26 October 1999 (1999-10-26) column 2, line 21 - line 35 column 3, paragraph 4 - paragraph 5 figure 1</p> <p style="text-align: center;">-----</p>	1,11

INTERNATIONAL SEARCH REPORT

Intern:	Application No
PCT/US	02/33441

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 5974496	A	26-10-1999	NONE