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(54) Title: DEPOSITION OF FLOWABLE SILICON-CONTAINING FILMS

(57) Abstract: Methods for seam-less gapfill comprising forming a flowable film by exposing a substrate surface to a silicon-containing precursor and a co-reactant are described. The silicon-containing precursor has at least one akenyl or alkynyl group. The flowable film can be cured by any suitable curing process to form a seam-less gapfill.

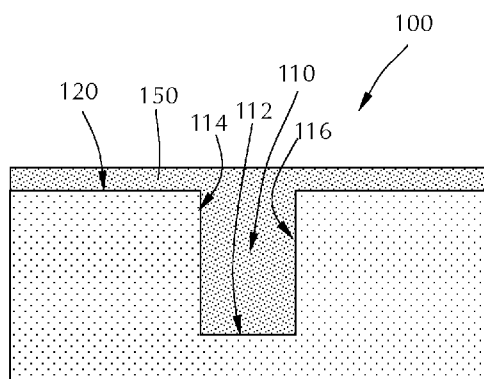


FIG. 2



DEPOSITION OF FLOWABLE SILICON-CONTAINING FILMS

TECHNICAL FIELD

[0001] The present disclosure relates generally to methods of depositing thin films. In particular, the disclosure relates to processes for filling narrow trenches with flowable silicon-containing films.

BACKGROUND

[0002] In microelectronics device fabrication there is a need to fill narrow trenches having aspect ratios (AR) greater than 10:1 with no voiding for many applications. One application is for shallow trench isolation (STI). For this application, the film needs to be of high quality throughout the trench (having, for example, a wet etch rate ratio less than two) with very low leakage. As the dimensions of the structures decrease and the aspect ratios increase post curing methods of the as deposited flowable films become difficult. Resulting in films with varying composition throughout the filled trench.

[0003] Conventional plasma-enhanced chemical vapor deposition (PECVD) of dielectric films form a “mushroom shape” film on top of the narrow trenches. This is due to the inability of the plasma to penetrate into the deep trenches. The results in pinching-off the narrow trench from the top; forming a void at the bottom of the trench.

[0004] Additionally, silicon-containing films, such as SiCO, SiCON, SiCN, are widely used in the manufacture of semiconductor devices. For example, these carbon-containing gap-fill films can be used for patterning applications. Due to the presence of high carbon levels, these films typically show high etch selectivity compared to oxide and nitride films. The etch selectivity can be important for gap-fill films to be used in patterning applications. Therefore, there is a need for precursors and methods for depositing silicon-containing films.

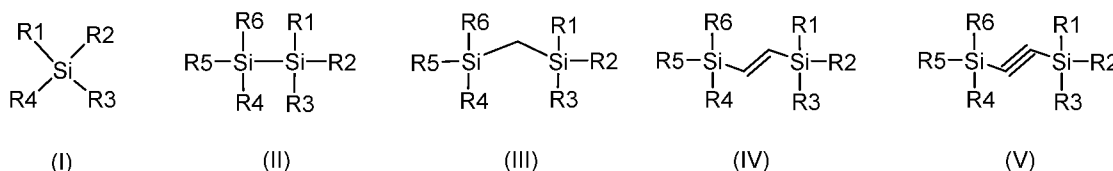
SUMMARY

[0005] One or more embodiments of the disclosure are directed to processing methods comprising exposing a substrate surface to a silicon-containing precursor

and a co-reactant to deposit a flowable film. The silicon-containing precursor has at least one akenyl or alkynyl group.

[0006] Additional embodiments of the disclosure are directed to processing methods comprising providing a substrate surface having at least one feature thereon.

- 5 The at least one feature extends a depth from the substrate surface to a bottom surface and has a width defined by a first sidewall and a second sidewall. The substrate surface is exposed to a silicon-containing precursor and co-reactant to form a flowable film on the substrate surface and the first sidewall, second sidewall and bottom surface of the at least one feature. The flowable film fills the feature with
- 10 substantially no seam formed. The silicon containing precursor comprises a compound having any of Structures I-V



where each of R1-R6 is independently selected from the group consisting of CR'CR''₂, CCR', H, methyl, ethyl, propyl, isopropyl, *n*-butyl, *sec*-butyl, isobutyl, *tert*-butyl, NR'₂ and OR', where R' and R'' are independently selected from the group consisting of H, methyl, ethyl, propyl, isopropyl, *n*-butyl, *sec*-butyl, isobutyl and *tert*-butyl, at least one of R1-R6 is selected from the group consisting of CR'CR''₂ or CCR'. The co-reactant comprises an ammonia plasma. The flowable film is cured to solidify the film and form a substantially seam-free gapfill.

- 20 **[0007]** Further embodiments of the disclosure are directed to processing methods comprising providing a substrate surface having at least one feature thereon. The at least one feature extends a depth from the substrate surface to a bottom surface and has a width defined by a first sidewall and a second sidewall. The substrate surface is exposed to a silicon-containing precursor and co-reactant to form a flowable film on
- 25 the substrate surface and the first sidewall, second sidewall and bottom surface of the at least one feature. The flowable film fills the feature with substantially no seam formed. The silicon containing precursor comprises one or more of tetravinylsilane (Si(CHCH₂)₄) or silicon tetraacetalide (Si(CCH)₄), and the co-reactant comprises an

ammonia plasma. The flowable film is cured to solidify the film and form a substantially seam-free gapfill.

BRIEF DESCRIPTION OF THE DRAWINGS

[0008] So that the manner in which the above recited features of the present invention can be understood in detail, a more particular description of the invention, briefly summarized above, may be had by reference to embodiments, some of which are illustrated in the appended drawings. It is to be noted, however, that the appended drawings illustrate only typical embodiments of this invention and are therefore not to be considered limiting of its scope, for the invention may admit to other equally effective embodiments.

[0009] FIG. 1 shows a cross-sectional view of a substrate feature in accordance with one or more embodiment of the disclosure;

[0010] FIG. 2 shows a cross-sectional view of the substrate feature of FIG. 1 with a flowable film thereon; and

[0011] FIG. 3 shows an SEM image of a film deposited in accordance with one or more embodiment of the disclosure.

DETAILED DESCRIPTION

[0012] Before describing several exemplary embodiments of the invention, it is to be understood that the invention is not limited to the details of construction or process steps set forth in the following description. The invention is capable of other embodiments and of being practiced or being carried out in various ways.

[0013] A "substrate" as used herein, refers to any substrate or material surface formed on a substrate upon which film processing is performed during a fabrication process. For example, a substrate surface on which processing can be performed include materials such as silicon, silicon oxide, strained silicon, silicon on insulator (SOI), carbon doped silicon oxides, amorphous silicon, doped silicon, germanium, gallium arsenide, glass, sapphire, and any other materials such as metals, metal nitrides, metal alloys, and other conductive materials, depending on the application. Substrates include, without limitation, semiconductor wafers. Substrates may be

exposed to a pretreatment process to polish, etch, reduce, oxidize, hydroxylate, anneal, UV cure, e-beam cure and/or bake the substrate surface. In addition to film processing directly on the surface of the substrate itself, in the present invention, any of the film processing steps disclosed may also be performed on an underlayer
5 formed on the substrate as disclosed in more detail below, and the term "substrate surface" is intended to include such underlayer as the context indicates. Thus for example, where a film/layer or partial film/layer has been deposited onto a substrate surface, the exposed surface of the newly deposited film/layer becomes the substrate surface.

10 **[0014]** Embodiments of the disclosure provide methods of depositing a gap-fill film (e.g., SiC, SiCO, SiCN, SiCON) in high aspect ratio (AR) structures with small dimensions. Some embodiments advantageously provide methods involving cyclic deposition-treatment processes that can be performed in a cluster tool environment. Some embodiments advantageously provide seam-free high quality silicon-containing
15 films to fill up high AR trenches with small dimensions. Some embodiments advantageously provide films containing high carbon content. In one or more embodiments, the high carbon content films can be advantageously used in hard mask and low-k flowable applications.

[0015] One or more embodiments of the disclosure are directed to processes
20 where flowable silicon-containing films are deposited which are able to fill high aspect ratio structures (e.g., AR >8:1). Embodiments of the disclosure provide new precursors to generate SiC, SiOC, SiCN, SiOCN, SiO and SiN flowable films using F-CVD (flowable chemical vapor deposition) for gapfill applications. The precursors of various embodiments include alkenyl (vinyl) and/or alkynyl groups. In one or more
25 embodiments, the precursors are exposed to reactive radicals to initiate a radical induced polymerization in the deposition chamber.

[0016] Flowable films, as deposited, are generally not stable and age upon exposure to atmospheric conditions. The flowable films of some embodiments are deposited by a silicon-containing precursor and radical forms of, for example, NH₃/O₂
30 as co-reactants. These films are then cured by ozone/UV/steam annealing/NH₃ annealing, etc., which results in a cured film.

[0017] For descriptive purposes, the deposition of flowable CVD films for gapfill applications is described. However, those skilled in the art will understand that the precursors and methods described are not limited to gapfill applications and can be used for any silicon-containing films. FIG. 1 shows a partial cross-sectional view of a substrate 100 with a feature 110. The Figures show substrates having a single feature for illustrative purposes; however, those skilled in the art will understand that there can be more than one feature. The shape of the feature 110 can be any suitable shape including, but not limited to, trenches and cylindrical vias. As used in this regard, the term "feature" means any intentional surface irregularity. Suitable examples of features include, but are not limited to trenches which have a top, two sidewalls and a bottom, peaks which have a top and two sidewalls. Features can have any suitable aspect ratio (ratio of the depth of the feature to the width of the feature). In some embodiments, the aspect ratio is greater than or equal to about 5:1, 10:1, 15:1, 20:1, 25:1, 30:1, 35:1 or 40:1.

[0018] The substrate 100 has a substrate surface 120. The at least one feature 110 forms an opening in the substrate surface 120. The feature 110 extends from the substrate surface 120 to a depth D to a bottom surface 112. The feature 110 has a first sidewall 114 and a second sidewall 116 that define a width W of the feature 110. The open area formed by the sidewalls and bottom are also referred to as a gap.

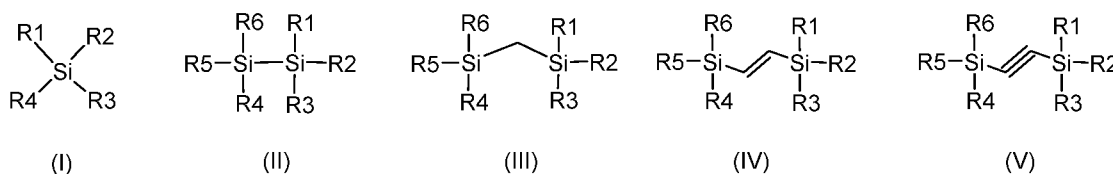
[0019] One or more embodiments of the disclosure are directed to processing methods in which a substrate surface with at least one feature thereon is provided. As used in this regard, the term "provided" means that the substrate is placed into a position or environment for further processing.

[0020] As shown in FIG. 2, a flowable film 150 is formed on the substrate surface 120 and the first sidewall 114, second sidewall 116 and bottom surface 112 of the at least one feature 110. The flowable film 150 fills the at least one feature 110 so that substantially no seam is formed. A seam is a gap that forms in the feature between, but not necessarily in the middle of, the sidewalls of the feature 110. As used in this regard, the term "substantially no seam" means that any gap formed in the film between the sidewalls is less than about 1% of the cross-sectional area of the sidewall.

[0021] The flowable film 150 can be formed by any suitable process. In some embodiments, the forming the flowable film is done by plasma-enhanced chemical vapor deposition (PECVD). Stated differently, the flowable film can be deposited by a plasma-enhanced chemical vapor deposition process.

5 **[0022]** Embodiments of the disclosure advantageously provide a class of silicon-containing precursors for use in deposition of flowable CVD films. Some embodiments advantageously provide methods of gapfilling without formation of a seam. Some embodiments advantageously provide methods for curing a flowable CVD film in which a trench or surface feature is filled in a seamless manner.

10 **[0023]** Embodiments of the disclosure are directed to processing methods comprising exposing a substrate surface to a silicon-containing precursor and a co-reactant to deposit a flowable film. The silicon-containing precursor has at least one akenyl or alkynyl group to allow participation in a radical polymerization reaction. Structures I-V provide examples of silicon-containing precursors for use with various
15 embodiments of the disclosure.



The R groups (R1-R6) of formula (I) are each independently selected from the group consisting of CR'CR''₂, CCR', H, methyl, ethyl, propyl, isopropyl, n-butyl, sec-butyl, isobutyl, tert-butyl, NR'₂ and OR'. The R' and R'' groups are independently selected from the group consisting of H, methyl, ethyl, propyl, isopropyl, n-butyl, sec-butyl, isobutyl and tert-butyl. At least one of R1-R6 (R1-R4 for Formula I) is selected from the group consisting of CR'CR''₂ or CCR'. Those skilled in the art will understand that the use of the term "R groups" refers to any of R1 through R6 (R1-R4 for Formula I).

20 In some embodiments, at least one of the R groups is not a hydrogen. In some embodiments, at least one of the R groups is a methyl group. In some embodiments, at least one of the R groups is an ethyl group. In some embodiments, at least one R

group is a vinyl group. In some embodiments, each of the R groups is the same group.

[0024] In one or more embodiments, the silicon precursor comprises an alkenyl group so that one or more of the R groups of the silicon-containing precursor comprises CR'CR''₂. In some embodiments, all of the R groups of the silicon-containing precursor comprises CR'CR''₂. In one or more embodiments, substantially all of the R groups of the silicon precursor comprises CR'CR''₂. As used in this regard, "substantially all" means that greater than or equal to about 95% of the R groups are the specified group. In some embodiments, each R' is the same substituent and each R'' is the same substituent. In one or more embodiments, the silicon-containing precursor comprises tetravinylsilane (Si(CHCH₂)₄).

[0025] In one or more embodiments, the silicon-containing precursor comprises an alkynyl group so that one or more of the R groups of the silicon-containing precursor comprises CCR'. In some embodiments, all of R groups of the silicon-containing precursor comprises CCR'. In one or more embodiments, substantially all of the R groups of the silicon precursor comprises CCR'. As used in this regard, "substantially all" means that greater than or equal to about 95% of the R groups are the specified group. In some embodiments, each R' is the same substituent and each R'' is the same substituent. In one or more embodiments, the silicon-containing precursor comprises silicon tetraacetalide (Si(CCH)₄).

[0026] A Si-containing precursor with any of Structures I-V may be vaporized to a CVD chamber, and a suitable co-reactant (e.g., NH₃/O₂/CO₂/CO/Ar/He/H₂, or combination thereof) can be delivered to the chamber through, for example, a RPS (remote plasma source) which will generate plasma active species as the co-reactants. Plasma activated co-reactant molecules (radicals) have high energies and may react with Si-containing precursor molecules in the gas phase to form corresponding flowable polymers. In some embodiments, the plasma is generated with a plasma gas that comprises one or more of NH₃, O₂, CO₂, CO, Ar, He or H₂. In some embodiments, the plasma gas comprises ammonia, or consists essentially of ammonia. As used in this regard, the term "consists essentially of" means that greater

than or equal to about 90 molecular % of the reactive species in the plasma are ammonia.

[0027] The plasma can be generated or ignited within the processing chamber (e.g., a direct plasma) or can be generated outside of the processing chamber and
5 flowed into the processing chamber (e.g., a remote plasma).

[0028] The flowable film 150 can be formed at any suitable temperature. In some embodiments, the flowable film 150 is formed at a temperature in the range of about -20 °C to about 100 °C. The temperature can be kept low to preserve the thermal budget of the device being formed. In some embodiments, forming the flowable film
10 occurs at a temperature less than about 300 °C, 250 °C, 200 °C, 150 °C, 100 °C, 75 °C, 50 °C, 25 °C or 0 °C.

[0029] Films with high carbon content may have many applications, for example, in the patterning of hard masks and for flowable low K films. In some embodiments, the flowable film using precursors of any of Structures I-V can deposit films which high
15 carbon content. In some embodiments, the film has a carbon content up to about 85 atomic %. In one or more embodiments, the flowable film has a carbon content greater than about 40, 45, 50, 55, 60, 65, 70, 75 or 80 atomic %, and less than about 95, 90 or 85 atomic %. In some embodiments, the flowable film has a carbon content in the range of about 40 to about 85 atomic %, or in the range of about 50 to about 85
20 atomic %, or in the range of about 60 to about 80 atomic %.

[0030] The composition of the flowable film can be adjusted by changing the composition of the reactive gas. In some embodiments, the flowable film comprises one or more of SiC, SiCO, SiCN, SiCON, SiO and SiN. To form an oxygen containing film, the co-reactant may comprise, for example, one or more of oxygen, ozone or
25 water. To form a nitrogen containing film, the co-reactant may comprise, for example, one or more of ammonia, hydrazine, NO₂ or N₂. To form a carbon containing film, the reactive gas may comprise, for example, one or more of propylene and acetylene. Those skilled in the art will understand that combinations of or other species can be included in the reactive gas mixture to change the composition of the flowable film.

[0031] The flowable film may deposit on the wafer (temperature of the wafer can be from -10 °C to 200 °C.) and due to their flowability, polymers will flow through
30

trenches and make a gap-fill. Then these films are subjected curing steps such as ozone/UV/steam annealing/ NH_3 annealing to get stable films. In some embodiments, the method provides one or more of SiC, SiCO, SiCN, SiCON, SiO, and/or SiN flowable films. Accordingly, after formation of the flowable film 150, the film may be

5 cured to solidify the flowable film and form a substantially seam-free gapfill. In one or more embodiments, curing the flowable film comprises exposing the flowable film to one or more of ozone, UV light, steam annealing, ammonia annealing and oxygen plasma. In some embodiments, the flowable film is cured by exposing the film to a UV curing process. The UV curing process can occur at a temperature in the range of

10 about 10 °C to about 550 °C. The UV curing process can occur for any suitable time frame necessary to sufficiently solidify the flowable film. The UV cure can be performed with different parameters, e.g., power, temperature, environment. In some embodiments, the UV cure occurs in an acetylene/ethylene environment.

[0032] In some embodiments, curing the flowable film comprises thermal annealing. Thermal annealing can occur at any suitable temperature and any suitable

15 environment. In some embodiments, the flowable film is cured by thermal annealing in an acetylene/ethylene environment.

[0033] In some embodiments, curing the flowable film comprises exposure to a plasma or an electron beam. A plasma exposure to cure the film comprises a plasma

20 separate from the PECVD plasma. The plasma species and processing chamber can be the same and the plasma cure is a different step than the PECVD process.

[0034] In some embodiments, curing the flowable film comprises exposing the flowable film to a steam anneal and/or oxygen plasma. The use of a steam anneal and/or oxygen plasma may reduce the carbon content of the flowable film so that the

25 cured film has a lower carbon content than the as-deposited flowable film. The use of steam anneal and/or oxygen plasma may convert the deposited flowable SiC, SiCN, or SiCO film to SiO. The carbon content of such films has been substantially removed; meaning that there is less than about 5% of the carbon present in the film pre-curing.

[0035] In some embodiments, the precursor of any of Structures I-V can be used

30 with another precursor (co-flow with another Si-containing precursor) in a flowable

process to deposit films of various compositions. As an example, precursors of Structures I-V can be used with trisilylamine (TSA)/NH₃ process to incorporate carbon in the film. The flowable films obtained from TSA/NH₃ process are either SiO or SiN films. By the addition of the silicon-containing precursor of Structures I-V, SiCO, SiCON or SiCN films can be deposited. In another example, a silicon-containing precursor can be used with an octamethylcyclotetrasiloxane (OMCTS)/O₂ process. OMCTS/O₂ process gives SiOC films and precursors of Structures I-V can be used to increase C% in the film. SiC, SiOC, SiCN, SiOCN, SiO, and SiN flowable films can be obtained by this method. In a further example, TSA/silanes(Si_xH_y)/OMCTS can mixed or co-flowed with a precursor of Structures I-V to deposit a SiCO, SiCON or SiN film.

[0036] In some embodiments, the silicon precursor of Structures I-V is added into another process to dope carbon into a silicon film. For example, a silicon deposition process may have some of precursors from formula I-V dosed into the process to incorporate carbon atoms into the film. Some embodiments of the disclosure are directed to methods of doping carbon into a film by co-flowing a carbon containing silane with a silylamine to form a flowable film. In one or more embodiments, tetravinylsilane is coflowed with trisilylamine and an ammonia plasma to form a film with a carbon content greater than or equal to about 50%, 60%, 70%, 75% or 80%, on an atomic basis.

[0037] In some embodiments, the flowable film is doped with another element. For example, some flowable films may be doped with one or more of B, As or P. The flowable films can be doped with elements such as boron (B) and phosphorous (P) to improve film properties. Precursors containing boron and phosphorous can be either co-flown with Si-containing precursors during the deposition process or can be infiltrated after the deposition is done. Boron containing precursors can be aminoboranes/boranes compounds and phosphorous containing precursors can be phosphate/phosphite compounds. In some embodiments, doping the flowable film comprises co-flowing a dopant precursor with the silicon-containing precursor. In some embodiments, doping the flowable film comprises implantation of the dopant element in a separate process.

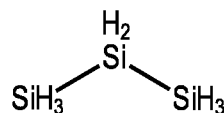
[0038] Some embodiments of the disclosure are directed to methods of obtaining SiC, SiCO, SiCON, SiCN, SiN, SiO flowable films from silane and hydrocarbons. Suitable hydrocarbons include, but are not limited to those showed as Structures VI-XV.



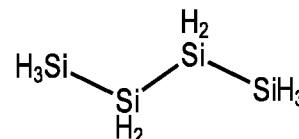
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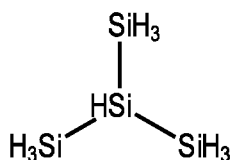
(VII)



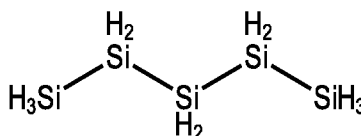
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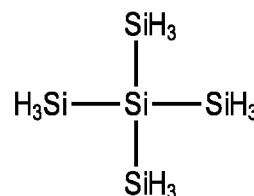
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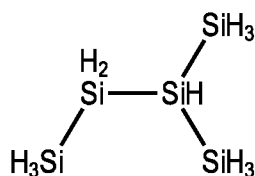
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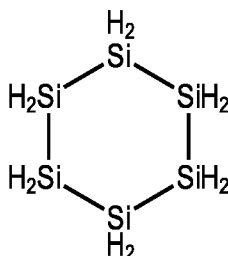
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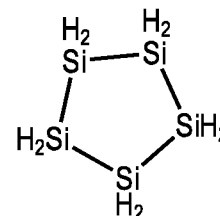
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(XV)

- 5 In some embodiments, the silane precursor comprises a compound with no carbon atoms. Those skilled in the art will understand that the compounds listed as Structures VI-XV are merely representative of some possible silane precursors. The structures illustrated can be modified to include silane substituents or other atoms.

[0039] At least one precursor of Structures I-V and at least one precursor from any hydrocarbon (C_xH_y) can be used in any combination. The silane precursor(s) of Structures I-V and hydrocarbon (C_xH_y) can be vaporized to a CVD chamber, and co-reactants (e.g., $\text{NH}_3/\text{O}_2/\text{CO}_2/\text{CO}/\text{Ar}/\text{He}/\text{H}_2$, or combinations thereof) can be delivered to the chamber through RPS (remote plasma source) which will generate plasma active species as the co-reactants. Plasma activated co-reactant molecules (radicals) have high energies and react with Si-containing precursor molecules in the

gas phase to form corresponding flowable polymers. These polymers will deposit on the wafer (temperature of the wafer can be from -10 C to 300 C.) and due to their flowability, polymers will flow through trenches and make a gap-fill. Then these films are subjected curing steps such as ozone/UV/steam annealing/NH₃ annealing to get stable SiC, SiCO, SiCON, SiCN, SiN, SiO films

[0040] Example 1 - *Deposition of flowable SiCON film from tetravinylsilane (Si(CHCH₂)₄, referred to as TVS) and NH₃ plasma*

[0041] Flowable films were obtained by CVD deposition of tetravinylsilane (TVS) and a remote NH₃ plasma at temperatures in the range of about 80 °C, 100 °C and 130 °C, with a pressure less than about 1 torr. The as-deposited film had a refractive index of 1.58 and a wet etch rate of 1.8 Å/min in 100:1 diluted HF. An SEM image of the flowable film is shown in FIG. 3.

[0042] Example 2 - *Deposition of flowable SiCON film from silicontetraacetalide (Si(CCH)₄ and NH₃ plasma*

[0043] Flowable films were obtained from *silicontetraacetalide* and a remote NH₃ plasma at temperatures in the range of about 0 °C to about 80 °C, 100 °C and 130 °C, with pressures less than about 1 torr. The as-deposited film had a refractive index of about 1.58.

[0044] Example 3 – Increase C% in TSA/NH₃ process by co-flowing TVS with TSA

[0045] Flowable films were obtained by co-flowing TVS/TVA and NH₃ plasma at 0 °C. The flowable films obtained from TSA/NH₃ had almost no carbon in the film while the flowable film obtained by co-flowing TVS and TSA had a carbon content up to 80%.

[0046] According to one or more embodiments, the substrate is subjected to processing prior to and/or after forming the layer. This processing can be performed in the same chamber or in one or more separate processing chambers. In some embodiments, the substrate is moved from the first chamber to a separate, second chamber for further processing. The substrate can be moved directly from the first chamber to the separate processing chamber, or it can be moved from the first chamber to one or more transfer chambers, and then moved to the separate

processing chamber. Accordingly, the processing apparatus may comprise multiple chambers in communication with a transfer station. An apparatus of this sort may be referred to as a "cluster tool" or "clustered system," and the like.

[0047] Generally, a cluster tool is a modular system comprising multiple chambers which perform various functions including substrate center-finding and orientation, degassing, annealing, deposition and/or etching. According to one or more embodiments, a cluster tool includes at least a first chamber and a central transfer chamber. The central transfer chamber may house a robot that can shuttle substrates between and among processing chambers and load lock chambers. The transfer chamber is typically maintained at a vacuum condition and provides an intermediate stage for shuttling substrates from one chamber to another and/or to a load lock chamber positioned at a front end of the cluster tool. Two well-known cluster tools which may be adapted for the present invention are the Centura® and the Endura®, both available from Applied Materials, Inc., of Santa Clara, Calif. However, the exact arrangement and combination of chambers may be altered for purposes of performing specific steps of a process as described herein. Other processing chambers which may be used include, but are not limited to, cyclical layer deposition (CLD), atomic layer deposition (ALD), chemical vapor deposition (CVD), physical vapor deposition (PVD), etch, pre-clean, chemical clean, thermal treatment such as RTP, plasma nitridation, degas, orientation, hydroxylation and other substrate processes. By carrying out processes in a chamber on a cluster tool, surface contamination of the substrate with atmospheric impurities can be avoided without oxidation prior to depositing a subsequent film.

[0048] According to one or more embodiments, the substrate is continuously under vacuum or "load lock" conditions, and is not exposed to ambient air when being moved from one chamber to the next. The transfer chambers are thus under vacuum and are "pumped down" under vacuum pressure. Inert gases may be present in the processing chambers or the transfer chambers. In some embodiments, an inert gas is used as a purge gas to remove some or all of the reactants. According to one or more embodiments, a purge gas is injected at the exit of the deposition chamber to prevent reactants from moving from the deposition chamber to the transfer chamber and/or

additional processing chamber. Thus, the flow of inert gas forms a curtain at the exit of the chamber.

[0049] The substrate can be processed in single substrate deposition chambers, where a single substrate is loaded, processed and unloaded before another substrate is processed. The substrate can also be processed in a continuous manner, similar to a conveyer system, in which multiple substrate are individually loaded into a first part of the chamber, move through the chamber and are unloaded from a second part of the chamber. The shape of the chamber and associated conveyer system can form a straight path or curved path. Additionally, the processing chamber may be a carousel in which multiple substrates are moved about a central axis and are exposed to deposition, etch, annealing, cleaning, etc. processes throughout the carousel path.

[0050] During processing, the substrate can be heated or cooled. Such heating or cooling can be accomplished by any suitable means including, but not limited to, changing the temperature of the substrate support and flowing heated or cooled gases to the substrate surface. In some embodiments, the substrate support includes a heater/cooler which can be controlled to change the substrate temperature conductively. In one or more embodiments, the gases (either reactive gases or inert gases) being employed are heated or cooled to locally change the substrate temperature. In some embodiments, a heater/cooler is positioned within the chamber adjacent the substrate surface to convectively change the substrate temperature.

[0051] The substrate can also be stationary or rotated during processing. A rotating substrate can be rotated continuously or in discrete steps. For example, a substrate may be rotated throughout the entire process, or the substrate can be rotated by a small amount between exposures to different reactive or purge gases. Rotating the substrate during processing (either continuously or in steps) may help produce a more uniform deposition or etch by minimizing the effect of, for example, local variability in gas flow geometries.

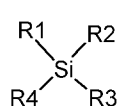
[0052] Reference throughout this specification to "one embodiment," "certain embodiments," "one or more embodiments" or "an embodiment" means that a particular feature, structure, material, or characteristic described in connection with the embodiment is included in at least one embodiment of the invention. Thus, the

appearances of the phrases such as "in one or more embodiments," "in certain embodiments," "in one embodiment" or "in an embodiment" in various places throughout this specification are not necessarily referring to the same embodiment of the invention. Furthermore, the particular features, structures, materials, or
5 characteristics may be combined in any suitable manner in one or more embodiments.

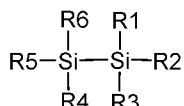
[0053] Although the invention herein has been described with reference to particular embodiments, it is to be understood that these embodiments are merely illustrative of the principles and applications of the present invention. It will be apparent to those skilled in the art that various modifications and variations can be
10 made to the method and apparatus of the present invention without departing from the spirit and scope of the invention. Thus, it is intended that the present invention include modifications and variations that are within the scope of the appended claims and their equivalents.

What is claimed is:

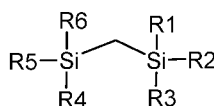
1. A processing method comprising exposing a substrate surface to a silicon-containing precursor and a co-reactant to deposit a flowable film, the silicon-containing precursor having at least one akenyl or alkynyl group.
2. The processing method of claim 1, wherein the co-reactant comprises a plasma comprising a plasma gas.
3. The processing method of claim 2, wherein the silicon-containing precursor comprises a compound having any of Structures I-V,



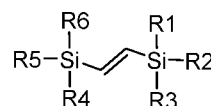
(I)



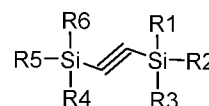
(II)



(III)



(IV)



(V)

where each of R1-R6 is independently selected from the group consisting of CR'CR''₂, CCR', H, methyl, ethyl, propyl, isopropyl, *n*-butyl, *sec*-butyl, isobutyl, *tert*-butyl, NR'₂ and OR',

where R' and R'' are independently selected from the group consisting of H, methyl, ethyl, propyl, isopropyl, *n*-butyl, *sec*-butyl, isobutyl and *tert*-butyl,

wherein at least one of R1-R6 is selected from the group consisting of CR'CR''₂ or CCR'.

4. The processing method of claim 3, wherein one or more of R1-R6 of the silicon-containing precursor is CR'CR''₂.
5. The processing method of claim 4, wherein all of R1-R6 of the silicon-containing precursor comprises CR'CR''₂.
6. The processing method of claim 5, wherein the silicon-containing precursor comprises tetravinylsilane (Si(CHCH₂)₄).

7. The processing method of claim 3, wherein one or more of R1-R6 of the silicon-containing precursor is CCR'.
- 5 8. The processing method of claim 7, wherein all of R1-R6 of the silicon-containing precursor comprises CCR'.
9. The processing method of claim 5 or 8, wherein each R' is the same and each R'' is the same substituent.
- 10 10. The processing method of claim 9, wherein the silicon-containing precursor comprises silicon tetraacetalide ($\text{Si}(\text{CCH})_4$).
11. The processing method of claim 2, wherein the plasma gas comprises one or more of NH_3 , O_2 , CO_2 , CO , Ar , He or H_2 .
- 15 12. The processing method of claim 2, further comprising curing the flowable film to form a cured film.
- 20 13. The processing method of claim 12, wherein curing the flowable film comprises exposing the flowable film to one or more of ozone, UV light, steam annealing, ammonia annealing and oxygen plasma.
14. The processing method of claim 2, further comprising doping the flowable film with one or more of B, As or P.
- 25 15. The processing method of any of claims 1 to 8 or 11 to 14, wherein the silicon-containing precursor is co-flowed with a silane precursor.

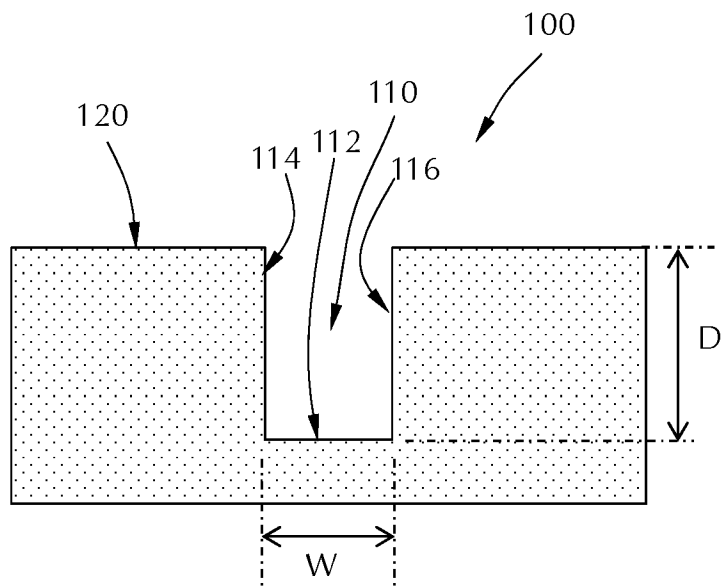


FIG. 1

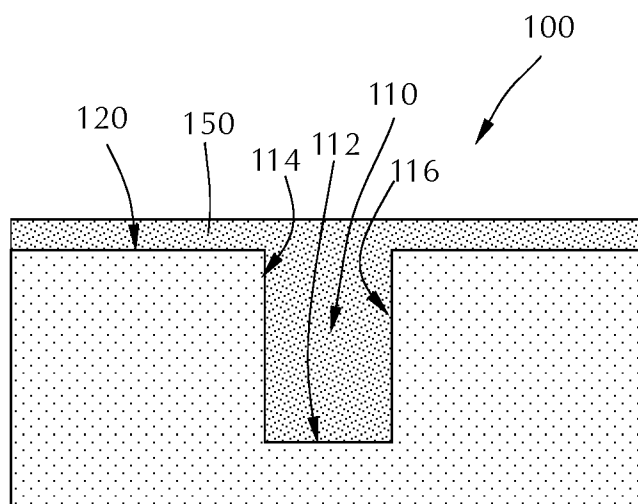


FIG. 2

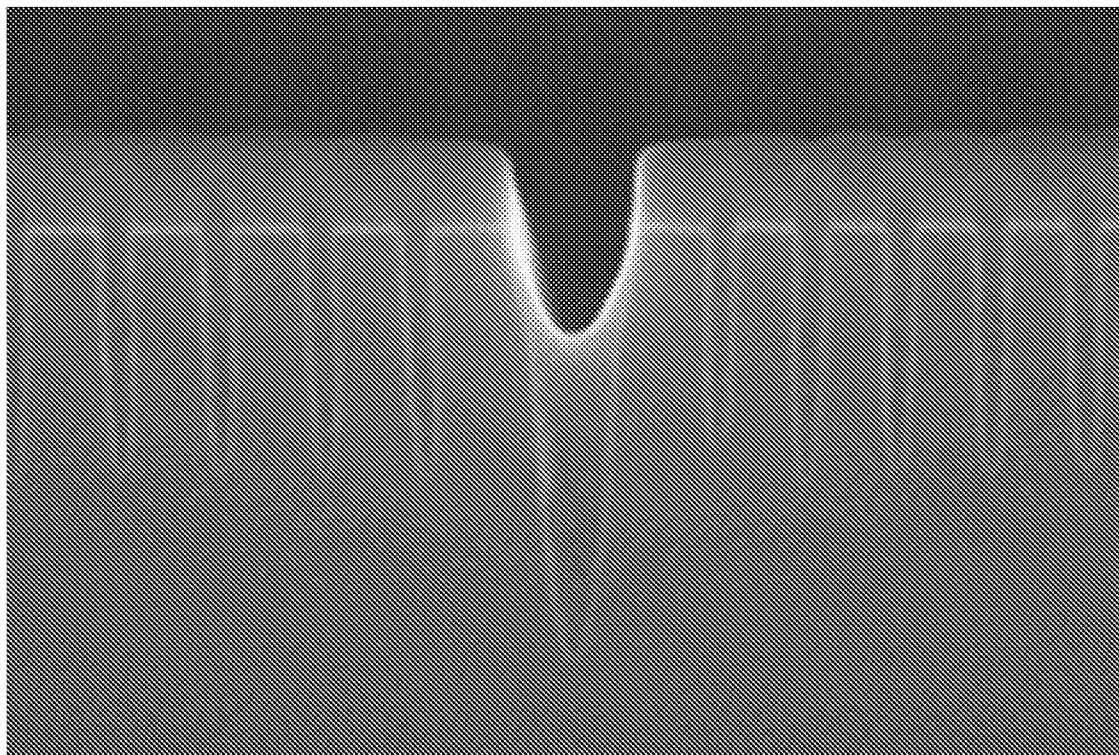


FIG. 3

A. CLASSIFICATION OF SUBJECT MATTER

C23C 16/30(2006.01)i, C23C 16/40(2006.01)i, C23C 16/36(2006.01)i, C23C 16/34(2006.01)i, C23C 16/32(2006.01)i, C23C 16/50(2006.01)i, C23C 16/56(2006.01)i, H01L 21/762(2006.01)i, H01L 21/02(2006.01)i

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

C23C 16/30; H01L 21/02; C23C 16/40; C23C 16/32; C23C 16/36; C23C 16/48; C23C 16/34; C23C 16/50; C23C 16/56; H01L 21/762

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Korean utility models and applications for utility models

Japanese utility models and applications for utility models

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

eKOMPASS(KIPO internal) & keywords: depositing, silicon, precursor, alkynyl, tetravinylsilane, oxygen, plasma, anneal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	HEO et al., Structure and property analysis of nanoporous low dielectric constant SiCOH thin films, Journal of the Korean institute of surface engineering, pp. 167-169 (2009 May 27) See abstract, page 167 and figure 1.	1-6, 11-15
Y		7-10
Y	GERAULT et al., X-ray photoelectron spectroscopy and raman spectroscopy investigations of amorphous SixCl-x(H) coatings obtained by chemical vapour deposition from thermally labile organosilicon compounds, Thin solid films, vol. 101, no. 1, pp. 83-96 (1983) See pages 83, 84 and figure 1.	7-10
A	US 2015-0004806 A1 (LAM RESEARCH CORPORATION) 01 January 2015 See paragraph [0057] and claims 1, 17.	1-15
A	US 2016-0017492 A1 (APPLIED MATERIALS, INC.) 21 January 2016 See paragraph [0017] and claims 1, 3.	1-15
A	US 2016-0122869 A1 (AIR PRODUCTS AND CHEMICALS, INC.) 05 May 2016 See paragraphs [0020], [0051] and claim 1.	1-15



Further documents are listed in the continuation of Box C.



See patent family annex.

* Special categories of cited documents:

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier application or patent but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

25 October 2017 (25.10.2017)

Date of mailing of the international search report

25 October 2017 (25.10.2017)

Name and mailing address of the ISA/KR

International Application Division

Korean Intellectual Property Office

189 Cheongsa-ro, Seo-gu, Daejeon, 35208, Republic of Korea



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INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/US2017/042811

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US 2016-0017492 A1	21/01/2016	KR 10-2016-0011157 A TW 201608052 A	29/01/2016 01/03/2016
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