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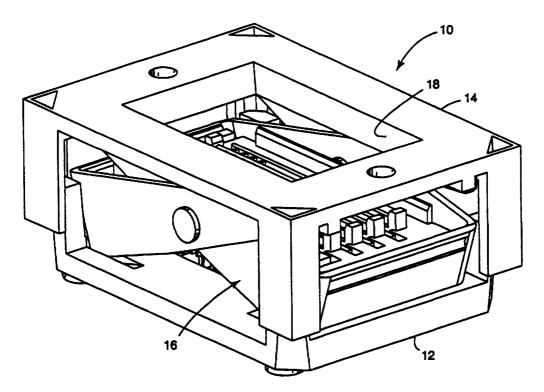
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(54) Title: TOP LOAD SOCKET FOR BALL GRID ARRAY DEVICES



(57) Abstract

A test socket for temporary connection of a ball grid array integrated circuit device to a test circuit includes an array of contacts each including two cantilever arms biased toward each other and terminating in tips adapted to capture one ball of the device.

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Top Load Socket For Ball Grid Array Devices

Technical Field

The present invention relates generally to sockets for the temporary connection of electronic devices to test circuitry, and, more particularly, sockets for ball grid array devices.

10 Background Art

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Integrated circuit (IC) devices are finding widespread use in the electronics industry and before they are bonded to a circuit the same are tested to

determine whether the IC device is functioning and that electrical continuity is present between the various portions of the device. To do this, the IC is placed in a socket which is attached to test circuitry. The whole assembly of test circuitry and IC may also be subjected to elevated temperatures while the IC is being electrically tested. Thus the procedure may be referred to as "test and burn-in" and the socket a test and burn-in socket.

One type of IC device is formed merely with small balls of solder attached to one planar surface of the device in a regular array of equally spaced rows and columns. Such a device is called a ball grid array (BGA) device and is designed to be mated to an interface circuit board by reflow soldering the BGA solder balls to an equal number of pads on the circuit board.

Test sockets for BGA devices in the past have contacted the solder balls with a single beam contact or a pointed rod contact which have caused undesirable damage to the solder balls or imparted unbalanced forces to the BGA device which had to be countered by

the structure of the socket. It would be desirable to provide a socket which eliminated these undesirable effects.

5 Summary of the Invention

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The present invention improves upon existing BGA test sockets by providing a contact for each ball which is comprised of two cantilever arms terminating in inwardly-bent contact tips. The arms are arranged to be biased toward each other in the manner of a pair of tweezers. Since there are two contact arms, the forces on the contact balls of the BGA are balanced and thus no forces are transmitted to either the IC device or the socket structure supporting the contacts. Because the tips are inwardly bent, the balls are positively captured and the IC device securely maintained within the socket.

In particular, the test socket of the present invention includes a base of electrically insulating 20 material, an array of contacts supported by the base, the array at least including a pattern of contacts corresponding to the ball grid array of the integrated circuit device, each contact including a set of two cantilever arms biased toward each other and 25 terminating in tips adapted to capture one ball of the ball grid array integrated circuit device, and means for simultaneously separating each of the sets of the array of contacts so that the balls of the ball grid array device may be inserted one within each of the set 30 of contact arms.

Brief Description of the Drawings

The present invention will be more thoroughly described with respect to the accompanying drawings,

> wherein like numbers refer to like parts in the several views, and wherein:

Figure 1 is a perspective view of a BGA test socket according to the present invention;

Figure 2 is a perspective view of a pair of contacts designed for use in the socket of Figure 1;

Figure 3 is a perspective view of a portion of the socket of Figure 1 illustrating the relationship between the contacts of Figure 2 and the BGA IC device which is to be tested;

Figure 4 is a perspective view, with a portion in cross-section, of contact actuating members of the test socket of Figure 1;

Figure 5 is a perspective view of the test socket 15 of Figure 1 with the cover removed; and

Figure 6 is a view similar to that of Figure 5 with the test socket in a position to accept a BGA device.

20 Description of the Preferred Embodiment

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Figure 1 illustrates a ball grid array (BGA) socket generally indicated as 10. The socket 10 includes a base 12 of electrically insulating material, a cover 14 and an operating mechanism 16 disposed between the base 12 and the cover 14. The cover 14 includes an opening 18 in the shape of the BGA integrated circuit device (not shown) with which the socket 10 is to be used.

30 Figures 2 and 3 illustrate the contacts 20 disposed within the operating mechanism 16 of the socket 10 which are designed to make an electrical connection with the BGA device. Each contact 20 includes two cantilever arms 22 and 24 which terminate in tips 26 and 28 offset from each other relative to the direction of travel of the arms 22 and 24. The

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arms 22 and 24 are of a resilient, highly conductive metal such as copper or an alloy of copper, and so attempt to return to the position shown in Figure 3 when the tips 26 and 28 are spread apart. The contacts 20 further include a tail 30 for electrical contact to a circuit board or the like.

Figure 2 shows the tails 30 of the contacts inserted through the base 12, with the contacts arranged in an array of rows and columns. Located between adjacent rows of contacts 20 are racks 32 having attached thereto triangular projections 34 which engage the tip portions 26 and 28 of the contacts 20. The racks 32 are connected at one end to a guide 36 which moves with the rack 32 to move as will be described below. It can be seen in Figure 2 that every other rack illustrated is connected to one of the quides 36 illustrated. This arrangement is such that every other rack 32 travels in the same direction with intermediate racks 32 traveling in the opposite direction. The direction of travel of two adjacent 20 racks 32 is indicated by the arrows 38.

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It will be noted that the tips 26 and 28 of adjacent rows of contacts 20 are offset in opposite directions. this allows one rack 32 with triangular projections 34 on each side to operate on two tips 26, 28 of two rows of contacts 20 and so reduce the complexity of the mechanism involved.

Figure 4 illustrates how the contacts 20 engage the solder balls 40 of a BGA device. The flat surfaces of the solder balls 40 indicate the plane at which the balls 40 are attached to the BGA device (not shown). As can also be seen in Figure 3, the tips 26 and 28 of the contacts 20 are twisted toward each other so that the engagement surfaces of the tips 26 and 28 are tangential to the solder balls. This arrangement allows a maximum area of the contact tips 26 an 28 to

contact the balls 40 and prevents any gouging of the balls 40. The amount of twist necessary to result in tangential engagement of the tips 26 and 28 and the solder balls 40 will depend upon the diameter of the balls 40. It will also be noted that the engagement portion of the tips 26 and 28 are bent inwardly where they engage the solder balls 40. This results in a downward force on the solder balls 40 which aids in the retention of the IC device within the socket 10.

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Figures 5 and 6 illustrate the socket 10 with the cover 14 removed to expose the operating mechanism 16 disposed within the socket 10. Figure 5 illustrates the mechanism 16 in a position wherein the contacts 20 would be closed and thus would grip the solder balls 40 of a BGA device if one were in place within the socket 10. Figure 6 illustrates the mechanism 16 in the open position wherein the contacts 20 are spread and ready to receive the solder balls 40 of the BGA device.

The mechanism 16 includes two identical U-shaped 20 brackets 42 and 44 each having a formed cross piece 46 and two upstanding arms 48 and 50 extending substantially perpendicular to the cross piece 46. two brackets 42 and 44 are nested facing each other and have their arms 48 and 50 pinned to each other at approximately their midpoints with a hole and slot 25 arrangement which allows the arms 48 and 50 of each bracket 42 and 44 to slide relative to each other as the ends of the arms 48 and 50 move up and down. lower portions of the cross pieces 46 are pivoted 30 within slots in the base 12 and the upper portions of the cross pieces 46 engage the ends of the racks 32 which include the guides 36. The dimensions of the brackets 42 and 44 are chosen such that the upper portions of the cross pieces 46 are forced apart, and thus the arms 48 and 50 forced upwardly, when the 35 contacts 20 are closed and thus the guides 36 are at

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their maximum separation. That the guides 36 are at their maximum separation is illustrated by comparing. the positions of the plate 52 which supports the guides 36 on the right side of the socket 10 and the ends 54 of the racks 32 adjacent these guides 36. In Figure 5, which corresponds to the closed position of the contacts 20, there is a large separation between the ends 54 of the racks 32 and the plate 52, which indicates that the guides 36 are at their widest separation. In Figure 6, which corresponds to the open position of the contacts 20, the ends of the racks 32 have fully approached the plate 52, indicating maximum travel of the racks 32 and thus full opening of the contacts 20.

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To insert a BGA device in the socket 10, the cover 14 is depressed which forces the ends of the arms 48 and 50 downwardly. This motion of the arms 48 and 50 causes the upper portions of the cross pieces 46 to approach each other and thus force the guides 36 and their associated racks 32 toward each other to open the 20 contacts 20. The guides 36 are then in a position to define the placement of the BGA device on the top of the racks 32.

Once the BGA device is placed between the guides 36, the solder balls 40 attached to its bottom side are positioned within the open contacts 20 and the socket 10 is ready to be closed. Releasing the downward pressure on the cover 10 allows the natural resiliency of the contacts 20 to cause the contacts 20 to close around the solder balls 40. At the same time, the racks 32 are forced in opposite directions to fully separate the guides 36. This action raises the arms 48 and 50 and thus the cover 14.

Thus there has been described a test and burn-in socket for ball grid array integrated circuit devices which uses two contact arms to grip each solder ball of

the device Since two arms are used, forces are balanced on the device and a large contact area per ball is defined which prevents damage to the solder balls.

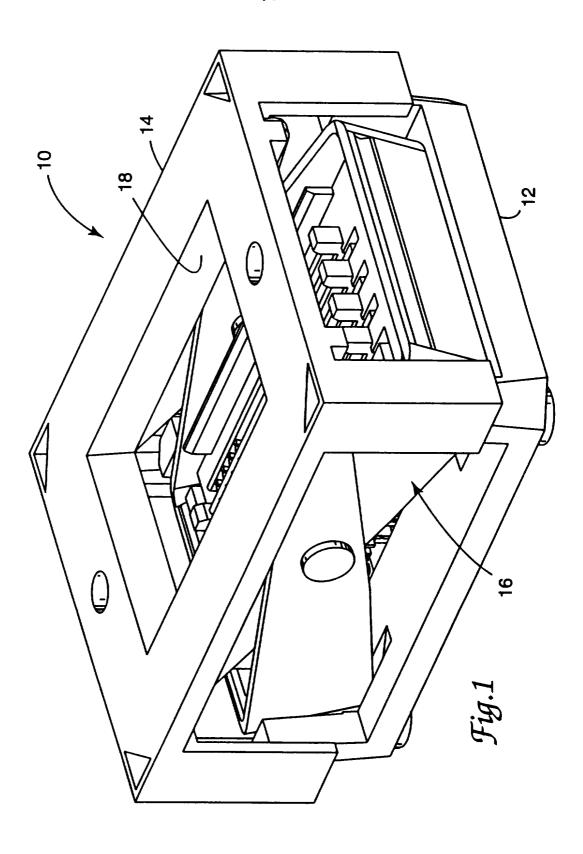
Although the invention has been described with respect to only a single embodiment, it will be recognized to those skilled in the art that many modifications are possible. For example, the cover could be removed entirely and the arms actuated directly. In addition, the arms could be longer or shorter to require a greater or lesser travel of the cover.

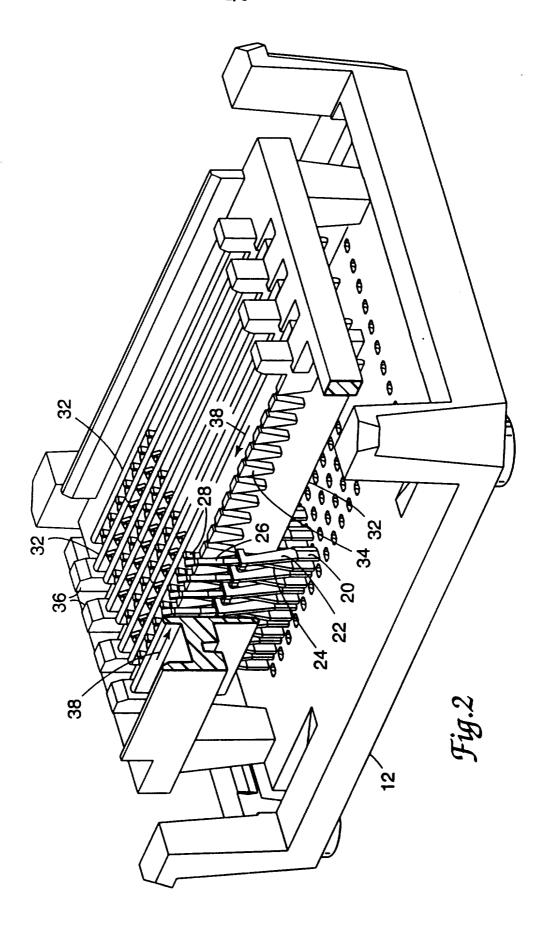
Claims:

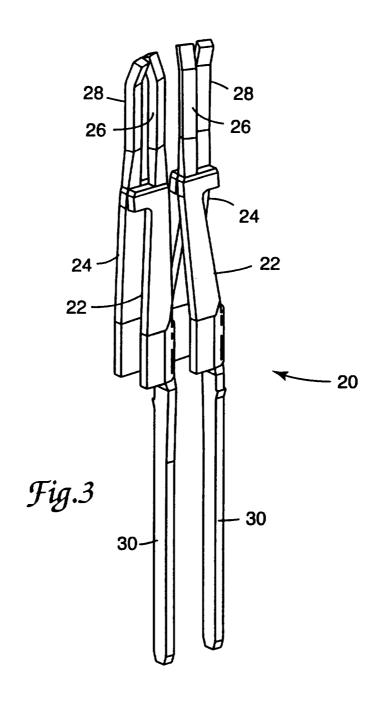
1. A test socket for temporary connection of a

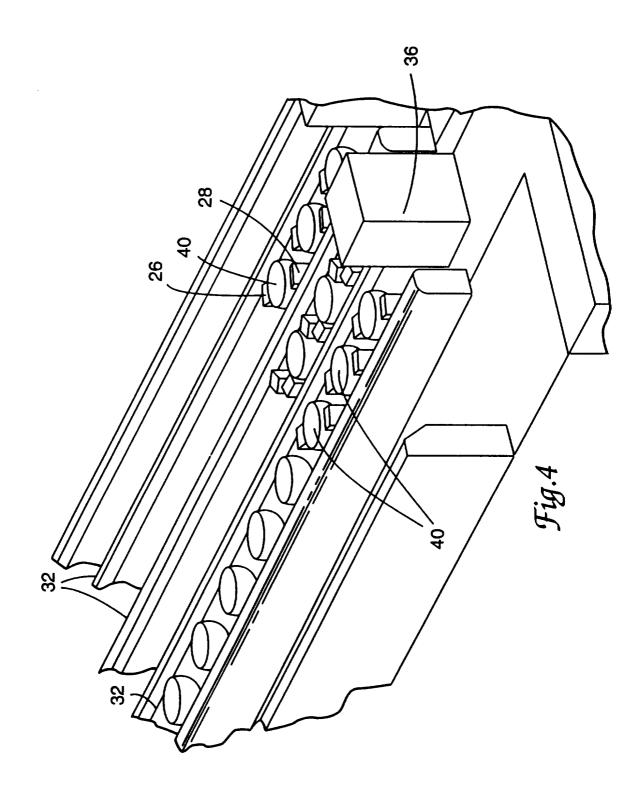
- 2 ball grid array integrated circuit device to a test
- 3 circuit, the test socket comprising:
- a base of electrically insulating material;
- an array of contacts supported by said base, said
- 6 array at least including a pattern of contacts
- 7 corresponding to the ball grid array of the integrated
- 8 circuit device, each contact including a set of two
- 9 cantilever arms biased toward each other and
- 10 terminating in tips adapted to capture one ball of the
- 11 ball grid array integrated circuit device;
- means for simultaneously separating each of said
- 13 sets of said array of contacts so that the balls of the
- 14 ball grid array device maybe inserted one within each
- 15 of said set of contact arms.
 - 1 2. A test socket according to claim 1 wherein
 - 2 said tips of said contacts are offset with respect to
 - 3 the direction of movement of said tips and wherein said
 - 4 tips are twisted toward each other to engage the balls
 - 5 over a maximum area.
 - 3. A test socket according to claim 1 wherein
 - 2 said means for separating said contact tips includes a
 - 3 rack associated with each of said tips and means for
 - 4 forcing adjacent racks in opposite directions.
 - 1 4. A test socket according to claim 3 wherein
 - 2 said means for forcing said racks in opposite
 - 3 directions includes a linkage of two U-shaped pieces
 - 4 each of a cross piece and two upstanding arms, and
 - 5 wherein said arms are connected such that pressure on
 - 6 said arms at a point distant from said cross pieces
 - 7 causes said cross pieces to approach each other, and

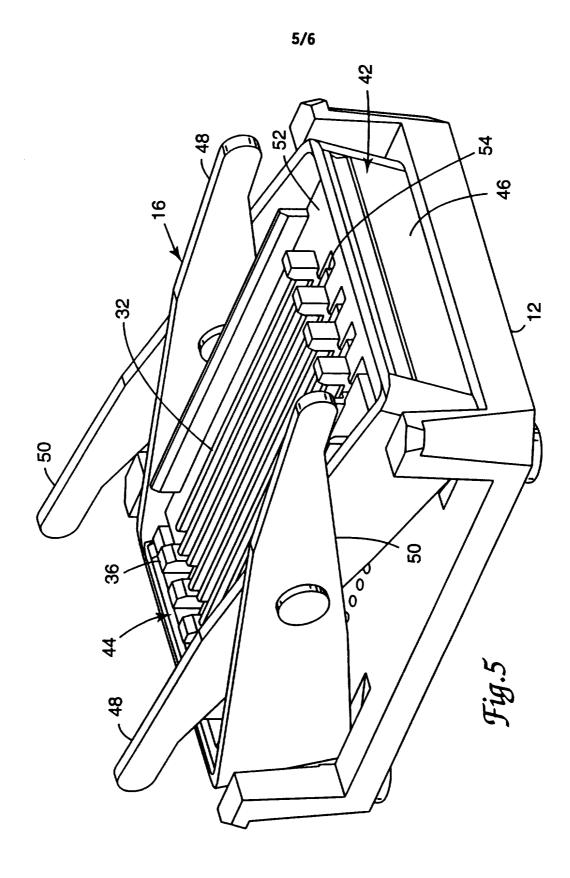
- 8 wherein said racks are disposed between said cross
- 9 pieces and move with motion of said cross pieces.
- 5. A test socket according to claim 4 further
- 2 including a cover including an opening for accepting a
- 3 ball grid array device and a surface contacting said
- 4 arm ends to pressure said arms as said cover is
- 5 depressed and thus cause said cross pieces to approach
- 6 each other.
- 1 6. A test socket according to claim 5 further
- 2 including means on said base and said cover for
- 3 cooperatively guiding said cover with respect to said
- 4 base as said cover is depressed.

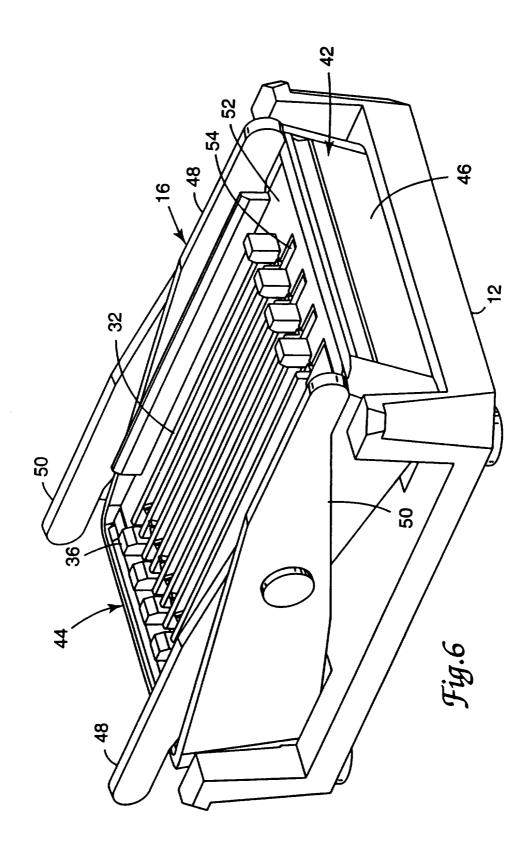












INTERNATIONAL SEARCH REPORT

Int ronal Application No PCT/US 95/16987

A. CLASS IPC 6	GO1R1/04			
According t	to International Patent Classification (IPC) or to both national of	classification and IPC		
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Minimum of IPC 6	documentation searched (classification system followed by class GOIR HO5K	ulication symbols)		
Documenta	ation searched other than minimum documentation to the extent	that such documents are included in the fields :	searched	
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C. DOCUM	MENTS CONSIDERED TO BE RELEVANT			
Category *	Citation of document, with indication, where appropriate, of	the relevant passages	Relevant to claim No.	
Y A	US,A,5 376 010 (PETERSEN) 27 D see abstract; figures 5-7	1 5		
Y	US,A,5 342 213 (KOBAYASHI) 30 see column 2, line 49 - column figures 1-3,5-7	1		
A	US,A,5 123 855 (PETERSEN) 23 J see column 1, line 32 - line 6 see column 2, line 26 - line 4 1,2,6-9	1,4		
A	GB,A,2 274 212 (YAMAICHI ELECT July 1994 see abstract; figures 1,4,7,10	1		
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Patent document cited in search report	Publication date	Patent family member(s)		Publication date	
US-A-5376010	27-12-94	EP-A- JP-A-	0666706 7240263	09-08-95 12-09-95	
US-A-5342213	30-08-94	JP-A-	5343146	24-12-93	
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US-A-3848221	12-11-74	NONE			