(19) World Intellectual Property Organization International Bureau





(43) International Publication Date 6 February 2003 (06.02.2003)

PCT

(10) International Publication Number WO 03/010813 A3

(51) International Patent Classification⁷: H01L 23/498, H05K 3/40, 7/10

(21) International Application Number: PCT/US01/42515

(22) International Filing Date: 3 October 2001 (03.10.2001)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:

09/912,111 23 July 2001 (23.07.2001) US

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(81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU,

CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, TZ, UA, UG, US, UZ, VN, YU, ZA, ZW.

(84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

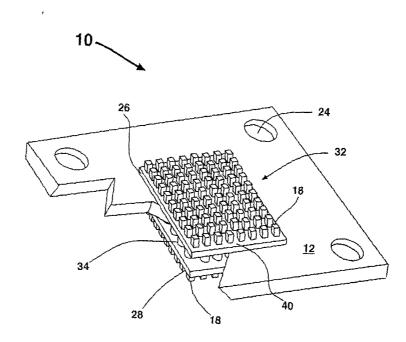
Published:

with international search report

(88) Date of publication of the international search report: 18 September 2003

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: GRID INTERPOSER



(57) Abstract: A grid interposer for testing electrical circuits and a method of making a grid interposer. The grid interposer includes upper and lower conductive surfaces sandwiched on either side of an insulating later, connected to each other by a plurality of vias filled with conductive material. The conductive surfaces are flat topped, and incised with a grid of flat topped peaks which are small enough to cut through oxidation of electrical contacts, and ensure good electrical contact with a variety of electrode shapes.



WO 03/010813 A3

INTERNATIONAL SEARCH REPORT

Inte Jonal Application No

PCT/US 01/42515 A. CLASSIFICATION OF SUBJECT MATTER IPC 7 H01L23/498 H05 H05K3/40 H05K7/10 According to International Patent Classification (IPC) or to both national classification and IPC **B. FIELDS SEARCHED** Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Electronic data base consulted during the international search (name of data base and, where practical, search terms used) EPO-Internal, PAJ, WPI Data, IBM-TDB C. DOCUMENTS CONSIDERED TO BE RELEVANT Category 9 Citation of document, with indication, where appropriate, of the relevant passages Relevant to claim No. χ PATENT ABSTRACTS OF JAPAN 1,3,5,6 vol. 2000, no. 16, 8 May 2001 (2001-05-08) -& JP 2001 004700 A (YAMAICHI ELECTRONICS CO LTD), 12 January 2001 (2001-01-12) paragraphs '0012!, '0023!, '0032! γ 7,8 paragraphs '0041!-'0046! figure 5 abstract Α 9.10 -/--Further documents are listed in the continuation of box C. Χ Patent family members are listed in annex. Įχ ° Special categories of cited documents: *T* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the *A* document defining the general state of the art which is not considered to be of particular relevance invention "E" earlier document but published on or after the international "X" document of particular relevance; the claimed invention filing date cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another 'Y' document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such docucitation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means ments, such combination being obvious to a person skilled *P* document published prior to the international filing date but later than the priority date claimed in the art. *&" document member of the same patent family Date of the actual completion of the international search Date of mailing of the international search report 20 June 2003 01/07/2003 Name and mailing address of the ISA Authorized officer European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Tx. 31 651 epo nl.

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