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(54) Title: METHOD AND SYSTEM FOR PRECHARGING OLED/PLED DISPLAYS WITH A PRECHARGE LATENCY

(57) Abstract: An Organic light emitting diode (OLED)/polymer OLED (PLED) displays and their operation with a precharge latency. Particularly, precharging operation of such a display device with a precharge switch latency. According to the operation, a capacitive aspect of a display element is precharged, i.e. the parasitic capacitance, and the display element is activated so as to conduct a current therethrough. The precharging is terminated after the activation of the display element. Then a current is supplied and conducted through the display element for exposure of the display element. In this operation, a precharge and exposure can be avoided or minimized by maintaining the connection of the voltage source to the column lines during an overlap period after the row line is grounded.



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# INTERNATIONAL SEARCH REPORT

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## A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 G09G3/32

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)  
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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

| Category * | Citation of document, with indication, where appropriate, of the relevant passages  | Relevant to claim No. |
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| X          | EP 1 071 070 A (INFINEON TECHNOLOGIES CORP) 24 January 2001 (2001-01-24)<br>paragraph '0013! - paragraph '0016!;<br>figure 1<br>paragraph '0029!; figure 3                    | 1-29                  |
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☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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| C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT |   |                       |
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Information on patent family members

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