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Description

The invention relates to a method of storing character data in a background memory of a digitally operable data display apparatus for displaying as an entity on a screen of a display device a quantity of data which is represented by digital codes stored in a display memory, the displayed data being in the form of discrete pixels or dots, each of which has its colour and/or luminance defined by a respective digital code in the display memory at a location corresponding to the position of the pixel in the display, the apparatus including a program-controlled processor for controlling digitally the storage, selection and display of data.

The invention further relates to a method of displaying data in a digitally operable data display apparatus for displaying as an entity on a screen of a display device a quantity of data which is represented by digital codes stored in a display memory, the displayed data being in the form of discrete pixels or dots, each of which has its colour and/or luminance defined by a respective digital code in the display memory at a location corresponding to the position of the pixel in the display, the apparatus including a program-controlled processor for controlling digitally the storage, selection and display of data.

This invention still further relates to a digitally operable data display apparatus for displaying as an entity on a screen of a display device a quantity of data which is represented by digital codes stored in a display memory, the displayed data being in the form of discrete pixels or dots, each of which has its colour and/or luminance defined by a respective digital code in the display memory at a location corresponding to the position of the pixel in the display, the apparatus including a program-controlled processor for controlling digitally the storage, selection and display of data.

The display produced by apparatus of the above type is termed a bit-map display, that is, for example, a 320 x 250 resolution dot matrix colour display which may be displayed on the screen of a CRT (cathode ray tube) or other display device. In the case of a raster scan display device the digital codes stored in the display memory are accessed repeatedly by a display generator to update the display in a recurrent cycle of scanning lines which may be produced with or without interlaced field scanning.

Where a data display apparatus of the above type is to provide comprehensive and visually aesthetic text displays, it is usually a requirement that data for a large number of different character fonts is provided in a background memory and the data for individual characters selected for display is read from the background memory and written into the display memory.

It is known to store the data for each character of each character font in a bit-map form which corresponds to the image and size of the character shape.

This character data, when selected, can then be transferred directly to the display memory with a minimum of processor logic.

With the known techniques the speed of transfer of the data from the background memory to the display memory is limited due to the relatively large number of processor instructions needed to effect the transfer. One solution is to provide dedicated hardware for the transfer of bit-map data from the background memory to the display memory, for example as described in US-A- 4,622,546 (Sfarti et al). Unfortunately, such special hardware is not always available in an existing apparatus, and will add to the cost and complexity of a new display apparatus.

It is an object of the present invention to enable the provision of data display apparatus of the above type in which the character data may be written into the display memory more quickly, but without the need for expensive display hardware.

The invention provides a method of storing character data as set forth in the opening paragraph, characterised in that the method consists of generating a respective machine code sub-routine for each of a plurality of characters, such that execution of each machine code subroutine by the processor will cause the processor to write digital codes into locations of the display memory dependent on the shape of the corresponding character, and storing the machine code sub-routines at respective locations in the background memory.

The main advantage that the present invention achieves is the significant saving in the number of processor operating steps that are required to write the data for a character into the display memory. Therefore, for a given speed of operation of the processor, the contents of the display memory can be updated for the display of a new page of text far more quickly than in apparatus where character data is stored in the background memory in bit-map form.

The invention further provides a method of displaying data as set forth in the second paragraph characterised in that the method comprises storing character data in the background memory as a respective machine code sub-routine for each of a plurality of characters such that execution of each machine code sub-routine by the processor will cause the processor to write digital codes into locations of the display memory dependent on the shape of the corresponding character, selecting a character for display, selecting the position of the character on the display screen, causing the corresponding sub-routine to be executed to write pixel data in the appropriate memory locations in the display memory, and causing the data in the display memory to be displayed on the display screen.

The invention still further provides a digitally operable data display apparatus as set forth in the third paragraph characterised by means for storing charac-

ter data in a background memory in the form of a respective machine code sub-routine for each of a plurality of characters, such that execution of each machine code sub-routine by the processor will cause the processor to write digital codes into locations of the display memory dependent on the shape of the corresponding character, means for selecting a character for display, and means for causing the sub-routine corresponding to the selected character to be executed to write pixel data in the appropriate memory locations in the display memory.

The background memory device may be a floppy disk or the like, or a read-only memory, in which a large number of different character data can be provided in a simple and relatively inexpensive way.

In further considering the nature of the invention, reference will now be made by way of example to the accompanying drawings of which:-

Figure 1 shows a block diagram of a data display apparatus in which the present invention can be embodied;

Figure 2 shows diagrammatically a bit-map display memory;

Figure 3 shows an example of character data as stored in bit-map form in a background memory;

Figures 4 and 5 show two machine code sub-routines which form character data in a background memory in accordance with the invention;

Figure 6 shows diagrammatically the bit map memory of Figure 2 with a different character represented;

Figure 7 shows a machine code sub-routine which forms the character data in the background store for the character represented in Figure 6;

Figure 8 is a flow diagram illustrating the selection and generation of characters for display; and Figure 9 is a flow diagram illustrating the transfer of the character data to the bit map display memory under the control of a selected machine code sub routine.

Referring to the drawings, the data display apparatus shown in Figure 1 comprises a display device 1, a display generator 2, a processor 3, a background memory 4, a display memory 5 and user interface apparatus 6 and 7. The display device is suitably a colour television monitor which is connected to receive R, G, B, video signals from the display generator 2. These R, G, B, video signals are produced in the display generator 2 by three digital-to-analogue converters 8, 9 and 10, respectively. The display generator 2 also included a colour look-up table 11 which is suitably a read/write memory and is responsive to dot information received from the display memory 5 over a bus 12 to produce digital signals for driving the converters 8, 9 and 10. A display timer 13 in the display generator 2 provides line and field synchronisation signals LS and FS for the television monitor 1 over a connection 14. The timer 13 also provides over a con-

nection 15 timing signals T for controlling the transfer of dot information from the display memory 5 to the colour look-up table 11.

The display memory 5 is a random-access memory which has a capacity for storing dot information for at least one display frame. The dot information comprises digital codes composed of one or more bits per dot to be display, depending on the range of colours afforded by the colour look-up table 11. A combined address-data bus 16 interconnects the display generator 2 and the display memory 5 with the processor 3. The background memory 4, which is also at least partially a random-access memory, is also connected to the address-data bus 16. The background memory 4 may also have a read-only memory part of which contains permanent program data for controlling the "house-keeping" operations of the processor 3. The user interface apparatus comprises a keyboard data entry device 6 and a writing tablet 7. Such interface apparatus is well-known in the art and specific details thereof are unnecessary for an understanding of the present invention. The processor 3 can be a commercially available microprocessor, for instance the Signetics S68000 μ p.

Consider now a prior art method for storing character data in a background memory. By way of illustration it will be assumed that a character which is available for display, and the data for which is stored in the background memory, is a simple cross (+) composed of five vertical pixels and five horizontal pixels. It will also be assumed that this cross (+) is to be written into a bit-map display memory BM/DM in the position CP shown in Figure 2. The bit-map display memory represented in Figure 2 is only of small size (15×15 pixels) which is adequate for the present description. In practice, a bit-map display memory would normally be much larger, for example, a 384×256 pixels. When the character data for this cross (+) is stored in the background memory in bit-map form in a cell pattern, it can take the form shown in Figure 3. This character data comprises a 5×5 bit matrix CH in which logic '1's represent the character shape and logic '0's represent background data. If the background memory contains memory locations of 8-bit bytes, as is common, then five such bytes b_1 to b_5 are required to store the character data. Consequently the byte area signified at B is redundant in this case.

In order to write the character data into the position CP the following programme steps have to be performed by the processor.

1. Determine the x size and the y size of the character. This could, for example, be encoded as one or more bytes of memory associated with each character.
2. Determine the character position in the bit-map display memory (start y , start x). This may be determined from the character position on the display selected by the keyboard 6 or writing tablet

7.

3. For y from start y to stop y (5 times) and for x from start x to stop x (5 times), that is for each of the 25 pixels in the character cell, read the bit data in the background memory and if the bit is a '1' write the bit information for the pixel concerned in the display memory.

The programme step will take of the order of 6-8 instructions, for each bit, so that a total of up to 200 instructions will be necessary to write the data for the cross (+) into the display memory.

The number of instructions for writing character data into the display memory can be reduced considerably by means of the invention. Figure 4 shows the machine code instructions that are required to write the cross (+) into the position CP by means of a method according to the invention. Before writing into the display memory, data which identifies the colour that the cross (+) is to be displayed in is entered into a first register (D0). Data which identifies a base dot position for the character position is then entered into another register (A0). The background memory includes an address table for each possible character shape that can be displayed. When a character is selected by keyboard operation, the selected character is identified and the address for the character data is read from the address table. This character data, which is in the form of machine coded instructions is then accessed by the processor to write the character data into the display memory.

The instruction (1) in Figure 4 provides the character colour. The instruction (2) provides the base dot address which is pixel bit position 82 in the bit-map display memory BM/DM. The remaining instructions (3) to (11) cause the processor to step in turn to each of the dot positions which are to contain a pixel for the character shape. Thus, writing into the display memory is achieved using far fewer programme instructions than are required when character data is stored in the background memory in bit-map form as in cell pattern.

The machine code instructions can also be arranged to write into the display memory the character data starting from a base dot position which is the first actual pixel position for a character.

Figure 5 shows an example of these alternative machine code instructions. In this instance, the register A0 is initially set to pixel position 148, and instruction (2) writes in a pixel at that position. Instructions (3) to (6) then successively write into pixel positions 132, 116, 100 and 84. Different instructions (7) to (10) then write into the remaining pixel positions 114, 115, 117 and 118.

Figure 6 shows the character W written into the bit map display memory BM/DM, the character W having its base position CP at pixel 177 in the memory.

Figure 7 shows the machine code instructions that are required to write the character W into the pos-

ition CP (177). Before writing into the display memory, data which identifies the colour that the character W is to be displayed in is entered into a first register (D0). Data which identifies a base dot position for the character position is then entered into second register (A0). The background memory includes an address table for each possible character shape that can be displayed. When a character is selected by keyboard operation, the selected character is identified and the address for the character data is read from the address table. This character data, which is in the form of machine coded instructions is then accessed by the processor to write the character data into the display memory.

The instruction (1) in Figure 8 provides the character colour. The instruction (2) provides the base dot address which is pixel bit position 177 in the bit-map display memory BM/DM. The remaining instructions (3) to (34) cause the processor to step in turn to each of the pixel dot positions which are to contain a pixel for the character shape.

Figures 8 and 9 are flow diagrams which illustrate the generation of characters for display on the display apparatus. In Figure 8 reference 200 represents the start of the process and reference 201 represents the selection of the character to be displayed, its colour, and its base position (equivalent to CP in Figures 2 and 6). Reference 202 represents the selection of the corresponding character machine code sub-routine from the background memory. Reference 203 represents the running of the machine code sub-routine to cause the selected character to be read in bit-map form into the display register. Reference 204 represents the display of the character on the display screen from the information in the bit-map display. Reference 205 denotes the end of the process.

Figure 9 illustrates the transfer of the character data from the background memory to the bit map memory BM/DM under the control of a selected machine code sub-routine. The colour and base position having been specified (reference 201, Figure 8) the running of the sub-routine accesses first the distance, in number of pixels, from the base pixel to the first active pixel dot, box 302. This combined with the base pixel address comprises the display memory address and the code in register D0 representing the pixel colour is read into the location in the display memory represented by that address, box 303. If that is the final active pixel for the selected character then the process ends, box 306. Other wise the distance in number of pixels from the base pixel to the next active pixel dot is accessed, box 305. Again the code in register D0 representing the pixel colour is read into the display memory location represented by the new distance and base address. This sequence is repeated until the final pixel element has been written, box 304.

Thus in order to generate each sub-routine the

bit-mapped version of the character is examined and the distance of each active dot in the character from the base dot is calculated. The sub-routine then consists of stepping through addresses of the active dots to read the colour information into the bit-mapped display memory. The sub-routine effectively comprises a list of machine code instructions each of which contain the distance, in terms of number of pixels, to the next active dot of the individual character with respect to a base dot of the matrix. It will be seen that instructions (1) and (2) of Figures 4, 5 and 7 are essentially the same and consequently these do not need to be stored with the separate character sub-routines but can be part of the display controlling program.

Claims

1. A method of storing character data in a background memory of a digitally operable data display apparatus for displaying as an entity on a screen of a display device a quantity of data which is represented by digital codes stored in a display memory, the displayed data being in the form of discrete pixels or dots, each of which has its colour and/or luminance defined by a respective digital code in the display memory at a location corresponding to the position of the pixel in the display, the apparatus including a program-controlled processor for controlling digitally the storage, selection and display of data, characterised in that the method consists of generating a respective machine code sub-routine for each of a plurality of characters, such that execution of each machine code sub-routine by the processor will cause the processor to write digital codes into locations of the display memory dependent on the shape of the corresponding character, and storing the machine code sub-routines at respective locations in the background memory.
2. A method as claimed in Claim 1, characterised in that the machine code sub-routine which forms the stored data for a character comprises a series of executable instructions within which are encoded respective pixel positions relative to a base dot position which locates the screen position of the character, said relative pixel positions defining the shape of the character.
3. A method as claimed in Claim 2, characterised in that for writing the pixel data for the character into the display memory, the machine code sub-routine includes a first further instruction which specifies in a first register data which identifies the colour in which the character is to be displayed, and a second further instruction which specifies in the second register the location in the display

memory that is to be base dot position for the character.

4. A method as claimed in Claim 2 or Claim 3, characterised in that each pixel position is identified in the relevant machine code instruction by a direct count of the pixels line-by-line from the base dot position.
5. A method as claimed in Claim 2 or Claim 3, characterised in that each pixel position is identified in the relevant machine code instruction by a pixel position count which includes an offset number corresponding to one or more whole pixel position lines that exist between the line containing the base dot position and the pixel position that is being written into.
6. A method of displaying data in a digitally operable data display apparatus for displaying as an entity on a screen of a display device a quantity of data which is represented by digital codes stored in a display memory, the displayed data being in the form of discrete pixels or dots, each of which has its colour and/or luminance defined by a respective digital code in the display memory at a location corresponding to the position of the pixel in the display, the apparatus including a program-controlled processor for controlling digitally the storage, selection and display of data, characterised in that the method comprises storing character data in the background memory as a respective machine code sub-routine for each of a plurality of characters such that execution of each machine code sub-routine by the processor will cause the processor to write digital codes into locations of the display memory dependent on the shape of the corresponding character, selecting a character for display, selecting the position of the character on the display screen, causing the corresponding sub-routine to be executed to write pixel data in the appropriate memory locations in the display memory, and causing the data in the display memory to be displayed on the display screen.
7. A digitally operable data display apparatus for displaying as an entity on a screen of a display device (1) a quantity of data which is represented by digital codes stored in a display memory (5), the displayed data being in the form of discrete pixels or dots, each of which has its colour and/or luminance defined by a respective digital code in the display memory at a location corresponding to the position of the pixel in the display, the apparatus including a program controlled processor (3) for controlling digitally the storage, selection and display of data, characterised by means (3)

for storing character data in a background memory (4) in the form of a respective machine code sub-routine for each of a plurality of characters, such that execution of each machine code sub-routine by the processor will cause the processor to write digital codes into locations of the display memory dependent on the shape of the corresponding character, means (3) for selecting a character for display, and means (3) for causing the sub-routine appertaining to the selected character to be executed to write pixel data in the appropriate memory locations in the display memory (5).

8. A data display apparatus as claimed in Claim 7, characterised in that the machine code sub-routine which forms the stored data for a character comprises a series of executable instructions within which are encoded respective pixel positions relative to a base dot position which locates the screen position of the character, said relative pixel positions defining the shape of the character.

9. A data display apparatus as claimed in Claim 8, characterised in that for writing the pixel data for the character into the display memory, the machine code sub-routine includes a first further instruction which specifies in a first register data which identifies the colour in which the character is to be displayed, and a second further instruction which specifies in a second register the location in the display memory that is to be base dot position for the character.

10. A data display apparatus as claimed in Claim 8 or Claim 9, characterised in that each pixel position is identified in the relevant machine code instruction by a direct count of the pixels line-by-line from the base dot position.

11. A data display apparatus as claimed in Claim 8 or Claim 9, characterised in that each pixel position is identified in the relevant machine code instruction by a pixel position count which includes an offset number corresponding to one or more whole pixel position lines that exist between the line containing the base dot position and the pixel position that is being written into.

Patentansprüche

1. Verfahren zur Speicherung von Zeichendaten in einem Hintergrundspeicher einer digital betriebenen Daten-Anzeigevorrichtung zur Anzeige einer Datenmenge, die durch in einem Anzeigespeicher gespeicherte digitale Codes dargestellt wird,

als ein Ganzes auf einem Bildschirm eines Anzeigegerätes, wobei die angezeigten Daten die Form von diskreten Bildelementen oder Punkten haben, deren Farbe und/oder Helligkeit durch einen entsprechenden digitalen Code im Anzeigespeicher an einem Speicherplatz definiert ist, der der Position des Bildelementes in der Anzeige entspricht, und wobei die Vorrichtung einen programmgesteuerten Prozessor zur digitalen Steuerung der Speicherung, Auswahl und Anzeige von Daten beinhaltet, dadurch gekennzeichnet, daß das Verfahren daraus besteht, ein entsprechendes Maschinencode-Unterprogramm für jedes der vielen Zeichen zu erzeugen, so daß die Ausführung jedes Maschinencode-Unterprogramms durch den Prozessor letzteren veranlassen wird, digitale Codes in Speicherplätze im Anzeigespeicher zu schreiben, die von der Form des entsprechenden Zeichens abhängen; und die Maschinencode-Unterprogramme in entsprechenden Speicherplätzen im Hintergrundspeicher zu speichern.

2. Verfahren nach Anspruch 1, dadurch gekennzeichnet, daß das Maschinencode-Unterprogramm, das die gespeicherten Daten für ein Zeichen erzeugt, eine Reihe von ausführbaren Befehlen enthält, in denen entsprechende Bildelementpositionen in bezug auf eine Basispunktposition codiert sind, die die Position des Zeichens auf dem Bildschirm angibt, wobei die genannten entsprechenden Bildelementpositionen die Form des Zeichens definieren.

3. Verfahren nach Anspruch 2, dadurch gekennzeichnet, daß das Maschinencode-Unterprogramm zum Schreiben der Bildelementdaten für das Zeichen in den Anzeigespeicher einen ersten weiteren Befehl enthält, der Daten in einem ersten Register spezifiziert, die die Farbe angeben, in der das Zeichen angezeigt werden soll, und einen zweiten weiteren Befehl enthält, der den Speicherplatz in dem zweiten Register im Anzeigespeicher spezifiziert, der die Basispunktposition für das Zeichen sein soll.

4. Verfahren nach Anspruch 2 oder 3, dadurch gekennzeichnet, daß jede Bildelementposition in dem entsprechenden Maschinencode-Befehl durch eine direkte Anzahl von Bildelementen Zeile für Zeile ausgehend von der Basispunktposition identifiziert wird.

5. Verfahren nach Anspruch 2 oder 3, dadurch gekennzeichnet, daß jede Bildelementposition in dem entsprechenden Maschinencode-Befehl durch eine Bildelementpositionanzahl identifiziert wird, die eine Versatzzahl enthält, welche ei-

ner oder mehreren ganzen Bildelementpositionszeilen entspricht, die zwischen der Zeile, die die Basispunktposition enthält, und der Bildelementposition existieren, in die geschrieben wird.

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6. Verfahren zur Anzeige von Daten in einer digital betriebenen Daten-Anzeigevorrichtung zur Anzeige einer Datenmenge, die durch in einem Anzeigespeicher gespeicherte digitale Codes dargestellt wird, als ein Ganzes auf einem Bildschirm eines Anzeigegerätes, wobei die angezeigten Daten die Form von diskreten Bildelementen oder Punkten haben, deren Farbe und/oder Helligkeit durch einen entsprechenden digitalen Code im Anzeigespeicher an einem Speicherplatz definiert ist, der der Position des Bildelementes in der Anzeige entspricht, und wobei die Vorrichtung einen programmgesteuerten Prozessor zur digitalen Steuerung der Speicherung, Auswahl und Anzeige von Daten enthält, dadurch gekennzeichnet, daß das Verfahren folgendes beinhaltet: Speicherung von Zeichendaten im Hintergrundspeicher als ein entsprechendes Maschinencode-Unterprogramm für jedes der vielen Zeichen, so daß die Ausführung jedes Maschinencode-Unterprogramms durch den Prozessor letzteren veranlaßt, digitale Codes in Speicherplätze im Anzeigespeicher zu schreiben, die von der Form des entsprechenden Zeichens abhängen; Auswahl eines Zeichens zur Anzeige; Auswahl der Position des Zeichens auf dem Anzeigebildschirm; Veranlassen des entsprechenden auszuführenden Unterprogramms, Bildelementdaten in die entsprechenden Speicherplätze im Anzeigespeicher zu schreiben; und Veranlassen der Darstellung der Daten im Anzeigespeicher auf dem Anzeigebildschirm.
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7. Digital betreibbare Daten-Anzeigevorrichtung zur Anzeige einer Datenmenge, die durch in einem Anzeigespeicher (5) gespeicherte digitale Codes dargestellt wird, als ein Ganzes auf einem Bildschirm eines Anzeigegerätes (1), wobei die angezeigten Daten die Form von diskreten Bildelementen oder Punkten haben, deren Farbe und/oder Helligkeit durch einen entsprechenden digitalen Code im Anzeigespeicher an einem Speicherplatz definiert ist, der der Position des Bildelementes in der Anzeige entspricht, und wobei die Vorrichtung einen programmgesteuerten Prozessor (3) zur digitalen Steuerung der Speicherung, Auswahl und Anzeige von Daten beinhaltet, dadurch gekennzeichnet, daß die Vorrichtung folgendes enthält: Mittel (3) zum Speichern von Zeichendaten in einem Hintergrundspeicher (4) in Form eines entsprechenden Maschinencode-Unterprogramms für jedes der vielen Zeichen, so daß die Ausführung jedes Maschinencode-

Unterprogramms durch den Prozessor letzteren veranlaßt, digitale Codes in Speicherplätze im Anzeigespeicher zu schreiben, die von der Form des entsprechenden Zeichens abhängen; Mittel (3) zum Auswählen eines Zeichens für die Anzeige; und Mittel (3), die das dem ausgewählten auszuführenden Zeichen entsprechende Unterprogramm dazu veranlassen, Bildelementdaten in die entsprechenden Speicherplätze im Anzeigespeicher (5) zu schreiben.

8. Daten-Anzeigevorrichtung nach Anspruch 7, dadurch gekennzeichnet, daß das Maschinencode-Unterprogramm, das die gespeicherten Daten für ein Zeichen erzeugt, eine Reihe von ausführbaren Befehlen enthält, in denen entsprechende Bildelementpositionen in bezug auf eine Basispunktposition codiert sind, die die Position des Zeichens auf dem Bildschirm angibt, wobei die genannten entsprechenden Bildelementpositionen die Form des Zeichens definieren.
9. Daten-Anzeigevorrichtung nach Anspruch 8, dadurch gekennzeichnet, daß das Maschinencode-Unterprogramm zum Schreiben der Bildelementdaten für das Zeichen in den Anzeigespeicher einen ersten weiteren Befehl enthält, der Daten in einem ersten Register spezifiziert, die die Farbe angeben, in der das Zeichen angezeigt werden soll, und einen zweiten weiteren Befehl enthält, der den Speicherplatz in dem zweiten Register im Anzeigespeicher spezifiziert, der die Basispunktposition für das Zeichen sein soll.
10. Daten-Anzeigevorrichtung nach Anspruch 8 oder 9, dadurch gekennzeichnet, daß jede Bildelementposition in dem entsprechenden Maschinencode-Befehl durch eine direkte Anzahl von Bildelementen Zeile für Zeile ausgehend von der Basispunktposition identifiziert wird.
11. Daten-Anzeigevorrichtung nach Anspruch 8 oder 9, dadurch gekennzeichnet, daß jede Bildelementposition in dem entsprechenden Maschinencode-Befehl durch eine Bildelementpositionanzahl identifiziert wird, die eine Versatzzahl enthält, welche einer oder mehreren ganzen Bildelementpositionszeilen entspricht, die zwischen der Zeile, die die Basispunktposition enthält, und der Bildelementposition existieren, in die geschrieben wird.

Revendications

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1. Procédé de stockage de données de caractères dans une mémoire de fond d'un appareil d'affichage de données à fonctionnement numérique pour afficher, sous forme d'une entité, sur un

- écran d'un dispositif d'affichage, une certaine quantité de données qui est représentée par des codes numériques stockés dans une mémoire d'affichage, les données affichées se présentant sous la forme de pixels ou de points discrets, chacun d'eux ayant sa couleur et/ou sa luminance définies par un code numérique respectif dans la mémoire d'affichage à un emplacement correspondant à la position du pixel dans le dispositif d'affichage, l'appareil comprenant un processeur commandé par programme pour commander numériquement le stockage, la sélection et l'affichage de données, caractérisé en ce qu'il consiste à générer un sous-programme de code machine respectif pour chaque caractère d'une pluralité de caractères, de telle sorte que l'exécution de chaque sous-programme de code machine par le processeur amènera celui-ci à écrire des codes numériques dans des emplacements de la mémoire d'affichage en fonction de la forme du caractère correspondant, et à stocker les sous-programmes de code machine à des emplacements respectifs de la mémoire de fond.
2. Procédé selon la revendication 1, caractérisé en ce que le sous-programme de code machine qui forme les données stockées pour un caractère comprend une série d'instructions exécutables à l'intérieur desquelles sont codées des positions de pixels respectives relatives à une position de point de base qui localise la position du caractère sur l'écran, lesdites positions de pixels relatives définissant la forme du caractère.
3. Procédé selon la revendication 2, caractérisé en ce que, pour écrire les données de pixels pour le caractère dans la mémoire d'affichage, le sous-programme de code machine comprend une première autre instruction qui spécifie, dans un premier registre, des données qui identifient la couleur dans laquelle le caractère doit être affiché, et une deuxième autre instruction qui spécifie, dans le deuxième registre, l'emplacement dans la mémoire d'affichage qui doit être la position du point de base pour le caractère.
4. Procédé selon la revendication 2 ou 3, caractérisé en ce que chaque position de pixel est identifiée dans l'instruction de code machine en question par un comptage direct des pixels, ligne par ligne, à partir de la position du point de base.
5. Procédé selon la revendication 2 ou 3, caractérisé en ce que chaque position de pixel est identifiée dans l'instruction de code machine en question par un comptage de positions de pixels qui comprend un nombre décalé correspondant à une ou plusieurs lignes de positions de pixels entières qui existent entre la ligne contenant la position du point de base et la position de pixel qui y est écrite.
6. Procédé de stockage de données de caractères dans une mémoire de fond d'un appareil d'affichage de données à fonctionnement numérique pour afficher, sous la forme d'une entité, sur un écran d'un dispositif d'affichage, une certaine quantité de données qui est représentée par des codes numériques stockés dans une mémoire d'affichage, les données affichées se présentant sous la forme de pixels ou de points discrets, chacun d'entre eux ayant sa couleur et/ou sa luminance définies par un code numérique respectif de la mémoire d'affichage à un emplacement correspondant à la position du pixel dans le dispositif d'affichage, l'appareil comprenant un processeur commandé par programme pour commander numériquement le stockage, la sélection et l'affichage de données, caractérisé en ce qu'il comprend le stockage des données de caractère dans la mémoire de fond en tant que sous-programme de code machine respectif pour chaque caractère d'une pluralité de caractères, de telle sorte que l'exécution de chaque sous-programme de code machine par le processeur amènera celui-ci à écrire des codes numériques dans des emplacements de la mémoire d'affichage en fonction de la forme du caractère correspondant, la sélection d'un caractère à afficher, la sélection de la position du caractère sur l'écran d'affichage, la provocation de l'exécution du sous-programme correspondant pour écrire des données de pixels dans les emplacements de mémoire appropriés de la mémoire d'affichage, et la provocation des données de la mémoire d'affichage à s'afficher sur l'écran d'affichage.
7. Appareil d'affichage de données à fonctionnement numérique pour afficher, sous la forme d'une entité, sur un écran d'un dispositif d'affichage (1), une certaine quantité de données qui est représentée par des codes numériques stockés dans une mémoire d'affichage (5), les données affichées se présentant sous la forme de pixels ou de points discrets, chacun d'eux ayant sa couleur et/ou sa luminance définies par un code numérique respectif de la mémoire d'affichage à un emplacement correspondant à la position du pixel dans le dispositif d'affichage, l'appareil comprenant un processeur commandé par programme (3) pour commander numériquement le stockage, la sélection et l'affichage de données, caractérisé par des moyens (3) permettant de stocker des données de caractères dans une mémoire de fond (4) sous la forme d'un sous-programme de code machine respectif pour cha-

- que caractère d'une pluralité de caractères, de telle sorte que l'exécution de chaque sous-programme de code machine par le processeur amènera celui-ci à écrire des codes numériques dans des emplacements de la mémoire d'affichage en fonction de la forme du caractère correspondant, des moyens (3) pour sélectionner un caractère pour affichage, et des moyens (3) pour amener le sous-programme appartenant au caractère sélectionné à être exécuté pour transférer des données de pixels dans les emplacements de mémoire appropriés de la mémoire d'affichage (5). 5 10
- 8.** Appareil d'affichage de données selon la revendication 7, caractérisé en ce que le sous-programme de code machine qui forme les données stockées d'un caractère comprend une série d'instructions exécutables à l'intérieur desquelles sont codées des positions de pixels respectives relatives à une position de point de base qui localise la position du caractère sur l'écran, lesdites positions de pixels relatives définissant la forme du caractère. 15 20 25
- 9.** Appareil d'affichage de données selon la revendication 8, caractérisé en ce que, pour écrire les données de pixels du caractère dans la mémoire d'affichage, le sous-programme de code machine comprend une première autre instruction qui spécifie, dans un premier registre, des données qui identifient la couleur dans laquelle le caractère doit être affiché, et une deuxième autre instruction qui spécifie, dans un deuxième registre, l'emplacement dans la mémoire d'affichage qui doit être la position du point de base pour le caractère. 30 35
- 10.** Appareil d'affichage de données selon la revendication 8 ou 9, caractérisé en ce que chaque position de pixel est identifiée dans l'instruction de code machine concernée par un comptage direct des pixels ligne par ligne à partir de la position du point de base. 40 45
- 11.** Appareil d'affichage de données selon la revendication 8 ou 9, caractérisé en ce que chaque position de pixel est identifiée dans l'instruction de code machine concernée par un comptage de positions de pixels qui comprend un nombre décalé correspondant à une ou plusieurs lignes de positions de pixels entières qui existent entre la ligne contenant la position du point de base et la position du pixel qui y est écrite. 50 55

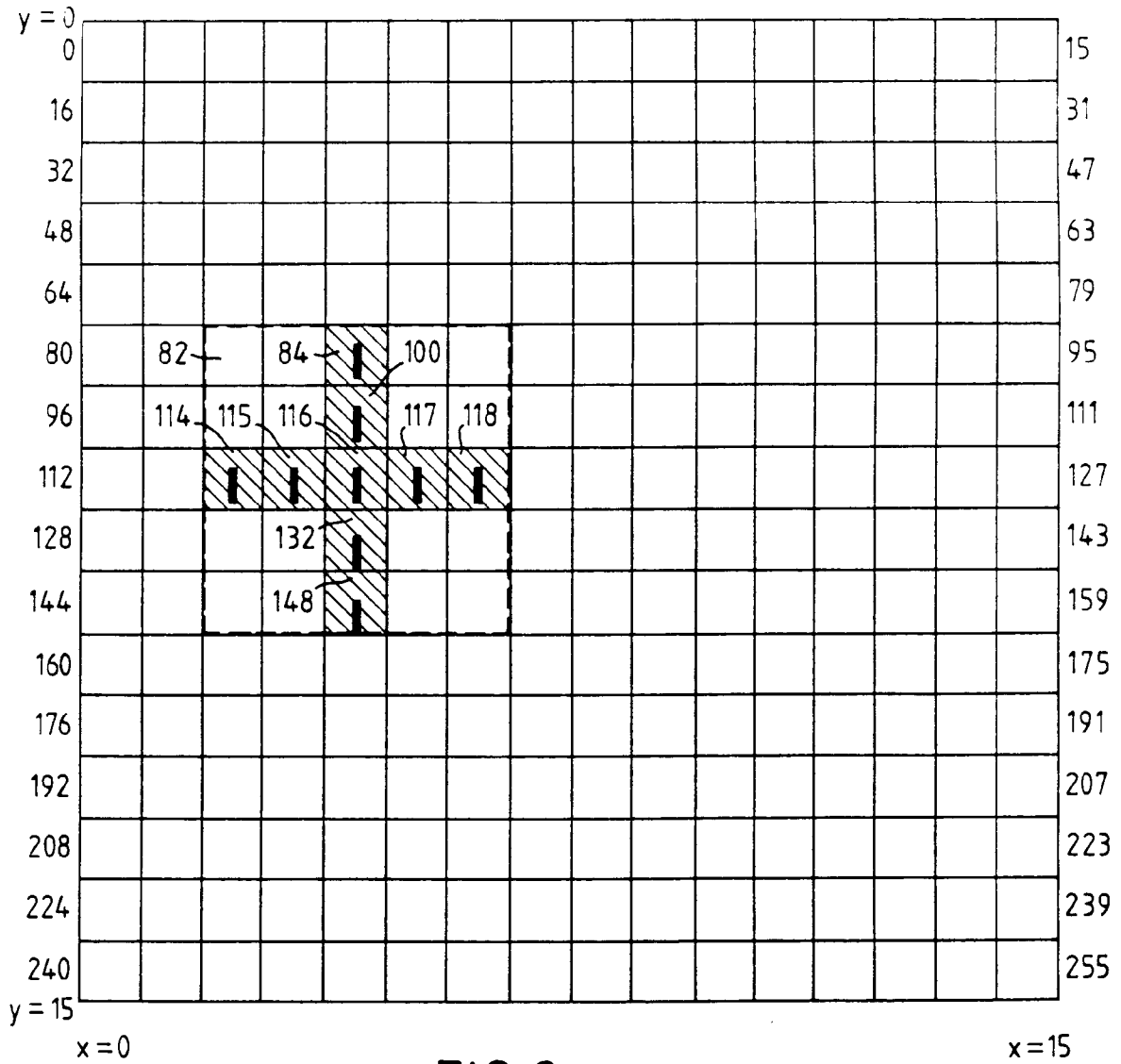


FIG. 2

	<u>CH</u>		<u>B</u>
<u>b1</u>	0 0 1 0 0		0 0 0
<u>b2</u>	0 0 1 0 0		0 0 0
<u>b3</u>	1 1 1 1 1		0 0 0
<u>b4</u>	0 0 1 0 0		0 0 0
<u>b5</u>	0 0 1 0 0		0 0 0

FIG. 3

<u>M/C INSTR.</u>	<u>PIXEL</u>
	<u>POSN</u>
1. MOVE B # 2, DO	= COLOUR
2. MOVE B # 82, AO	= 82
3. MOVE B DO, 2 (AO)	= 84
4. MOVE B DO, 18 (AO)	= 100
5. MOVE B DO, 32 (AO)	= 114
6. MOVE B DO, 33 (AO)	= 115
7. MOVE B DO, 34 (AO)	= 116
8. MOVE B DO, 35 (AO)	= 117
9. MOVE B DO, 36 (AO)	= 118
10. MOVE B DO, 50 (AO)	= 132
11. MOVE B DO, 66 (AO)	= 148
12. JUMP ----	

FIG. 4

<u>M/C INSTR.</u>	<u>PIXEL</u>
	<u>POSN</u>
1. MOVE B # 2, DO	= COLOUR
2. MOVE B # 148, AO	= 148
3. MOVE B DO, (AO-16)	= 132
4. MOVE B DO, (AO-32)	= 116
5. MOVE B DO, (AO-48)	= 100
6. MOVE B DO, (AO-64)	= 84
7. ADD DO, # - 2(AO-64)	= 114
8. ADD DO, # - 1(AO-64)	= 115
9. ADD DO, # + 1(AO-64)	= 117
10. ADD DO, # + 2(AO-64)	= 118
11. JUMP ----	

FIG. 5

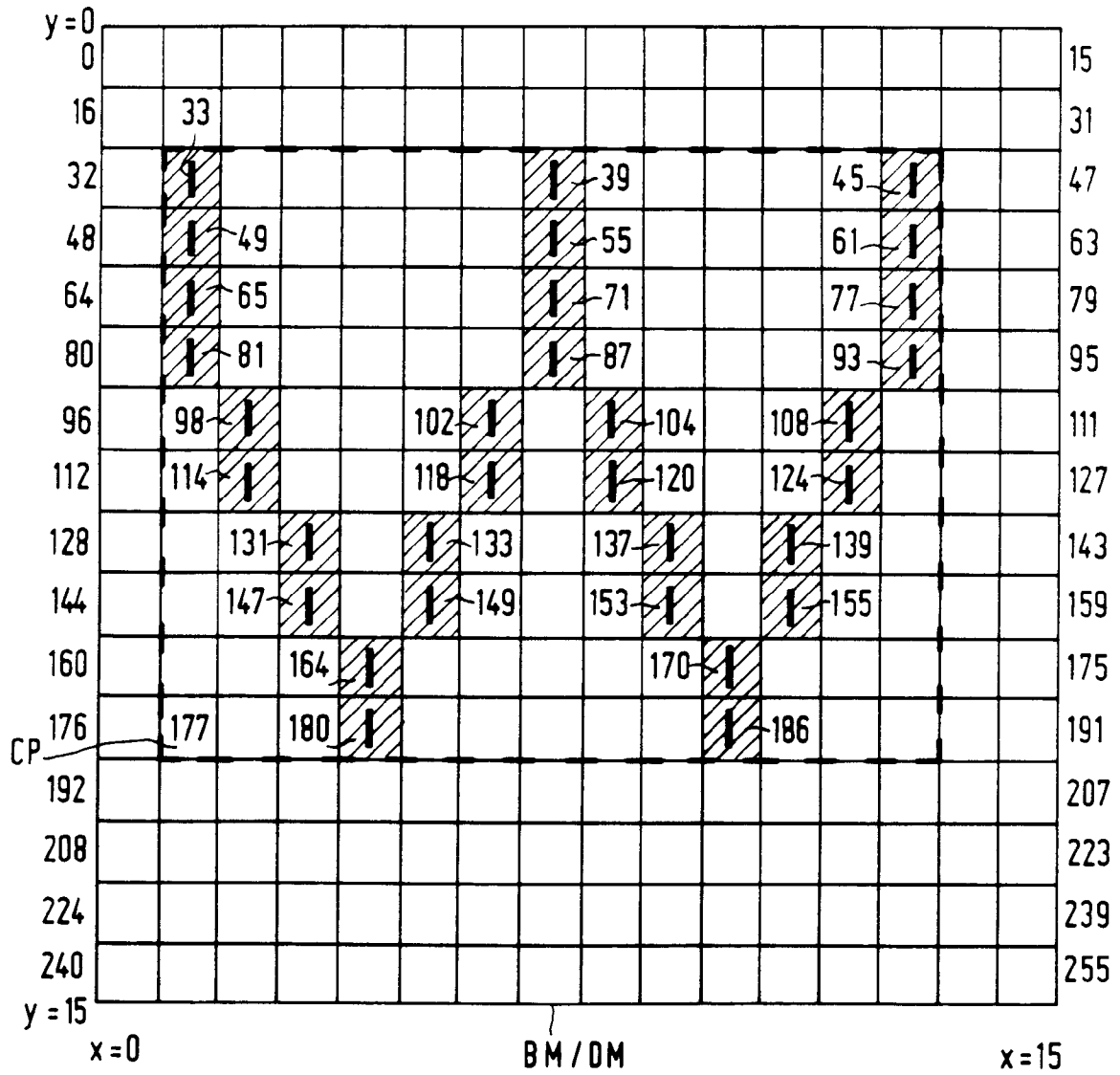


FIG.6

	<u>M/C INSTR.</u>	<u>PIXEL</u>	<u>POSN</u>
1.	MOVE B # 2, DO	=	COLOUR
2.	MOVE B # 177, AO	=	177
3.	MOVE B DO, 3 (AO)	=	180
4.	MOVE B DO, 9 (AO)	=	186
5.	MOVE B DO, -7 (AO)	=	170
6.	MOVE B DO, -13 (AO)	=	164
7.	MOVE B DO, -22 (AO)	=	155
8.	MOVE B DO, -24 (AO)	=	153
9.	MOVE B DO, -28 (AO)	=	149
10.	MOVE B DO, -30 (AO)	=	147
11.	MOVE B DO, -38 (AO)	=	139
12.	MOVE B DO, -40 (AO)	=	137
13.	MOVE B DO, -44 (AO)	=	133
14.	MOVE B DO, -46 (AO)	=	131
15.	MOVE B DO, -53 (AO)	=	124
16.	MOVE B DO, -57 (AO)	=	120
17.	MOVE B DO, -59 (AO)	=	118
18.	MOVE B DO, -63 (AO)	=	114
19.	MOVE B DO, -69 (AO)	=	108
20.	MOVE B DO, -73 (AO)	=	104
21.	MOVE B DO, -75 (AO)	=	102
22.	MOVE B DO, -79 (AO)	=	98
23.	MOVE B DO, -84 (AO)	=	93
24.	MOVE B DO, -90 (AO)	=	87
25.	MOVE B DO, -96 (AO)	=	81
26.	MOVE B DO, -100(AO)	=	77
27.	MOVE B DO, -106(AO)	=	71
28.	MOVE B DO, -112(AO)	=	65
29.	MOVE B DO, -116(AO)	=	61
30.	MOVE B DO, -122(AO)	=	55
31.	MOVE B DO, -128(AO)	=	49
32.	MOVE B DO, -132(AO)	=	45
33.	MOVE B DO, -138(AO)	=	39
34.	MOVE B DO, -144(AO)	=	33
35.	JUMP ----		

FIG.7

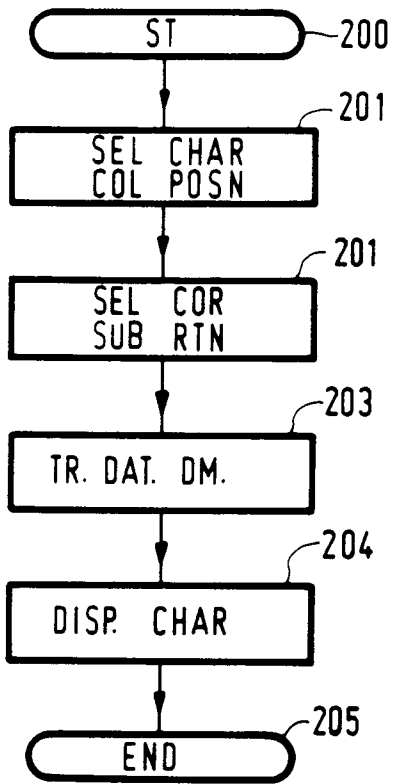


FIG. 8

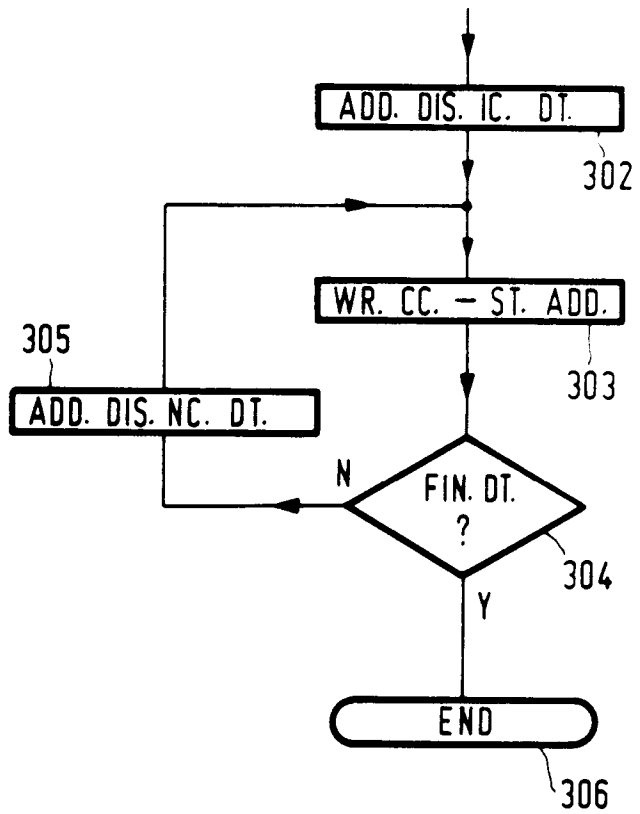


FIG. 9